

REVIEW

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A mini-review focusing on ambient-pressure chemical vapor deposition (AP-CVD) based synthesis of layered transition metal selenides for energy storage applications

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With high power consumption and energy densities in demand, there has been a surge in the research on next-generation electrochemical energy storage devices (lithium-ion batteries (LIBs), sodium-ion batteries (NIBs), and magnesium-ion batteries (MIBs)). 2D layered transition metal selenides (TMSeS) are promising in terms of scalability and portability for electronic devices and electric vehicles. Such large-scale applications rely on exploring these TMSeS containing high capacity, good cycling stability, and superior rate capability. Their exotic anisotropic properties have pushed their research towards energy storage. Since chemical vapor deposition (CVD) is an efficient and scalable technique to synthesize these materials, we summarize the synthesis of TMSeS using ambient-pressure CVD and highlight their many properties, preparation methods, and applications in LIBs, NIBs, and MIBs. We discuss how tuning surface morphology dominates the reaction kinetics. This review finally discusses the challenges and opportunities of these materials in terms of modifications in synthesis and their constant applications in the ever-growing field of energy storage.

Introduction

High energy and power densities are the most sought-after aspects of energy storage. Layered 2D electrode materials are of

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high significance as their performance in energy storage systems relies on their intricate structure–property relationships. The novel discovery of graphene in 2004 and its successful implementation since then has paved a pathway for 2D layered materials, which exhibit unique properties.^{1–10} Among the many layered materials, transition metal dichalcogenides (TMDCs) with an X–M–X structure (M: transition metal element; X: S, Se, or Te) have the utmost growing potential for application in energy storage, catalysis, gas sensing, nanophotonics, *etc.*^{11–18}

The layered structure of these TMDCs makes them active candidates for energy storage and supercapacitor applications due to the following advantages: (i) the large specific surface area ensures a large contact area between the active materials and the electrolyte, enabling fast “Faradaic” and “non-faradaic” reactions at the surfaces of layered TMDCs; (ii) the edge sites of layered TMDCs act as adsorption sites for metal ions and thus contribute to extra metal-ion storage capacities; (iii) the adjacent X–M–X layers in layered TMDCs are coupled by weak van der Waals forces and the interlayer space between layers can realize fast ion diffusion, insertion, and extraction, and better material utilization during the metal-ion insertion process; and (iv) the thin and flexible characteristics of 2D TMDC nanosheets allow them to be incorporated into flexible electrochemical/energy storage devices.^{6,19–26}

2D layered TMDCs range from insulating to semiconducting and semi-metallic to true metallic.^{27–35} Therefore, these materials are used in electronics, catalysis, photovoltaics, and batteries.^{36–44} Often, layered TMDCs have different polymorphs, such as 1T, 2H, and 3R (trigonal, hexagonal, and rhombohedral phases, respectively, as shown in Fig. 1(a)–(c)).^{45–47} Different polymorphs endow layered TMDCs with other properties, resulting in varying

electrochemical performance in energy storage devices. For example, the thermodynamically stable phase of WSe₂ is 2H, which is a semiconducting phase.^{11,48} However, this material can exhibit a metallic 1T phase under certain conditions, *e.g.*, due to alkali metal intercalation or solvothermal synthesis conditions.⁴⁹ The key to producing metastable crystal phases has also been recently reported based on a kinetically controlled colloidal synthesis regime to enable such metastable phase formation.⁴⁹ In such cases, the electrochemical performance (in terms of electrocatalysis) of these modified or structure-altered 2D layered TMDCs increases due to the increased conductivity.

Transition metal sulfides (TMSs) and transition metal selenides (TMSe) are extensively investigated as promising electrode materials in energy storage systems.^{47,50,51} Although TMSes have similar properties to transition metal sulfides, we often notice that the volume energy density and rate capability of TMSes are comparatively higher than those of most of the transition metal sulfides due to the relatively higher density and electrical conductivity of selenium. Recently, TMSes have received much interest from researchers as promising electrode materials in electrochemical energy storage systems.^{52–54} A typical layered or sandwiched-like structure is always observed for these TMSes, where the metal atom and the selenium atoms are covalently bonded to each other (in the case of transition metal di-selenides, the metal atom stays in the center between two selenium atoms).⁵⁵ Atoms in these three layers are covalently bonded, whereas the individual sheets are held by weak van der Waals forces. The latter force is weak enough to allow other atoms to reversibly intercalate in the inter-chain spaces, thereby forming intercalation compounds or intercalates; thus, the energy can be stored in the layered conductors.

In energy storage applications, layered TMSes are often hybridized with other materials, such as carbon, metal sulfides, metal oxides, and conducting polymers.^{56–62} Carbon finds usage as a composite with many-layered TMSes due to its high conductivity that accelerates electron transport in layered TMSe/carbon hybrids.⁶³ Such behavior reduces the charge transfer resistance and improves the electrochemical performance. Besides possessing the advantages of individual components, layered TMSe-hybrids exhibit unexpected properties. For instance, the aggregation of TMDC nanosheets induced by van der Waals attraction can be effectively inhibited when hybridizing with other materials.^{64–73} Thus, the large surface area of TMDC nanosheets could be well maintained. In addition, other layered TMSes with intricate hierarchical structures or variable morphology can be synthesized by growing layered TMSes on substrates.⁷⁴ This leads to excellent electrochemical performance in many systems.

Many widely researched TMSes have been applied in electrochemical energy storage because of their unique electrical properties.^{75–78} Compared to traditional graphite in lithium-ion batteries, transition metal selenides have a much higher theoretical capacity.⁶⁸ Most of these TMSes can accommodate sodium and magnesium ions, in addition to lithium intercalation.^{79–82} TMSes also have good merits such as low cost, earth abundance, and convenience for fabrication, all of which greatly benefit their

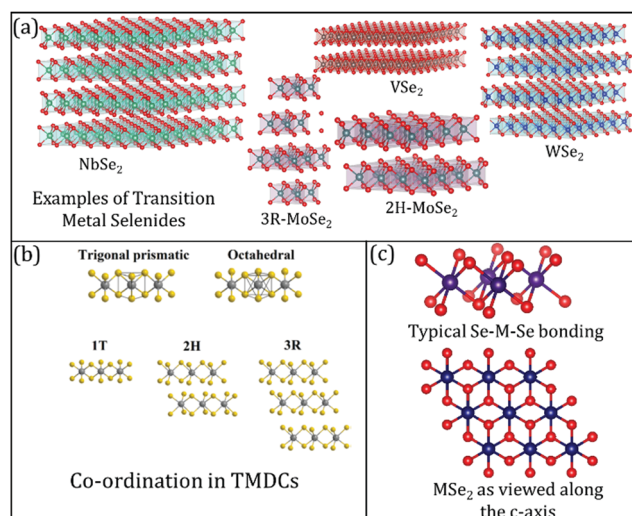


Fig. 1 (a) Common examples of transition metal selenides used in lithium-ion batteries (LIB), sodium-ion batteries (NIB), and magnesium-ion batteries (MIB); (b) differences between the co-ordinations' geometries in TMDCs, in general. Reproduced with permission from ref. 7; (c) visualising the transition metal selenide bonding between the transition metal (M) and selenium (Se) along different axes.



practical application in many electrical devices depending on the energy storage characteristics of TMSeS.^{83,84} More importantly, these characteristics make them up-and-coming candidates for the electrodes of proposed electrochemical energy storage systems. Recently, many groups have highlighted their significant work and the impact of the applications of transition metal chalcogenides or modified-transition metal chalcogenides in energy conversion and storage.⁸⁵ But, they often focus on sulfides more than selenides. This short review focuses on and highlights the fabrication of transition metal selenides, their challenges while being prepared by ambient pressure chemical vapor deposition (CVD), and the areas where they can be extensively explored. We systematically categorize their applications in electrochemical energy storage systems, including lithium-ion batteries, sodium-ion batteries, and magnesium-ion batteries.

Methods to prepare 2D layered transition metal selenides (TMSeS)

The successful preparation of layered TMSeS with high microstructural quality is crucial for their application in energy storage devices. During the past decade, various preparation methods have been developed to realize the morphology and structure-controlled synthesis of pure layered TMSe materials.^{86–91} Meanwhile, hybridization of layered TMSeS with other materials, *e.g.*, carbon materials, metal oxides, and metal sulfides, is also beneficial for their energy storage application due to the synergistic effect between the new materials and the composites. This signifies how the development of TMSeS has been dramatically enriched by adopting one or more different techniques to enable significant tailoring of material properties in the past few years. The most commonly used techniques to prepare TMSeS and their nanohybrids are the hydrothermal/solvothermal method, chemical vapor deposition (CVD), microwave irradiation, chemical bath deposition, ion-exchange, chemical exfoliation, *etc.*^{84,92–94} Here we only highlight the CVD-based routes (especially examples of ambient pressure CVD growth) for preparing TMSeS and discuss their implementation in energy storage separately.

Advantages of chemical vapor deposition (CVD) towards TMSe fabrication

As explained in the previous sections, 2D TMSeS have many different atomic arrangements and electronic states. Each of these phases or polymorphs contributes to their catalytic or energy storage possibilities. 2H TMSeS are characterized by trigonal prismatic coordination of metal atoms in each layer, and the individual layers are stacked in an ABAB order. 3R TMSeS simply contain a shifted position of metal and chalcogen atoms, resulting in a different stacking order in their unit cell. On the other hand, metastable TMSeS usually have an octahedral configuration of the metal atoms in an AA stacking mode. Sometimes, these 1T TMSeS can distort into the 1T' phase. Therefore, a meticulous synthesis route having proper phase-control in 2D TMSeS is highly important. Among the different methods, chemical vapor deposition (CVD) is a very reliable synthesis route. CVD also provides a simple fabrication

approach for high-quality variable-layer TMSeS on suitably selected substrates. Therefore, monolayer TMSeS with uniform film thickness can be easily synthesized using CVD. Issues with interface modification, such as phase/boundary modulation, are best realized in vapor deposition routes for 2D TMSeS. Even, in-plane activation strategies can be rationally designed for many TMSe systems. So, up-scalable TMSe synthesis is possible by CVD.

There are many examples of TMSe electrodes synthesized by CVD and used for energy storage applications. For example, Xiao *et al.* have reported the interlayer coupling in CVD-grown 2D MoSe₂ flakes with screw dislocations on amorphous SiO₂ substrates.⁹⁵ They show how screw dislocations slightly widen the d spacing in MoSe₂. They also attribute the weakened interlayer coupling to the presence of spirals that breaks the reflection and inversion symmetries of the lattices.⁹⁵ Often scalability issues arise while trying to increase the yield of TMSeS in the traditional CVD set-ups. So, proper precursor control and spatially uniform growth can be achieved using vertical CVD (VCVD). As an example, Lei *et al.* used VCVD to synthesize uniform TMSe monolayers by controlling the precursor concentration, gas flow, and temperature.⁹⁶

Apart from direct-vapor assisted CVD synthesis on variable substrates, many complex TMSe nanostructures can also be synthesized. Such structural engineering will surely attract increasing attention for both fundamental studies and practical applications. Until now, hard-templating and self-template/template-free methods are some of the commonly researched areas for battery-grade TMSe synthesis.⁹⁷ These can lead to complex structures having spherical, tubular, and polyhedral shapes, which are compatible with energy storage systems. In some of these cases, precursor modification is done in CVD, but we see very few reports which completely focus on TMSe fabrication starting with their growth and further modification in CVD.

Many in-plane variations and phase transformations can allow new accessible TMSe functionalities when prepared by CVD. Under suitable growth conditions, such TMSe modification can be achieved partially or entirely. The layer-by-layer variation can also be easily controlled in the TMSeS when subjected to proper parameter control to understand their microstructural evolution and relations towards electrode preparation.

The reactivity of TMSeS can be effectively modulated through interface engineering, which is of paramount importance in exploring the intrinsic activity of their in-plane atoms. The huge advancement in understanding reaction optimization, substrate choice, and precursor optimization have paved the way for modulating the electronic structure of TMSeS by CVD-based routes, which include defect engineering, substitutional doping, and hybridization with different active functional species. In fact, based on the existing reports of TMSe for batteries, it can be assumed that the next-generation energy materials require further research based on TMSe surface and interface engineering in different CVD-based systems. Therefore, a proper insight into intermediate formation, growth and seeding strategies can be advantageous towards synthesizing the next generation layered TMSe battery materials.



Chemical vapor deposition (CVD)-based growth routes for synthesizing layered TMSe

Four common routes for TMSe growth using different CVD-based systems have been reported so far.⁹⁸ They can be summarized as follows:

(1) **Thermal decomposition of elemental precursors.** The thermal decomposition route can be a single step or a two-step CVD process. A single step CVD-assisted thermal decomposition can indicate the use of single growth zone or a single furnace when elemental chalcogen reacts with metal or metal oxide precursors. This process can include a pre-annealing step in the presence of H₂ and subsequent reaction with the incoming chalcogen vapor. The parameter variations improve the crystallinity and electrical performance of the as-synthesized TMDCs.

(2) **Direct chalcogenization of pre-deposited metal or metal oxide thin films.** Usually, this procedure involves the deposition of the transition metal or its oxide on a suitable substrate, such as Si, Si/SiO₂, MgO, *etc.* Techniques such as e-beam evaporation, spin coating, sputtering, *etc.*, assist in the deposition of the transition metal thin film or its oxide using either ambient pressure CVD or low-pressure CVD. A well-known example using this procedure is the thermal-assisted soft selenization of metal or metal-oxide thin films on different substrates.

(3) **Direct vapor transport of TMSe to deposit their high-quality monolayers or few layers on substrates.** This synthetic procedure uses already synthesized bulk TMSe to be directly deposited on different substrates. It involves heating the TMSe of choice at elevated temperatures on Si or Si/SiO₂ directly to form large-domain-sized crystals. The layers of such large-area layered single-crystal deposition can be varied by controlling the deposition time and temperature.⁹⁹

(4) **Synthesis of TMSe using CVD with vapor phase reaction or solid-vapor interaction of two precursors in different growth zones or different furnaces.** This type of CVD-based process is classified into (a) atmospheric pressure CVD (APCVD), (b) modified metal-organic CVD (MOCVD), (c) aerosol-assisted CVD (AACVD), and (d) low-pressure CVD (LPCVD). These are the most common techniques to synthesize variable TMSe layers either by a top-down or a bottom-up growth process. This indicates that the choice of precursors can be transition metal oxide (APCVD), transition metal (APCVD), transition metal halide (MOCVD or AACVD), transition metal carbonyl (MOCVD or AACVD), transition metal thin film (APCVD, MOCVD, or AACVD), transition metal oxide thin film (APCVD, MOCVD, or AACVD) and chalcogen (such as elemental Se or H₂Se gas or Ar/H₂ together with the sublimating Se) precursors.¹⁰⁰

Reports focusing on why ambient pressure CVD synthesis is favorable for layered TMSe

Different strategies have been applied within the CVD technique to obtain high quality, large-scale mono- and few-layer TMDCs to explore their promising properties in various fields of applications.⁹⁸ Typically, all the CVD-based synthesis methods have been classified into “top-down” or “bottom-up” approaches.

Generally, exfoliation methods are considered “top-down” while CVD is considered “bottom-up”.^{101–104} In a typical CVD process, precursors are reacted or decomposed and deposited as a mono- or few-layer film on the exposed substrate at relatively high temperatures. Many fundamental research experiments have been done to produce ultrathin TMDC materials with high crystal quality, scalable lateral dimensions, tunable thickness, and excellent electronic properties.

As we see from the points mentioned in the previous section, CVD produces variable TMSe architectures and microstructures with high quality and precisely controlled layer numbers.^{100,105} This particular synthesis method is favorable for its simplicity, ease of TMSe preparation, efficient handling of elemental precursors, and precise control over defect-engineering or structure modification. Besides the planar SiO₂/Si, graphite paper, and metal foil substrates, carbon cloths, graphene foams, and other homemade porous inorganic foams are often employed as templates to utilize their intrinsic porous structures to prepare electrodes with large surface areas.^{106–111} For example, Geng and co-workers prepared hierarchical 3D MoS₂ nanosheet arrays by a CVD process using graphene-mediated Ni foam as a template.¹¹² They first grew graphene on the Ni foam by CVD. This was treated with the molybdenum and sulfur vapors derived from the decomposition of tetrakis (diethylaminodithiocarbamate) molybdate(IV) (Mo(dedtc)₄) at 400 °C in a tube furnace to form vertically aligned MoS₂ nanosheet arrays on the surface of the graphene. Besides direct growth on substrates, 3D TMDC architectures can be transformed from 3D metal oxide structures by reacting with chalcogen vapors. In a recent study, vertical MoO₂ nanosheet arrays were first grown on insulating SiO₂/Si or conductive carbon cloth by a CVD process with MoO₃ and H₂.¹¹³ After calcination in Se vapor, vertical core-shell MoO₂/MoSe₂ nanosheet arrays were fabricated. Among all synthesis techniques, CVD is promising for synthesizing high-quality TMD layers with controllable layer number and domain size and excellent properties due to its simplicity and compatibility with industry standards. Compared with the chemical vapor transport (CVT) method, which is commonly used to synthesize bulk single-crystal materials, the typical CVD method, as discussed, is an efficient process to synthesize any mono- or few-layer TMDC, especially TMSe.¹¹⁴ For CVT-based growth, thermodynamic equilibrium is closely maintained by tuning the reaction temperature. The temperature ramp in CVT synthesis is generally slow which takes a few days to complete the reaction. A closed reaction vessel, such as quartz ampoules, sealed with the reactants under an inert atmosphere is generally used for CVT syntheses. In comparison, being kinetically driven, CVD processes are often faster than CVT to obtain few layers of material for structural studies and device applications. This makes CVD a preferred and reliable route to prepare high-quality two-dimensional (2D) TMSe. Typically, CVD-based growth of 2D TMSe involves activated chemical reactions of precursors in a specially designed environment. The precursors, conditions, atmosphere, substrates, and catalysts (if necessary) are several key factors affecting the final quality of 2D TMSe or TMSe.



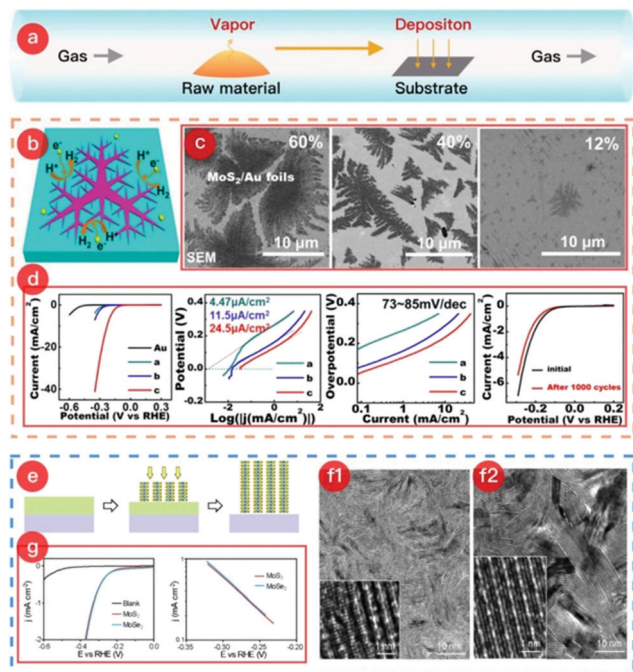


Fig. 2 (a) Schematic of the CVD process. (b) Schematic of the dendritic MoS_2 @Au foil. (c) SEM images of each MoS_2 @Au sample with MoS_2 coverages of 60%, 40%, and 12%, named a, b, and c, respectively. (d) Electrochemical performances of samples for HER. (The images of (b–d) were reproduced with permission. Copyright 2014, American Chemical Society, ref. 119.) (e) Schematic of the synthesis mechanism of MoS_2 @Mo or MoSe_2 @Mo. (f1) SEM image of the MoS_2 @Mo sample. (f2) SEM image of the MoSe_2 @Mo sample. (g) HER catalytic performance of both MoS_2 @Mo electrode and MoSe_2 @Mo electrode. (The images of (e–g) were reproduced with permission. Copyright 2013, American Chemical Society, ref. 117.)

CVD is also reliable for preparing TMCs@metal electrodes (where the TMC can be a TMS or TMSe) because of its simple setup and straightforward synthesis.^{115,116} This benefits the selenization or sulfurization process and helps with *in situ* reduction or doping processes. A figure highlighting the CVD method is presented in Fig. 2, showing a typical example of MoS_2 grown on Au foil.^{117–119} The growth schematic is also shown in Fig. 2(e). The starting materials during the synthesis, such as chalcogen powder, precursors, and metal oxides, are packed into quartz boats, which are subsequently placed in the upstream heating zone center of a tube furnace. Simultaneously, the pre-treated metal substrate (typically cleaned or acid-washed) is placed in the downstream heating area of the furnace. During the experiment, the raw materials become gaseous in the upstream zone and move downstream with the gas flow.

Meanwhile, the reaction between the raw material vapor and the substrate occurs at the desired temperature, and the product is deposited on the metal surface. After cooling to room temperature, a TMCs@metal self-supporting electrode is obtained. The facile growth method shows how a dendritic MoS_2 monolayer on Au foil is easily formed *via* the CVD method. The scheme clearly illustrates the increased number of active sites on the edge of the dendritic MoS_2 monolayer (see Fig. 2b). Furthermore, this study investigated the influence of

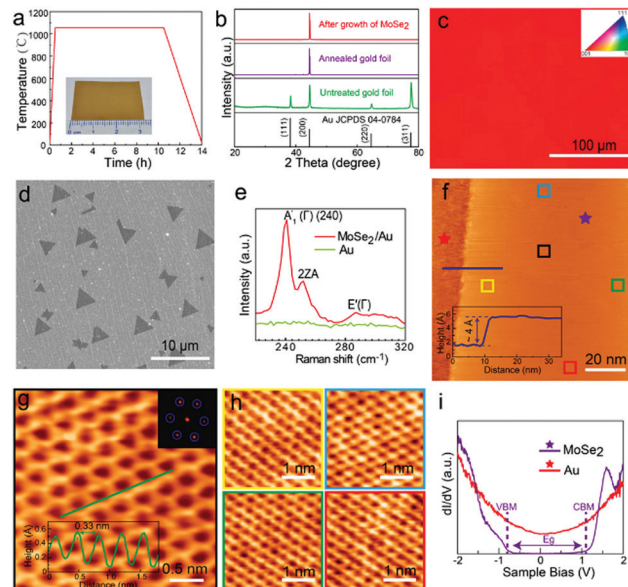


Fig. 3 Substrate pre-treatment and characterization of monolayer MoSe_2 : (a) annealing pretreatment of Au foil. The inset shows Au foil after annealing; (b) XRD patterns of the Au foil before and after the annealing process; (c) EBSD image of a randomly selected position on the processed Au foil; (d) SEM image of triangular MoSe_2 grown on Au foil by CVD; (e) Raman spectroscopy of the MoSe_2 domain and the Au substrate; (f) large-scale STM image of MoSe_2 with the bare Au substrate; the line profile across the substrate and MoSe_2 is presented in the inset; the height of MoSe_2 is around 4 Å; the sample bias is 0.5 V and the tunnel current is 1 nA; (g) atomically resolved STM image of a small MoSe_2 domain with the honeycomb structure; the sample bias is 180 mV and the tunnel current is 200 pA. The FFT image shown in the inset reveals that the lattice constant is around 0.33 nm; (h) atomic resolution images (sample bias 5 mV, tunnel current 1 nA) at different positions on the MoSe_2 sheet marked by variable color squares in (f), demonstrating that the MoSe_2 domain is defect-free at different positions and has a long-range high-quality crystalline structure; (i) dI/dV spectra of MoSe_2 and the substrate obtained from red and purple stars in (f). (The images of (a–i) were reproduced with permission: copyright 2021, American Chemical Society, ref. 120.)

the synthetic temperature on MoS_2 coverage. As shown in Fig. 2c, temperatures of 880, 780, and 750 °C were applied during the growth of the dendritic MoS_2 monolayer, which led to coverages of 60, 40, and 12%, respectively. Interestingly, the sample with the lowest coverage range (12%) exhibited the best catalytic performance and highest stability for the HER (Fig. 2d) due to the action of the macroscopically uniform, high-quality MoS_2 monolayer.

In a similar fashion, Wu *et al.* have also synthesized MoSe_2 on (100) oriented Au foils (as per Fig. 3), thereby producing highly crystalline MoSe_2 .¹²⁰ The weak coupling between MoSe_2 and Au surface is highly useful for direct transfer of the MoSe_2 layers to other substrates (the quality of the layers being corroborated using Raman and STM techniques). Other examples of TMSe syntheses include ReSe_2 synthesis on gold foils using ReO_3 and Se powders, or ReO_3 and Se powders being reduced and transferred on Si/SiO₂ and sapphire substrates; ZrSe_2 on sapphire; NbSe_2 monolayers on Si/SiO₂; ZnSe on Zn foil, *etc.*^{27,28,30,31,35}

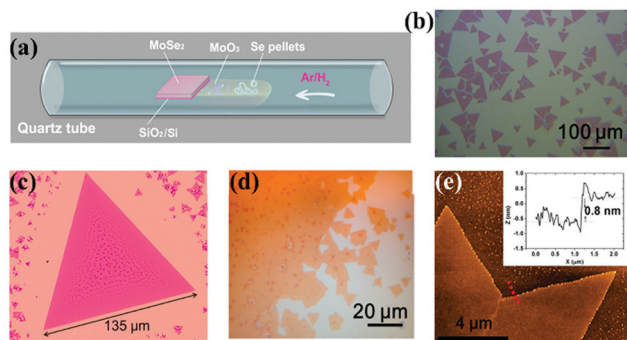


Fig. 4 MoSe₂ monolayer synthesis and morphology. (a) Schematic of the controlled synthesis of monolayer MoSe₂ via CVD. Se pellets and MoO₃ powder are positioned in the same ceramic boat at the center of the tube furnace. (b–d) Typical optical images of monolayer triangles and continuous film. Small bilayer domains with darker color can be observed in (c) and (d). (e) AFM height topography of monolayer MoSe₂, and the height profile (inset) showing a thickness of ~ 0.8 nm, as measured along the red dotted line. (Image reproduced with permission from American Chemical Society, ref. 121.)

Previous reports also show how Se pellets and transition metal oxide powders such as molybdenum oxide (MoO₃) powder have been used as precursors.¹²¹ When they are heated, and a clean Si wafer with a 275 nm SiO₂ top layer is placed face down in the boat, they yield large MoSe₂ triangular domains or films (as shown in Fig. 4(a)–(e)). In this case, H₂ was used for reducing the MoO₃ powder at 750 °C.¹²¹

There are reports of direct selenization of transition metal foils to obtain uniform and highly crystalline few-layered TMSe.³³ MoSe₂ on Mo foil is one of the best-known examples of TMSe growth. The material prepared by using such an approach by ambient pressure CVD has dimensions over the order of centimetres by directly selenizing annealed Mo foil surfaces in selenium (Se) vapor at 550 °C for 60 min under atmospheric pressure. After preparation, the obtained films are easily transferable to various substrates by removing the Mo foil using a diluted ferric chloride (FeCl₃) solution. The thickness of the thin films transferred onto a 285 nm SiO₂/Si substrate is in the range of 3.4 to 6 nm (as detailed in Fig. 5(a)–(k)).

Studies also focused on the importance of growth temperature and on how the metal/chalcogen flux in atmospheric pressure chemical vapor deposition influences the growth of MoSe₂ and WSe₂ on Si/SiO₂ substrates.⁹² From scanning electron microscopy and atomic force microscopy, it was confirmed that the growth temperature and transition metal flux dictate the domain sizes and microstructures.⁹² The triangular or hexagonal domains further evolve into branched structures as the growth temperature (metal flux) is changed. The competition between adatom attachment to the domain edges and diffusion of adatoms along the domain boundary determines the evolution of the observed growth morphology. Depending on the growth temperature and flux, two different branched structures, fractals and dendrites, were reported during ambient pressure CVD growth.⁹²

Apart from the elemental precursor or oxide precursor examples mentioned above, we also observe other materials

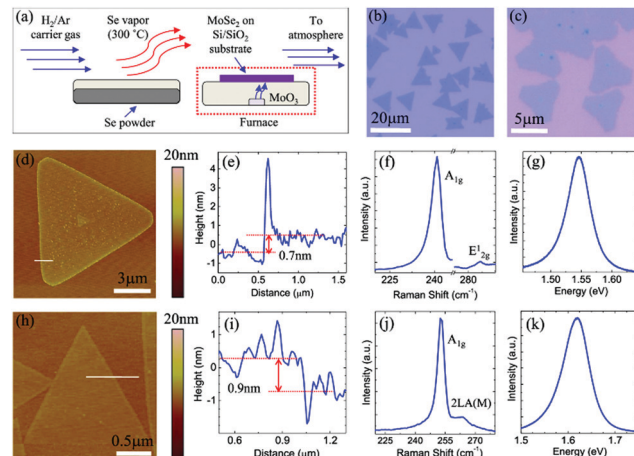


Fig. 5 (a) Schematic of the AP-CVD setup used for the growth of 2D TMDCs such as MoSe₂. Optical microscopy images of (b) MoSe₂ and (c) WSe₂ monolayer domains grown on Si/SiO₂. AFM image, height profile across the line marked on the AFM image, and Raman and PL spectra of (d–g) MoSe₂ and (h–k) WSe₂. (Figure reproduced from ref. 92.)

such as elemental powders being used. But such transition metal powder precursors are used in CVT synthesis than in CVD synthesis. Also, selenization of as-deposited oxide or transition metal thin films in ambient pressure CVD is well-known.¹⁰³ There are other reports based on the WSe₂ platelet formation on W foil by using already synthesized WSe₂ powder.¹²²

Other than the TMSe preparation routes in the ambient pressure CVD which concentrate mostly on the small area domains of triangles or islands formed on the exposed surfaces of the substrates, there are also previous reports which highlight continuous TMSe film formation. For example, Malik *et al.* discuss in a related review how semiconducting TMSe and TMSs have been synthesized using various single source precursors by CVD.¹²³ The synthesis procedure using such a method has been widely investigated to prepare defect-free and complex hetero-films of various TMSe and TMSs.¹¹⁰ But most of these syntheses use AACVD or MOCVD as their fundamental growth optimizing system. Additionally, evaporating oxide-based precursors on the substrate using MBE, or sputtering is also common for post-selenization treatments in AP-CVD.⁹⁴

Factors influencing the growth and development of TMSe by ambient pressure CVD synthesis

As we understand from the previous section, there are several factors influencing the growth characteristics of various TMSe. The growth and microstructural evolution of TMSe synthesized by ambient pressure CVD depend on

- type of precursors used in the synthesis,
- reaction temperatures and temperature gradients,
- substrates used for growth or deposition, and
- gases used for synthesis (and for post-treatments).

(a) Type of precursors used in the synthesis. Most synthetic procedures including transition metal oxide or metal powders with Se powders involve sublimation of both precursors. For oxide-based precursors, an additional reducing agent or



reducing step is used during APCVD synthesis. This can result in partial or full conversion of the powders to the corresponding TMSe as a bulk. The reaction can also be controlled in such a way that if an appropriate substrate is placed on top of the boat containing the powders on in a separate growth zone, a few layers of the TMSe are deposited on it. However, if transition metal foils are directly used as the growth precursors, the presence of native oxides, foil pre-annealing, surface etching treatments, available foil surface area, *etc.* determine the layer variation and growth of the TMSe.¹²⁴

(b) Reaction temperatures and temperature gradients between growth zones and furnaces. The available phase diagrams of the TM and Se determine the expected phases of the TMSes produced during ambient pressure CVD growth. This in turn also determines the reaction temperatures (which is also closely correlated with the melting temperatures of the TM or TMO). If multiple zones or furnaces are used for the ambient pressure CVD-based TMSe synthesis, the temperature gradient between the growth zones or furnaces influences the quality and number of layers of the final TMSe produced. Often elevated temperatures ranging from 300 to 1100 °C are used for TMSe growth.

(c) Substrates used for growth or deposition. As mentioned under point (a) above, vapours of sublimated TMSe or TMO + Se can be deposited on various substrates placed in different zones during synthesis. An inert carrier gas facilitates such transport of TMSe vapours. Choices of appropriate substrates for ambient pressure CVD growth of TMSes can be di-electrics such as Si/SiO₂, MgO, Al₂O₃, *etc.*, foils such as Au, glass or sapphires, *etc.* In this case, total reaction time influences TMSe quality, defect formation, domains, number of layers, *etc.*

(d) Gases used for synthesis (and for post-treatments). Inert atmospheres facilitate the transport of Se vapors during TMSe synthesis in APCVD. Argon (Ar), nitrogen (N₂), helium (He), hydrogen (H₂), and argon-hydrogen (Ar/H₂) mixtures are used depending on the reactants used. In many cases, an additional post-treatment annealing step is added which improves the crystallinity and the domain sizes of the TMSes.

The factors mentioned above influence

- (i) the number of layers of the TMSes formed,
- (ii) the quality of the layers (*i.e.*, if defects are present),
- (iii) the domain sizes,
- (iv) the ease of transfer of TMSe through mechanical or liquid-phase exfoliation.

Mechanistic insights and further examples of the microstructural fine-tuning of TMSes by ambient pressure CVD

The TMSe growth is directly correlated to their layers and lateral sizes. Generally, the fundamental premise for layer-by-layer growth from solid-gas reactions can follow any of the three widely known growth modes (Volmer-Webber, Frank Van-der Merwe, or Stranski-Krastanov), but an ideal highly crystalline TMSe growth should follow Frank-van der Merwe mode. Nevertheless, these growth modes can be properly verified if the substrate is only used for the deposition (such as TMSe deposition on Si, Si/SiO₂, sapphire, *etc.*). In such reactions, the TMSe vapors deposit and form nanocrystals by

random nucleation on the substrate surface. After nucleation, the gaseous sources tend to diffuse to the edges of the nuclei. The crystals show preferential growth along the lateral dimension of the nanocrystals, forming distinct nanosheet islands on the substrate surface. Such evolution on a prolonged reaction time can induce coalescence of adjacent nanosheets or layers into islands to produce continuous films. It is, therefore, necessary to develop a complete understanding of the seeding and growth profiles of the TMSe nanosheets. Perhaps the most critical factor in the usefulness of the gas-phase growth strategy is the orientation of the sheets on the substrate. TMSes can grow perpendicular (edge-maximizing) or parallel (epitaxial) to the substrate.¹²⁵ TMSe films grown in these differing orientations exhibit vastly different properties. For many such recent examples, the deposited materials grow perpendicular to the surface of the substrate to minimize the energy penalty due to the interactions of the two surfaces, unless specific experimental parameters are chosen to reduce this effect and obtain laterally oriented nanosheets.^{116,125} In this situation, several conditions are required to promote the epitaxial growth of TMSe nanosheets on a substrate.

There have been many other attempts to grow transition metal selenides using CVD. Zou's work reported that WSe₂ and W(Se_xS_{1-x})₂ nanoflakes on carbon nanofibers were synthesized by placing the Se and S powder upstream at a distance of approximately 20 cm from the furnace center, where the pre-prepared WO₃ NPs were placed.¹²⁶ Taking WSe₂ as an example, as the furnace temperature increased to 1000 °C at 5 °C min⁻¹, the Se vapor diffused and reduced the WO₃ NPs on the surface, forming volatile sub-oxides WO_{3-x}.¹²⁶ The formed WO_{3-x} were rapidly selenized into the WSe₂ product for lateral growth or incorporation of WSe₂ nanoflakes on the substrate. Through performing CVD experiments at different temperatures, Zou *et al.* synthesized metal selenide catalysts with other nanostructures, *e.g.*, nanoplates at 880 °C, nanosheets at 940 °C, and microparticles at 980 °C.¹²⁶ For fabricating the material of MoSe₂, Se powder and MoO₂ were separately placed in two ceramic boats and heated to a set temperature at a heating rate of 25 °C min⁻¹ under mixed gas flow (Ar and H₂). Changing the growth temperature changed the amount of Mo and Se vacancies in the following order: nanosheets > nanoplates > microparticles.¹²⁷ In addition, it also gives further insight into doping or substitution strategies of different TMSes using CVD.

Considering all the examples mentioned in the previous sections, we believe that more unique approaches will be developed for the CVD-based synthesis of transition metal selenides. Most of the syntheses highlighted above show the nature of growth based on films deposited on dielectric substrates. In the next section, we discuss how transition metal foils can be a suitable substrate for few-layer to bulk growth of various TMSes.

Importance of ambient pressure CVD-based TMSe syntheses using transition metal foils as self-nucleating precursors

There are many studies focusing on the synthesis of TMDCs from transition metal (TM) foil.¹²⁸⁻¹³¹ The existing literature indicates that a lot of research is still needed to understand the



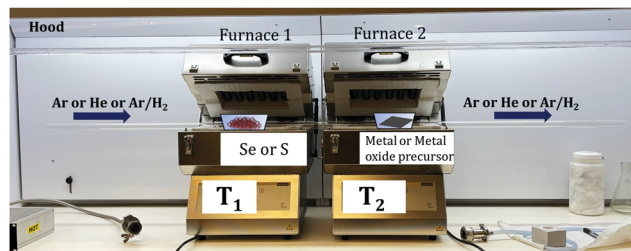
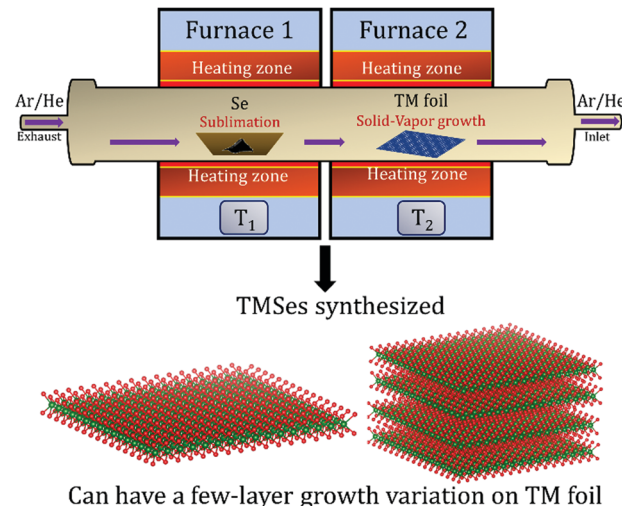


Fig. 6 Furnace-based ambient-pressure chemical vapor deposition (AP-CVD) setup in the Nessim Lab. This system is composed of two furnaces in series. Digital mass-flow controllers (MFCs) for many gases, including Ar, forming gas, H_2 , C_2H_4 , N_2 , etc., control gas flow. In the first furnace, we sublime the chalcogen (S or Se) powder, while the reaction with the metal foil occurs in the second furnace. The two temperatures, T_1 and T_2 , can be independently controlled.



complex growth mechanisms when TMSeS are synthesized directly from TM foil. We will describe here specific examples, with many illustrations taken from the research done in our lab.

The Nessim group previously showed how TMCs form starting from the elemental foils and chalcogens as precursors.¹³² The photograph is shown in Fig. 6. A schematic of the setup is shown in Fig. 7. This two-furnace-based CVD setup is typically used as a part of many synthesis strategies (all operating at ambient pressure).¹³³ Fig. 7 also provides further examples of how we have successfully designed layered TMSeS (rhenium selenide (ReSe_2), tungsten selenide (2H-WSe_2), niobium selenide (NbSe_2), and molybdenum selenide (2H-MoSe_2) using the AP-CVD approach. All the materials were upscaled starting from the respective transition metal foils (Re, W, Nb, and Mo), leading to few-layered growth, a significant yield, or an almost complete conversion of the metal precursors to the TMSe.

There are many reasons behind choosing the AP-CVD route for TMSe synthesis. Simplicity of initial precursors can be one of the major reasons as to why elemental precursors like TM foil and elemental Se are preferable. If only TM foil is used as the starting prototype for the synthesis, it eases the solid-vapor growth approach. In some cases, it can provide greater control over the total number of layers being grown on the foil. In other cases, the reaction can prove to be cumbersome and difficult to control if the desire is to obtain only a few-layers of the final TMSe. Such issues over number of layers formed can be solved by ideally fine-tuning the reaction parameters such as gas flow, amount of Se introduction, total reaction time, pre-annealing procedures, nature of gas used, and introduction of reducing agents or atmosphere.

We tried to find further examples in the literature for the discussion on the specific TMSeS being synthesized directly on TM foil by AP-CVD. Based on our survey, we discuss the few existing reports concentrating on the TMSe syntheses from the corresponding transition metal (TM) foil using ambient pressure CVD.

Tungsten selenide (WSe_2). Tungsten selenide (WSe_2) has three polymorphs, namely, 2H (hexagonal), 1T, and 1T' (trigonal). The most thermodynamically stable polymorph is the 2H phase. Generally, the 2H polymorph is obtained by APCVD

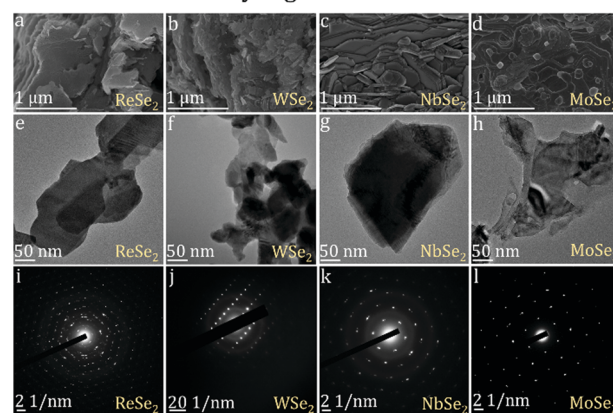


Fig. 7 Schematic of the furnace with the direction of Se vapor flow in the setup as photographed in Fig. 6 (the setup was used to synthesize ReSe_2 , WSe_2 , NbSe_2 , and MoSe_2 from the respective transition metal foils as per a previous solid-vapor CVD growth approach in the Nessim Lab). Fig. 6 shows the initiation of primary and secondary surface nucleation during CVD synthesis of layered TMSeS on the transition metal foil depicted in blue. (a), (b), (c), and (d) denote high-resolution scanning electron microscopy (HRSEM) images of few-layered ReSe_2 , WSe_2 , NbSe_2 , and MoSe_2 ; (e), (f), (g), and (h) denote low magnification transmission electron microscopy (TEM) images of ReSe_2 , WSe_2 , NbSe_2 , and MoSe_2 ; (i), (j), (k), and (l) denote the selected area diffraction (SAED) pattern of ReSe_2 , WSe_2 , NbSe_2 , and MoSe_2 , indicating that they are of a single-phase. The bulk and exfoliated images are conclusive towards the layered morphology of TMSeS being prepared by the ambient pressure CVD. Each material was thoroughly optimized with respect to reaction parameters to convert the transition metal foil to the corresponding TMSe fully. Therefore, such CVD-based reactions are highly scalable, providing a better yield of the layered TMSeS.

processes while the 1T or 1T' forms are obtained primarily *via* solvothermal routes.⁴⁹ Other examples of 2H to 3R (rhombohedral) transformation on high-temperature annealing are also available in the literature.¹²⁴

We take as an example the synthesis of WSe_2 using AP-CVD. Closer inspection for finding out references of the phase diagram for W and Se shows us that they are not available. Therefore, to understand the possible reactions between elemental Se and W precursors as a function of temperature, vapor pressure, reactants, gases, etc., we relied on previous reports



based on the synthesis of this material. It was also reported that 2H-WSe₂ could be prepared using solid-state mixing of WO₃ powder with elemental Se. Other reported methods of WSe₂ preparation include surface selenization of as-deposited tungsten oxide (WO₃) powder on Au foils, or CVD-based growth on W foil, or halide-assisted growth of monolayer WSe₂ crystals, *etc.*^{32,134,135}

The growth of WSe₂ nanowires on W foil is reported in the literature, using Se powder and N₂ atmosphere but the reaction mechanism is not discussed in detail.¹³⁶ Another work by Konar *et al.* indicates how AP-CVD can be used effectively to produce WSe₂ layers on W foil.¹¹ A specific temperature gradient of 250 °C between the two furnaces is vital throughout the reaction. It helps the vapor from the elemental Se to react and deposit on the surface of the W metal foil. The Se boat was immediately pulled out once the reaction was over, and the WSe₂ sample was slowly cooled to room temperature. The effects in the final products due to change in the reaction parameters.¹¹ The effects of optimization of carrier gas flow, total reaction time, Se precursor quantity, nature of inert carrier gas, surface acid-etching, reducing atmospheres, *etc.*, are discussed in detail. These optimized reaction conditions prove that TMSes can be effectively synthesized with ease by AP-CVD using TM foil as the initial reactant. It also highlights that transition metals with otherwise higher activation energy can also be used to prepare different selenide polymorphs using this AP-CVD method and very simple precursors. The as-modified process parameters also confirmed the formation of the phase-pure material. Extensive characterization of the bulk and exfoliated material confirmed that 2H-WSe₂ was layered. The growth of this material also highlights its exfoliable nature and the propensity towards homogenous dispersions when ultra-sonicated in low-boiling point solvents. In addition to the successful few-layered WSe₂ development on the W foil, the microstructure can also be fine-tuned in terms of the layer number. For example, shorter reaction times within the range of 15–10 min produced few-layered growth of WSe₂ thin film on the W foil. The exfoliated material from each reaction time (*i.e.*, as collected from 15 min, 30 min, 45 min, 1 h, and 2 h) suggested the strong anisotropic behavior of the material as per the *ex situ* XRD measurements. The anisotropic line broadening is evident at all reaction times and can explain its structural defects. The underlying growth mechanisms were evaluated as a function of time, temperature, surface etching, and nature of the inert carrier gas used for the synthesis. The proposed growth mechanism in WSe₂ show that the layers have evolved due to the close interaction between the Se vapor and the solid W foil surface. Such solid-vapor interaction formed various nucleation sites on the foil surface, leading to primary and secondary particles. The growth optimization also shed light on further experiments that eventually led to the W foil's total consumption, leading to the complete transformation of the elemental W precursor into the 2H-WSe₂ powder. This shows promise in terms of scalability of TMSe synthesis by APCVD where the yields of the as-synthesized material are quite low.

Studies based on the post exfoliation characterization of the TMSe (in this case, 2H-WSe₂) indicated that the individual

layers can be easily separated using the liquid-phase exfoliation (LPE) technique. In LPE-assisted layer separation, the idea is to oversee if the 2D layered TMSe can be dispersed in a low-boiling-point solvent such as ethanol or isopropanol. For dispersing 2H-WSe₂, both ethanol and isopropanol were identified as the most suitable solvents. The layered nature of the as-deposited flakes or nanosheets of 2H-WSe₂ on the Si/SiO₂ substrate confirmed the material's thickness and lateral dimensions (1.7 nm for bi-layered 2H-WSe₂ with an average lateral size of about 2–8 μm) using atomic force microscopy (AFM) measurements.¹¹ In-depth high-resolution transmission electron microscopy (HRTEM) measurements confirmed the average particle sizes and the point defects present in the exfoliated layers of 2H-WSe₂.¹¹

The literature based on the synthesis of 2H-WSe₂ highlights that the material can be prone to surface oxidation. Previous reports on monolayer WSe₂ (synthesized by a bottom-up approach) indicate that the single-layer 2H-WSe₂ can easily be oxidized on air-induced protrusions at its edges.¹³⁷ However, the oxidation with air exposure occurs mainly at step edges after the exposure of 2H-WSe₂ to air for close to 9 weeks, and is less detected in the internal terraces. The oxidation of the step edges in WSe₂ appears to be a self-terminating process. Since the studies combined bottom-up bulk synthesis of 2H-WSe₂ (starting from the elemental precursors) with a top-down exfoliation method in ethanol and isopropanol as solvents, the effects of the ambient conditions toward the bulk 2H-WSe₂ were also investigated.¹³ The bulk material showed no visible or morphological changes when exposed to air at room temperature (RT).¹¹ This confirmed that the process is very suitable and prevents ambient oxidation of the material. To better understand the effect of surface oxidation, the effect of variation in the oxidation reaction parameters was studied, which revealed that the material is highly stable until 200 °C while annealing in the furnace in the presence of air.¹³

Molybdenum selenide (MoSe₂). A similar approach was utilized to synthesize other layered 2D TMSes such as MoSe₂ starting from elemental precursors.¹³⁸ Since there are many reports on MoSe₂ synthesis using fundamental precursors by ambient-pressure CVD, the effect of variable reaction parameters is required to be studied. For example, Kaupmees *et al.* studied the selenization procedures of glass/Mo, Mo foil, glass/Mo/In, and glass/Mo/Cu stacked layers in closed vacuum tubes by isothermal and/or two-temperature zone annealing in Se vapors.¹³⁹ The experiments show that the samples were selenized from 375 to 580 °C for 30 and 60 minutes.¹³⁹ The applied Se pressure was varied between 130 and 4.4 × 10³ Pa. The increase of MoSe₂ film thickness depended on the origin of Mo and the Se vapor pressure. The residual oxygen content in the formed MoSe₂ layers was much lower in the two-zone selenization process. Other studies on large-area Mo foil selenization to obtain few-layer flakes demonstrated the effectiveness of using an ambient-pressure CVD to get such flakes of MoSe₂ (as explained in detail in the previous sections).^{121,128}

In a recent work on the AP-CVD synthesis of 2H-MoSe₂ from the elemental Mo foil and Se powder, the underlying growth



mechanism of the few-layered MoSe₂ growing on the Mo foil surface (schematic of the synthesis and variable morphology of MoSe₂ is shown in Fig. 8) is discussed.¹⁴⁰ The effect of surface-etching leading to the formation of a variable morphology of

the as-grown 2H-MoSe₂ in a two-furnace system is also evident. The images of *ex situ* high-resolution scanning electron microscopy (HRSEM) indicate different assemblies of the as-grown 2H-MoSe₂ nanosheets. An exhaustive refinement of reaction parameters showed a complete reaction of the transition metal foil with the elemental Se precursor leading to a precise arrangement in its overall flake organization as suggested by the layered formation of MoSe₂ on the exposed Mo foil surface. The exfoliation parameter optimization also consolidates that the binding forces between the layers in each morphology are different. It yields a different number of layers (with a significant difference in the lateral dimensions of the nanosheets).

Copper selenide (Cu₂Se). Copper selenide is a very well-known thermoelectric material.^{141,142} It also has many different stoichiometries (according to the phase diagram between Cu and Se).¹⁴³ There are reports of copper selenide being synthesized directly on Cu foils, but in most of these cases, the as-grown material is used for analysing electrocatalytic reactions (such as oxygen-evolution reaction or OER). In one such case, Masud *et al.* report how Cu₂Se was synthesized by AP-CVD using a horizontal tube furnace at 700 °C under a constant flow of N₂ as carrier gas, where Cu foil and Se shots acted as the precursors.¹⁴⁴ Another example of direct Cu₂Se synthesis by AP-CVD involved the reaction between Cu grids and elemental Se, where Cu₂Se formed as a coating material on the grid (this was further used as an electrode for sodium-ion batteries).¹⁴⁵

Silver selenide (Ag₂Se). Silver selenide (Ag₂Se) has two polymorphs, namely α -Ag₂Se (high-temperature polymorph) and β -Ag₂Se (low-temperature polymorph), according to its phase diagram.¹⁴⁶ The synthesis methods of Ag₂Se involve only using the Ag foil as the substrate in a solvothermal approach where β -Ag₂Se dendrites grow in the (0001) orientation. In a recent work, the full microstructural evolution of layered β -Ag₂Se on Ag foil using only elemental Se and Ag foil in a two-furnace AP-CVD system is highlighted and discussed in detail.¹²

Zinc selenide (ZnSe). There are reports of zinc selenide being synthesized on Zn foil.^{147,148} In a typical AP-CVD procedure, the Zn foil was directly selenized to few-layered ZnSe using Se powder sublimated in the presence of Ar/H₂.³³ The AP-CVD procedure indicated that the reaction was very facile and scalable in terms of surface coverage of the Zn foil. Such a simple design using ZnSe@Zn as the anode shows promise in Zn-based energy storage systems.

Study of structural parameters and defects in APCVD-synthesized TMSes

We still need to study structural parameter changes while using the ambient pressure CVD-based synthesis of TMSes. TMSes of the type MSe₂ contain easily exfoliable layers (as explained before), resulting in essentially 2D materials. Planar structural defects, such as stacking faults (SFs), can effectively change the atomic arrangement.¹⁴⁹ It influences electronic properties and makes these materials useful for various applications (opto-electronic devices, spintronics, gas sensing, catalysis, energy storage, thermoelectrics, *etc.*). Previous reports on TMDC synthesis with specific SFs highlight issues during synthesis.

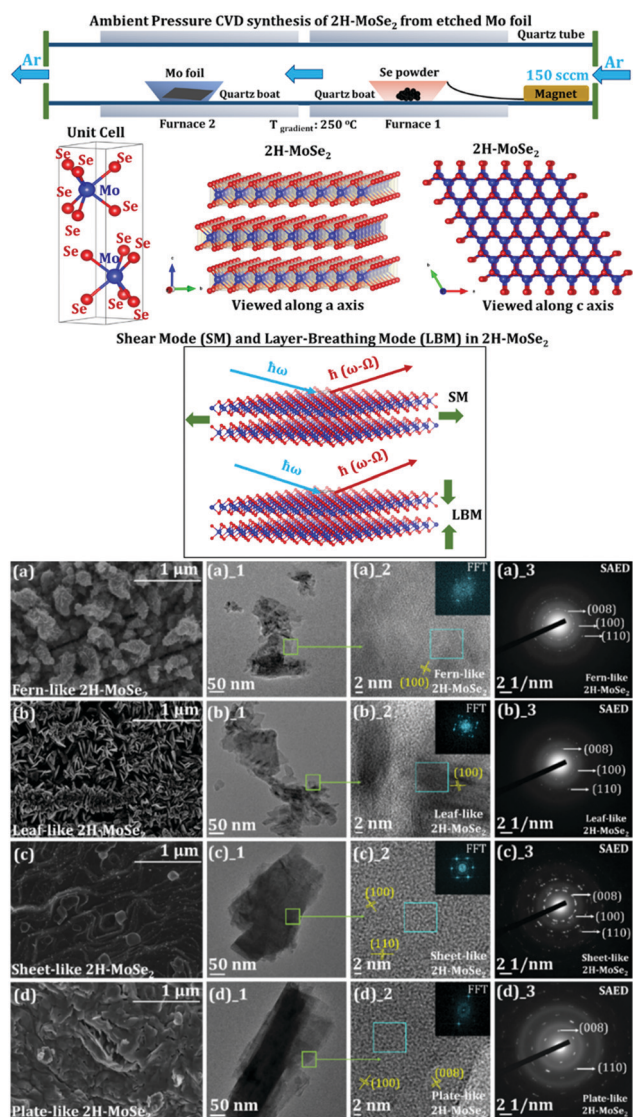


Fig. 8 Schematic for ambient pressure CVD synthesis of 2H-MoSe₂ starting from etched Mo foil and Se powder. The Mo foil is etched with different HCl strengths (giving rise to 4 different morphologies with varying strengths of HCl). Furnace 1 is set at 650 °C, and furnace 2 is set at 900 °C (maintaining a temperature gradient of 250 °C). 2 cm × 2 cm of Mo foil and 1.5 g of Se are shown in the schematic. The flow of Ar gas is set to 150 sccm. A ball and stick representation of the 2H variant of 2H-MoSe₂ is demonstrated. The red balls indicate Se, and the blue balls show Mo in the unit cell drawn beside (where $a = 3.288$ Å and $c = 12.91$ Å). The structures are visualized along the a axis and the c axis. A representation of shear mode (SM) and layer breathing mode (LBM) of 2H-MoSe₂ is also shown (reproduced with permission from ACS Omega); evolution in the morphology of 2H-MoSe₂ concerning variable etching conditions using HCl (starting from etching of pristine Mo foil in (a) 0.0001 M (fern-like), (b) 0.001 M (leaf-like), (c) 0.01 M (sheet-like), and (d) 0.1 M (plate-like) HCl solutions). All the visible interlayer fringes are shown in each HRTEM image corresponding to the respective morphologies (reproduced with permission from ACS Omega, ref. 140).



Such 2D defects must also be stable and consistent under experimental variations. The facile preparation of 2H-WSe₂ on W foil by an ambient-pressure CVD resulted in inherent selenium vacancy formation during the synthesis. The 2D planar defects in these 2H-MSe₂ systems were of intrinsic type-I.¹⁴⁹

The spontaneity with which the chalcogen will react often depends on chalcogen vapor pressure at different sublimation temperatures. For instance, both selenium and sulfur have similar behavior in their sublimation points.¹⁵⁰ The reactions between transition metal precursor and elemental sulfur are reportedly more vigorous when compared to similar reactions with Se.¹⁵¹ In most cases, sulfur itself tends to be a reducing agent, and therefore *in situ* reduction of the transition metal oxide might not be necessary. There are several reports of preparing the TMSes by ambient-pressure CVD using a small quantity of S to react better between the precursors.¹⁵² The use of S as a promoter in the selenization process played an essential role. It partially reduced WO₃ while keeping the WO_{3-x} concentration low while leading to the formation of WSe₂ layers.¹⁵² In such a case, it also forms defects such as screw-dislocations due to WO₃ partial reduction modulation.¹⁵²

Ambient-pressure CVD-synthesized TMSes for energy storage applications

Energy storage systems have gathered huge hype as one of the most promising alternative energy sources in the past few decades. Recently, lithium, sodium, and magnesium batteries have been highly sought after for abundant lithium reserves, sodium precursors, and magnesium's high specific volume capacity.^{153–156} Therefore, transition metal selenides play an essential part as lucrative electrode materials for energy storage. Some of their properties are shown in Fig. 9.

Although many reports are available in the literature regarding the energy storage capabilities of TMSes in LIBs, NIBs, and MIBs, most of these studies are focused solely on the materials prepared *via* the conventional hydrothermal or solvothermal, CVT, microwave-assisted, sonochemical, chemical bath deposition and other routes.⁴⁵ Very few studies are based on applying

ambient pressure CVD-synthesized TMSes towards energy storage.

As discussed before, the structures of many 2D materials and the presence of many different defects can play an essential role in introducing facile ion transport channels, leading to high ion conductivity in batteries.^{157,158} Such attributes support high current densities without any chemical and mechanical instability. In addition, the diffusion properties are enhanced, and ion mobility barriers for ultrafast ion transport are lowered due to their superionic conduction properties.¹⁵⁷

Most of the studies on TMSes focus on understanding the underlying mechanisms towards reversible intercalation (in the case of LiB storage) and inducing a lesser degree of polyselenide formation (in the case of NIBs), a result of a decade-long optimization using solvothermal routes. Most of the syntheses include a pre-solvothermal step with an additional selenization route to post-process the microstructures. Therefore, much study is yet to be done using such layered selenides synthesized by the ambient pressure CVD.

Most of the materials discussed in the previous sections were synthesized entirely by the CVD using only elemental precursors. The optimization of the reaction parameters indicated that most of them could be wholly converted into bulk powder, which can then be pre-exfoliated and used in several energy storage systems.

Lithium-ion adsorption of TMDCs is a process of ion/electron transfer.¹⁵⁷ The high concentrations of potential carriers, vacancies, and interstitials, and the low activation energies of ions hopping to the adjacent sides make the alkali metals have good conductivity in TMDCs. The exfoliable X–M–X layers in TMSes allow lithium/sodium/magnesium to be adsorbed and occupy vacant octahedral interstices in the hexagonal crystal lattice. The increased lithium concentration causes lattice distortion and strain, and even dislocations and lattice fragmentation, which provides diffusion channels for lithium ions to penetrate further into the TMDs material. Therefore, TMSes with tunable interlayer spacing enhance the lithium-ion capacity. However, it is worth noting that lattice strain can also cause cracks in the material and reduce the lifetime. It is also well known that defects weaken lattice strain in TMSes and improve lithium-ion adsorption behavior to some extent.¹⁵⁹ For example, point defects in TMDCs help lower the migration barrier as the energy of the activated state is mainly determined by the electrostatic repulsion between the activated lithium-ion and its neighboring cations.¹⁶⁰ Huang *et al.* predicted that the conductivity of the WS₂/NbSe₂ heterostructure is improved and the structure is more stable due to the new electronic states and large elastic constant, and the lithium diffusion barrier is as low as 0.18 eV.¹⁶¹

TMSes for SIB applications, such as MoSe₂, have also gained increasing attention. For example, Ko *et al.* reported for the first time the synthesis of yolk-shell-structured MoSe₂ microspheres by a facile selenization of the precursor for application as anode materials in SIBs.¹⁶² The synthesized yolk-shell morphology presented a flower-like structure, which, according to the authors, facilitated Na⁺ and electronic kinetics and assisted in the volume changes during charge and discharge cycles.¹⁶²

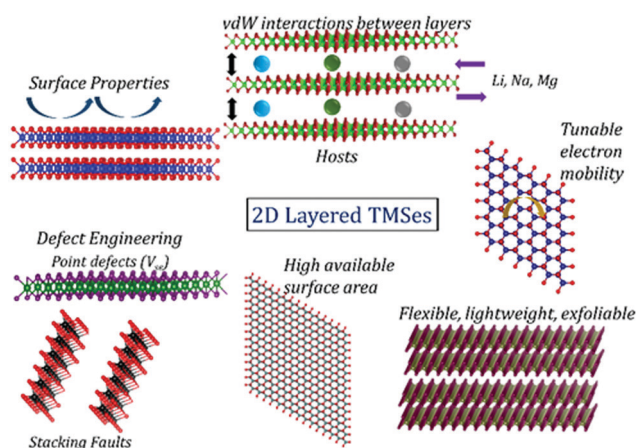


Fig. 9 Properties of 2D materials that can effectively enhance the performance of energy storage applications.



Nevertheless, this also explains that the material was synthesized only partly using a combination of spray pyrolysis and CVD-based selenization. These MoSe_2 microspheres showed 85% coulombic efficiency, with an initial charge capacity of 448 mA h g^{-1} at 200 mA g^{-1} .¹⁶² These reports based on MoSe_2 studies during Na insertion processes highlight that CVD-synthesized layered MoSe_2 must be studied in more detail.

The ongoing progress in nanostructured TMSes has a promising future in magnesium batteries by using such novel TMSes as electrode materials. Liu *et al.* recently constructed layered tungsten diselenide nanowires directly on tungsten foil and showed how the material works as a binder-free cathode in magnesium batteries.¹³⁶ WSe_2 nanowires exhibit efficient intercalation/de-intercalation activity toward Mg ions without evident morphology change.¹³⁶ As shown in Fig. 10, the battery exhibited a reversible capacity of approximately 203 mAh g^{-1} at 50 mA g^{-1} , whereas bulk WSe_2 showed a relatively low capacity retention of 10% after 100 cycles. The electrodes had capacities of 142, 120, and 103 mA h g^{-1} at 800, 1500, and 3000 mA g^{-1} , respectively, for 50 cycles. This is a clear indication of stable cycling and rate capacity. The CE of WSe_2 nanowires was approximately 98.5% for 160 cycles.¹³⁶ This points towards steady electrochemical behavior compared to the bulk having a CE of 93.8% for 50 cycles.

The TMSe fabrication using the precursor selenization process is not limited to MSe_2 analogs. We also see reports on metal-rich TMSes such as copper selenide (Cu_2Se) electrodes as explained before synthesized on a copper grid substrate tested as a promising material for sodium-ion batteries (as shown in Fig. 11).¹⁶³ Cu_2Se showed enhanced reversible capacities (about 250 mA h g^{-1}), commendable cyclic stability, and low polarization.¹⁶³ These findings corroborated Cu_2Se as an encouraging cathode material for sodium-ion batteries.

Regarding cyclability in as-purchased WSe_2 , Share *et al.* demonstrated this material as an electrode for the first time in NIBs (shown in Fig. 12).¹⁶⁴ The WSe_2 pristine electrode had a reversible capacity of 200 mA h g^{-1} at 20 mA g^{-1} .¹⁶⁴ Other composites of WSe_2 , like carbon-coated WSe_2 , were further used by Zhang *et al.*¹⁶⁵ In this study, tungsten and selenium, in their powder forms, were firstly ground by mechanical milling and subsequently annealed with carbon black. Afterwards, WSe_2/C was directly used on the electrode. WSe_2/C nanomaterials exhibited a discharge capacity of 467 mA h g^{-1} at 200 mA g^{-1} current density, owing to the buffering of the carbon matrix and enhanced electronic conductivity of the composites.¹⁶⁵

As described in the sections above, the previous work by Konar *et al.* deals with understanding the physico-chemical properties while synthesizing 2H-WSe_2 by ambient pressure CVD from elemental precursors without any reducing agent.¹¹ It deals with the issues of scalability of the final materials by a thorough parameter optimization process. The final as-synthesized TMSe shows appreciable performance when used as a LIB anode material.¹¹ An elaboration on the redox processes of lithium-ion batteries (LIBs) indicates capacity increase up to 500 cycles. The discharge capacity up to the

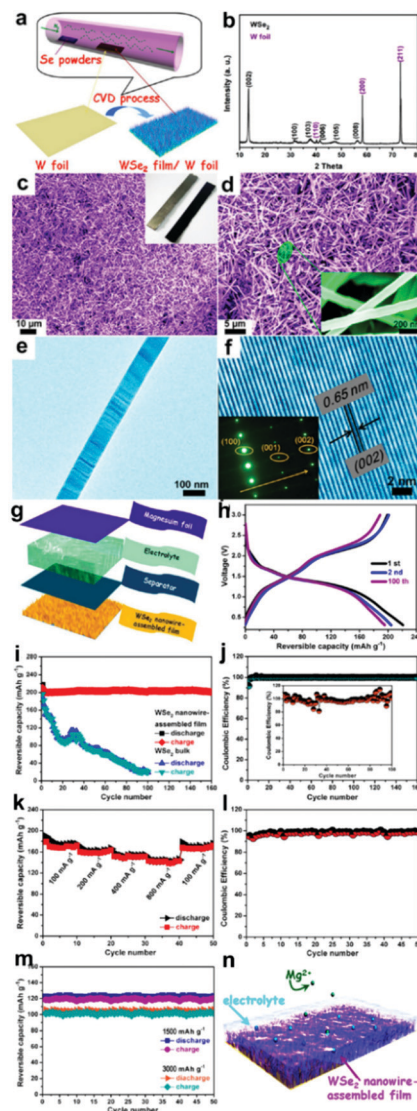


Fig. 10 (i) Sample fabrication and morphological characterization of WSe_2 nanowires grown on W foil. (a) Schematic illustration of the CVD procedures for the growth of the WSe_2 nanowire film on W foil and its corresponding (b) XRD pattern, (c) and (d) SEM images, and (e) and (f) TEM images. The inset in (f) is the SAED pattern; electrochemical properties of the coin-type batteries measured at a current density of 50 mA g^{-1} between 0.3 and 3 V: (g) schematic representation of the as-assembled cell composed of magnesium foil anode, electrolyte, separator, and WSe_2 film cathode, (h) charge/discharge curves, (i) cycling performance, and coulombic efficiency (CE); (j) rate-capability analysis of these electrodes. (k) Rate performance of the WSe_2 -based electrodes at various current densities. (l) The corresponding CE. (m) Long cycling performance of these electrodes at significant rates of 1500 and 3000 mA g^{-1} . (n) Schematic of the operation principle of rechargeable magnesium-ion batteries based on WSe_2 nanowire-assembled film. (Reproduced with permission from ACS Nano, 2013, ref. 136.)

50th cycle at 250 mA h g^{-1} of the material shows a stable value of 550 mA h g^{-1} on prolonged cycling. The material showed a capacity of $\sim 300 \text{ mA h g}^{-1}$ when the current density was increased to 500 mA h g^{-1} after ~ 750 cycles at 250 mA h g^{-1} . After additional 500 cycles (total of 1200+ cycles) at 500 mA h g^{-1} ,



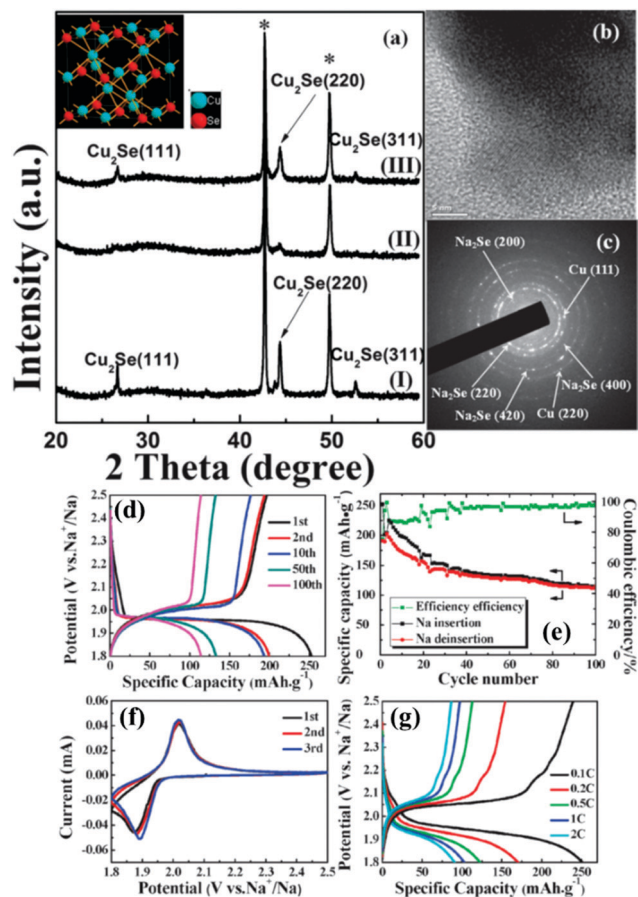


Fig. 11 (a) *Ex situ* XRD pattern of the Cu_2Se thin film at various states during the first cycle of the $\text{Cu}_2\text{Se}/\text{Na}$ cell with a voltage range of 1.8–2.5 V. (I) As-prepared; (II) after discharging to 1.8 V; and (III) after charging to 2.5 V. The crystal structure of Cu_2Se is shown in the inset. (b) TEM image and (c) corresponding SAED pattern after discharging to 1.8 V; (d) typical charge–discharge voltage profiles of the initial 100 cycles of the $\text{Na}/\text{NaClO}_4/\text{Cu}_2\text{Se}$ battery at a constant current rate of 0.1 C between 1.8 and 2.5 V; (e) cycling performance of the $\text{Na}/\text{NaClO}_4/\text{Cu}_2\text{Se}$ battery cycled at 0.1 C between 1.8 and 2.5 V; (f) the initial three CV curves of the $\text{Na}/\text{NaClO}_4/\text{Cu}_2\text{Se}$ cell between 1.8 V and 2.5 V versus Na^+/Na at a scan rate of 0.1 mV s^{-1} ; (g) charge–discharge curves at various current densities (0.1 C, 0.2 C, 0.5 C, 1 C, and 2 C). (Figure reproduced with permission from Chem Comm. (RSC), ref. 163.)

a capacity of 271 mA h g^{-1} with a retention of $\sim 91\%$ was recorded. These results for 2H-WS_2 indicate that the material's prime rate and stable cycling performance in its pristine form can be attributed to its layered 2D microstructure (Fig. 13). This can pave the possibilities towards high energy-efficient devices. Moreover, in a recent work, the functionality of CVD-synthesized layered 2H-WS_2 as an unconventional 2D coating material is also proven towards stabilizing high-voltage LIB-based cathodes (NCM and LNMO).¹⁶⁶

In a recent work, Yang *et al.* demonstrate the facile and highly scalable preparation of $\text{ZnSe}@/\text{Zn}$ metal foil being used as an anode in Zn aqueous batteries.³³ The work demonstrates a high Zn utilization of 99.2% and an elongated cycle life of 1530 h during repetitive Zn plating/stripping. Under harsh experimental conditions ($30.0 \text{ mA cm}^{-2}/10.0 \text{ mA h cm}^{-2}$), it

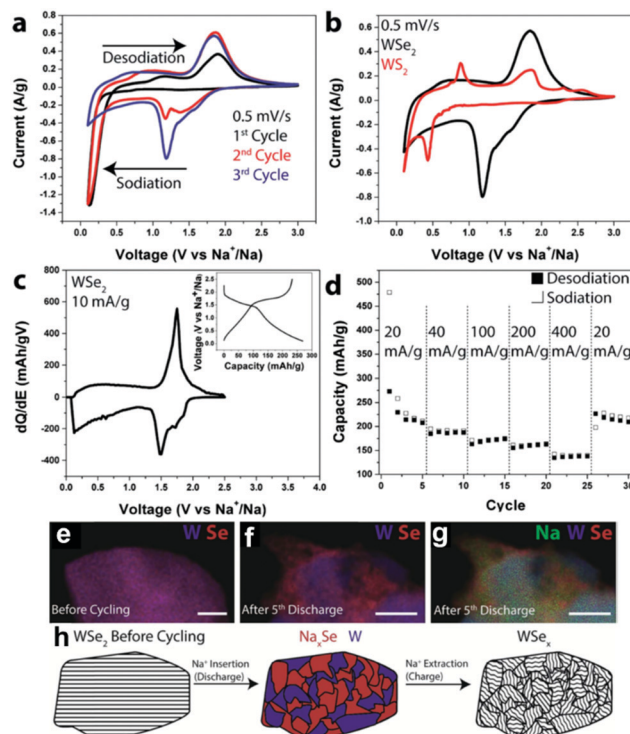


Fig. 12 (a) CV scans for the first three cycles from WSe_2 devices with CMC and EC/DEC. (b) CV scans comparing the energetics of chemical storage in WSe_2 and WS_2 during the 3rd cycle. (c) dQ/dE of a WSe_2 electrode at 10 mA g^{-1} ; inset: charge–discharge curves at 10 mA g^{-1} . (d) Galvanostatic rate study at 20, 40, 100, 200, and 400 mA g^{-1} . (e) EDS composite map of a WSe_2 electrode before cycling; scale bar $\frac{1}{4} 100 \text{ nm}$. (f) EDS composite map of a WSe_2 battery electrode after the 5th discharge showing W and Se; scale bar $\frac{1}{4} 100 \text{ nm}$. (g) The same EDS spectrum as (f) but with Na added. (h) Schematic illustrating the storage mechanism of Na in WSe_2 . Upon Na^+ insertion, segregated domains of Na_2Se and W form. After Na^+ has been removed, WSe_2 is reformed, but it is less crystalline than in its original state. (Figure reproduced with permission from RSC Advances, ref. 164.)

can still sustain to operate as long as 172 h, outperforming the state-of-the-art anode counterparts.³³ Impressively, such layered ZnSe formation on the Zn foil anode can act as a cultivator to guide the preferential growth of the $\text{Zn}(002)$ plane at the infancy stage of cycling.³³

Additionally, we provide a table summarizing all the literature mentioning transition metal selenides being researched so far for their energy storage capabilities, which were synthesized or structurally modified by CVD (Table 1). Other reports of TMSes (synthesized mostly by hydrothermal routes) for energy storage are discussed in a recent review which shows the challenges and progress in using transition metal selenides to electrochemically store Na^+ and K^+ .⁵²

Conclusion and future perspectives

The recent literature shows how the conventional materials for energy storage, such as in LIBs, have evolved greatly in terms of their energy density, cyclability, cost, and safety since their invention and commercialization. Their success is immeasurable

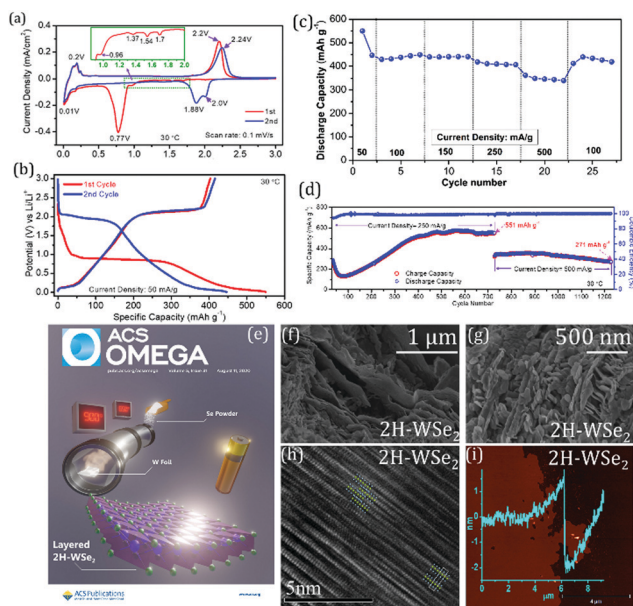


Fig. 13 (a) Cyclic voltammograms for the first two cycles of 2H-WSe₂ vs. Li at a scan rate of 0.1 mV s⁻¹ over a potential window of 0.01–3.0 V. (b) Galvanostatic voltage profiles for the first two cycles of WSe₂ in a half-cell recorded at a current density of 50 mA g⁻¹. (c) Variation of the discharge capacity as a function of the applied current density measured using 2H-WSe₂ vs. Li half-cell. (d) Prolonged electrochemical cycling performance of the 2H-WSe₂ electrode in half coin cell configuration (vs. Li/Li⁺). After the initial 27 cycles of rate performance, the cells were subjected to long-term cycling for 700 cycles at 250 mA h g⁻¹ and another 500 cycles at 500 mA h g⁻¹. (e) Schematic depicting a facile and straightforward approach towards bulk 2H-WSe₂ synthesis by ambient pressure CVD for LIBs. (f and g) High-resolution scanning electron microscopy (HRSEM) images of 2H-WSe₂; (h) high-resolution transmission electron microscopy (HRTEM) image of 2H-WSe₂; (i) atomic force microscopy (AFM) measurement of the 2H-WSe₂ drop-cast flake showing a thickness of about 1.8 ± 0.3 nm. (Figure a–g reproduced with permission from ACS Omega, ref. 11; figure h and i reproduced from Materials Characterization, Elsevier, ref. 149.)

in terms of applicability towards flexible electronics and electric vehicles. However, much research is required for the current commercial LIBs to meet all the demands for performance, cost-effectiveness, and upscaling. The natural availability of lithium, cobalt, and nickel can be challenging for LIBs. Nevertheless, such imposed restrictions motivate future research on alternative chemistries for the next-generation batteries.¹⁶⁸

Other multivalent batteries (such as NIBs, MIBs) seem to be different but often share similar scientific/operating principles,

which can be understood using similar characterization approaches. Huge efforts have been dedicated to understand the electrochemical mechanism of storing, say for example, Na⁺ or Mg²⁺, towards understanding the electrochemical reactivity/storage phenomena in TMSeS. Sometimes contradictory ion-storage processes are also reported in the literature of TMSeS, but it simply indicates that the conversion processes need to be studied in further detail for the NIBs or MIBs. The shuttle effect of intermediate polyselenides, effect of electrolyte composition, and full-cell investigations are required to facilitate cost-effective strategies for designing TMSe-based electrodes compatible with the energy storage systems. Among all the energy storage systems discussed here, we must mention that MIBs provide higher energy densities with minimum dendritic growth in the TMSe based electrodes. This can facilitate solid-state mobility without compromising the voltage and capacity.

Therefore, there are certain factors which will determine the commercialization of TMSeS towards appropriate energy-storage settings. An optimization of these factors in the fabrication process of electrodes will help achieve the realistic goal towards fabricating Se-based batteries by CVD. Such deciding factors include

- A gradual increase of TMSe mass loading will ease the transfer of materials/technology towards future commercialization;
- Fabrication of safe TMSe active materials that minimize thermal runaway during electrochemical ion storage;
- Develop new high-voltage (HV) cathodes or hetero-composites of TMSe and existing HV cathodes to mitigate instability issues towards the commonly used electrolytes in such energy storage systems.

From the CVD-based TMSe engineering perspective, the recent research in such materials focuses on synthesizing them through many approaches, where AP-CVD remains the least explored technique. We mostly observe the post-annealing or selenization of the precursors (sometimes prepared using a different route) in ambient pressure CVD. The APCVD-based process remains limited in usage towards TMSe fabrication for batteries. Few-layered TMSe films have recently shown great potential in energy storage devices. However, further research is required to improve and understand APCVD's mechanisms, material characteristics, and modification of the fabrication process. The scalability of TMSe yield through APCVD remains a major factor. Optimizing reaction parameters and precursors' fine-tuning will ensure better insight into the synthesis of 2D

Table 1 Recent reports on CVD-based synthesis/modifications of TMSeS for energy storage applications

Transition metal selenides	Morphology	Application	Rate capability (mA h g ⁻¹)	Ref.
WSe ₂ NWs on W foil	Nanowires	MIB	103/3000	136
Cu ₂ Se on Cu grids	Thin film	NIB	250	163
WSe ₂	Exfoliated powder	NIB	200	164
WSe ₂ from W foil	Nanosheets after exfoliation	LIB	550	11
MoSe ₂ from MoO ₃	Yolk shell modified by CVD	NIB	527	162
MOF-derived SnSe nano-platelets in nitrogen-doped graphene cages	Combined steps of thermal selenization of Sn-MOF and direct plasma-enhanced chemical vapor deposition (PECVD) wrapping of the NG shell	NIB	650	167

TMSes on a large scale. Better experimental design of many such 2D layered TMSes using CVD can better corroborate the defect-nature, large-area flakes, *etc.*, for practical applications in energy storage. In addition, surface structure, material strain, and surface charge are critical, and a variety of growth substrates or other precursors must be examined. In such cases, precursor adsorption, nucleation, domain sizes, substrate orientation, and growth direction also need to be studied in detail. Furthermore, reproducibility, uniform property distribution, and upscaling remain challenging issues that need to be addressed through research. We strongly believe that a simple APCVD-based fabrication approach can provide better tuning of physico-chemical properties, leading to better batteries in the near future.

Conflicts of interest

The authors have no conflicts to declare.

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