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PAPER

Freestanding Co_3O_4 nanowire array for high performance supercapacitors†

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We report a single-crystalline Co_3O_4 nanowire array grown on a nickel foam prepared by a hydrothermal synthesis method for supercapacitor application. The Co_3O_4 nanowires show sharp tips and have an average diameter of 70 nm, and a length up to 25 μm . Impressively, the as-prepared single-crystalline Co_3O_4 nanowire array exhibits noticeable pseudocapacitive performance with a high capacitance of 754 F g^{-1} at 2 A g^{-1} and 610 F g^{-1} at 40 A g^{-1} as well as excellent cycling stability. The enhanced supercapacitor performance is due to the unique one-dimensional (1D) architecture, which provides fast diffusion paths for ions and facilitates the electron and ion transfer on the Co_3O_4 /electrolyte interfaces. Moreover, the 1D nanowire array can accommodate the volume expansion and restrain the pulverization and deterioration of Co_3O_4 during the repeated cycling process, resulting in enhanced cycling stability.

1. Introduction

High-performance electrochemical energy storage systems are highly desirable in today's information-rich, mobile society.¹ Of the various power sources, supercapacitors represent an emerging energy storage technology that offers fast recharge ability, high power density and long cycle life.^{2,3} They have an important role in complementing or replacing batteries in the energy storage field ranging from portable electronics to hybrid electric vehicles. Early studies of supercapacitors mainly focus on electrical double-layer capacitors (EDLCs) based on carbonaceous materials, which suffer from relatively low specific capacitance and instability at a high charge-discharge rate.⁴ Such a limited specific capacitance of EDLCs is far from perfect to meet the higher requirements for peak-power assistance in electric vehicles. In recent years, supercapacitors based on pseudocapacitive materials have evoked considerable interest due to their much higher energy density and specific capacitance (several times larger than those of carbonaceous materials) originating from reversible multielectron redox faradaic reactions.^{5–8} Among these available pseudocapacitive candidates, Co_3O_4 is considered to be one of the most attractive materials due to its high specific capacitance (theoretical specific capacitance up to 3560 F g^{-1}), good capability retention and high redox reactivity.⁹

Despite high capacitance, pseudocapacitive materials often present a compromise between the power performance and reversibility due to the slow kinetics of ion and electron transport in electrodes and at the electrode/electrolyte interface. In order to enhance the redox kinetics, great efforts have been devoted to

creating porous nanostructured materials with large surface area and short diffusion path of ions and electrons to improve the utilization of pseudocapacitive materials at high power density.¹⁰ Recently, one-dimensional (1D) array architecture (nanorods, nanotubes and nanowires, *etc.*) built on conductive substrates has been demonstrated to be an optimized architecture for boosting the pseudocapacitive performance since single nanowire/nanorod directly contacting with current collector can serve as a superhighway for fast ion and electron transport, resulting in reduced internal resistance and improved high-power performance.^{11–15} To date, 1D Co_3O_4 arrays such as nanotubes, nanowires and nanoneedles have been synthesized by using porous alumina,¹⁶ ammonia-evaporation induction,^{17,18} virus as templates,¹⁹ hydrothermal synthesis method,²⁰ and their enhanced electrochemical performance for lithium ion batteries have been demonstrated. However, there are only a few reports about self-supporting 1D Co_3O_4 arrays for supercapacitor applications. Previously, Gao *et al.*²¹ reported a Co_3O_4 nanowire array prepared by an ammonia-evaporation induction method for supercapacitor application. Despite its high capacity of 746 F g^{-1} , the cycling stability and high-rate performances are far from satisfactory. The Co_3O_4 nanowire array obtained by Gao *et al.*²¹ exhibited poor cycling stability with 14% capacitance loss after 500 cycles at a current density of 10 mA cm^{-2} (corresponding to a current density of 0.6 A g^{-1}). Besides, our group reported a self-supported hollow Co_3O_4 nanowire array with oxygen induction and investigated its pseudocapacitive performance.²² In the present work, we present a single-crystalline Co_3O_4 nanowire array *via* a facile hydrothermal method and apply it as cathode material for supercapacitors. Impressively, the single-crystalline Co_3O_4 nanowire array exhibits noticeable pseudocapacitive performance with high capacitance of 754 F g^{-1} at 2 A g^{-1} and 610 F g^{-1} at 40 A g^{-1} as well as excellent cycling stability.

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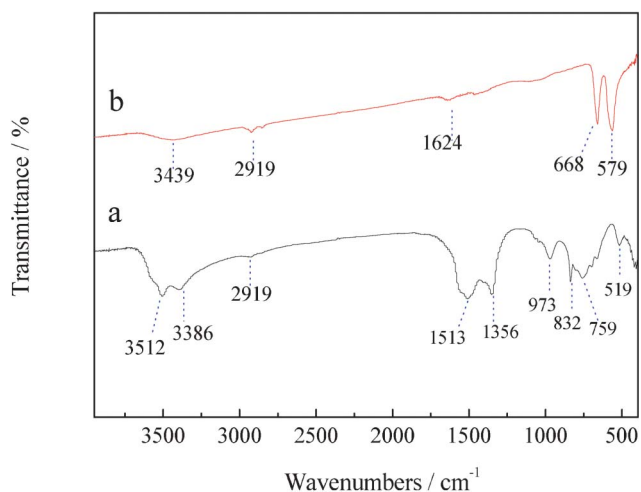


Fig. 2 FTIR spectra of (a) $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ precursor film and (b) Co_3O_4 nanowire array.

deformation of molecular water. The small band around 2919 cm^{-1} comes from the stretching vibration of $\nu\text{C-H}$. After annealing treatment, the bands of the $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ precursor film disappear and two very strong peaks centered at 668 and 579 cm^{-1} characteristic of spinel Co_3O_4 are noticed, which are consistent with the XRD result.

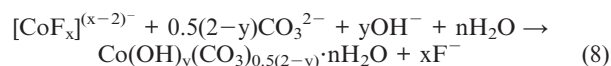
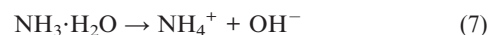
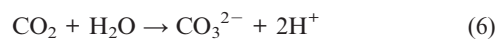
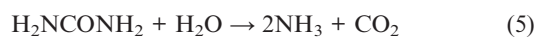
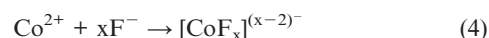
SEM images of the $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ precursor and Co_3O_4 films are presented in Fig. 3. The $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ precursor film shows 1D nanowire arrays architecture (Fig. 3a and b). Apparently, the skeletons of nickel foam are uniformly covered by the nanowires, which grow densely and almost vertically to the substrate. The morphology of the sample before and after annealing treatment does not change much, maintaining the nanowire array structure. The Co_3O_4 nanowires show sharp tips and have an average diameter of 70 nm , length up to around $25\text{ }\mu\text{m}$ (Fig. 3c and d). The length of nanowires could be easily controlled by the growth time.

The structural characterization of an individual nanowire is performed in detail by TEM and HREM observation. The basic cobalt carbonate hydroxide $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ nanowire shows a smooth texture and single crystalline feature (Fig. 4a and b). According to the pattern of selected area electronic diffraction (SAED), the basic cobalt carbonate hydroxide $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ nanowire exhibits a growth direction along $[010]$. Typical TEM images confirm that the average diameter of an individual Co_3O_4 nanowire is about 70 nm (Fig. 4c and d). The Co_3O_4 nanowire consists of numerous interconnected nanoparticles and presents a rough appearance with a large quantity of mesoporous structures, which is ascribed to the successive release and loss of CO_2 and H_2O during the thermal decomposition of $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ precursor. SAED pattern of a Co_3O_4 nanowire shows a single-crystalline pattern. Three sets of diffraction spots are indexed as (111) , (220) and (311) planes of Co_3O_4 , respectively. It is noticed that the long axis of the nanowire is parallel to the line that contains both (000) and (220) spots in the SAED pattern, indicating the single-crystalline Co_3O_4 nanowire grows along the $[110]$ direction, similar to those reported by Du *et al.*,²³ and Xie *et al.*,²⁴ and different from those thermal-oxidized Co_3O_4 nanowire with $[111]$ preferential growth.²⁵



Fig. 3 Typical SEM images of (a), (b) $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ precursor film and (c), (d) Co_3O_4 nanowire array grown on nickel foam (side view of the arrays and magnified top view presented in insets).

Fig. 4e shows a HRTEM image of the side of a Co_3O_4 nanowire. The lattice fringes with a lattice spacing of about $2.42\text{ }\text{\AA}$ corresponds to the (311) planes of Co_3O_4 , also revealing that the nanowire is crystalline. Taking the above results together, a plausible growth mechanism of single-crystalline Co_3O_4 nanowires arrays is given as follows.²⁰



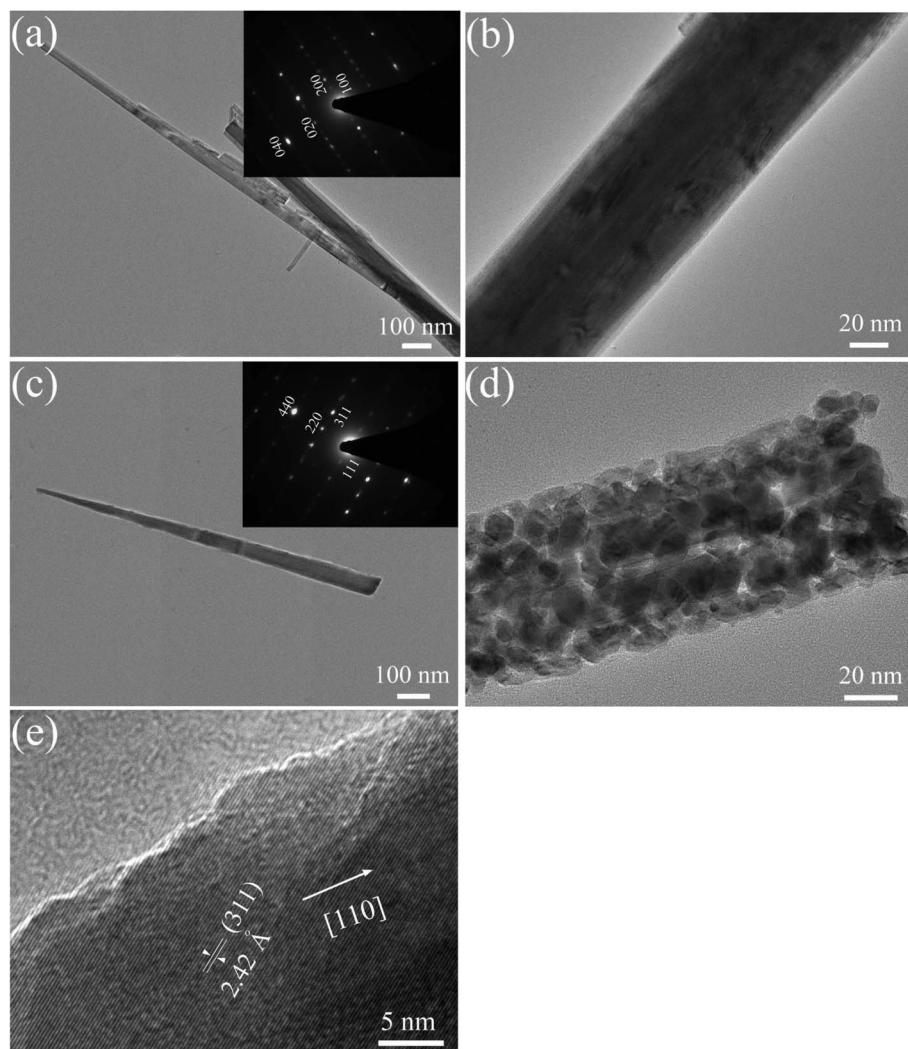
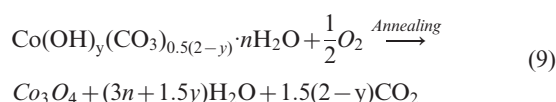
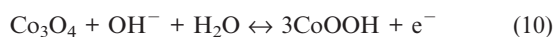


Fig. 4 TEM images of (a), (b) the $\text{Co}_2(\text{OH})_2(\text{CO}_3)_2$ nanowire and (c), (d) Co_3O_4 nanowire; (e) HRTEM image (SAED patterns of the nanowires presented in inset).

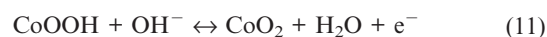


3.2 Electrochemical analysis

The pseudocapacitive behavior of the single-crystalline Co_3O_4 nanowire array was elucidated by cyclic voltammograms (CV) measurements. For Co_3O_4 materials, it is well accepted that their pseudocapacitive process is associated with two redox couples, which are reflected in the CV curve. Two typical redox couples characteristic of Co_3O_4 are observed in the CV curve (Fig. 5a), similar to those reported in the literature.^{25,26} The first redox couple A_1/C_1 corresponds to the conversion between CoOOH and Co_3O_4 as illustrated as follows:^{21,22}



The second redox couple A_2/C_2 is attributed to the change between CoOOH and CoO_2 , represented by the following reaction:^{21,22}



Note that the nickel foam shows a redox process P1/P2 with low current intensities. This redox couple is attributed to the reversible reaction of Ni(II)/Ni(III) formed on the nickel surface. Compared to Co_3O_4 nanowires array, the signal of nickel foam is quite small, indicating that the nickel foam contribute little to the capacitance of the Co_3O_4 nanowires array. The CV behavior of the Co_3O_4 nanowire array changes much as the scanning rate increases (Fig. 5b). The oxidation and reduction peaks shift continuously to higher and lower potentials respectively, leading to a larger potential separation between the oxidation and the reduction peak. Furthermore, two oxidation (A_1 and A_2) and reduction peaks (C_1 and C_2) merges with each other leaving one oxidation and reduction peaks at a scanning rate of 200 mV s^{-1} , respectively. The pseudocapacitor properties are

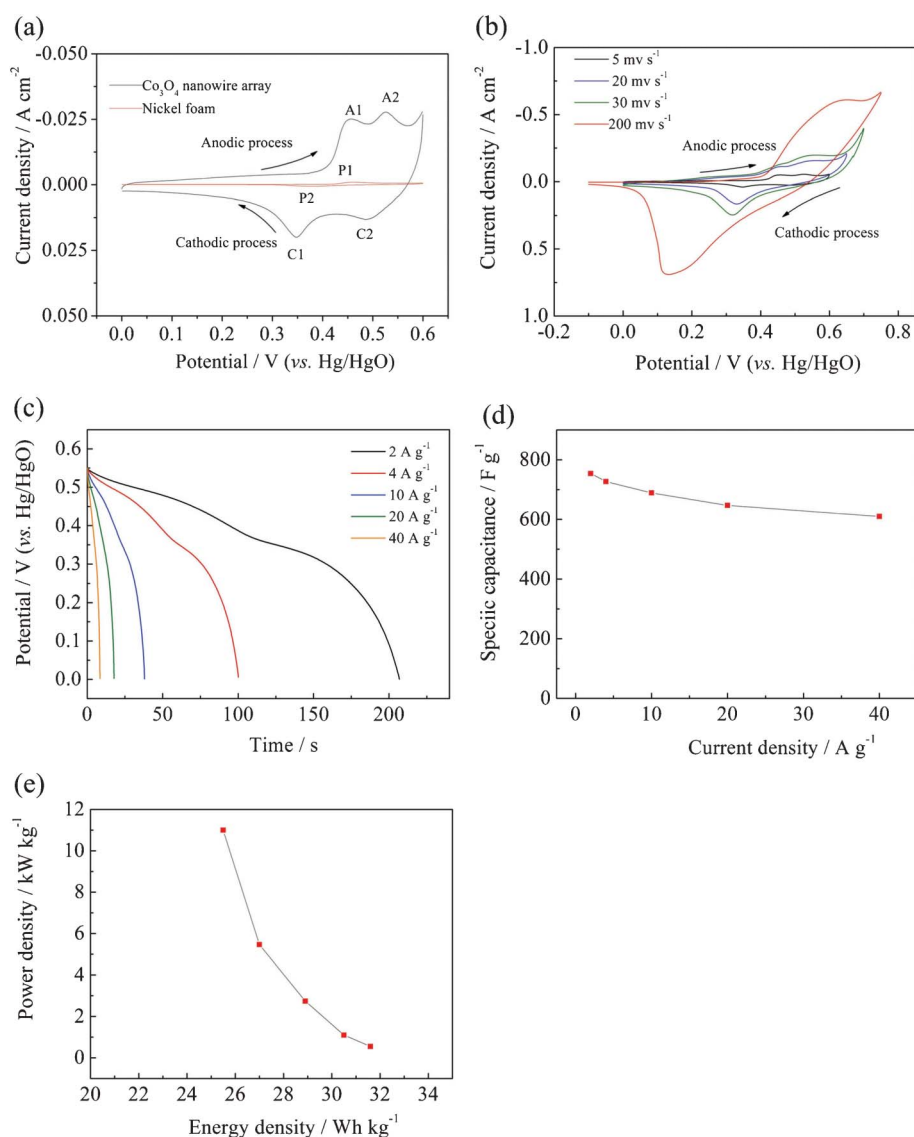


Fig. 5 (a) CV curves of the Co_3O_4 nanowire array and nickel foam in the potential region of 0–0.6 V at a scanning rate of 5 mV s^{-1} at the 10th cycle; (b) CV curves of the Co_3O_4 nanowire array at different scanning rates; (c) discharge curves of the Co_3O_4 nanowire array at different discharge current densities after activation for 1000 cycles at 2 A g^{-1} and (d) corresponding specific capacitance at different discharge current densities; (e) Ragone plot (power density vs. energy density) of the single-crystalline Co_3O_4 nanowire array.

tested by galvanostatic charge–discharge at different current densities. In our case, the Co_3O_4 nanowire array takes approximately 1000 cycles to activate and then delivers the highest capacitance, indicating that the Co_3O_4 nanowire array needs long cycles to activate its potential capacitance. Before activation, the Co_3O_4 nanowire array exhibits pseudocapacitances with 323 F g^{-1} at 2 A g^{-1} , 309 F g^{-1} at 4 A g^{-1} , 290 F g^{-1} at 10 A g^{-1} , 272 F g^{-1} at 20 A g^{-1} , 248 F g^{-1} at 40 A g^{-1} , respectively (Fig. S3, ESI[†]). After activation for 1000 cycles at 2 A g^{-1} , the discharge specific capacitances at various current densities increase greatly with 754 F g^{-1} at 2 A g^{-1} , 727 F g^{-1} at 4 A g^{-1} , 689 F g^{-1} at 10 A g^{-1} , 647 F g^{-1} at 20 A g^{-1} , 610 F g^{-1} at 40 A g^{-1} , respectively (Fig. 5c and d), maintaining 81% of capacitance when the charge–discharge rate changes from 2 A g^{-1} to 40 A g^{-1} . In our case, the increase in capacitance is quite large due to the subsequent activation of Co_3O_4 nanowires. As we know, a key advantage of supercapacitors is their fast

recharge capability from a few seconds to several minutes. The charge–discharge process is so fast that the active Co_3O_4 could not completely convert to other active phases, resulting in low utilization and low capacitances. In other words, only a fraction of the material is active during the first cycles, while the other material is not activated. As the electrolyte gradually penetrates into the inside of the Co_3O_4 nanowire, more and more part of the nanowire becomes activated and contributes to the increase of capacitance. These obtained values are higher than our previous self-supported hollow Co_3O_4 nanowire arrays (599 F g^{-1} at 2 A g^{-1}),²² and other power forms,^{27,28} comparable to those obtained from Co_3O_4 arrays prepared Gao *et al.* (746 F g^{-1} at 0.3 A g^{-1}),²¹ lower than the nanostructured nanoporous Ni/ Co_3O_4 films (2200 F g^{-1}) grown by Deng's group,²⁶ whose superior capacitance arises from its pseudocapacitive material–conductive matrix composite nanostructures.

Fig. 5e shows the Ragone plot (power density vs. energy density) of the Co_3O_4 nanowire array grown on the nickel foam. Our single-crystalline Co_3O_4 nanowire array delivers an energy density of $\sim 25.5 \text{ Wh kg}^{-1}$ at a high power density of $\sim 11 \text{ kW kg}^{-1}$, superior to other Co_3O_4 powder materials.^{27,28} Moreover, the as-prepared Co_3O_4 nanowire array exhibits excellent capacitance retention properties. Increasing up to 1000 cycles, the capacitance keeps stable and shows negligible loss after 4000 cycles (Fig. 6), indicating its long term cycling stability and high power capability. The cycling characteristic is much better than Co_3O_4 arrays prepared by Gao *et al.*,²¹ which exhibits poor cycling stability with 14% capacitance loss after 500 cycles at a current density of 0.6 A g^{-1} . The enhanced pseudocapacitive performance is ascribed to the following morphological benefits: (1) The Co_3O_4 nanowire array directly grown the nickel foam provides good electrical contacts for each nanowire and ensures every nanowire participating in the electrochemical reaction. (2) The open geometry between nanowires allows easier electrolyte penetration into the inner region of the electrode, in which nanowires are highly exposed and accessible by electrolyte, resulting in reduced internal resistance and faster kinetics. (3) It eliminates the need for binder and conducting additive, which adds extra contact resistance and supplementary, undesirable interfaces. (4) The high surface area of the nanowire array favors the efficient contact between active materials and electrolytes, providing more active sites for electrochemical reactions. (5) The nanowire array structure helps to alleviate the structure damage caused by volume expansion during the cycling process and keep the morphology stable. This feature is particularly helpful for high rate applications, resulting in better cycling performance. The basic morphology of the single-crystalline Co_3O_4 nanowire array is preserved after 4000 cycles (Fig. 7a and b). A TEM image of the Co_3O_4 nanowire after 4000 cycles shows that the nanowire keeps the structure integrity and maintains the single-crystalline structure made up of numerous interconnected nanoparticles with almost the same crystal orientations (Fig. 7a). It is indicated that the crystalline structure of the nanowire does not change, implying that the

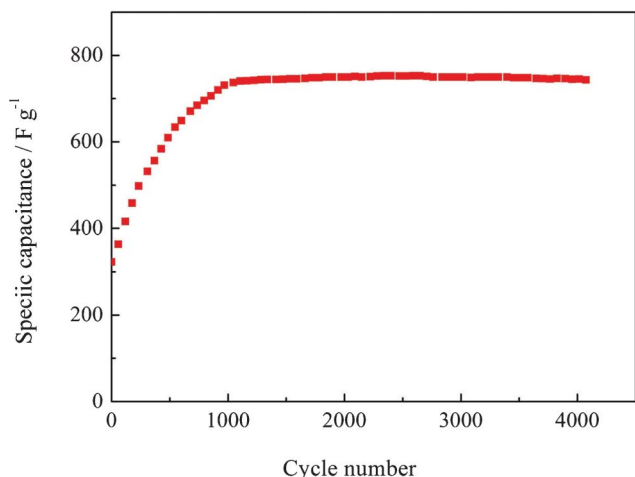


Fig. 6 Cycling performance of the Co_3O_4 nanowire array at 2 A g^{-1} .

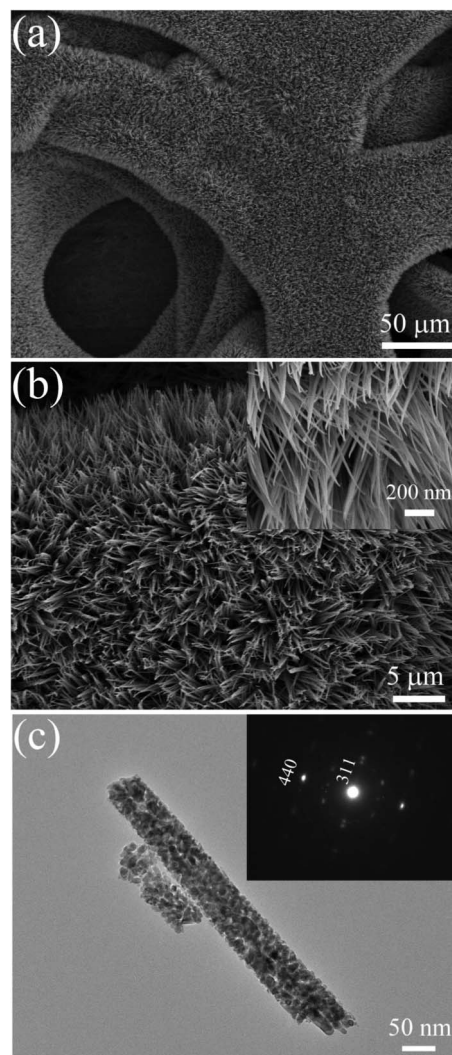


Fig. 7 (a), (b) SEM images of the Co_3O_4 nanowire array after cycling for 4000 cycles; (c) a TEM image of the Co_3O_4 nanowire after cycling for 4000 cycles (SAED pattern in inset).

charge–discharge process mainly happens on the shallow surface.

4. Conclusions

In summary, we have demonstrated a self-supported single-crystalline Co_3O_4 nanowire array grown on the nickel foam as an interesting material for pseudocapacitors with high capacitance, good cyclability and high rate capability. We believe that its outstanding performance comes from the unique porous architecture of the nanowire array. With their ease of fabrication and good performance, this nanowire array will hold promise for application in supercapacitors.

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