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Transport mechanism of copper sulfide embedded carbon nitride thin films: a formation free memristor†

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Nonvolatile electrical resistive behaviour was demonstrated for a copper sulfide nanoparticle decorated carbon nitride (CSCN) based device. The copper sulfide–carbon nitride composite system was synthesized using a high temperature *in situ* protocol, where the sulfide particles were highly dispersed on the carbon nitride matrix. The CSCN composite was characterized by using different analytical techniques. The transport mechanism of the device followed Poole–Frenkel (PF) for the OFF-state, whereas ohmic behaviour dominated for the ON-state. An endurance study of the device was performed with a duty-cycle of 50% for 2×10^3 cycles and the nonvolatile behavior of the device was studied with a duty-cycle of 0.16% for 2×10^3 s. Both the endurance and nonvolatile behavior exhibited excellent stability of the device with an ON to OFF ratio of 10^4 .

1. Introduction

Nonvolatile resistive memory has attracted significant attention for next-generation data-storage applications^{1,2} due to faster device performance, data nonvolatility and low power consumption.^{3,4} A resistive memory device utilizes the bistable resistances of the high-resistance state and low-resistance state of materials.⁵ The simple architectural design also makes a resistive memory device more attractive over other memory candidates, such as ferroelectric and magnetoresistive memory devices.^{6,7} Resistive switching between the high-resistance state and low-resistance state has been observed in a large variety of materials, including transition metal oxides,⁸ post-transition metal oxides,⁹ perovskite materials,¹⁰ organic materials¹¹ and organic–inorganic hybrid systems¹² with a metal–insulator–metal structure.

Among the various transition metals, copper based materials have been studied extensively for resistive memory applications. A device made with copper sulphide (Ti–Cu₂S–Cu) exhibited bipolar resistive switching behaviour with an ON–OFF ratio of 0.20×10^2 ,¹³ whereas another copper sulphide based device with a different electrode combination (Pt–Cu₂S–Pt) exhibited resistive switching behaviour with an ON–OFF ratio of 10^2 .¹⁴ The electrical bistability of a reported device made with an active component of a Cu₂FeSnS₄ thin film, sandwiched between ITO and Al-electrodes, displayed bipolar-type current-voltage

characteristics with an ON–OFF ratio of 10^3 .¹⁵ Highly oriented Cu₂O films showed a unipolar resistive switching phenomenon, originating from the creation of metallic filaments due to the O^{2–} generation and the O₂ gas evolution.¹⁶ A copper oxide based resistive switching memory device, for electronic textile applications, showed a switching ratio of 10^2 between the high and low resistance states where the two stable states were retained for 10^7 seconds.¹⁷ Bipolar switching behavior was reported¹⁸ for the graphene–copper oxide system and the switching behavior was controlled by the migration of oxygen ions from the copper oxide layer. Based on two separate experiments, it was reported¹⁹ that the junction properties of a Ti–CuO–Cu device displayed two different transport characteristics when the active layer was combined with an organic material. The hybrid system of CuO and hexa-*peri*-hexabenzocoronene (HBC) exhibited unipolar switching behavior, whereas on replacing HBC with a fluorine derivative of HBC (hexafluoro-hexa-*peri*-hexabenzocoronene) bipolar resistive switching was observed. It was assumed that the incorporation of a strong-electron withdrawing fluorine component plays a significant role in the electronic properties of the device. The incorporation of a CuO layer with the ZnO layer in a Cu–ZnO–AZO (Al-doped ZnO) based device showed improved switching performance compared to the ZnO based single layer system. The conduction behaviour of the high and low resistance states followed trap-controlled space charge limited current (SCLC) and ohmic mechanisms, respectively, and the CuO layer served as the reservoir and supplier of oxygen ions in the SET and RESET process, respectively, which played a critical role for the forming and rupture of the filament.²⁰ Copper oxide nano-wires²¹ and

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nanodots²² showed formation induced resistive switching behaviour due to the copper ion migration. A device made with a copper sulfide thin film, sandwiched between a copper and another metal electrode (Au/Pt/Ti), demonstrated the effect of nonvolatile memory. The resistive switching process was explained by the formation and disruption of a conducting path, created due to the copper ion migration from the Cu-electrode to the copper sulfide film.²³

Graphitic carbon nitride, which is composed of carbon and nitrogen, can be easily synthesized under thermal condensation and has lots of advantages, such as moderate bandgap, non-toxic nature, high surface area, and stability in the atmospheric environment and also at high temperature. Due to these merits, carbon nitride (CN) has been applied as a support material for a wide-range of inorganic nanomaterials for diverse applications.²⁴ However, the application of carbon nitride for the fabrication of resistive random-access memory (RRAM) devices has rarely been reported. A device made with CN powder, as an active layer, was reported for RRAM applications with a switching ratio < 100.²⁵ A further report on CN for RRAM applications has been published where a CN in combination with poly-methyl methacrylate based device (Al-PMMA-CN-PMMA-ITO) exhibited typical bipolar resistive switching behavior with an ON-to-OFF ratio of 10^3 .²⁶

In this study, a high temperature *in situ* protocol was adopted to synthesise a copper sulfide nanoparticle decorated carbon nitride based thin composite film, where thiourea and copper nitrate were used as the precursors. The CSCN hybrid system was used as an active material for resistive random-access memory applications. The endurance and nonvolatile behavior of the device were studied for 2×10^3 cycles and for 2×10^3 s, respectively.

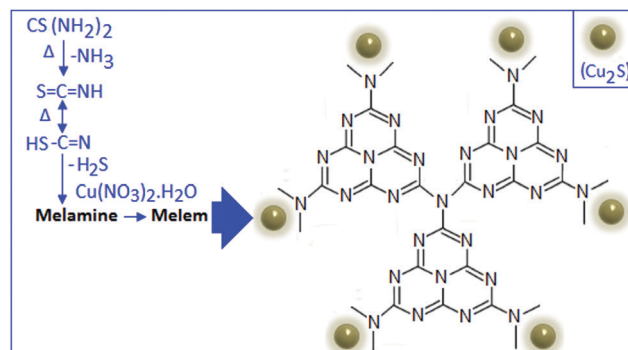
2. Experiment

2.1. Materials

Analytical grade chemicals were used in this investigation without further purification.

2.2. Preparation of the carbon nitride stabilized copper sulfide composite system (CSCN)

A mixture of copper nitrate, $\text{Cu}(\text{NO}_3)_2 \cdot 3\text{H}_2\text{O}$, and thiourea, $\text{CS}(\text{NH}_2)_2$, in a ratio of 1:5 (by weight), respectively, was kept inside a programmable furnace at 550 °C for 4 h and cooled down to room temperature. A constant argon atmosphere was maintained in the furnace during the synthesis process by continuous purging of argon gas. A dark brown coloured solid power CSCN (a) was recovered from the reaction chamber and characterized using different techniques. Two other samples, CSCN (b) and CSCN (c), were prepared under identical reaction conditions as above, using a copper nitrate and thiourea composition ratio of 0.5:5 and 2:5 (by weight), respectively. The formation mechanism of CSCN is schematically represented in Scheme 1.



Scheme 1 The formation mechanism of embedded copper sulfide in the carbon nitride matrix.

2.3. Device fabrication and electrical characterization

Glass slide supported flexible paper was used for the device fabrication, where the role of the slide was to provide mechanical strength. The CSCN (a) material was ground thoroughly and made into a slurry with methanol. The CSCN based slurry was dispersed in the form of a thin film on previously deposited bottom electrodes (gold) on the flexible paper using the spin coating technique. After proper drying, the top gold electrodes

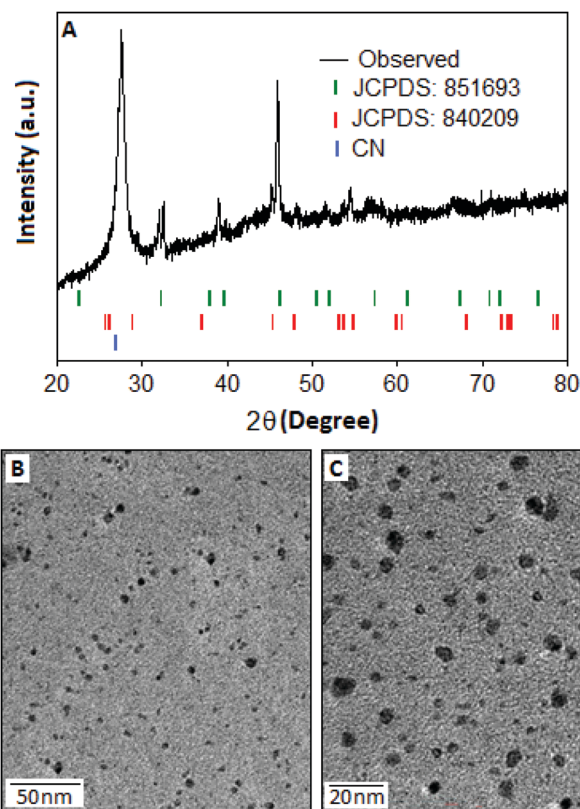


Fig. 1 (A) X-ray diffraction pattern of Cu_{2-x}S particles embedded within the carbon nitride matrix. The pattern consists of mixed phases of $\text{Cu}_{1.8}\text{S}$ (green bar, JCPDS: 851693) and Cu_2S (red bar, JCPDS: 840209) compositions. The hexagonal phase of carbon nitride (blue bar) is identified in the diffraction pattern. (B and C) TEM images of copper sulfide nanoparticles on the carbon nitride matrix, with different magnification.



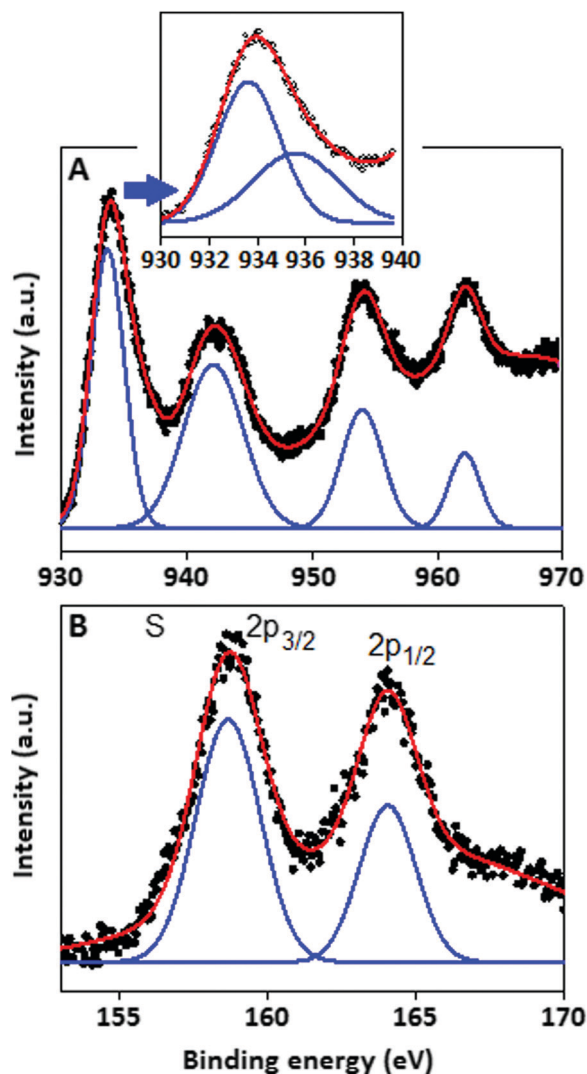


Fig. 2 X-ray photoelectron spectroscopy characteristics of the copper sulfide particles. XPS signal for (A) copper species and (B) sulphur species.

were deposited in a cross-bar fashion and a sandwich structured device was fabricated (Au||CSCN||Au). Fig. S1 inset (A) shows the schematic diagram of the cross-section image of the fabricated device (ESI[†]). The thickness of the fabricated Au-electrodes and the CSCN-layer is 200 nm and 0.5 μm , respectively. In a similar fashion, two other devices were fabricated using the samples CSCN (b) and CSCN (c).

The bottom electrode was connected to the high voltage terminal whereas the top electrode was connected to the ground terminal of the electrical characterization unit. The current-voltage (I - V) measurement was performed with a 0.17 V s^{-1} scan rate and 0.1 V step size. Keithley 2401 and 2612B source meters were used for the electrical characterization.

3. Results and discussion

The structural pattern and stoichiometry composition of the Cu-S system depend on the thermodynamics and kinetics of

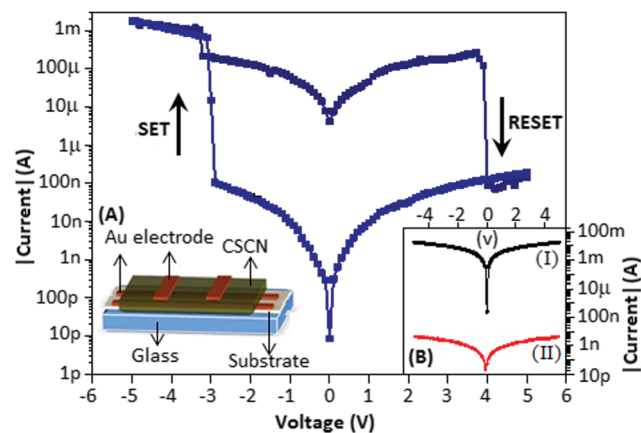


Fig. 3 Semi-log current-voltage (I - V) characteristics of the DUT, Au||CSCN||Au is bipolar in nature (main panel). Inset (A): Cross-bar structure of the Au||CSCN||Au device. Inset (B): Semi-log I - V of 0.5:5 and 2:5 of copper nitrate to thiourea (by wt) compositions.

the solid state reaction.²⁷ The phase of the synthesized products was characterized by XRD analysis and the spectrum was recorded within the range (2θ) from 20° to 80° . The X-ray diffraction pattern (Fig. 1A) of Cu-S revealed that the majority of the diffraction peaks belong to the non-stoichiometric rhombohedral phase of $\text{Cu}_{1.8}\text{S}$ (JCPDS: 851693, green bar) with a lattice constant of $a = b = c = 5.55 \text{ \AA}$, whereas some of the diffraction peaks are matched with the hexagonal pattern of the Cu_2S component (JCPDS: 840209, red bar). In the copper sulfide system, sulphur has a higher diffusion coefficient than copper, which results the formation of nonstoichiometric Cu_{2-x}S structure.^{28,29} In the XRD pattern, the most intense peak can be indexed as the interlayer spacing of the stacked aromatic segments in the (002) planes with a distance of 0.324 nm of the conjugated aromatic ring with lattice constant values of $a = b = 4.72 \text{ \AA}$ and $c = 6.72 \text{ \AA}$ derived from the hexagonal ($P6m2$) phase of graphitic carbon nitride (JCPDS: 871526)³⁰ at 27° and also as the Cu_2S particles originating from the same (002) plane at 26.18° .

The TEM images of CSCN are shown in Fig. 1(B and C) with different magnifications. Copper sulfide nanoparticles (dark spots), with large size distribution, within the range of 2–10 nm, are observed on the carbon nitride matrix. The surface elemental composition and the electronic states of the copper sulfide were characterized by the X-ray photoelectron spectroscopy (XPS) technique. The copper (Cu) spectra (Fig. 2A) are well fitted with Cu $2p_{3/2}$ and Cu $2p_{1/2}$ located at 933.91 and 953.82 eV respectively. Two other peaks positioned at 942.2 eV and 962.3 eV could be assigned as shake-up peaks for Cu $2p_{3/2}$ and $2p_{1/2}$, respectively. After deconvolution of the peak located at 933.91 eV, two separate peaks at 933.55 and 935.50 eV were generated (Fig. 2A, inset), which indicate the presence of copper species in the form of Cu(I) and Cu(II), respectively,³¹ with different levels of concentration. Apart from the copper peaks, the material showed characteristic sulfur peaks positioned at 158.73 and 164.0 eV, Fig. 2B, assigned to S $2p_{3/2}$ and S $2p_{1/2}$, respectively.³²



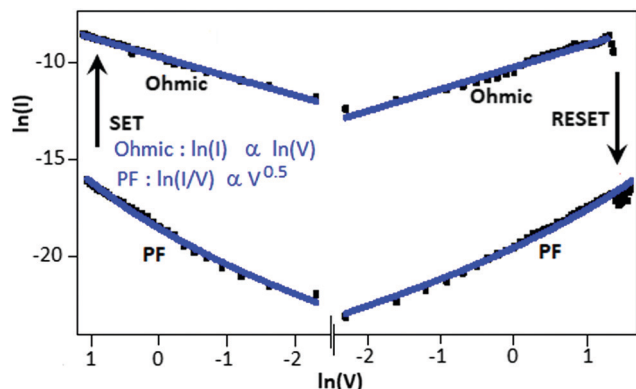


Fig. 4 The current–voltage graph (log-scale) of the Au||CSCN||Au device exhibited Poole–Frenkel emission for the low conductance state and the ohmic mechanism for the high conductance state.

The electrical property of the device is displayed in Fig. 3 (main panel). The schematic diagram of the device is presented in Fig. 3A. During the voltage sweep from +5 to −5 V, the device exhibited the SET process (the current value shifts from the low to the high conductance state) at −2.9 V. Again, when the voltage sweep was from −5 to +5 V, the device exhibited the RESET process (the current value shifts from the high to the low

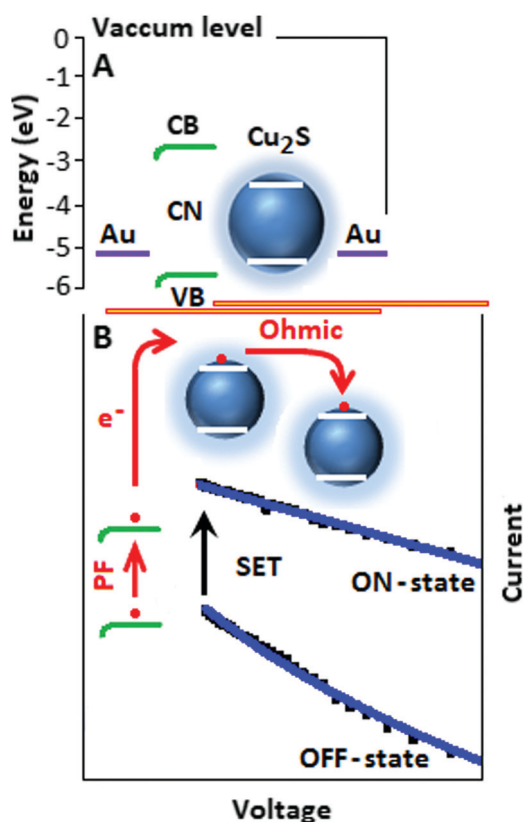


Fig. 5 (A) The energy diagram of the Au||CSCN||Au device. (B) The electron transport mechanism within the device. The electron transfer, from the HOMO to the LUMO of the CN system, followed Poole–Frenkel emission for the OFF-state and the ohmic mechanism was followed for the ON-state through the Cu_2S particles.

conductance state) at 3.8 V. The device has been defined as bipolar as the SET and RESET process occurred at the opposite polarity (voltage). The endurance study of the device for multiple voltage sweeps is documented in the ESI† (Fig. S2). After the fourth cycle the device reached the stable form. The maximum ON–OFF ratio was achieved at −1.0 V and was 10^4 . The device made with CSCN (c) exhibited a higher conduction state, Fig. 3B(I), black in colour, as compared with the device made with CSCN (b), Fig. 3B(II), red in colour. For both the devices no resistive switching effect was observed. The above phenomenon can be explained by the amount of copper sulfide particle formation. The higher conductance state indicates that nano-particles played a major role in the conduction process, whereas the low conductance state suggests that carbon nitride contributes the charge transport process.

To understand the switching mechanism of the device, the current–voltage graph was fitted with different theoretical models for both the high conductance (ON) and low conductance (OFF) states. Fig. 4 shows the I – V characteristics on a double logarithmic scale, $\ln(I)$ vs. $\ln(V)$, where the low conductance state and high conductance state followed Poole–Frenkel (PF) and

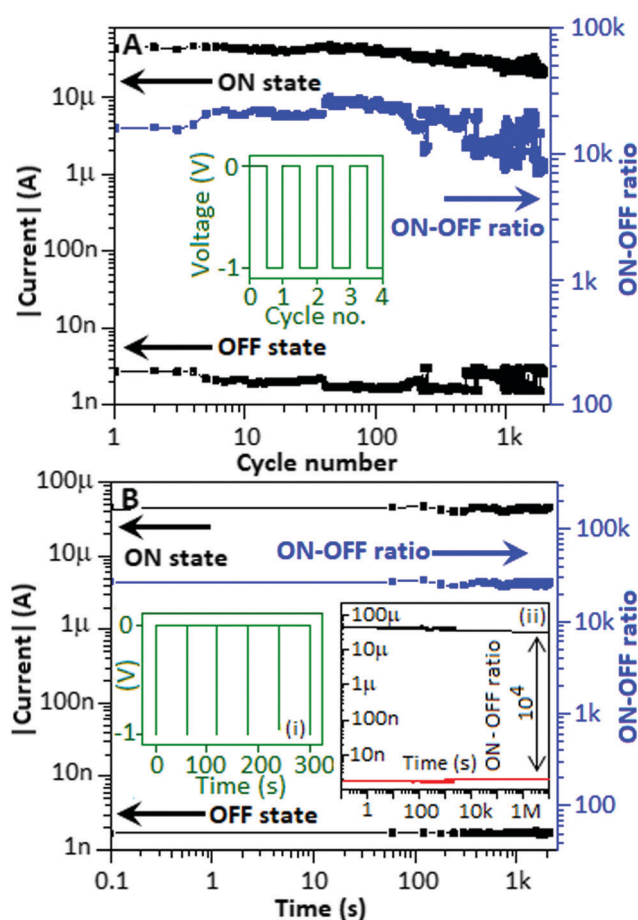


Fig. 6 (A) The endurance study of the device for 2×10^3 cycles. The pulse train employed for the endurance study (inset). (B) The nonvolatile study of the device for 2×10^3 s. The pulse train employed for the nonvolatile study with a duty cycle of 0.16%, inset (i). The estimated retention of the device after 10^7 s, inset (ii).

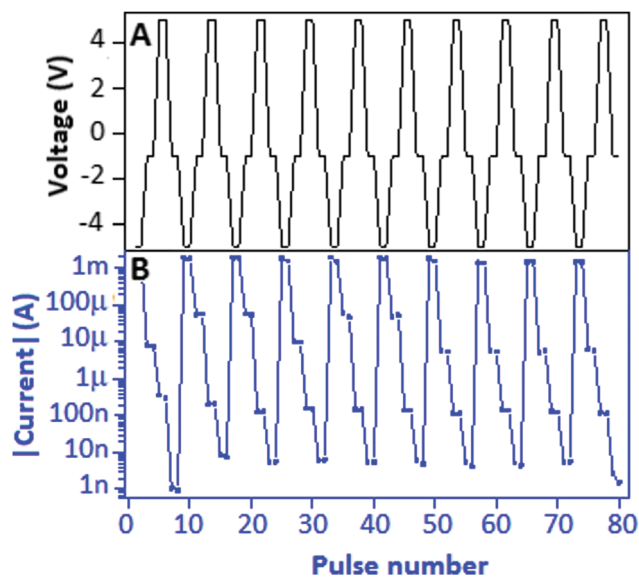


Fig. 7 (A) The Write–Read (1)–Erase–Read (0) voltage cycle (-5.0 , -1.0 , $+5.0$ and -1.0 V) applied to the device and (B) the respective current response (semi-log scale) received from the device.

ohmic conduction mechanisms, respectively. From the graph, it is clear that during the voltage sweep from $+5$ to -5 V, the conductive path changed from PF to ohmic at -2.9 V (SET), whereas during the voltage sweep from -5 to $+5$ V, the conductive path changed from ohmic to PF at 3.8 V (RESET).

The graphical representation of the current–voltage characteristic, in a linear form, is incorporated in the supplementary information, with realization of the PF and ohmic behaviour (Fig. S1, main panel, ESI[†]).

The energy diagram of the device (Fig. 5A) explains the transport mechanism on the basis of different energy of states.^{33–35} Initially the electrons are trapped and localized on carbon nitride and under a suitable potential the trapped electrons are released and move slowly from the valence band (VB) to the conduction band (CB) of the carbon nitride.

The conduction band electrons migrate to the conduction band of copper sulfide. The transport of conduction band electrons among the copper sulfide particles follows the ohmic conduction mechanism (slope ~ 0.96), where the current is linearly dependent on the electric field (Fig. 5B). The devices CSCN (b) and CSCN (c) follow the PF and ohmic type transport mechanisms, respectively.

The properties of memory devices are mainly based on three major factors: the (a) endurance property, (b) nonvolatile property and (c) electrically rewritable property. For the endurance study, initially the device was exposed to $+5$ V and -5 V for a few seconds to imprint the low conductance state and high conductance state, respectively. The device was subjected to a pulse train consisting of -1 V for a 0.1 s time period separated by 0.1 s (Fig. 6A, inset) to investigate the stability of the device for the high conductance and low conductance states. In this experiment, the endurance study was carried out for 2×10^3 cycles and Fig. 6A shows that both the high and low conductance states are moderately stable with an ON–OFF ratio of 10^4 , which is comparable with the ON–OFF ratio achieved from the I – V curve (Fig. 3, main panel). Nonvolatile behaviour of the device is one of the major advantages of resistive switching memory devices. The nonvolatile study investigates the stability of the data retained within the DUT irrespective of the refreshing power supply. Prior to conducting the nonvolatile study, -5 V and $+5$ V were applied to the device for a few seconds to imprint the high and low conductance state, respectively. For the nonvolatile study, -1 V pulses for a 0.1 s time period, separated by 60 s, were applied to the device, Fig. 6B, inset (i). The duty cycle of each pulse was 0.16% in the pulse train. The device exhibited nonvolatile behavior (the states neither alter nor fluctuate) throughout the study for 2×10^3 s with an ON–OFF ratio of above 10^4 . Fig. 6(B) inset (ii) shows the proposed retention of the device with an estimable ON-to-OFF ratio of $\sim 10^4$ after 10^7 s, by using the graphical extrapolation technique.³⁶

To study the electrically rewritable property, a Write–Read (1)–Erase–Read (0) (WR_1ER_0) cycle was designed to investigate

Table 1 Some copper based reported materials for RRAM applications

Material (M)	Device structure	Mechanism	ON–OFF ratio	Endurance	Retention	Ref.
Cu ₂ S	Ti/M/Cu	NA	0.2×10^2	NA	NA	13
Cu ₂ S	Pt/M/Pt	NA	10^2	NA	NA	14
Cu ₂ FeSnS ₄	ITO/M/AL	NA	10^3	NA	NA	15
CuS/Cu _{2-x} S	FTO/M/Al	Ohmic, SCLC	10^2	10^2	NA	37
p-ASA–Cu	Au/M/Au	PF, ohmic	2×10^3	10^3	10^3	12
Cu _x O	Cu/M/Ag	NA	10^2	NA	NA	38
Cu _x O	Al/M/Cu	NA	10^3	$> 10^2$	NA	39
CuO _x	Ni/M/Ni	Ohmic, PF	10^1	10^2	10^4	40
CuO	Al/M/ITO	Ohmic, SCLC, TCLC	10^5	2×10^2	10^4	41
CuO	Al/M/ITO	Ohmic, SCLC	10^3	2×10^2	10^4	42
Cu _x O	KCu ₇ S ₄ /M/Au	Ohmic	10^2	2×10^1	4×10^2	43
CuO/Nb:SrTiO ₃	Ag/M/Ag	NA	10^2	NA	NA	44
Cu ₂ O	TiW/M/Cu	SE, VRH	10^2	NA	NA	45
Cu _x O	Au/Al/M/Au	SE, PF	50	50	NA	46
CSCN	Au/M/Au	Ohmic, PF	$> 10^4$	2×10^3	2×10^3	Present work

SCLC: space-charge limited conduction, PF: Poole Frenkel, SE: Schottky emission, TE: thermionic emission, VRH: Mott's variable range hopping, p-ASA: poly-aminosalicylic acid.



the ability of the device for differentiating the low conductance state (0) from the high conductance state (1) after the erasing and writing process, respectively. WR_1ER_0 consists of four pulses of -5 V, -1 V, $+5$ V and -1 V corresponding to the Write, Read (1), Erase and Read (0) process of the device as shown in Fig. 7A. The current response of the WR_1ER_0 cycle is plotted on a semi-log scale (Fig. 7B), which exhibits a clear difference from Read (1) to Read (0) with a value of ~ 10 μ A. The current response (linear) is documented in Fig. S3(b), ESI.† The endurance, nonvolatile and electrically rewritable properties of the copper sulfide nanoparticle doped carbon nitride based hybrid device displayed competence for nonvolatile, bistable memory applications. As a ready reference for the readers, some copper based RRAM devices with parameters such as device structure, mechanism, ON-to-OFF ratio, endurance and retention are incorporated in this manuscript in a tabular form (Table 1).

4. Conclusion

A copper sulfide nanoparticle doped carbon nitride composite was synthesized using a high temperature *in situ* protocol, where the sulfide particles are highly dispersed within the carbon nitride matrix. To the best of our knowledge, this is the first kind of report where an *in situ* method has been applied for the simultaneous synthesis of carbon nitride and copper sulfide nanoparticles. The XRD and XPS analysis revealed the formation of nonstoichiometric copper sulfide and the TEM images displayed a large size distribution of the copper sulfide nanoparticles, within the range of 2–10 nm, on the carbon nitride matrix. The device exhibited bipolar characteristics for the resistive switching process where the low conductance state followed a Poole–Frenkel and the entire high conductance state followed an ohmic conduction mechanism. The device displayed excellent endurance and nonvolatile behaviour for 2×10^3 cycles and 2×10^3 s, respectively, and maintained a stable ON–OFF ratio of 10^4 for both. The electrically rewritable property clearly exhibited a difference among the Write, Read (1), Erase and Read (0) processes, which indicates that the device has the capability to process binary information for memory applications.

Conflicts of interest

There are no conflicts to declare.

Acknowledgements

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