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NiO-GDC nanowire anodes for SOFCs: novel growth, characterization and cell performance[†]

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A solid-oxide fuel cell (SOFC) is a sustainable energy resource that has been efficiently used for large-scale applications such as gas-turbines. However, its miniaturization for small-scale applications requires novel electrode materials especially anodes (other than Ni-YSZ) in different configurations and designs such as in the nanostructure form. Herein, we are proposing the growth of a novel nickel oxide–gadolinium doped ceria (NiO-GDC) nanowire-based anode using a vapor–liquid–solid (VLS) mechanism. To the best of our knowledge, NiO-GDC has never been grown before in the form of nanowires using the VLS mechanism. The nanowires are prepared at different evaporation temperatures and exhibit dense morphology. Detailed Raman spectroscopy reveals that during the growth, reorganization of NiO-GDC particles results in the formation of a more complex structure that diverges from pristine NiO-GDC powder. Furthermore, the temperature-dependence of the electrical conductivity reveals that the nanowires prepared at 1400 °C (evaporation temperature) possess high conductivity due to better charge-carrier transport, confirmed by their low activation energies. The electrolyte-supported button cell synthesized using a NiO-GDC nanowire anode exhibits a maximum power density of ~ 178 mW cm⁻² at 800 °C and concentration polarization is found to be the major loss, as revealed by electrochemical impedance spectroscopy (EIS) data. Based on our preliminary investigations, these nanowires have great potential to be used as an anode for SOFCs.

Introduction

In the increasing demand for renewable energy resources due to worldwide industrial and technological developments, fuel cells (FCs) represent a class of technology which is efficient, clean, sustainable, secure and affordable.^{1–3} FCs are static energy conversion devices that produce electricity *via* an electrochemical reaction between a fuel and an oxidizing agent. There are six different kinds of FCs depending upon the electrolyte and the fuel such as alkaline fuel cells (AFCs), solid oxide fuel cells (SOFCs), direct methanol fuel cells (DMFCs) and others.⁴ Among them, SOFCs are highly efficient and use a solid electrolyte such as dense yttria stabilized zirconia (YSZ) for their operation.⁵ Moreover, one of the major advantages of SOFCs is the possibility of utilizing different kinds of fuels such as hydrogen, hydrocarbons, carbon monoxide, *etc.*⁶ However, there are still several critical issues that need attention such as high operational temperature, slow start up and high cost. In particular, small-scale applications such as small electrical generators, micro-CPH (combined heat and power systems)

and other portable electronic devices require novel electrode materials and designs that can be operated at relatively lower temperatures (lower than YSZ based SOFCs) and in turn produce high efficiency.⁷ For example, in the development of micro-SOFCs, major challenges that need to be overcome are bulky design and high temperature operation.⁷

In this regard, the nickel oxide–gadolinium doped ceria (NiO-GDC) anode and GDC electrolyte are attracting considerable attention for developing intermediate-temperature SOFCs (IT-SOFCs) due to their high conductivity at temperatures ranging from 500 °C to 650 °C and exceptional physical/chemical properties.^{8–11} Furthermore, for miniaturization, nanostructured materials can be the best option due to their exceptional physical/chemical properties such as compact size, a high surface to volume ratio, suitable transport properties, better electrical properties and so on.¹² Among different nanostructure forms, nanowires (NWs) exhibit well-defined crystal orientation, single crystallinity and exceptional physical/chemical properties.¹³ Indeed, NWs also offer suitable morphology for the flow of fuel and oxidizing agents. Moreover, when developing layered structures like SOFCs, lattice matching between different layers plays an extremely important role especially in the mechanical properties of the device. It has been found that in nanowire (NW) morphology lattice mismatch strains can be relaxed elastically at the NW free surface

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without the formation of dislocations.¹⁴ This implies that the critical thickness is much greater in the NW morphology as compared to other structures such as thin films. Hence, this property of NWs to combine materials regardless of any need for lattice-matching can be explored in SOFC devices.

Herein, we propose novel NiO-GDC nanowires synthesized *via* the vapor liquid solid (VLS) mechanism in a tubular furnace for their application as an anode material for SOFCs. NWs were grown at two different evaporation temperatures and the deposition time was kept constant. To the best of our knowledge, NiO-GDC has never been grown before in the form of nanowire morphology especially using the VLS mechanism. Indeed, VLS is a high yield technique and is immensely used for nanowire growth due to its many advantages such as direct growth on an active substrate, no requirement for precursors, and lowered reaction energy and because it only needs a furnace and primary pumping system.¹⁵ The nanowires were characterized to investigate their surface morphology, structure and electrical properties *via* different characterization techniques. Finally, the nanowires were grown on a commercial single electrode (cathode only) button cell to investigate the cell and its electrochemical performance. Detailed investigations reveal that the NiO-GDC nanowires have the potential to be used as an anode for SOFCs.

Experimental

Substrate preparation

In this work, 3×3 and 10×10 mm² alumina substrates (99% purity, Kyocera, Japan) were used to grow the NiO-GDC nanowires. Specifically, 10×10 mm² alumina substrates were mainly used for structural characterization using nano-Raman spectroscopy, while 3×3 mm² alumina substrates were employed for surface morphology and electrical studies. Prior to the growth, substrates were ultrasonically cleaned with acetone for 20 minutes and then dried with synthetic air. Afterward, a gold (Au) catalyst was deposited on the alumina substrates using RF-magnetron sputtering. The catalyst deposition was done at 70 watts with 7 sccm of argon (Ar) plasma for 5 seconds. The distance between the Au-target and the alumina substrate was fixed at 7.1 cm. The Au catalysts acted as nucleation sites for the growth of highly crystalline nanowires. Indeed, during the growth, these metal catalysts liquidized and when

the supersaturation was reached, acted as a preferential site for the crystallization of nanowires.¹⁶ It should be pointed out that the middle area of the alumina substrate was exposed to Au-catalyst and the sides were masked for the deposition of Pt-contacts (see Fig. 1).

Growth of NiO-GDC nanowires using the VLS mechanism and characterization

NiO-GDC nanowire growth was performed in a lab-made tubular furnace equipped with vacuum pumps and heating systems by using the vapor liquid solid (VLS) mechanism. The VLS mechanism is named after the three different material phases involved in the process of nanowire growth *i.e.* vapor state of the source material, liquid catalyst droplets and solid crystalline nanostructures produced. The vacuum pump maintains a pressure of 10 mbar inside the alumina tube during the deposition. The NiO-GDC powder (fuelcellmaterials, 60% NiO and 40% GDC ($\text{Gd}_{0.1}\text{Ce}_{0.9}\text{O}_{1.95}$)) was placed inside the furnace in the maximum temperature region to promote evaporation. In this work, the depositions were carried out at two different evaporation temperatures *i.e.* at 1400 °C and 1450 °C, while the deposition was carried out for 40 minutes. It should be pointed out that the selection of these temperatures was done after the optimization of nanowire formation. In particular, no evaporation of NiO-GDC powder was observed for the temperatures below 1400 °C and our furnace could reach only up to 1450 °C. For the deposition at 1400 °C, the substrates were placed in a relatively colder region of the furnace at 1120 °C, allowing the condensation in the form of nanowires. However, for nanowires deposited at 1450 °C, the substrates were placed at 1170 °C inside the tubular furnace. These substrate temperatures were selected after the optimization of nanowire growth conditions (see the ESI,† Fig. S1). An argon flow (set at 100 sccm) was used as the carrier gas transporting NiO-GDC particles from source materials to substrates during the deposition. First of all, the furnace was heated up to reach the desired deposition temperature (1400 °C and 1450 °C). In this stage the argon gas flow was maintained from the substrate to surface material direction to avoid any undesired deposition. When the furnace reached the deposition temperature, the argon flow was directed from the source to the substrate. Indeed, the VLS mechanism is named after the three different phases of the material involved in the growth process: the vapor state of the source

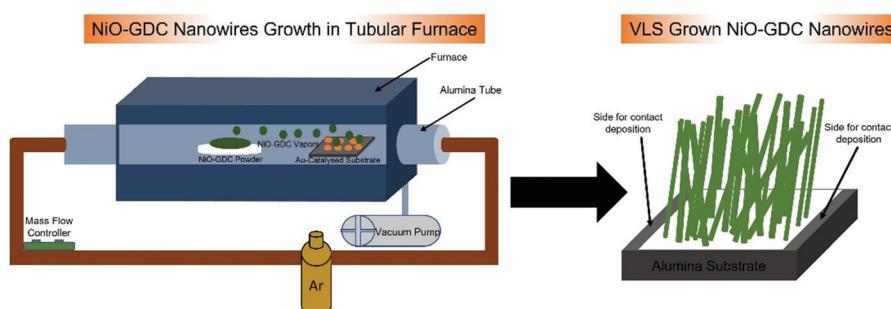


Fig. 1 Schematic representing the growth of NiO-GDC nanowires in a tubular furnace.



material (NiO-GDC), the formation of the liquid catalyst droplet (Au), and the solid crystalline nanostructure that is produced.¹³ Hence, in the present case, the formation of a liquid droplet of a Au-catalyst occurs at the substrate temperature (1120 °C and 1170 °C) and NiO-GDC vapors adsorb on its surface. Since vapors are continuously provided, the liquid alloy starts to saturate forming a solid precipitate, which grows in the form of a 1D nanostructure.¹⁷ As can be seen from the above discussion, the metal-catalyst plays an extremely important role in the growth of nanowires. Indeed, catalysts need to fulfill certain important requirements *i.e.* they must be a liquid at the deposition temperature, they must be inert to chemical reactions, the vapor pressure of the catalyst component over the liquid alloy should be small, and they must not produce intermediate solids. For further details about the VLS mechanism for nanowire growth, the reader can refer to these review articles.^{13,18}

The whole process of NiO-GDC NW growth is summarized in Fig. 1. The surface morphology of bare nanowires was examined using two different FE-SEM microscopes *i.e.* LEO 1525 (Gemini model; Carl Zeiss AG, Oberkochen, Germany) and MIRA 3 LMU (TESCAN, Brno, Czech Republic). The surface morphological images were acquired by operating the FE-SEM at 7 kV. Raman spectra were measured by using an XploRA Nano system (Horiba Jobin Yvon Srl, Italy) with a confocal microscope (Olympus BX) and a 1200 g mm⁻¹ reticule. A Peltier-cooled Open Electrode CCD was used to record the Raman spectra excited by a 638 solid-state laser. Spectra were recorded in the wavelength range 100–1800 cm⁻¹. The crystalline properties of bare NiO/GDC NWs were examined using the XRD technique (Empyrean diffractometer, PANalytical, the Netherlands). The XRD analysis was performed using Cu-LFF ($\lambda = 1.5406 \text{ \AA}$) operated at 40 kV and 40 mA in the diffraction angle range of 10–90°.

Device fabrication for electrical measurements

The devices for temperature dependent electrical measurements were prepared by using DC magnetron sputtering

(sputtering parameters: power = 75 W, carrier gas: argon (7 sccm flow) and deposition pressure = 5.3×10^{-3} mbar). Again, the distance between the Au-target and the alumina substrate was fixed at 7.1 cm. Firstly, 50 nm thick adhesion layers of a titanium tungsten (TiW) alloy in the form of parallel pads were deposited on the sides of samples by using a shadow mask. The deposition was carried out for 3 minutes. Afterwards, 20 minutes deposition of platinum contacts (parallel pads, thickness: 1 μm) was carried out again by using a shadow mask with the same sputtering parameters. Furthermore, for temperature dependent electrical measurements, Pt heating elements were also deposited using a DC magnetron on the back side of the substrates. For this purpose, successive depositions of Pt pads (using a TiW adhesion layer) and a Pt heater were performed. The prepared devices were finally mounted on TO packages using electro-soldered gold wires. The whole process of device fabrication and a picture of the TO packaged NiO-GDC nanowire device are shown in Fig. 2.

To investigate the electrical properties of nanowires, a custom-made stainless-steel climatic chamber was used. The conductance of the nanowires was investigated for temperatures ranging from 100 to 500 °C. The temperature of the devices was controlled by modulating the electric power applied to the heater using a Thurlby-Thandar PL_330DP power supply system. For the thermal stabilization, the devices were heated at 300 °C for 10 h. An Agilent E3631A was used as a power supply system and the current was recorded using a Keithley 6485 electrometer. Indeed, the I – V measurements were performed at a constant temperature of 500 °C by varying the voltage from 0 to 1 V (step: 0.2 V).

The conductivity (σ) of nanowires was calculated from the following expression:

$$\sigma = G \frac{l}{A} \quad (1)$$

where G is the conductance of the nanowires, while l and A are the length and area of the devices.

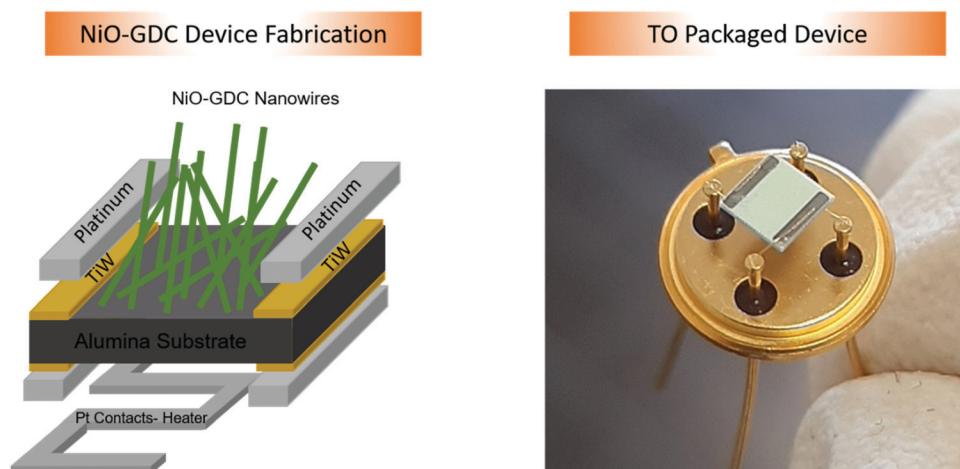


Fig. 2 Deposition of electrical contacts on NiO-GDC nanowires (left) and picture of the final TO packaged mounted device (right).



Fabrication of an electrolyte-supported button cell and electrochemical impedance spectroscopy

To further investigate the performance of the nanowire anode for potential SOFC application, an electrolyte-supported button cell was fabricated. For this purpose, commercially available single electrode cell-cathode only (Hionic electrolyte support: scandia doped zirconia (diameter: 20 mm and thickness: 0.13–0.17 mm) and cathode: LSM/LSM-GDC ($\text{La}_{0.80}\text{Sr}_{0.20}\text{O}_{0.95-\text{x}}\text{MnO}_{3-\text{x}}(\text{Gd}_{0.1}\text{Ce}_{0.9})\text{O}_{1.95}$, diameter: 12.5 mm and thickness: 50 μm) was procured from fuelcellmaterials. Firstly, the Au catalyst was deposited on the electrolyte support *via* using the same deposition conditions mentioned in substrate preparation by covering the effective areas of the cell with a mask. NiO-GDC nanowires were deposited *via* the VLS mechanism on a Au-catalyzed button cell by placing the cell inside the tubular furnace and using the same growth conditions as described above (see Subsection: growth of NiO-GDC nanowires using the VLS mechanism). After the growth, the nanowire morphology was again examined by FE-SEM. The effective area of the cell was found to be 1.23 cm^2 .

For the fuel cell *I*-*V* characteristics and EIS measurements, a SOFC test set-up (Open FlangesTM test Set-up) from Fiaxell was used, which is also integrated with an impedance analyzer. The button-cell was sandwiched between the anode and cathode collector discs and placed inside a Kitted Squadro muffle furnace, linked to appropriate gas tight fittings for the delivery of air (flow: 200 sccm) and dry hydrogen (flow: 100 sccm) fuel to the cell.

To measure the current and voltage from the cells, the wire of the mesh was connected to a NuvantTM Powerstat 05 potentiostat and galvanostat. The *I*-*V* characteristics of the cell were obtained by varying the load current from 0 to 400 mA. The EIS measurements were performed using an impedance analyzer (Ivium) integrated with the test set-up in the frequency range from 0.1 Hz to 100 kHz under a load current of 200 mA. Both the measurements were performed at 800 °C.

Results and discussion

Surface morphology of NiO-GDC nanowires

Fig. 3(a and b) show the surface morphology of NiO-GDC nanowires synthesized using the VLS mechanism at evaporation temperatures of 1400 °C and 1450 °C respectively. Clearly, in both cases, the nanowires possess dense morphologies and are extremely long (in the μm range). Moreover, the SEM images acquired at higher magnification (Fig. 3(c and d)) reveal that the nanowires seem to exhibit different diameters. Indeed, the diameters of nanowires were found to be 118 ± 50 nm (1400 °C) and 220 ± 40 nm (1450 °C). Thus, the diameter of the nanowires is increased as we increase evaporation and substrate temperatures. This increase in diameter of nanowires is associated with the substrate temperature.¹⁹ In the VLS mechanism, the diameter of nanowires is significantly influenced by the size of Au-nanoparticles which is in turn dependent upon the substrate temperature. It has been observed that the merging of Au-nanoparticles (*i.e.* increase in the diameter of Au-nanoparticles) tends to increase with substrate temperature, influencing the nanowire diameter.²⁰ Hence, nanowires grown at an evaporation temperature of 1450 °C (substrate temperature: 1170 °C) possess higher diameters as compared to the ones grown at 1400 °C (substrate temperature: 1120 °C).

Furthermore, in order to estimate the thicknesses of the nanowire layer, SEM images were acquired by tilting the samples. It should be noted that in the case of randomly oriented nanowires, it is very difficult to find the exact value of thickness as the nanowires were grown in all possible directions such as parallel, perpendicular, *etc.* Fig. 3e and f present the tilted SEM images of the NiO/GDC nanowires grown at evaporation temperatures of 1400 °C and 1450 °C, respectively. It can be clearly seen that the nanowires were grown even on the sides of the alumina substrates, thus making it more difficult to find the edges. Moreover, some of the islands on the edges of the alumina substrates belong to the

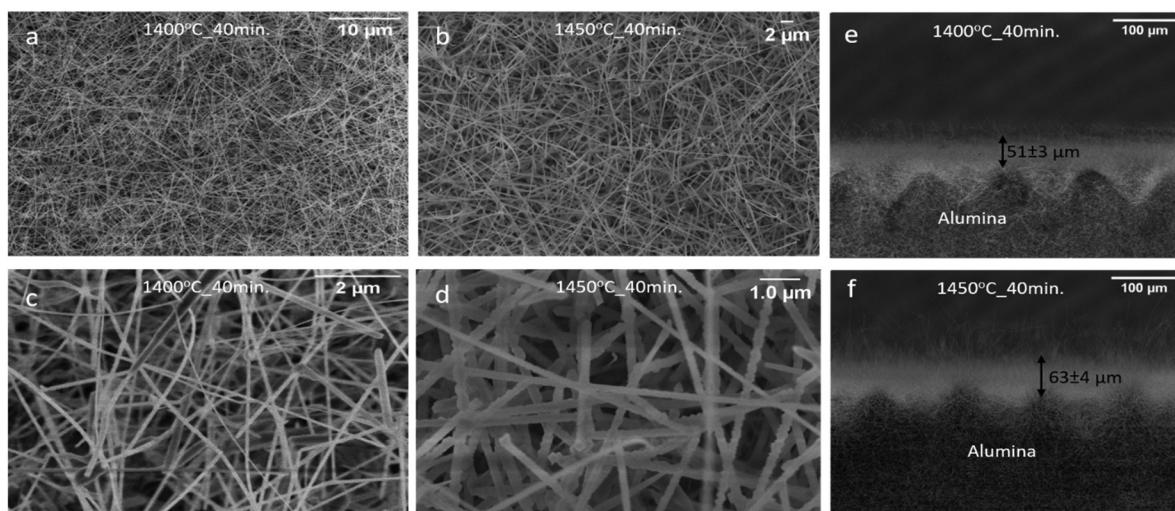


Fig. 3 FE-SEM images of NiO-GDC nanowires grown at evaporation temperatures of 1400 °C (a, c and e) and 1450 °C (b, d and f). In both cases, nanowires possess dense morphology and their estimated thickness was found to be $51 \pm 3 \mu\text{m}$ (1400 °C) and $63 \pm 4 \mu\text{m}$ (1450 °C).



laser beam used during the cutting process of the substrate. The estimated thickness values for the whole nanowire layers were found to be $51 \pm 3 \mu\text{m}$ (1400°C) and $63 \pm 4 \mu\text{m}$ (1450°C). The higher thickness of nanowires at 1450°C can be due to the deposition of a higher amount of material (high evaporation rate) as compared to nanowires grown at 1400°C .

X-Ray diffraction study of NiO-GDC nanowires

Fig. 4 shows the XRD patterns of VLS grown NiO-GDC nanowires along with the pristine NiO-GDC powder. The peaks indexed (*) belong to the alumina substrates and are observed in the case of nanowires only, as deposition was done on alumina substrates.¹⁵ In the case of powder, peaks at $2\theta = 37.3^\circ, 43.3^\circ, 63^\circ$ and 75.4° belong to the (111), (200), (220) and (311) *hkl* planes of NiO respectively^{8,21} while, for GDC, peaks at $2\theta = 28.5^\circ, 33^\circ, 47.4^\circ, 56.2^\circ, 59.1^\circ, 76.7^\circ$ and 88.2° correspond to the (111), (200), (220), (311), (222), (331) and (422) *hkl* planes respectively.⁸

In the case of nanowires, peaks belonging to GDC *i.e.* (111), (200), and (220) have not been observed, while most NiO peaks are present. This indicates that NiO-GDC nanowires have more NiO concentration than GDC. This can be due to the difference in the rate of evaporation of NiO and GDC. Furthermore, the nanowires grown at an evaporation temperature of 1450°C (substrate temperature = 1170°C) seem to possess higher crystallinity along with a higher concentration of both NiO and GDC (higher intensities of peaks especially NiO (200) and GDC (331)) as compared to the ones grown at 1400°C . Thus, as GDC possesses ionic conductivity, more ionic contribution to the electrical properties of nanowires grown at 1450°C can be expected.²² For further investigation of structural and compositional analysis nano-Raman spectroscopy has been performed.

Nano-Raman spectroscopy of NiO-GDC nanowires

To examine the structure, composition, and defects in VLS grown NiO-GDC nanowires, Raman spectroscopy has been performed. Indeed, Raman spectroscopy is a powerful tool that can provide information about the structural defects that may be formed during the growth of nanowires. Furthermore, to eliminate any contribution coming from the alumina substrate,

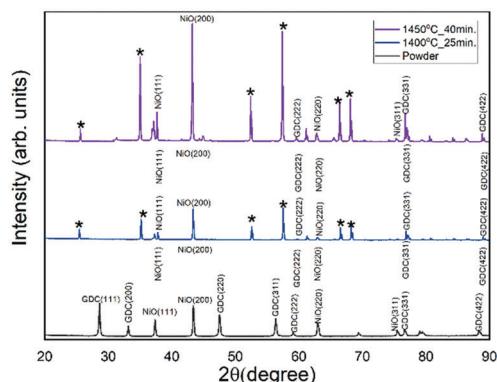


Fig. 4 XRD spectra of NiO-GDC powder and nanowires grown at evaporation temperatures of 1400°C and 1450°C .

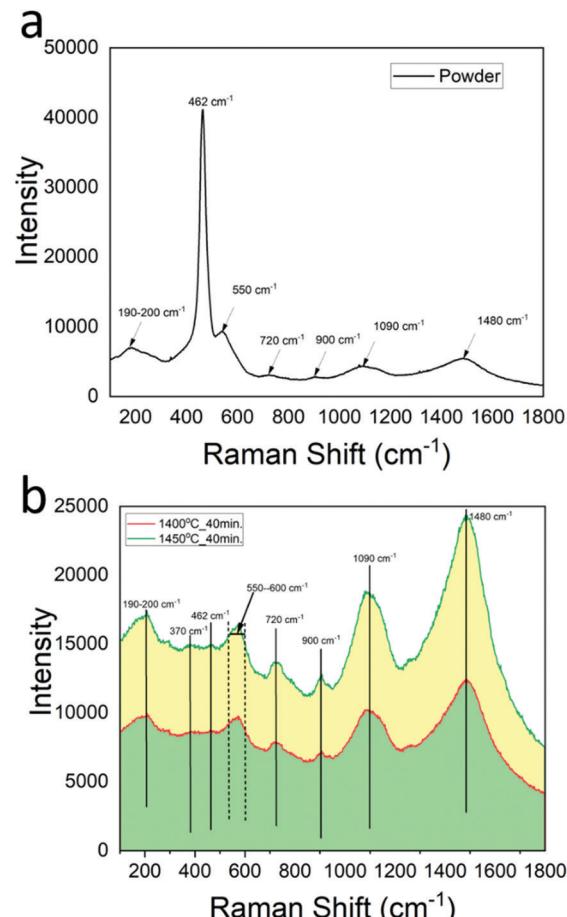


Fig. 5 (a) Nano-Raman spectrum of NiO-GDC powder indicating the presence of major NiO and GDC peaks. (b) Nano-Raman spectra of NiO-GDC nanowires indicating the formation of a complex structure with defects.

NiO-GDC nanowires were scratched and transferred onto a silicon substrate. For detailed investigation, the Raman spectrum of NiO/GDC powder was also acquired and is shown in Fig. 5a. The high-intensity peak observed at 462 cm^{-1} is the characteristic band of ceria (F_{2g} symmetry) representing the typical cubic fluorite structure of ceria-based materials. In particular, the F_{2g} symmetric band indicates the symmetrical stretching vibration of Ce–O₈ units and involves Ce–O and O–O force constants. Moreover, the intensity of the peak at 462 cm^{-1} suggests that in the powder the Ce–O bonds exhibit high polarizability with a highly symmetrical CeO_2 structure.²³ On the other hand, in the case of NiO-GDC nanowires (see Fig. 5b) F_{2g} symmetric bands become broader and extremely low in intensity. This is the indication of asymmetry in the ceria structure and the presence of various defects such as oxygen vacancies. A peak at $550\text{--}600\text{ cm}^{-1}$, which is observed in both powder and nanowires, is the D-band, where D stands for defects. These defects arise from the oxygen vacancies that were created due to the doping of ceria with gadolinium (Gd^{3+} replaces Ce^{4+} ions in the ceria lattice). In addition to these defects, another distinctive low intensity peak is observed at



370 cm^{-1} only in the case of NiO-GDC nanowires (absent in NiO-GDC powder) due to Gd-O vibration, indicating the presence of the cubic Gd_2O_3 phase.^{24,25} Thus, the presence of peaks at 462 cm^{-1} and 370 cm^{-1} in the NiO-GDC nanowires suggests the simultaneous existence of both C- and F-based microdomains in the hybrid regions of nanowires.²⁶ Furthermore, the existence of the 370 cm^{-1} peak is also an indication of the fluorite–double fluorite transition which depends upon the doping concentration.²⁷ However, these features are not present in the NiO-GDC powder. Furthermore, many other peaks that are observed in both powder and NiO-GDC nanowires are at ~ 200 , 720, 900, 1090 and 1480 cm^{-1} . The peaks observed at 1090 and 1480 cm^{-1} belong to the cubic phase of NiO. Indeed, the peaks at 1090 cm^{-1} represent the 2LO modes and those at 1490 cm^{-1} are an indication of two magnon (2 M) scattering while, peaks at 720 cm^{-1} (2TO (transverse optical)) and 900 cm^{-1} (TO + LO (longitudinal optical)) occur due to the second order phonon scattering in NiO. Lastly, the peak at ~ 190 –200 cm^{-1} arises from the interaction of oxygen vacancies and four nearest neighbor metal atoms.²⁸

Hence, the broadening of the low intensity peak at 462 cm^{-1} and the coexistence of both C- and F-based microdomains in NiO-GDC nanowires (due to the observation of the peak at 370 cm^{-1}) imply that during the growth of nanowires, reorganization of NiO-GDC particles results in the formation of a more complex structure with defects that diverges from pristine NiO-GDC powder.

Electrical properties of NiO-GDC nanowires

Fig. 6 shows the temperature-dependent electrical conductivity (σ) of NiO-GDC nanowires grown at different evaporation temperatures. The temperature is varied from 100 to 500 $^{\circ}\text{C}$. In both cases, conductivities of the nanowires increase with the increase of temperature. Indeed, nanowires grown at 1400 $^{\circ}\text{C}$ exhibit slightly higher conductivity as compared to the ones deposited at 1450 $^{\circ}\text{C}$. Furthermore, the conductivities of nanowires follow the Arrhenius-type activated process, and hence

Table 1 Activation energies of NiO-GDC nanowires grown at different evaporation temperatures. The standard deviation is related to the fitting parameters

NiO-GDC nanowires	Growth conditions	Substrate temperature ($^{\circ}\text{C}$)	E_a (eV)
Nanowires	1400 $^{\circ}\text{C}$ _40 min.	1120	0.33 ± 0.18
Nanowires	1450 $^{\circ}\text{C}$ _40 min.	1170	0.40 ± 0.19

can be expressed with the following equation:²⁹

$$\sigma = A \exp\left(\frac{-E_a}{K_b T}\right) \quad (2)$$

where A is the pre-exponential factor, E_a is the activation energy, $K_b = 8.167 \times 10^{-5}$ eV K^{-1} is the Boltzmann constant and T is the absolute temperature. By fitting the equation linearly, the values of activation energies were determined and are shown in Table 1. As can be clearly seen from Table 1, nanowires grown at 1400 $^{\circ}\text{C}$ possess the lowest activation energy. It is well known that the conductivity of the materials is inversely proportional to the activation energy.³⁰ Thus, a low activation energy signifies a better charge carrier transport in the NiO-GDC nanowires, which results in high conductivity. Furthermore, the slightly higher electrical conductivity of nanowires grown at 1400 $^{\circ}\text{C}$ can be associated with the well-defined surface morphology of the nanowires that allows the smooth flow of charge carriers. Another possible reason can be the variation in the concentration of NiO and GDC contents in the nanowires.²² It has been observed that high NiO concentration in the NiO-GDC composite improves the conductivity due to the domination of electronic conduction while high GDC concentration enhances the ionic charge transport, which results in low electrical conductivity of the composite.²² As we have already seen for XRD investigations, the nanowires grown at 1450 $^{\circ}\text{C}$ exhibit a higher concentration of GDC as compared to nanowires deposited at 1400 $^{\circ}\text{C}$. Hence, the better conductivity of nanowires grown at 1400 $^{\circ}\text{C}$ is also attributed to the lower GDC concentration as compared to other grown nanowires. Furthermore, due to the limitation of our electrical measurement system we were not able to go beyond 500 $^{\circ}\text{C}$, but an increase in the conductivities of nanowires with temperature is expected at higher temperatures *e.g.* for intermediate-temperature (500–750 $^{\circ}\text{C}$) operation of SOFCs. Furthermore, the I - V characteristics of nanowires acquired at 500 $^{\circ}\text{C}$ showed that the nanowires grown at an evaporation temperature of 1400 $^{\circ}\text{C}$ exhibit better conductivity (see the ESI†).

Cell performance and electrochemical impedance spectroscopy

It can be clearly seen from the previous discussion that the nanowires grown at the evaporation temperature of 1400 $^{\circ}\text{C}$ possess superior structural and electrical properties. Thus, these nanowires were selected for the construction of the cell and to investigate cell and electrochemical performance. An electrolyte-supported button cell was fabricated by depositing the NiO-GDC nanowires (1400 $^{\circ}\text{C}$ _40 min) on the commercial single electrode cell-cathode only (see Fig. 7). Fig. 8a shows the

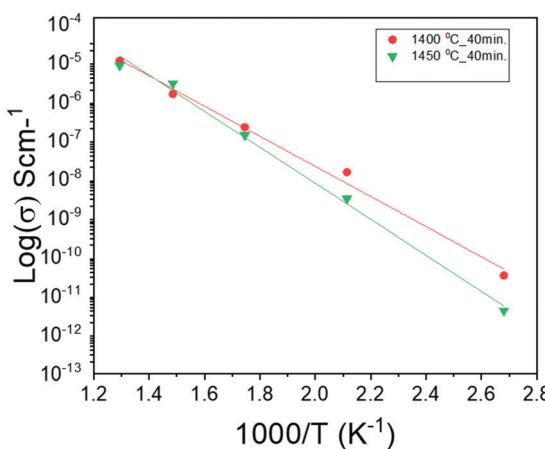


Fig. 6 Temperature dependence of electrical conductivity of NiO-GDC nanowires grown at different evaporation temperatures.



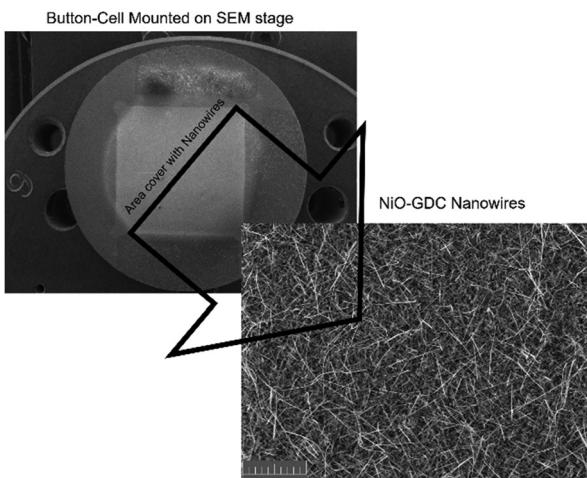


Fig. 7 Button-cell mounted on a SEM stage along with the SEM image of NiO-GDC nanowires. The same morphology of nanowires was achieved as shown in Fig. 3.

plots of cell voltage and power density *versus* current density at 800 °C. Dry hydrogen and air were used as a fuel and oxidizing agent, respectively. The open-circuit voltage (OPV) was found to be ~1 V which is similar to the theoretical value of the cell.⁴ The maximum power density of the cell was found to be ~178 mW cm⁻². We believe the cell performance can be furthermore improved by choosing other cell components such as GDC as an electrolyte (more compatible with the NiO-GDC anode) and other high-performance cathodes. Furthermore, the continuous drop in cell-voltage with respect to current density indicates the presence of typical SOFC losses such as polarization losses *etc.* in the cell. To investigate it in more detail, electrochemical impedance spectroscopy was performed under the supply of hydrogen fuel and oxidant air. Fig. 8b shows the Nyquist plot (graph between imaginary and real parts of impedance) of the cell indicating the presence of two different semicircles, one is toward the higher frequency region and the other is toward the lower frequency region. These two semicircles were fitted to the equivalent circuit model as shown in the inset of the figure.^{31,32} Here, R_{ohm} represents the sum of the ohmic resistance offered by anode, cathode and electrolyte. The resistance of the high frequency region (R_{high}) is due to the activation polarization, while mainly results frequency region resistance (R_{low}) mainly results from the concentration polarization.³¹ In fact, the diameter of low frequency semicircle seems to possess higher values than the diameter of high frequency semicircle, indicating the domination of concentration polarization losses at 800 °C. On the other hand, constant phase elements (CPE1 and CPE2) related to both semicircles in the Nyquist plot represent the capacitance of non-ideal and non-homogeneous systems.^{33,34} The expression for such an impedance is given by

$$Z_{\text{CPE}} = \frac{1}{Q(j\omega)^n} \quad (3)$$

where ω is the angular frequency of the AC signal, j is the imaginary unit, and the value of n lies in the range -1 to 1 . The different parameter values obtained after fitting are shown in Table 2. In

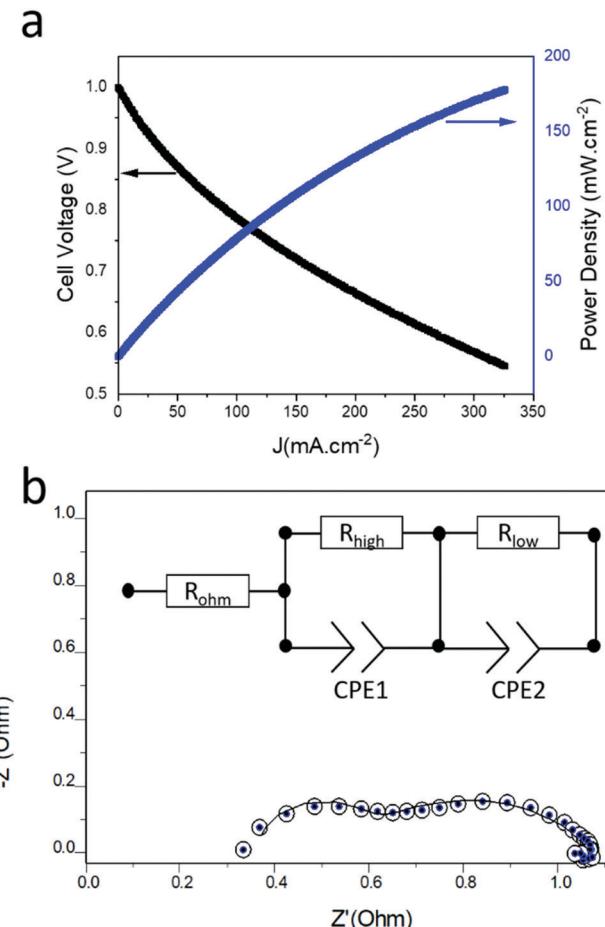


Fig. 8 (a) Voltage and power density *versus* current density at 800 °C of the button cell fabricated using NiO-GDC nanowires grown at an evaporation temperature of 1400 °C. (b) Nyquist plot of the button cell at 800 °C indicating the presence of two semicircles. The equivalent circuit for the cell is shown in the inset of the Nyquist plot.

Table 2 The EIS parameters obtained after fitting the Nyquist plot to the equivalent circuit presented in Fig. 8b

Fit parameters	Values
R_{ohm} (ohm cm ²)	0.44
R_{high} (ohm cm ²)	0.27
R_{low} (ohm cm ²)	0.58
CPE1	1.3×10^{-5}
CPE2	6.9×10^{-3}
n_1	1
n_2	~0.7

particular, the area specific ohmic resistance of the cell was found to be 0.44 Ohm cm². While the total resistance of the cell ($R_{\text{tot}} = R_{\text{ohm}} + R_{\text{high}} + R_{\text{low}}$) was found to be 1.29 Ohm cm².

Conclusions

In conclusion, novel NiO-GDC nanowires were successfully synthesized using the VLS mechanism under different experimental conditions for their potential application as an anode of



small-scale SOFCs. These nanowires exhibit dense morphology with varied diameters. Indeed, the observed higher diameter of nanowires grown at 1450 °C evaporation temperature (substrate temperature: 1170 °C) is found to be associated with the merging of Au-nanoparticles. Furthermore, the observation of an additional Raman peak at 370 cm⁻¹ and the broadening of the low intensity peak at 462 cm⁻¹ in the case of nanowires suggest that during the growth of nanowires reorganization of NiO-GDC particles results in the formation of a more complex structure, with defects that diverge from the pristine NiO-GDC powder. On the other hand, the high temperature dependence of the electrical conductivity and lowest activation energy of NiO-GDC nanowires grown at 1400 °C indicate an improved charge-carrier transport as compared to nanowires grown at 1450 °C. Furthermore, the electrolyte supported button cell fabricated using NiO-GDC nanowires grown at 1400 °C exhibits a maximum power density of ~178 mW cm⁻² at 800 °C. EIS reveals the domination of concentration losses in the cell performance, and the total resistance (R_{tot}) of the cell was found to be 1.29 Ohm cm². Hence, based on our preliminary investigations, NiO-DGC nanowires grown at the evaporation temperature of 1400 °C have the potential to be used as an anode of SOFCs especially for small-scale applications.

Author contributions

Mandeep Singh: performed the synthesis, optimization, characterization, and data analysis. The author also wrote the manuscript. Dario Zappa: was involved in all the characterization and edited the manuscript. Elisabetta Comini: supervised the work and edited the manuscript. The idea was conceived with collective discussion among all the authors.

Conflicts of interest

There are no conflicts to declare.

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