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Induced growth of quasi-free-standing graphene on SiC substrates

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Free-standing graphene grown on SiC substrates is desirable for micro- and nano-electronic device applications. In this work, an induced growth method to fabricate quasi-free-standing graphene on SiC was proposed, where graphene nucleation sites were generated on the SiC substrate and active carbon sources were subsequently introduced to grow graphene centered along the established nucleation sites. The structure and morphology of the cultivated graphene were characterized by using X-ray photoelectron spectroscopy (XPS), Raman spectroscopy, and high-resolution transmission electron microscopy (HRTEM). Compared to the traditional epitaxial growth methods on SiC substrates, this approach shows a significant reduction of the buffer layer. This study provides an efficient method for growing quasi-free-standing graphene on SiC substrates and is believed to be able to broaden the application of graphene in electronic devices as SiC is an intrinsically outstanding wide bandgap semiconductor.

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1. Introduction

Graphene, a single atomic layer, 2-dimensional thin-film material, has been considered as the most promising material for next generation electronic devices due to its unique properties,^{1,2} such as ultrahigh intrinsic carrier mobility³ and room-temperature quantum Hall effects.⁴ Among the current methods in the fabrication of high-quality graphene for electronic device applications, the thermal decomposition of SiC (epitaxial growth method) has shown the most potential in obtaining wafer-scale and high-quality graphene materials.⁵ Compared with the chemical vapor deposition (CVD) method using a catalytic substrate (Cu,⁶ Ni,⁷ and Pt⁸), the thermal decomposition of SiC directly deposits graphene on the insulating substrate.⁹ Hence, the chemical contaminants and defects introduced in the transfer step of the CVD method can be avoided and the intrinsic properties of graphene can be maintained.

Current research of epitaxial graphene growth on SiC is mainly focused on the Si-face [the (0001) facet of the SiC substrate], since the graphitization on the Si-face of SiC wafers is more controllable and the grain size of graphene is larger in comparison with that prepared on the C-face [the (000 $\bar{1}$) facet of the SiC substrate].^{10,11} SiC single crystal has an atomic step-shaped structure, which is not

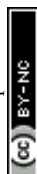
conducive to large scale graphene growth on these narrow step terraces, where wider terraces may offer better platforms. The hydrogen-etching (H-etching) process has proven to be an effective and environmentally-friendly method to broaden the step terraces as H₂ can react with SiC terrace at the terrace edge area and the step-shaped structure can be reconstructed under high temperature. To achieve uniform terrace structure and high-quality graphene growth, H-etching is performed prior to the graphitization process to broaden the step terraces on the SiC substrate.¹² Even with the advantages of the Si-face in epitaxial graphene growth, there is still a big challenge in obtaining free-standing graphene on the Si-face of the substrate, which is the so-called buffer layer (BL). The BL forms prior to the growth of the first layer graphene, which is partially covalently-bonded with the SiC substrate.¹³ The buffer layer leads to the disruption of the π -band electronic structure and degradation of the electron mobility, which is unfavorable in graphene-based applications.

To date, significant investigations have been performed to eliminate the BL effect. The commonly used method to decouple the interactions of graphene and the SiC substrate are intercalating various elements such as hydrogen,¹⁴ oxygen,¹⁵ silicon¹⁶ and fluorine.¹⁷ However, there are still some challenges, such as hydrogen desorbing at elevated temperatures leading to graphene transforming into a buffer layer again. For fluorine or oxygen intercalation, contaminants can be introduced to the pristine structure due to the extra elements. Some other methods have also been attempted to decouple the buffer layers. Hu *et al.*¹⁸ obtained near-free-standing epitaxial graphene by flash annealing at high temperature, and Bao *et al.*¹⁹ reported a similar method to obtain free-standing graphene via

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a rapid-cooling process using liquid nitrogen. Though the capability of buffer-free graphene growth was reported, the requirements for the equipment were stringent and the homogeneity of the obtained graphene was hard to control. Combining the advantages of the CVD and epitaxial growth methods is an effective means to prepare free-standing graphene on a SiC substrate, however, related studies are very rare²⁰ and free-standing graphene is difficult to attain since the mechanism is not clear yet.

In this work, an induced growth method to cultivate graphene on SiC (0001) substrate was presented and quasi-free-standing high-quality graphene with low interfacial stress was reported. The structure and quality of the graphene were characterized by using X-ray photoelectron spectroscopy (XPS) and high resolution transmission electron microscope (HRTEM). Moreover, the contributions of the initial nucleation and the subsequent growth process were clarified, and the mechanism of induced graphene growth was studied.

2. Experimental section

The preparation of graphene on the Si-face (0001) of an on-axis 4H-SiC substrate was performed in a homemade high temperature chemical vapor deposition (HT-CVD) system. The substrates used in this study were 10 mm × 10 mm pieces prepared from a 3-inch semi-insulating SiC wafer. The SiC substrates were chemically cleaned in acetone and methanol to remove the surface contaminants, followed by etching in HF solution to wipe off the oxide on the SiC surface. The cleaned substrate was placed into the HT-CVD furnace, and the chamber was pumped to the low pressure of 1×10^{-5} mbar. The preparation procedures were divided into three steps: H-etching, nucleation, and growth, as described in Fig. 1(a–c). Firstly, the H-etching was processed in 800 mbar of H₂ for 20 min at 1475 °C to broaden the width of the step terrace. After H-etching, the SiC substrate with uniform and flat terraces was obtained for high quality graphene growth. The substrate was then heated to 1650 °C for 3 min with an ambient pressure of 600 mbar (Ar) to build a condition feasible for carbon atom nucleation. After that, the growth step was executed at a temperature of 1450 °C under the pressure of 20 mbar for 5 min. Meanwhile, the CH₄ was introduced as a carbon source for graphene growth centering around the established nucleation sites on the SiC substrate.

As three steps (H-etching, nucleation and growth) were involved in the proposed method, comparative experiments were carried out to study the respective roles they played in this process. The detailed experimental procedures were summarized in Table 1,

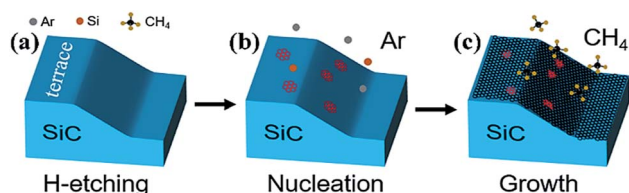


Fig. 1 Schematic diagram of the graphene growth process on a SiC substrate: (a) H-etching, (b) nucleation, and (c) growth.

where different groups of samples named as A1, A2, A3, and A4 were prepared, as shown in Fig. 2. In contrast to the growth step, the epitaxial growth represented the graphitization of the SiC substrate itself, since the chamber was filled with inert Ar gas instead of CH₄. For the A1 group samples, the substrate went directly into the growth step after H-etching, bypassing the nucleation step. The A2 group samples only proceeded through the H-etching and nucleation processes. Then they were cooled down to room temperature, bypassing the growth step. Different from the A2 group samples, the A3 group samples underwent all three procedures, but the graphene growth was supported by the SiC substrate itself rather than the introduced CH₄ source. For comparison, the A4 group samples experienced all the induced growth procedures.

The surface morphology characterization was conducted by an Atomic Force Microscope (AFM) from the Veeco Instruments Company, and the results were analyzed by using the NanoScope Analysis software. Raman measurements were utilized since it does not require any pretreatment, and the sample is not destroyed. Raman spectra were collected by an HR800 Raman spectrometer system from Horiba Jobin Yvon with a 532 nm wavelength laser (the numerical of the aperture is 0.90). The XPS was recorded by ESCALAB 250 software from Thermo Fisher Scientific. To improve the resolution, the Al K α X-ray radiation in an ultra-high vacuum was monochromatized with a photon linewidth of 0.16 eV. The experimental data were processed in Thermo Advantage software, and the SiC component was fixed at around 283.5 eV, which was attributed to the carbon atoms of the SiC substrate. The HRTEM observation was performed on an FEI Talos F200X microscope at 200 kV. For quasi-free-standing graphene characterization, the HRTEM specimen was prepared in advance by a focused ion beam (FIB) on FEI Helios NanoLab 460HP and observed under scanning electron microscopy (SEM).

3. Results and discussion

The chemical composition of the prepared samples was characterized by XPS. Fig. 3(a–d) show the XPS C 1s spectra of A1, A2, A3, and A4, respectively. For making a comparison, the graphene sample prepared by conventional epitaxial growth (named as

Table 1 Experimental procedures of the four sample groups

	H-etching	Nucleation	Epitaxial growth	Growth
Atmosphere	H ₂	Ar	Ar	CH ₄ , H ₂
Temperature	1475 °C	1650 °C	1450 °C	1450 °C
A1	Yes	No	No	Yes
A2	Yes	Yes	No	No
A3	Yes	Yes	Yes	No
A4	Yes	Yes	No	Yes



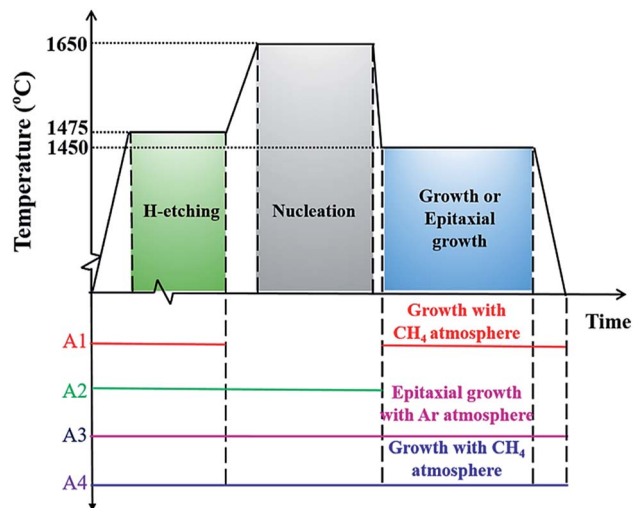


Fig. 2 Schematic diagram of the graphene growth process of A1–A4 samples.

sample B) was also presented. The graphene growth method was the same as that described in the reported literature,²¹ and the corresponding XPS spectra were given in Fig. 3(e). The black solid curves in Fig. 3 are the experimental data, and the gray solid curves are the fitted results relevant to the components, SiC, graphene, C=O, S1, and S2. The peaks around ~ 283.5 eV and ~ 284.5 eV

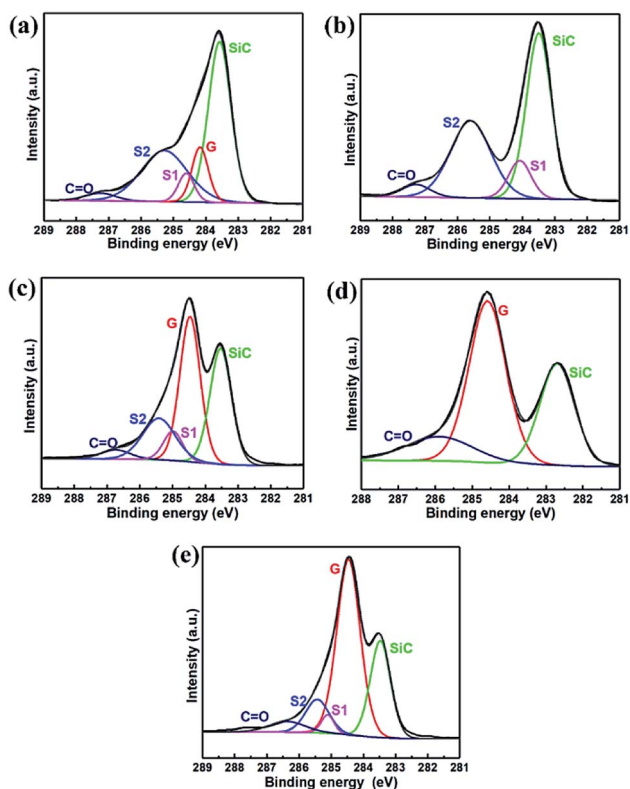


Fig. 3 XPS C 1s spectra of (a) A1, (b) A2, (c) A3, (d) A4, and (e) B, respectively. Black solid curves are the experimental data and the gray solid curves are the envelope line of the fitted components (SiC, graphene, C=O, S1, and S2).

represent the SiC and graphene, respectively,²² and the remaining two components, S1 and S2, are related to the buffer layer. The S1 component is attributed to one-third of the carbon atoms in the buffer layer covalently bonded to the SiC substrate, and the S2 component results from the other two-thirds of the carbon atoms with the sp^2 configuration in the buffer layer.^{23,24} Additionally, a minor contribution at high binding energy is attributable to the presence of C=O,^{25,26} which might be formed during the process of graphitization,²⁵ or related to the sample preparation process during the XPS test.

As depicted in Fig. 3(a), the XPS spectrum of the A1 sample consists of all four components, graphene, S1, S2, and SiC peaks. The intensity of graphene is similar to that of the buffer layer, which means the graphene was formed on the top of the SiC substrate and the buffer layer still exists at the interface. Taking the growth condition into consideration, the graphene was presumed to be formed randomly on the substrate by the decomposition of CH_4 . The buffer layer formation can be ascribed to the partial pyrolysis of the substrate during the growth process since Si atoms already sublimated at the temperature of 1450 °C.²⁷ It was observed that the A2 sample consisted of three components (S1, S2, and SiC) in the spectrum, as presented in Fig. 3(b). The absence of the graphene component can be attributed to the missing graphene growth process. The S1 and S2 peaks indicated the individual nucleation step would also result in the formation of the buffer layer. For the A3 sample, the shape and location of the four component peaks in the XPS C 1s spectra [Fig. 3(c)] were found to be very similar to those of the sample prepared by the traditional epitaxial growth method, as shown in Fig. 3(e), indicating that in the epitaxial growth process, adding a nucleation step does not change the intrinsic growth mechanism. Based on the above comparative experiments, it was observed that the exclusive step of nucleation or growth did not reduce the formation of the buffer layer. However, the A4 sample that underwent the complete induced growth process exhibited symmetric SiC and graphene components [Fig. 3(d)]. Moreover, it was noted that the energy difference between SiC and graphene is about 2 eV, which is consistent with the same property of quasi-freestanding graphene.^{24,28} The shift of the SiC peak to lower binding energy may be caused by band bending,²⁸ and further study is needed. The comparative results proved that the proposed induced growth method could reduce the formation of buffer layer structure effectively.

More detailed investigations were executed on the graphene prepared by the proposed induced growth method. The surface morphologies of the Si-face substrate were depicted in Fig. 4. As was observed in Fig. 4(a), the SiC substrate with uniform and flat terraces was obtained after H-etching, which is in favour of high quality graphene growth. The regular steps with an average height of 2.7 nm and terraces with an average width of 1.9 μm were formed on the surface of the SiC substrate. Fig. 4(b) shows the surface morphology of the substrate with graphene after induced graphene growth, noting that the step morphology was changed and the width of terraces broadened to $\sim 5 \mu m$. In addition, the terrace edges became indistinct and the roughness (Ra value) of the sample increased from 0.76 nm to

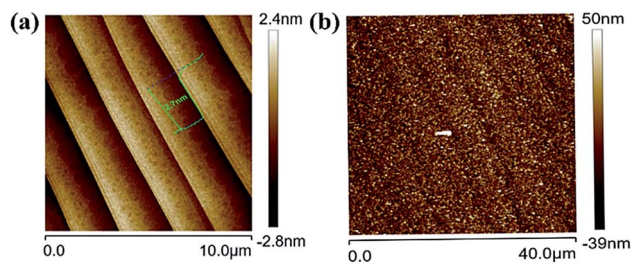


Fig. 4 AFM topography of the SiC substrate after H-etching (a) and induced graphene growth (b).

9.66 nm after graphene growth, which was ascribed to the growth procedure where the sediments generated by decomposition of an external carbon source (CH_4) aggregated on the surface of the SiC substrate.

It is well known that in the Raman spectrum of graphene film, there are three major peaks around $\sim 1350\text{ cm}^{-1}$, $\sim 1580\text{ cm}^{-1}$, and $\sim 2700\text{ cm}^{-1}$, corresponding to the D-, G- and 2D bands, respectively.²⁹ The G-band is an important standard to reflect the symmetry and crystal quality of graphene. The full width at half maximum (FWHM) values of the 2D-band exhibits a relationship with the number of graphene layers,³⁰ while the D-band is the characteristic peak of non-ordered vibration of graphene and related to the concentration of defects.³¹

To study the chemical composition and buffer layer structure on the SiC substrate after the induced graphene growth, Raman measurements were employed. Fig. 5(a) and (b) show the Raman spectra for samples A4 and B, respectively. It is clear that the Raman intensity of sample A4 was higher than that of sample B. Furthermore, the G and 2D peaks of sample A4 were not only more independent and symmetrical than sample B but also significantly higher than those of the SiC substrate, indicating that the graphitization of sample A4 is more complete and the microscopic coverage is more uniform. The layer

number of graphene grown on the SiC substrate was estimated by the full-width at half-maximum (FWHM) of the 2D peak in Raman spectra by the following equation:³⁰

$$\text{FWHM (2D)} = \left(-45 \left(\frac{1}{N} \right) + 88 \right) [\text{cm}^{-1}] \quad (1)$$

where N is the number of graphene layers. For samples A4 and B, the FWHM (2D) are 64 cm^{-1} and 48 cm^{-1} , respectively. So the layer number of grown graphene in sample A4 is ~ 2 layers and that for sample B is ~ 1 layer. The increase of the D peak intensity probably resulted from the inconsistency between the nucleation and growth steps of sample A4, which could be further improved by growth condition optimization. Fig. 5(c) and (d) show the Raman spectra around the G peak for samples A4 and B, respectively. To eliminate the contribution of SiC substrate on the G peak, the spectrum of SiC sample has been subtracted from the spectrum. The symmetric G peak is fitted in the sample A4, as shown in Fig. 5(c). In contrast, sample B can be fitted by the G_B peak (buffer layer response peak) and G peak in Fig. 5(d). These results indicate that the buffer layer appears in sample B rather than sample A4,^{32,33} which is also in line with the XPS results.

It is interesting to note that the 2D peak position of sample A4 (2700 cm^{-1}) shows a relatively lower Raman shift compared to the sample B (2742 cm^{-1}), and the G peak also has a relatively small shift from 1601 cm^{-1} (sample B) to 1584 cm^{-1} (sample A4). The Raman shift could reflect the variation of compressive stress between the substrate and the grown graphene. It was reported that the different thermal expansion coefficients between SiC and graphene would generate the compressive stress in the graphene layer, showing the blue shift of G and 2D peaks in the Raman spectra.³⁴ So the red shifts of the 2D and G peaks obtained in sample A4 could be attributed to the release of the compressive stress of grown graphene. The compressive strain of graphene was estimated according to the shift of the Raman G-band with the following empirical equation:³⁴

$$\omega_\alpha - \omega_0 = \alpha\sigma \quad (2)$$

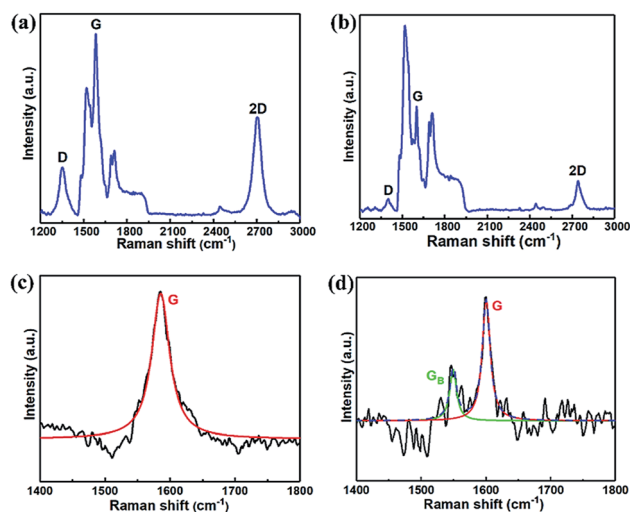


Fig. 5 Raman spectra of sample A4 (a) and sample B (b), and the Raman spectrum around the G peak after subtracting the SiC contribution for sample A4 (c) and sample B (d), respectively.

where ω_α is the Raman G-band frequency of the sample, ω_0 stands for the Raman G-band frequencies of stress-free-graphene (1580 cm^{-1}), α is the stress coefficient with an empirical value of $7.47\text{ cm}^{-1}\text{ GPa}^{-1}$, and σ represents the stress. Based on the above equation, the compressive stress of graphene in sample A4 was evaluated and found to be 0.53 GPa , approximately one order of magnitude lower than that in sample B (2.81 GPa). From the above analysis, we can confirm that the proposed growth method can reduce the coupling effect between the graphene and SiC substrate to a great extent.

For the purpose of elucidating the formation of nucleation sites and the induced graphene growth on SiC substrate, HRTEM measurements were performed. In order to find the graphene nucleation and growth regions, we extended the growth time of samples which underwent the same procedure as sample A4 to achieve multilayer graphene to distinguish the graphene layer and SiC layer more clearly and achieve the diffraction of graphene from the cross-section sliced by FIB. Fig. 6(a) exhibits the schematic



diagram of slicing the specimen from the SiC substrate using FIB for HRTEM observation. The HRTEM specimen shown as the red box in Fig. 6(a) was lifted off the 10 mm $\times$ 10 mm SiC substrate, and the specimen was selected to contain the nucleation site and the induced grown graphene layer. Fig. 6(b) shows the SEM *in situ* photograph of the HRTEM specimen prepared from the substrate and transferred to the copper net for further thinning prior to the HRTEM test. Fig. 6(c) presents the typical cross-sectional HRTEM image, from which we can separate the SiC substrate, grown graphene and nucleation regions. The distance between the red dashed lines shown in Fig. 6(c) was measured to be 3.3 ± 0.2 Å, representing free-standing and single layer graphene. In addition, the distance between the first carbon layer (buffer layer) and the silicon layer at the top of the SiC substrate was measured to be 2.0 ± 0.2 Å, corresponding to the length of Si-C bonds in SiC.^{35,36}

The whole HRTEM image can be divided into three parts, labeled as I, II and III, as shown in Fig. 6(c). In the Part I region with a diameter of ~ 10 nm shown in the left inset, the distance between the first carbon layer and SiC was found to be 2.2 ± 0.2 Å, which is similar to that of the reported buffer layer. In combination with the growth procedure, it was presumed that this area originated from SiC decomposition and served as the nucleation site for the following graphene growth. In the part II region, the structure is slightly disordered, which is considered as the transitional area of the nucleation sites contributed from SiC decomposition and the induced grown graphene with the assistance of CH₄. In the right-most Part III region, the graphene layers were

found to grow along Part II. It is interesting to find that the distance of the first graphene layer on the SiC substrate was measured to be 3.6 ± 0.2 Å [the right inset in Fig. 6(c)], and the rest of the graphene layers were obtained in the distance of 3.3 ± 0.2 Å, indicating the free-standing graphene was achieved. These results are in good agreement with the XPS analysis. The diffraction patterns were collected from the 4H-SiC substrate region and the connection region between induced-grown graphene and 4H-SiC substrate. Results were presented in Fig. 6(d) and (e). Fig. 6(d) gives the diffraction pattern obtained from the SiC substrate, where the interplanar spacing was measured and found for the (0004) plane, $d_{0004} = 2.53$ Å. It was noticed that there were two sets of diffraction patterns in Fig. 6(e): one set of diffraction patterns was ascribed to the 4H-SiC (0004) plane and another set belonged to the graphene (0002) plane. The interplanar spacing of the graphene layer was measured to be $d_{0002} = 3.57$ Å, slightly higher than the theoretical interplanar distance in the graphite ($d_{0002} = 3.35$ Å (ref. 36)).

Based on the above characterizations and analysis, the mechanism of the proposed induced growth method for cultivating graphene on SiC was clarified. At first, the regular step morphology of SiC was obtained by H-etching [Fig. 1(a)]. Then the Si atoms were partially sublimated from the SiC surface when heating the sample to 1650 °C in Ar atmosphere. The remaining carbon atoms reconstructed and nucleated on the SiC substrate as shown in the red six-membered rings [Fig. 1(b)]. The inert Ar gas was used to inhibit the sublimation rate of Si atoms, thus nanoscale graphene with uniform morphology could be obtained. Afterward, low concentrations of CH₄ were introduced into the chamber and decomposed as active C radicals at high temperatures. The C radicals grow preferentially centered around the nucleation sites due to the structural induction effect of the nucleation sites. Larger area graphene would be grown on the SiC substrate with continuous C radical aggregation [Fig. 1(c)]. Finally, the quasi-free-standing graphene was fabricated directly on the SiC substrate.

4. Conclusion

In this report, we presented an induced graphene growth method to prepare quasi-free-standing graphene on the Si-face of SiC substrate. The obtained quasi-free-standing graphene was further characterized and verified using XPS, Raman spectroscopy, and HRTEM measurements. The only coexistence of the graphene and SiC was revealed in the XPS spectra. Raman tests further indicated that the compressive stress of graphene grown by the induced growth method was nearly one order of magnitude lower than that prepared by the traditional epitaxial growth method on the SiC substrate. Moreover, quasi-free-standing graphene was directly observed from the typical HRTEM image, where the distance between the grown graphene and SiC substrate was 3.6 ± 0.2 Å. All these results support the significant reduction of interactions between the grown graphene and SiC substrate, showing the feasibility of this method to grow high-quality graphene for uses in the microelectronic device field.

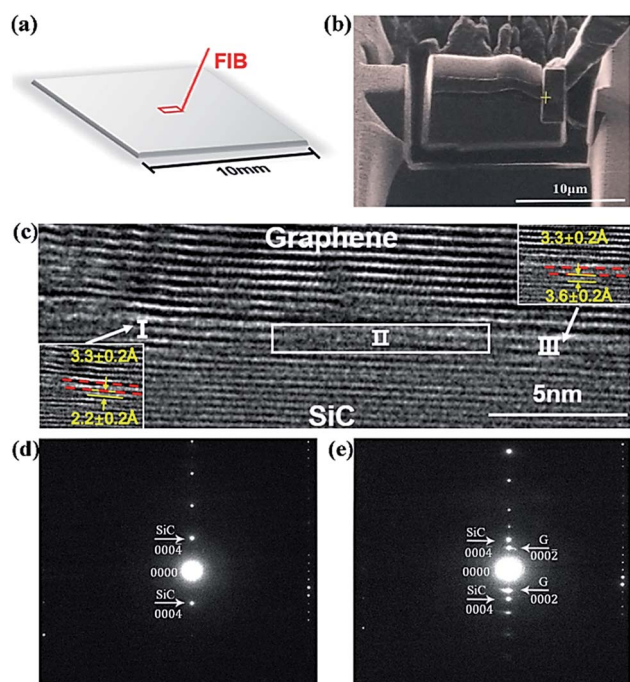


Fig. 6 (a) Schematic diagram of slicing HRTEM specimen with FIB on a SiC substrate. (b) SEM photograph of the HRTEM specimen. (c) Cross-sectional HRTEM images of graphene layers observed on the SiC substrate. The diffraction patterns of 4H-SiC (d) and grown graphene (e) are shown.



Author contributions

Zhenxing Liu, Zhen Su, and Zhiyuan Yang grew the graphene materials on SiC substrate. Qingbo Li, Li Sun and Xue Zhang characterized the graphene using the Raman and AFM. Xizheng Liu and Yingxian Li helped to do the HRTEM observations. Fapeng Yu, Yanlu Li and Xian Zhao directed this research. Zhenxing Liu, Li Sun and Fapeng Yu prepared the manuscript.

Conflicts of interest

The authors declare that there are no conflicts of interest.

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