

Cite this: *RSC Appl. Interfaces*, 2024,
1, 1426Surface photovoltage predicts open circuit
voltage in GaP/PEDOT:PSS and GaP/CuSCN
heterojunction solar cells†Anna C. Kundmann, * Kathleen Becker  and Frank E. Osterloh *

The cost-effective fabrication of inorganic photovoltaic (PV) devices is important for their implementation on a global scale. Solution processing techniques, such as spray coating, spin coating, and electrodeposition, can drive down costs; however, a better understanding of the charge transfer characteristics of the resulting semiconductor heterojunctions is needed to minimize photovoltage losses at interfaces. In this work, we generate solution-processed heterojunctions by spin coating PEDOT:PSS and electrodepositing CuSCN as hole transport layers (HTLs) onto GaP wafers. After adding silver paint as a front contact, we obtain devices with short circuit current densities of 0.40 mA cm⁻² and 0.18 mA cm⁻², open circuit voltages of 0.47 V and 0.43 V, and power conversion efficiencies of 0.045% and 0.031% for PEDOT:PSS and CuSCN HTLs, respectively. Surface photovoltage spectroscopy (SPS) is used to study photochemical charge separation at the illuminated GaP interfaces. We find that the surface photovoltage signal is a good predictor of the photovoltage of the devices, as confirmed by comparison with open circuit potential data. SPS also reveals improved hole collection by the HTLs and a detrimental Schottky junction at the In/GaP back contact, further evidenced by S-shaped current-voltage profiles in electric measurements. Reducing the Schottky barrier height will be essential to improve device performance.

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Introduction

Solar power is an essential energy source for meeting the growing global energy demand in a sustainable way. Global cumulative photovoltaic (PV) capacity surpassed 1 TW in 2022, but to meet the 70 TWs of solar energy power demand by mid-century, PV deployment must continue at exponential rates.^{1–3} To meet this target, low cost, scalable, solution-based PV fabrication methods are highly desired. Spin coating methods are already well established in organic solar cells,⁴ as well as inorganic absorber (such a lead halide perovskite)⁵ and inorganic hybrid solar cells.⁶ For example, the hole transport layer (HTL) poly(3,4-ethylenedioxythiophene):poly(4-styrenesulfonate) (PEDOT:PSS) has been spin coated on Si,^{7–10} GaAs,^{11,12} lead halide perovskite,¹³ and organic¹⁴ absorbers to make solar cell devices of reasonable efficiency. Electron transport layers (ETLs) of ZnO or SnO₂ have been deposited using similar techniques.^{15–19} Nanoparticle absorber layers^{19–21} and silver electrodes made of silver nanowire suspensions^{22,23} can likewise be prepared using spin-coating

techniques. Thus, it is technically feasible to produce solar cells with these low-cost solution-based methods.

However, for many absorber materials, the power conversion efficiency of solution-processed heterojunctions still lags behind that of junctions created by chemical or physical vapor deposition, sputtering, or ALD.^{8,24,25} This is in part because imprecise interfaces between the semiconductor and the electron and hole transport layers introduce charge recombination sites that cause significant voltage losses.^{7,12,26,27} To overcome this limitation, studies on the charge transfer and charge recombination characteristics of solution-processed interfaces are thus of great interest.

Here we use gallium phosphide (GaP) as a model semiconductor to study photochemical charge transfer at GaP/PEDOT:PSS and GaP/CuSCN interfaces and to manufacture a solar cell. Because of its large bandgap of 2.26 eV, GaP has been a popular absorber for water-splitting photoelectrochemical cells, which require a photovoltage in excess of 1.23 V.^{28,29} Its use in photovoltaics is less established, likely because the bandgap limits the PCE to 17%.³⁰ However, GaP would make a suitable top absorber in multijunction tandem photovoltaic devices. GaP homojunction PVs have been fabricated with molecular beam epitaxy, photolithography, and electron beam evaporation.^{31,32} However, demonstrated GaP devices suffer from significant voltage deficits of 0.48 V, requiring further optimization.

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Fig. 1a and b show the expected energetic alignment of the components used in this study. The HTLs are p-type materials with Fermi level (E_F) positions slightly above the valence band of GaP and are therefore suitable for hole collection from that band. PEDOT:PSS has previously been shown as an effective HTL in the construction of a GaP PV, indicating that solution-processed junctions are a promising avenue for device construction.³³

We use vibrating Kelvin probe surface photovoltage spectroscopy (VKP-SPS) to estimate the contribution of particular interfaces to the photovoltage of GaP solar cell interfaces in a contactless way for the first time. As we showed recently for BiVO₄-liquid junctions,^{38,39} the surface photovoltage (SPV) signal closely correlates with the V_{OC} in photoelectrochemical cells. Similar correlations are available for silicon, lead iodide perovskite, and organic solar cells in the literature.^{40–44}

We confirm that the light intensity-dependent SPV signals closely match the light intensity-dependent open circuit potential (OCP) in our GaP devices. This further supports the utility of SPS to estimate the photovoltage of partially assembled devices. These findings may have broader implications for other optoelectronic or photocatalytic devices relying on carrier-selective contacts to promote device operation.

Results and discussion

GaP heterojunctions were fabricated as shown in Fig. 2. A commercial sulfur-doped n-GaP wafer was first etched in 3 : 1 : 1 H₂SO₄ : H₂O₂ : H₂O at 60 °C for 3 min and indium metal was then soldered on the polished side of the wafer. Next, the HTL (PEDOT:PSS or CuSCN) was coated on the rough side of the wafer within a masked area. Since chemo-mechanical polishing is a costly step in preparing III–V wafers for fabrication of solar cells⁴⁵ and larger interface area was shown to improve photocurrent in previous GaP/PEDOT:PSS devices,³³ we were interested in studying the properties of the larger but less well defined interfaces made at unpolished surfaces. PEDOT:PSS was deposited using the spin coating procedure outlined by Wang *et al.*,³³ with the active area defined by masking tape.

CuSCN was applied by electrodeposition from an aqueous solution of 15 mM CuSO₄, 67.5 mM diethanolamine (DEA), and 45 mM KSCN using a procedure adapted from Pan *et al.*,⁴⁶ with the electrodeposition area defined by polyester tape. A bias of −4.5 V *vs.* calomel electrode (CE, 3.5 M KCl) was applied for 3 min to produce the CuSCN HTL. Here, the large negative potential is necessary to overcome the electric resistance of the weakly n-doped GaP wafer. Finally, silver paint was applied with a brush around the edge of the mask, leaving a defined active area for illumination (S. Maldonado, personal communication).

Scanning electron microscopy images of the unpolished n-GaP wafer before and after addition of the HTLs are shown in Fig. 3. The unpolished wafer shows a rough surface morphology, as expected (Fig. 3a). The PEDOT:PSS film is shown in Fig. 3b with the contours of the underlying wafer visible and no apparent discontinuities in the film observed. The electrodeposited CuSCN appears as a layer of small (200–300 nm) particles (Fig. 3c). A cleaved film of CuSCN on GaP shows that the particles are in close contact with the rough GaP surface to form the interface studied in this work (Fig. S1†). The energy dispersive X-ray spectroscopy (EDS) data for the GaP wafer and the HTL films on GaP are shown in Fig. S2.† Uncoated n-GaP shows a small surface carbon contamination. EDS of the PEDOT:PSS-coated wafer reveals a high amount of C and O with a trace amount of S, indicating that PEDOT:PSS was successfully deposited on the n-GaP surface. In the CuSCN-coated wafer, Cu, S, C, and N are all present, but not in the ratio expected for CuSCN. Specifically, there is a much higher amount of Cu indicated in the EDS data. Therefore, X-ray diffraction (XRD) was additionally employed to verify the composition of the film. Initial attempts did not yield clear XRD peaks associated with CuSCN because the films were too thin. Therefore, a thick CuSCN film was deposited at the same applied bias (−4.5 V *vs.* 3.5 CE) for 12 min, instead of 3 min, and an XRD pattern was recorded (Fig. 4). Indeed, this thicker film displays XRD peaks that match the known β -CuSCN phase. However, there is a diffraction peak at 43.5°, indicating that copper metal is also present, consistent with the EDS data. This copper formed by reduction of CuSO₄ during deposition of the

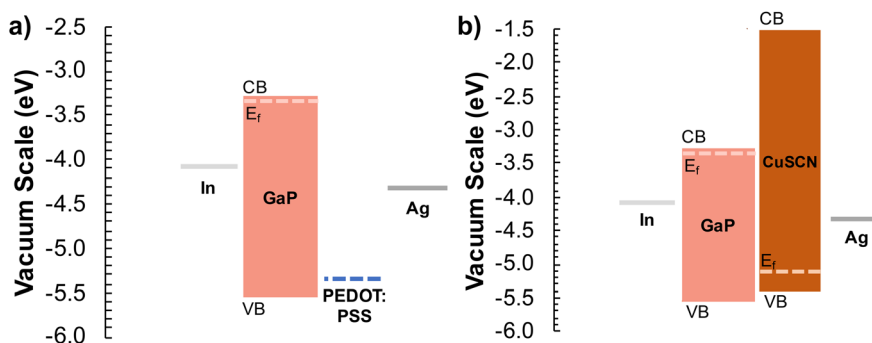


Fig. 1 Band energy diagrams depicting band edge alignment of n-GaP-based devices using a) PEDOT:PSS or b) CuSCN HTL. The work function of PEDOT:PSS³⁴ varies (−5.05 to −5.6 eV) depending on fabrication conditions, so an average is shown. Band positions for CuSCN are taken from Treat *et al.*³⁵ Note the difference in energy scale to accommodate the bandgap of CuSCN. Band edges for GaP are from Van de Walle and Neugebauer.³⁶ Work functions for In and Ag are from the CRC Handbook.³⁷



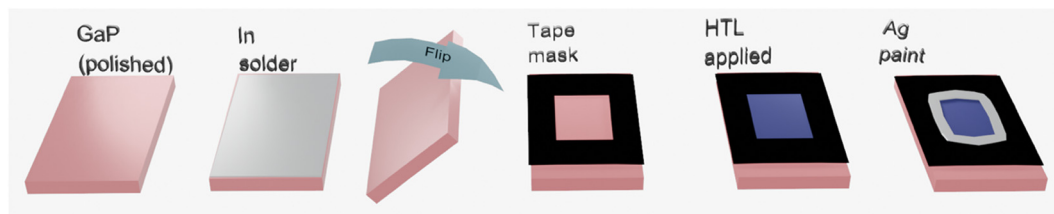


Fig. 2 Fabrication steps for n-GaP-based devices. Indium (In) is used as the back contact while silver paint is used as the front contact (Ag).

CuSCN layer. We believe the metallic copper is primarily present at the top surface of the films, as we have found some tendency for the copper to form clusters on the surface

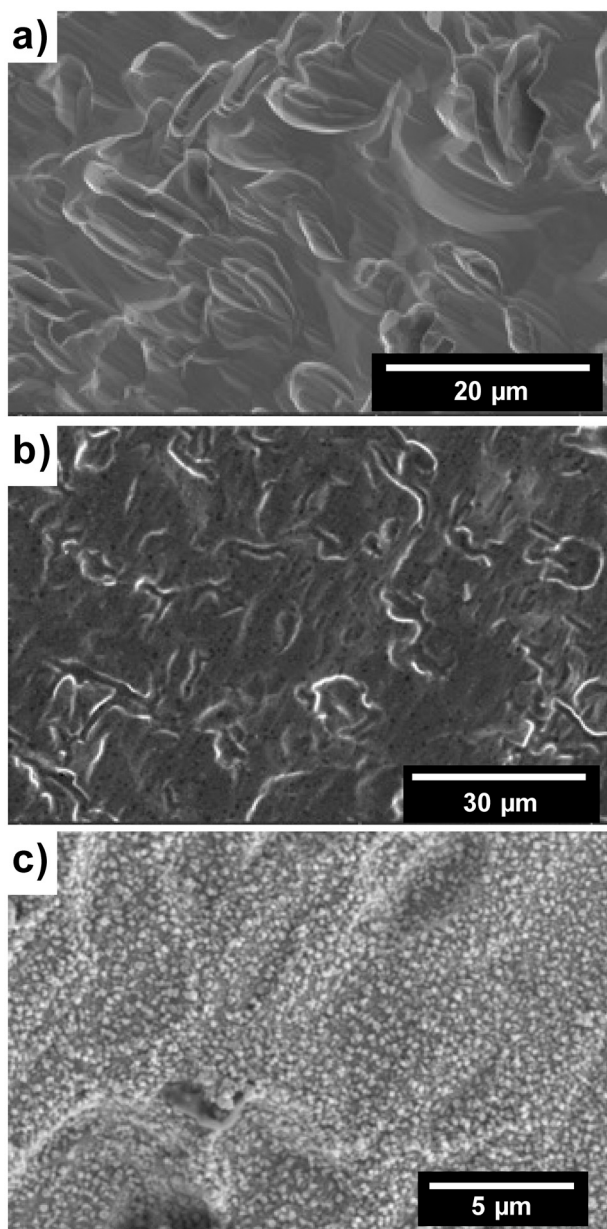


Fig. 3 Scanning electron micrographs of unpolished GaP wafer surface (a), PEDOT:PSS film on GaP (b), and CuSCN particle film on GaP (c).

of some electrodeposited CuSCN films (Fig. S3†). When comparing the EDS results of areas with clusters to areas without clusters, we find that the Cu amount is about 6-fold higher in the cluster, while the other lighter elements change by <5%. While we cannot entirely rule out the presence of some metallic copper at the GaP/CuSCN interface, we do not believe this to be the main constituent present. It is unclear whether this is a benefit to devices, from enhanced conductivity, or if this is a detriment, resulting in decreased charge carrier selectivity. Finally, XRD confirms the (100) orientation of the GaP wafer, as seen by the high peak intensity at 32.9°.

UV-vis diffuse reflectance spectroscopy was used to observe the optical properties of the samples.^{47,48} The spectrum for the GaP wafer in Fig. 5 shows a broad absorption above 620 nm (<2 eV) and an absorption shoulder at 560 nm (2.2 eV). The former corresponds to the free carrier excitation of the S-doped wafer,⁴⁹ whereas the latter is the bandgap absorption of GaP. Addition of the

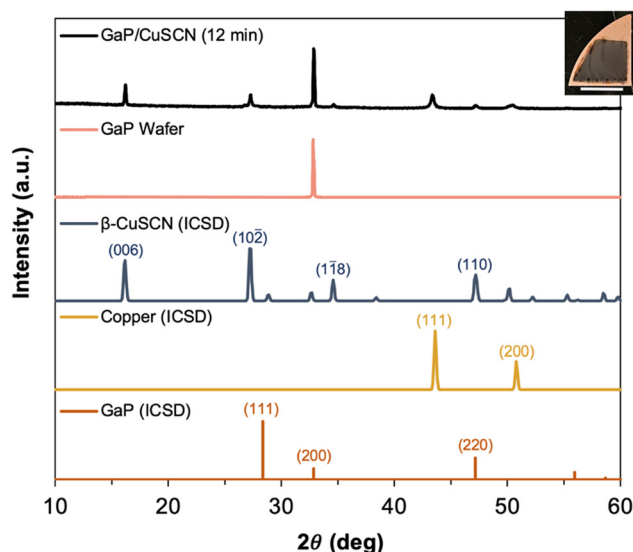


Fig. 4 XRD pattern of a CuSCN film deposited on n-GaP for 12 min at -4.5 V vs. 3.5 CE (black line). An image of the sample is shown in the upper right corner (scale bar = 1 cm). The XRD pattern for an uncoated GaP wafer is shown in pink. The 32.9° peak for GaP is enhanced due to the (100) orientation of the wafer. Standard XRD patterns for β -CuSCN (ICSD collection code #24372; dark blue), copper metal (ICSD coll. code #7954; dark yellow), and GaP (ICSD coll. code #635041; dark orange) are shown for comparison with their Miller indices.



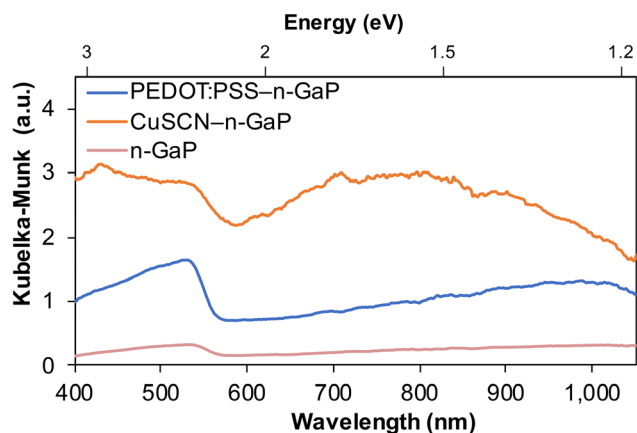


Fig. 5 Kubelka-Munk transformation ($F(R) = (1 - R)^2/2R$) of the UV-vis diffuse reflectance spectra of PEDOT:PSS-n-GaP (blue), CuSCN-n-GaP (orange), and n-GaP wafer (pink).

PEDOT:PSS film enhances the absorption above 620 nm, which is caused by bipolarons, a feature of PEDOT films doped with PSS.^{50,51} Similarly, the CuSCN film contributes to light absorption at 600–900 nm, likely due to the presence of copper metal in the film, as the bandgap of CuSCN exceeds 3.8 eV.⁵² This is expected to reduce light reaching the GaP wafer (shading). Overall, the spectra of the modified wafers are the sum of the spectra of the individual components.

In order to observe the effect of the HTLs on charge separation, vibrating Kelvin probe surface photovoltage spectroscopy (VKP-SPS) was employed. VKP-SPS measures the contact potential difference (CPD) with a Kelvin probe oscillating 1 mm over the specimen. Sample illumination through the semi-transparent probe produces a surface photovoltage signal $SPV = CPD(\text{light}) - CPD(\text{dark})$, whose size and polarity provides information about the effective bandgap of the material and the charge carrier separation direction.^{53–56} For solar cells, it has been shown that the SPV signal closely tracks the V_{OC} .^{38,40–44} That makes it possible to

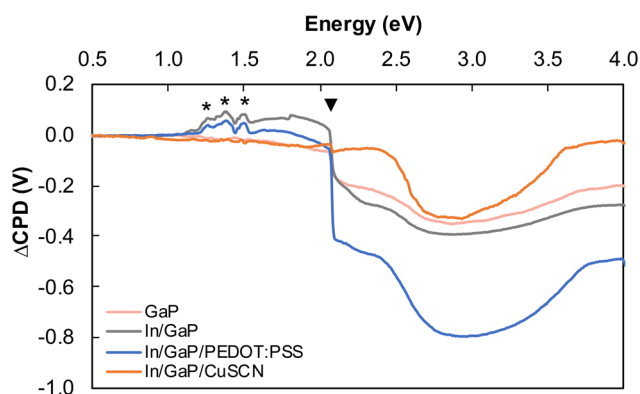


Fig. 6 Surface photovoltage spectra of etched n-GaP wafer (pink), n-GaP wafer with In back contact (grey), PEDOT:PSS-n-GaP (blue), CuSCN-n-GaP (orange). Samples with HTLs applied have In as a back contact. Asterisks (*) denote Xe emission lines and triangle (▼) filter change at 2.1 eV. Conditions: air, Xe arc lamp, (1–10 mW cm⁻² irradiance at the sample).

use VKP-SPS to observe contributions of the individual interfaces to the total V_{OC} .^{40,41,43,57}

SPV spectra for the GaP wafer before and after modifications are plotted in Fig. 6. All spectra show a discontinuity at 2.1 eV that is the result of an optical filter change. While this obscures the 2.26 eV bandgap of GaP,⁵⁸ it does not affect the diagnostic value of the data. For the n-GaP wafer, the SPV signal is negative, and reaches a maximum of -350 mV at 2.9 eV, where GaP absorbs light and the intensity of the Xe arc lamp is greatest. According to the scheme in Fig. 7a, the SPV is caused by photogenerated charge carriers separated by the depletion layer at the GaP surface, which causes photoholes to move towards the GaP surface/Kelvin probe.

Application of the indium back contact does not cause significant changes in the negative photovoltage but introduces a positive (+90 mV) sub-bandgap signal at 1–2 eV. Note that the SPV fine structure at 1.2–1.6 eV is an artifact of

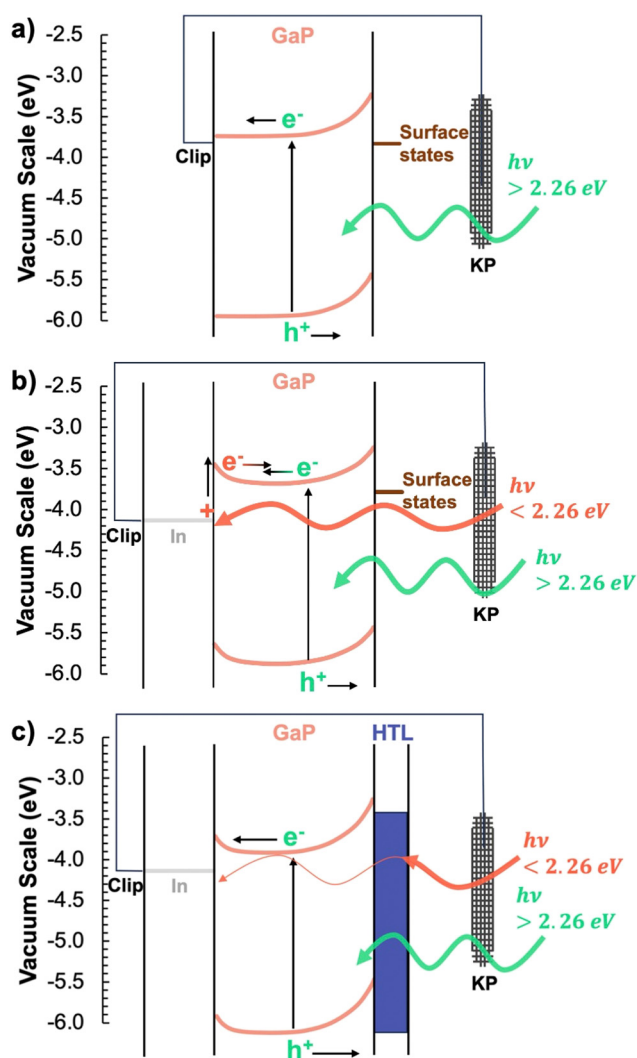


Fig. 7 Schematic band diagrams of GaP (a), In/GaP (b), and In/GaP/HTL (c) layer stacks with charge separation based on surface photovoltage spectra. KP stands for Kelvin probe.

the characteristic emission lines from the Xe arc lamp, and the onset around 2.5 eV corresponds to an increase in the monochromated light power (Fig. S4†). The positive sign of the sub-bandgap signal indicates that electrons are repelled by a Schottky-type junction as shown in Fig. 7b. We have previously demonstrated a similar SPV signal inversion due to the formation of a depletion layer between thin particle films and their conductive substrates.^{57,59} In the present case, the positive signal is generated by photons that are only weakly absorbed by the GaP wafer and that can excite sub-bandgap states at the In/GaP back contact. These states may involve In-P species formed at the GaP-In interface.

When PEDOT:PSS is added to the GaP front, the negative SPV signal increases by a factor of 2, indicating improved hole transfer to the front. This is due to the ability of PEDOT:PSS to accept and conduct positive charge carriers (Fig. 7c).

Additionally, the sub-bandgap signal is decreased, which is attributed to a shading effect from the HTL. This shading effect is caused by the 1–2 eV parasitic light absorption from PEDOT:PSS, as seen in the UV-vis spectra in Fig. 5. Lastly, the SPV spectrum for the CuSCN-coated GaP wafer is also shown in Fig. 6. It is similar to that for the PEDOT:PSS-n-GaP sample, except the negative photovoltage is smaller than for the unmodified GaP wafer, and the SPV onset is shifted to 2.5 eV. Both changes are a result of parasitic light absorption by CuSCN in the 1.5–3.0 eV regime, as confirmed by the absorption spectrum in Fig. 5.

To investigate the intensity-dependent photovoltage of GaP in contact with the HTLs, SPV data were collected under intermittent 405 nm (3.1 eV) illumination, where GaP absorbs all incident light (Fig. 8a and b). The photovoltage is negative, in agreement with the spectra in Fig. 6 and the

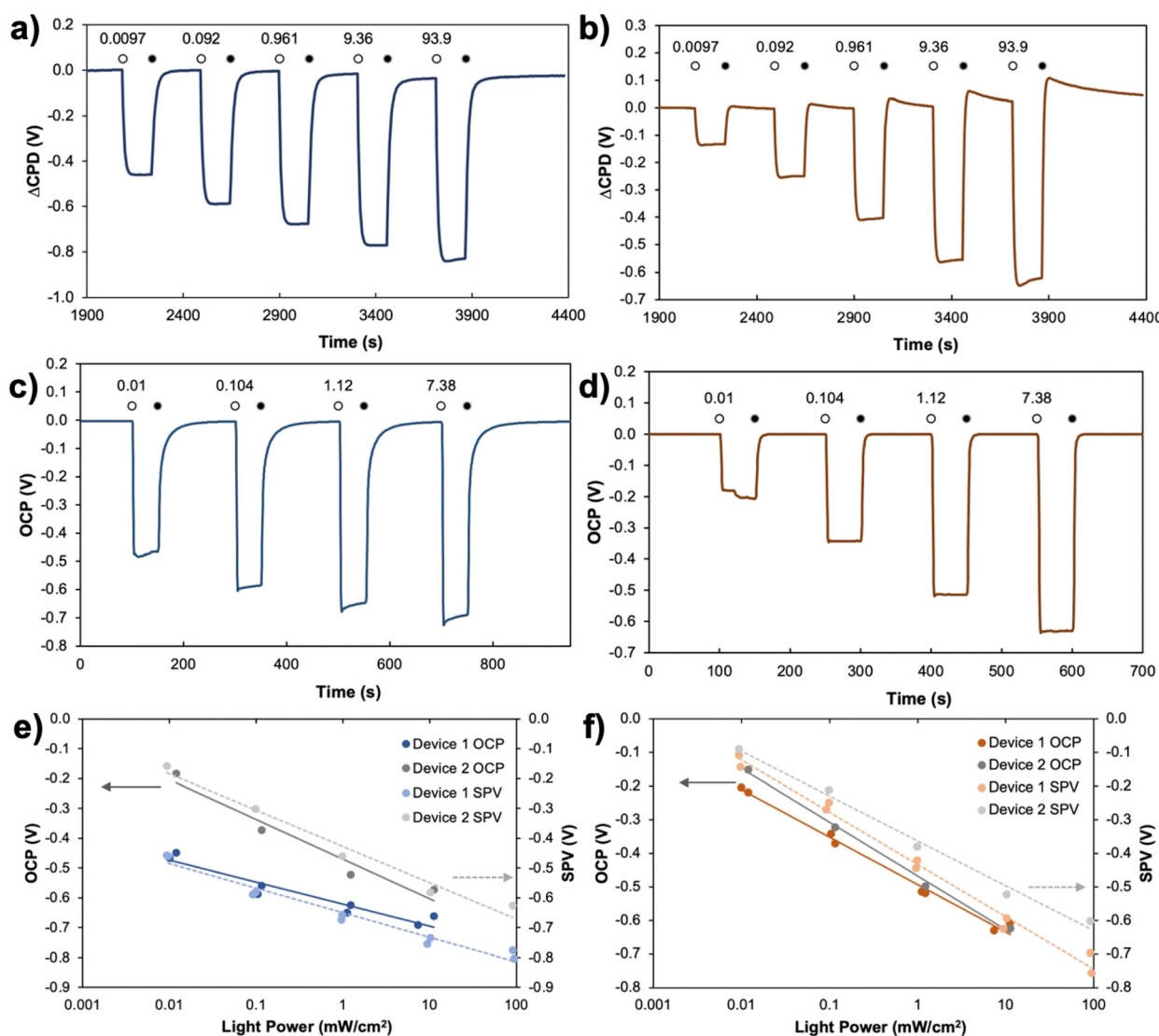


Fig. 8 Surface photovoltage (change in contact potential difference [ΔCPD]) and open circuit potential (OCP) measurements of PEDOT:PSS-n-GaP (a and c) and CuSCN-n-GaP (b and d) under variable light intensity (mW cm^{-2}) from a 405 nm LED. White circles indicate when the LED is turned on and black circles indicate when the LED is turned off. Semi-logarithmic plot of OCP and surface photovoltage (SPV) vs. incident light power for PEDOT:PSS-n-GaP device (e) and CuSCN-n-GaP device (f).



model in Fig. 7. The SPV signal forms reversibly and increases with light intensity. Time constants for SPV formation and decay are, respectively, 10 s and 15 s for PEDOT:PSS-n-GaP and 10 s and 10 s for CuSCN-n-GaP, with the time resolution limited by the 5 second time interval between data points. This is much faster than what is found for thin films of metal oxides.⁶⁰ It is a result of the high mobilities of the electrons ($160 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$) and holes ($135 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$) in gallium phosphide.⁶¹ A very minor drift of the CPD baseline to positive values is evident for the CuSCN-n-GaP sample. This suggests trapping of electrons at the front contact, possibly involving reduction of Cu(+) to Cu(0). When the SPV data is plotted *versus* the logarithmic light intensity, straight lines are observed for both systems. This agrees with the diode equation (eqn (1))⁶² for a semiconductor junction. This equation predicts a linear increase of the open circuit voltage (V_{OC} , photovoltage) with the logarithmic irradiance. In the equation, k , T , and e have their usual meanings, and j_{phot} and j_0 are the photocurrent density and dark reverse bias current density of the junction, respectively.

$$V_{\text{OC}} = n \frac{kT}{e} \ln \left(\frac{j_{\text{phot}}}{j_0} + 1 \right) \quad (1)$$

For ideal junctions, the V_{OC} increases by 59 mV for every decadic increase of the irradiance, corresponding to a diode ideality factor (IF) $n = 1$. However, the experimental slope for the PEDOT device is -0.086 V per decade and for the CuSCN device is -0.159 V per decade, corresponding to IFs of 1.5–2.7. Higher IFs indicate a non-radiative or trap-assisted recombination mechanism, especially in the space charge region, and are due to the defects, present in most solar cell devices.^{63–65} In the case of the CuSCN device, the film of particles produces a non-uniform interface between GaP and CuSCN, possibly increasing the interface defect density. The large IF for the CuSCN device may also be caused by parasitic light absorption from the CuSCN layer, with higher incident intensity required to reach the underlying GaP. Using the SPV data, the open circuit voltage of the finalized devices can be estimated when using the same illumination conditions. For example, at 9.36 mW cm^{-2} of 405 nm illumination, the PEDOT and CuSCN devices should be able to generate 0.75 V and 0.63 V, respectively, and at 93.9 mW cm^{-2} , the V_{OC} increases to 0.80 V and 0.76 V, respectively.

To test these V_{OC} predictions, solar cells (images in Fig. S5†) were completed by applying silver paint to the HTLs, as shown in Fig. 2, and open circuit potential (OCP) data were recorded under intermittent 405 nm LED illumination (Fig. 8c and d). As can be seen, the OCP changes quickly under illumination and decays to zero when the illumination is turned off. Signal reversibility and timescales are nearly identical to the behavior seen for the SPV. A plot *versus* the logarithmic irradiance (Fig. 8e and f) shows that OCP and SPV values track closely across replicate measurements on the same device and across different devices. For example, a second PEDOT:PSS-n-GaP device has a lower SPV compared to the first, which is matched by a degraded V_{OC} and J - V

response (Fig. S6†). By contrast, the CuSCN devices showed better consistency across devices in SPV and V_{OC} . Overall, OCP values are within 100 mV of the SPV values. Not surprisingly, the OCP slopes (-0.077 V per decade for the PEDOT:PSS device and -0.150 V per decade for the CuSCN device) are similar to those seen for the SPV data, confirming that SPV is a useful tool for determining the IF and identifying defect-assisted recombination in devices. Overall, this data demonstrates the value of VKP-SPS on semiconductor junctions for predicting V_{OC} of the corresponding assembled solar cells.

To observe the solar power conversion efficiency (PCE), J - V curves were recorded under simulated AM1.5G illumination (Fig. 9 and Table 1). Our PEDOT:PSS device shows a V_{OC} of 0.47 V, short circuit current density (J_{SC}) of 0.40 mA cm^{-2} , fill factor (FF) of 0.24, and PCE of 0.045%. This J_{SC} is more than double the J_{SC} of the planar n-GaP devices fabricated by Wang *et al.* (see Table 1),³³ although, the V_{OC} is lower. This voltage loss appears to primarily be due to the detrimental Schottky barrier observed in the VKP-SPS data from the In-GaP back contact (see discussion below). The CuSCN device shows a V_{OC} of 0.43 V, J_{SC} of 0.18 mA cm^{-2} , FF of 0.41, and PCE of 0.031%. The lower V_{OC} was already apparent in the OCP and SPV measurements, although the monochromatic and AM1.5G illumination conditions are not quantitatively comparable. It is attributed to light shading from the CuSCN layer. Parasitic light absorption by the CuSCN layer also reduces the photocurrent of the GaP-CuSCN device.

While devices of reasonable PCE and larger V_{OC} have recently been obtained from Si/PEDOT:PSS heterojunctions,^{6,66} devices on GaP reported here and previously by Wang *et al.* have considerable room for improvement. As a wide-bandgap semiconductor, ohmic contacts to GaP can be more difficult to form compared to low-bandgap Si, a key finding in our devices. Additionally, due to the proliferation of Si-based PV, solar-grade Si wafers with very low defect density can be obtained,⁶⁷ while the lowest-defect III-V materials are made *via* epitaxial methods not employed in this study. While our devices do not show

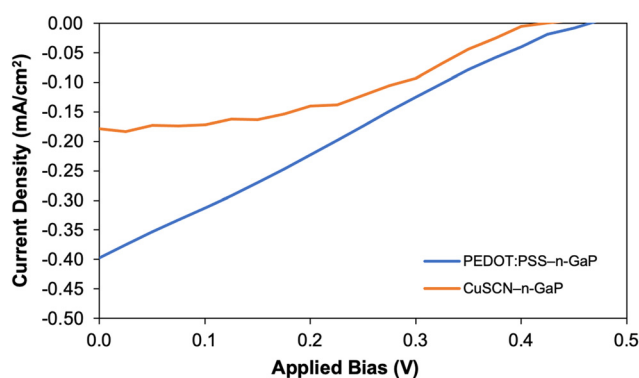


Fig. 9 Current-voltage curves for PEDOT:PSS-n-GaP device (blue) and CuSCN-n-GaP device (orange) under simulated AM1.5G illumination.



Table 1 n-GaP solar cell characteristics under AM1.5G illumination

Sample	J_{sc} (mA cm ⁻²)	V_{oc} (V)	FF	PCE (%)	Ref.
n-GaP/PEDOT:PSS	0.40	0.47	0.24	0.045	This work
n-GaP/CuSCN	0.18	0.43	0.41	0.031	This work
Planar GaP/PEDOT:PSS	0.2	0.82	0.49	0.08	6
Planar GaP/Au-NP/PEDOT:PSS	0.15	0.69	0.44	0.05	6

high performance, they are in line with other reported planar GaP heterojunction solar cells,³³ and serve as a reasonable platform for the surface photovoltage measurements that are the main focus of our study.

Lastly, cyclic voltametric scans were recorded to evaluate hysteresis behavior and investigate the electronic operation of the devices. As can be seen from Fig. S7† while there is minimal hysteresis in the CuSCN device, the PEDOT:PSS device has strong hysteresis behavior, showing a loss of photocurrent under reverse bias during the cyclic scan. This behavior is in contrast to the observation by the Maldonado group for a similar device.³³ It is attributed to a photoelectrochemical reaction in the PEDOT:PSS film and at its interfaces with GaP and silver. PEDOT:PSS is known to undergo electrochemical redox reactions in the presence of an electrolyte.^{68,69} The J - V curves in Fig. S7† also exhibit a very prominent S-shape, seen as the plateau in current at 0.5–1.6 V applied bias, which is degrading the fill factor and photovoltage. By contrast, a typical diode would pass current exponentially in the forward bias direction. The S-shape, also known as “roll-over”, indicates the presence of a barrier to charge extraction at one or more of the GaP contacts, consistent with the presence of a Schottky barrier at the back contact, as revealed in VKP-SPS measurements and depicted in Fig. 7b. This barrier acts like a second diode opposing the main photodiode, requiring more applied bias to achieve charge carrier extraction in either direction, thus degrading the fill factor.^{70–72} This is a common phenomenon in Si heterojunction,^{73–75} CdTe,⁷⁶ organic,^{77,78} and perovskite^{79–81} solar cells.

The devices by Wang *et al.*³³ do not show this S-shape, likely due to the forming gas anneal of the In back contact, as such charge transfer barriers are quite sensitive to metal workfunction and interface states. Indeed, VKP-SPS performed on n-GaP with an In back contact that was subjected to a 10 min forming gas anneal at 400 °C shows a near elimination of the positive sub-bandgap SPV (Fig. S8†). It is notable that VKP-SPS detected this Schottky barrier before devices were completed and highlights the utility of VKP-SPS when building photovoltaic device structures. Moreover, while there are slight deviations between the OCP and SPV, the OCP and SPV data clearly show a close correlation, with the SPV response giving a contactless indication of device performance. This holds true despite the S-shape in the J - V plots. This expands on previous works^{38,39} demonstrating the alignment of SPV and V_{oc} data to include heterojunction devices with charge transfer barriers, showing the robustness of the correlation. Additionally, this is the highest bandgap device studied using this technique.

Emerging solar cell device technologies are largely based on heterostructures with many layers needing to align energetically, and VKP-SPS can serve as an initial contactless method to ensure each layer is contributing as expected to the total photovoltage.

Conclusion

Two GaP photovoltaic devices were prepared from n-type GaP wafers by spin coating and electrodeposition of PEDOT:PSS and CuSCN, respectively, and using silver paint and In metal as contacts. VKP-SPS identified a Schottky barrier between n-GaP and In that could be eliminated with forming gas annealing. VKP-SPS further showed that HTLs improve charge separation and there is a close correlation between the SPV signal and the OCP data. Although electrodeposited CuSCN was found to have high parasitic light absorption, it was shown to be a more stable and reproducible HTL than PEDOT:PSS. Devices show power conversion efficiencies of less than 0.05%. The current-voltage curves show strong S-shapes, indicating that the Schottky barrier at the back contact limits the fill factors and power conversion efficiencies of the devices. This work demonstrates the utility of VKP-SPS in diagnosing barriers to charge transfer and contactlessly measuring the photovoltage contribution of each layer to the final operating device. This work provides insight into the formation of carrier-selective contacts on III-V materials without epitaxial growth methods.

Experimental

Chemicals

Poly(3,4-ethylenedioxythiophene):poly(4-styrenesulfonate) (PEDOT:PSS; 3.2% w/v in water, Sigma-Aldrich), Triton X-100 (electrophoresis grade, Fisher Scientific), dimethyl sulfoxide (DMSO; ≥99.9%, EM Science), copper(II) sulfate (≥99%, Sigma-Aldrich), diethanolamine (99%, Sigma-Aldrich), potassium thiocyanate (99%, Sigma, Aldrich), GaIn eutectic (99.99%, Sigma-Aldrich), and conductive silver paint (Ladd Research) were used as described in the text without additional modifications. Water was purified to 18 MΩ cm resistivity using a Nano-pure system. n-Type sulfur-doped gallium phosphide wafers (n-GaP, carrier density 4.8–16 × 10¹⁷ cm⁻³, purchased via EL-CAT Inc. from Elma-Malachit) were etched in freshly prepared 3:1:1 solution of H₂SO₄:H₂O₂:H₂O (piranha acid) at 60 °C for 3 minutes immediately before use. Films on n-GaP were stored in a glovebox with O₂ concentration ≤60 ppm.



Spin coating of PEDOT:PSS³³

2.5 mL PEDOT:PSS solution, 0.5 mL DMSO, and 0.1 mL Triton X-100 were dissolved in water to produce a total solution volume of 10 mL. The solution was kept in a refrigerator between uses. Spin coating was performed in a model WS-400BZ-6NPP spin coater from Laurell Technologies Corporation connected to a vacuum pump and N₂ tank. GaP wafer pieces were taped onto a clean glass slide and the glass slide was secured to the spin coater. Wafer pieces were not directly attached to the holder of the spin coater because it applies a strong suction force that would break the fragile wafer. After substrates were secured to the spinning base, a micropipettor was used to deposit the designated amount of spin coating solution (typically 20–40 μL , depending on substrate size) before the programmed spinning procedure was initiated. Substrates were spun at 500 rpm for 5 minutes and films were left to completely dry. Samples were briefly heated to 100 °C on a hot plate under N₂ to drive off residual water.

Electrodeposition of CuSCN⁴⁶

15 mM CuSO₄, 67.5 mM diethanolamine (DEA), and 45 mM KSCN were dissolved in water to make the electrodeposition bath. Etched n-GaP was used as the working electrode, with the back polished side and clip contact protected with polyester tape, in a three-electrode set up with a calomel electrode (3.5 M KCl) as the reference electrode and a Pt wire as the counter electrode. A CV scan was first performed to see where cathodic current began to flow. The films were deposited using chronoamperometry set to an applied voltage of $-4.5\text{ V vs. }3.5\text{ CE}$. The current density was approximately -4.7 mA cm^{-2} .

Surface photovoltage spectroscopy

Surface photovoltage (SPV) spectra were measured in air. A semitransparent, vibrating gold Kelvin probe (Delta PHI Besocke) served as the reference electrode. Spectral scans were performed from 3390 cm^{-1} to $35\,000\text{ cm}^{-1}$, and samples were illuminated with monochromatic light from a 300 W Xe lamp filtered through an Oriel Cornerstone 130 monochromator ($1\text{--}10\text{ mW cm}^{-2}$; Newport Corporation, Irvine, CA), using a step size of 100 cm^{-1} . The contact potential difference (CPD) spectra were corrected for drift effects by subtracting a fitted logarithmic curve of a dark scan from the spectral scan. Intensity-dependent measurements used an air-cooled 405 nm LED. The light was chopped by disconnecting the power supply.

Preparation of front and back conductive contacts

To create a front contact to the HTL-coated GaP wafers, a dot of conductive silver paint was applied on top of the HTL around the edge of the masked area. The silver paint was allowed to dry with mild heating ($\sim 80\text{ }^{\circ}\text{C}$). The back contact was formed by soldering indium metal onto the back polished side before HTLs were applied.

Solar cell current–voltage measurements

Devices were placed on top of thick copper substrates to make contact with the indium layer. One probe from the potentiostat was placed on the silver paint contact, while the other was placed on the copper substrate (in contact with the indium). AM1.5G illumination was provided by a Xe lamp with a water filter reflected with a parabolic mirror. Illumination intensity was adjusted to AM1.5G by illuminating a Si solar cell placed at the sample test location. LED illumination intensity was checked with a photometer. Measurements were conducted in air. Current–voltage curves were measured at a scan rate of 100 mV s^{-1} sweeping from positive to negative applied bias.

The fill factor was calculated by first identifying the point where the product of the applied voltage and the photocurrent was greatest in the lower right quadrant, giving the maximum power point. The maximum power point was then divided by the product of the open circuit voltage and the short-circuit current to obtain the fill factor.

Scanning Electron Microscopy (SEM) and Electron Dispersion X-Ray Spectroscopy (EDS)

SEM was performed in an FEI Scios Dualbeam focused ion beam (FIB)-scanning electron microscope (Thermo Fisher Scientific, Waltham, MA). EDS was performed using an Oxford X-Max EDS Detector and analysed with Oxford Aztec EDS software (Oxford Instruments, Abingdon, Oxfordshire, England). Images were taken and EDS was performed in Standard mode using 5 kV beam voltage and 1.6 nA beam current.

Data availability

Data will be made available upon request.

Conflicts of interest

There are no conflicts of interest to declare.

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