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Resistive switching in benzylammonium-based Ruddlesden-Popper layered hybrid perovskites for non-volatile memory and neuromorphic computing

Artificial synapses based on resistive switching emerged as promising platforms for brain-inspired computing technologies, and hybrid halide perovskite materials provided an opportunity to simplify these device architectures. However, their instabilities under operating conditions compromise reliability, stimulating research efforts to address this critical challenge. This work demonstrates reliable resistive switching and synaptic behaviour in layered benzylammonium-based halide perovskites. Moreover, it reveals a unique transformation from digital to analogue upon changing the halide composition from bromide to iodide anion, highlighting these materials as promising candidates for non-volatile memory and neuromorphic computing.

As featured in:



See Mahesh Kumar and Jovana V. Milić *et al.*, *Mater. Adv.*, 2024, 5, 1880.

Cite this: *Mater. Adv.*, 2024,  
5, 1880Received 29th August 2023,  
Accepted 11th December 2023

DOI: 10.1039/d3ma00618b

rsc.li/materials-advances

# Resistive switching in benzylammonium-based Ruddlesden–Popper layered hybrid perovskites for non-volatile memory and neuromorphic computing†

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Artificial synapses based on resistive switching have emerged as a promising avenue for brain-inspired computing. Hybrid metal halide perovskites have provided the opportunity to simplify resistive switching device architectures due to their mixed electronic–ionic conduction, yet the instabilities under operating conditions compromise their reliability. We demonstrate reliable resistive switching and synaptic behaviour in layered benzylammonium (BzA) based halide perovskites of (BzA)<sub>2</sub>PbX<sub>4</sub> composition (X = Br, I), showing a transformation of the resistive switching from digital to analog with the change of the halide anion. While (BzA)<sub>2</sub>PbI<sub>4</sub> devices demonstrate gradual set and reset processes with reduced power consumption, the (BzA)<sub>2</sub>PbBr<sub>4</sub> system features a more abrupt switching behaviour. Moreover, the iodide-based system displays excellent retention and endurance, whereas bromide-based devices achieve a superior on/off ratio. The underlying mechanism is attributed to the migration of halide ions and the formation of halide vacancy conductive filaments. As a result, the corresponding devices emulate synaptic characteristics, demonstrating the potential for neuromorphic computing. Such resistive switching and synaptic behaviour highlight (BzA)<sub>2</sub>PbX<sub>4</sub> perovskites as promising candidates for non-volatile memory and neuromorphic computing.

## Introduction

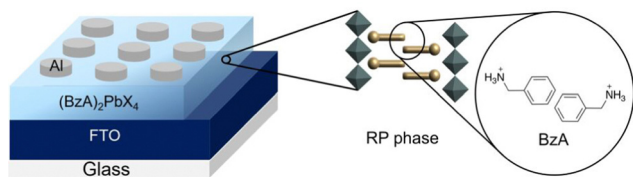
Resistive switching has garnered significant attention in non-volatile memory technology due to its rapid switching speed, remarkable endurance, exceptional density, and minimal power consumption.<sup>1</sup> The principle of resistive switching is

associated with resistive random-access memory (RRAM) operation, characterized by an abrupt variation in resistance.<sup>2</sup> These systems can be classified into digital or analog, depending on whether the resistive switching effect is abrupt or gradual. While digital memristors are primarily studied for non-volatile memory applications, analog ones hold significant potential in the field of neuromorphic computing.<sup>2</sup> To this end, two-terminal devices with a vertical metal–insulator–metal structure have provided a simplified design for artificial synapses owing to their capability to mimic the transmission properties of biological synapses.<sup>3</sup> Such devices play a pivotal role as fundamental functional units in neuromorphic computing,<sup>4</sup> where electrical stimuli must transfer from a pre-synaptic to a post-synaptic neuron for an artificial synapse to operate. In a ‘memristor’, a resistive switching memory element that can mimic the operation of a biological system, one metal electrode serves as a pre-neuron, another as a post-neuron, and the insulating layer acts as the synapse.<sup>5,6</sup> A wide range of materials have demonstrated the ability to exhibit resistive switching, including polymers,<sup>7</sup> organic materials,<sup>8</sup> transition metal dichalcogenides,<sup>9</sup> binary oxides,<sup>10</sup> and perovskites.<sup>11</sup> While oxides are the most extensively studied materials for resistive memories, their efficacy is impeded by the difficult fabrication, high-power consumption, and lack of precise control over filament formation that is associated with memristive functions, which compromises their performance.<sup>12,13</sup> In particular, the necessity of high-temperature processing and vacuum conditions limits the application of perovskite oxide materials in RRAM.<sup>14</sup> Alternatively, hybrid halide perovskite materials have emerged as promising contenders in the realm of optoelectronic devices, notably in applications such as photovoltaics,<sup>15</sup> light-emitting diodes,<sup>16</sup> lasing,<sup>17</sup> and photodetectors.<sup>18,19</sup> Due to their mixed ionic–electronic conduction,<sup>20,21</sup> low leakage currents, and tuneable band gap, hybrid metal halide perovskites have extended their utility to memory devices,<sup>13,22,23</sup> offering the possibility of integrating the benefits of both inorganic and organic resistive switching. However, the instability of halide

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perovskites under operating conditions poses a challenge for their application.<sup>21,24</sup>

Toward overcoming the instability of metal halide perovskites, layered (2D) hybrid perovskites incorporating larger organic moieties have shown potential for stabilizing perovskite materials and their devices toward more reliable memory elements.<sup>25,26</sup> These materials are often defined by the general formulation of  $S_2A_{n+1}MX_4$ , where S and A are (*i.e.*, alkyl or arylammonium) spacers and central (*e.g.*, methylammonium (MA), formamidinium (FA), *etc.*) organic cations, respectively, M is a divalent metal ion (mostly  $Pb^{2+}$ ) and X halide anion ( $I^-$ ,  $Br^-$ , or  $Cl^-$ ).<sup>22,25,26</sup> The value of  $n$  represents the number of  $\{MX_6\}$  corner-sharing octahedra between organic spacer (S) layers. While these systems present a diverse class of materials defined by different compositional elements and number of layers ( $n$ ), they are mostly associated with Ruddlesden–Popper (RP) phases formed by van der Waals interactions between spacer cations with a half-a-unit-cell displacement between the adjacent slabs (Fig. 1).<sup>26,27</sup> As a result, the 2D perovskite structure exhibits anisotropic charge transport, particularly in the out-of-plane direction, and this unique characteristic holds promise for designing resistive memories with low programming currents.<sup>28</sup> However, the application of 2D perovskites in resistive switching memories has so far been limited, primarily relying on archetypical *n*-butylammonium (BA) and 2-phenylethylammonium (PEA) based materials.<sup>22,28</sup> For instance, it was shown that the reduction of dimensionality from 3D to 2D induced the resistive switching properties of BA/MA-based RP phases with an improvement in the endurance and on/off ratios. The 2D structure was also found to be advantageous for the formation of a conductive filament, primarily due to the anisotropic migration of defects.<sup>28</sup> Similarly, 2D RP  $(PEA)_2PbBr_4$  perovskite single crystals exhibited resistive switching behaviour with significantly reduced currents of 10 pA, attributed to the mixed transport.<sup>22</sup> A recent study also reported an exceptionally high on/off ratio of  $10^9$  using a quasi-2D perovskite based on  $(PEA)_2Cs_3Pb_4I_{13}$  composition, higher than that of the 3D  $CsPbI_3$ , which was attributed to the wider bandgap and thus an increased Schottky barrier.<sup>29</sup> The composition of halide perovskite has also been found to impact its switching properties in 3D  $CH_3NH_3PbX_3$  ( $X = I, Br$ ), where the set voltage was found to decrease when the iodide is replaced by bromide due to the lower barrier for  $Br^-$  migration compared to  $I^-$  ions.<sup>30</sup>



**Fig. 1** Layered hybrid perovskite resistive switching device. Schematic of a resistive switching device architecture (left) based on Ruddlesden–Popper (RP) layered perovskite phase as active material (right) comprising benzylammonium (BzA) cations between aluminium (Al) contacts and fluorine-doped tin oxide (FTO) conductive glass used in this study.

This is unexploited in 2D perovskites, which still remain underrepresented in resistive switching devices despite their potential.

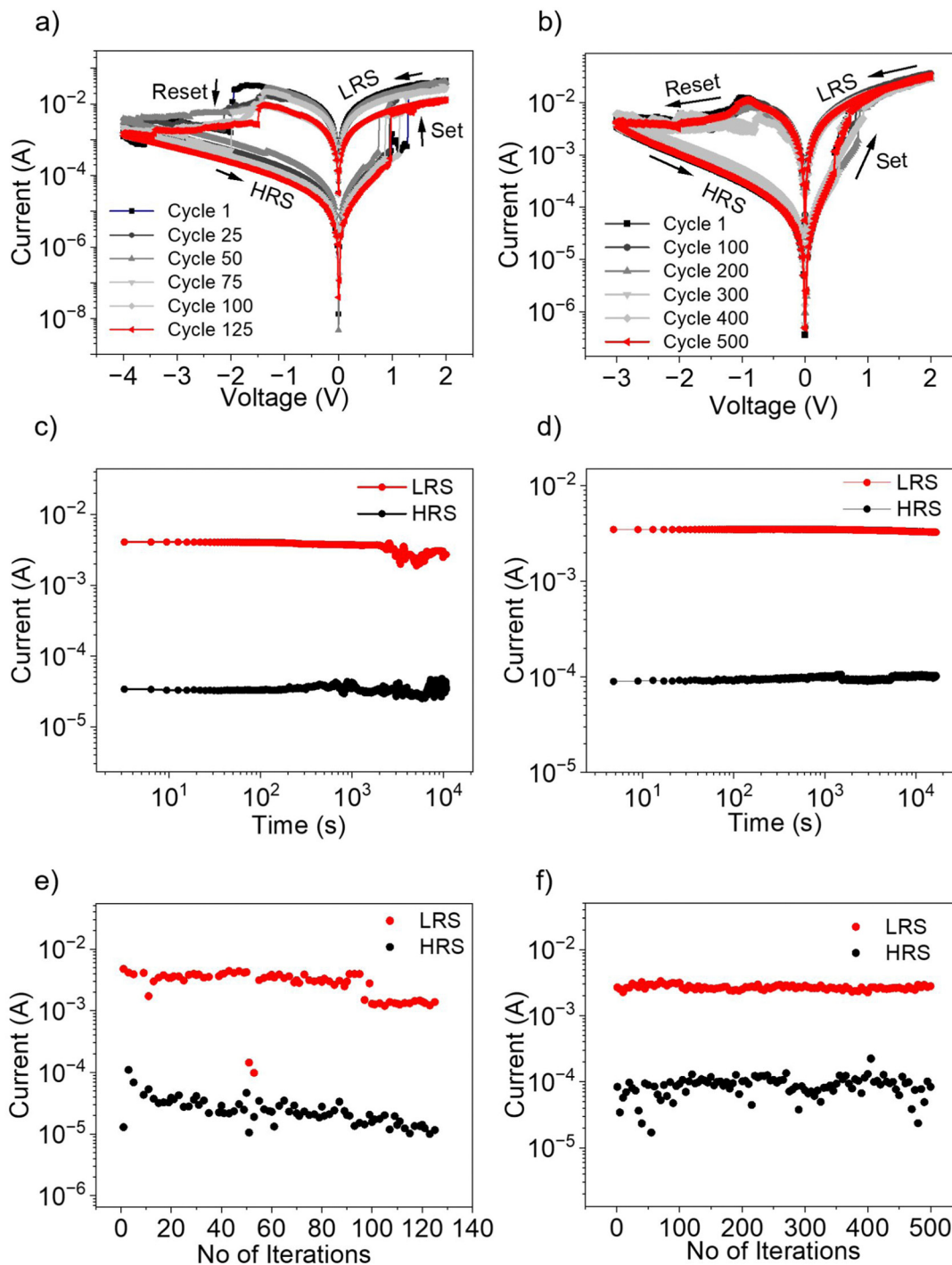
Here, we report the resistive switching behaviour of layered benzylammonium (BzA) based hybrid halide perovskite of  $(BzA)_2PbX_4$  ( $X = Br, I$ ) composition. The memory devices based on a simple Al/ $(BzA)_2PbX_4$ /fluorine-doped tin oxide (FTO) architecture exhibited bipolar resistive switching behaviour with low switching voltage, good data retention, and excellent endurance. Moreover, we observed an unusual transformation from digital to analog resistive switching upon the substitution of Br with I ions. A model is proposed in which a conductive filament is formed by halide vacancies to explain the underlying mechanism.<sup>31</sup> Finally, voltage pulses were applied to the  $(BzA)_2PbI_4$  device to validate its capability to emulate a synapse and function as an artificial synapse.<sup>32</sup> The observed gradual set and reset behaviour of the device signifies its potential for neuromorphic applications, where the ability to tune conductance dynamically is highly desirable. The artificial synapse effectively emulated key synaptic characteristics, such as potentiation, depression, and paired-pulse facilitation.<sup>5</sup> This study thereby reveals unique resistive switching characteristics for layered BzA-based hybrid halide perovskites, providing a better understanding of their memristive effects and revealing the potential for neuromorphic computing in the future.

## Results and discussion

The resistive switching properties of  $(BzA)_2PbX_4$  ( $X = I, Br$ ) perovskites were studied in a metal–insulator–metal device based on a thin film spin-coated on the FTO substrate and top circular Al electrode contacts (Fig. 1). The experimental details are provided in the Experimental Section of the Electronic Supporting Information (ESI,† Fig. S1–S5). Layered (2D) perovskite materials were prepared mechanosynthetically by ball-milling, followed by solution-based spin-coating and subsequent annealing. The formation of a 2D perovskite was evidenced by X-ray diffraction, UV-vis absorption, and photoluminescence spectroscopy (Fig. S1, ESI†).<sup>27</sup>

Resistive switching characteristics of the Al/ $(BzA)_2PbX_4$ /FTO devices reveal a distinct bipolar switching (Fig. 2a and b). Digital switching is observed in  $(BzA)_2PbBr_4$  upon application of voltage in the range between +2 V and –4 V (Fig. 2a).<sup>33</sup> Upon sweeping the voltage from 0 to 2 V, an abrupt transition from a high (HRS) to a low resistance state (LRS) is observed at +1.04 V, indicating the *set* process. The LRS persisted until a negative bias voltage was applied, after which the state reverted back to HRS at –1.33 V, signifying the *reset* process. In contrast, Al/ $(BzA)_2PbI_4$ /FTO showed a more gradual set and reset (Fig. 2b). The set and reset voltages of +0.5 V and –1 V for  $(BzA)_2PbI_4$  were also smaller, and the device featured lower power consumption, with the estimated set and reset powers of 0.63 mW (at 0.5 V and 1.26 mA) and 12.25 mW (at –1 V and 12.2 mA), respectively. In addition, current–voltage characteristics exhibited a negative





**Fig. 2** Resistive switching in bromide (left) and iodide (right) based layered hybrid perovskites. Current–voltage characteristics for bipolar resistive (a) digital switching in  $(\text{BzA})_2\text{PbBr}_4$  and (b) analog switching in  $(\text{BzA})_2\text{PbI}_4$  devices at the voltage scan rate of 20 mV and 8 ms time sweep. More information about the frequency and contact dependence is detailed in ESI† (Fig. S3 and S4). To calculate the set voltage for the I-based system, the current–voltage curve was replotted on a linear scale to visualise the point where the current starts increasing (Fig. S5, ESI†). (c) and (d) Retention time and (e) and (f) endurance over repeated cycles for resistive switching devices at 0.2 V bias over (e) 125 cycles in  $(\text{BzA})_2\text{PbBr}_4$ -based system and (f) 500 cycles in  $(\text{BzA})_2\text{PbI}_4$ -based system. Current in HRS (black) and LRS (red) was read at +0.2 V for the endurance and retention tests.

differential resistance (NDR), where the current decreased as the voltage increased after reset, which is relevant for achieving diverse resistance states in neuromorphic computing applications.<sup>34</sup>

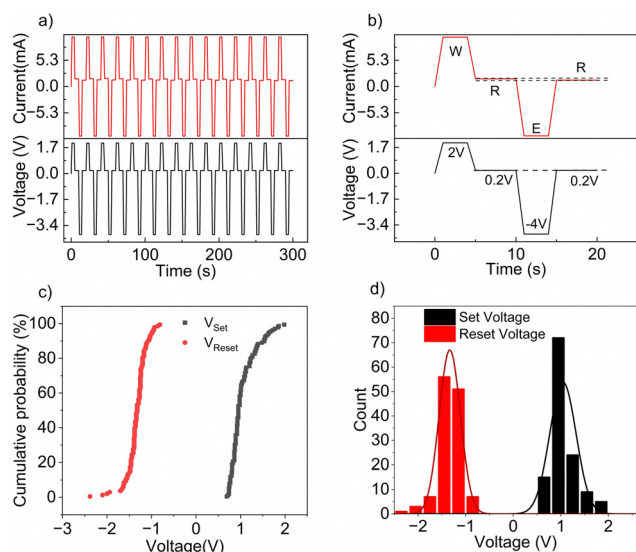
To assess the nonvolatility and reliability of each state (*i.e.*, resistance level) in  $\text{Al}/(\text{BzA})_2\text{PbX}_4/\text{FTO}$  devices, retention time and endurance characteristics were monitored (Fig. 2c–f). The iodide-based ( $\text{X} = \text{I}$ ) system exhibited better retention and



endurance than the bromide-based ( $X = \text{Br}$ ) system by showing a stable switching for more than 500 cycles with a retention time of  $1.5 \times 10^4$  s (Fig. 2d and f), whereas the bromide-based system showed a superior on/off ratio of  $>10^2$  as compared to iodide system (Fig. 2c and e). These characteristics highlight the non-volatile potential of the devices that can be further optimised in terms of morphology, thickness, and device architecture beyond the scope of this study, such as by tailoring contact layers and using interlayers to reach longer retention time and better on/off ratios for practical applications.

To ensure that the device can reliably store and retrieve data, write-read-erase-read (WRER) pulses were applied (Fig. 3a and Fig. S6, ESI<sup>†</sup>). A pulse voltage of 2 V was first applied for a duration of 4 s to write data into the  $(\text{BzA})_2\text{PbBr}_4$  device (Fig. 3a). This voltage pulse causes the resistive state of the device to change and store the data. After the writing process, a read voltage of 0.2 V was applied for 6 s to verify the data stored. Finally, an erase voltage of  $-4$  V was applied for 4 s, causing the resistive state to change and erase the data. Once the erase process was complete, a pulse voltage of 0.2 V was used for 6 s to read the erased state (Fig. 3b). A reliable switching between these states was observed for 100 WRER cycles. Moreover, a comparable procedure was applied to the  $(\text{BzA})_2\text{PbI}_4$  system, with an erase pulse of  $-3$  V instead of  $-4$  V (Fig. S6, ESI<sup>†</sup>), evidencing the utility of the system for storing and retrieving data.

The practicality of WRER cycles for RRAM applications was further assessed for their consistency and reliability by analysing the uniformity in switching voltages. For this purpose, cumulative probabilities of the set and reset voltages of the  $(\text{BzA})_2\text{PbBr}_4$  device were calculated using data obtained from

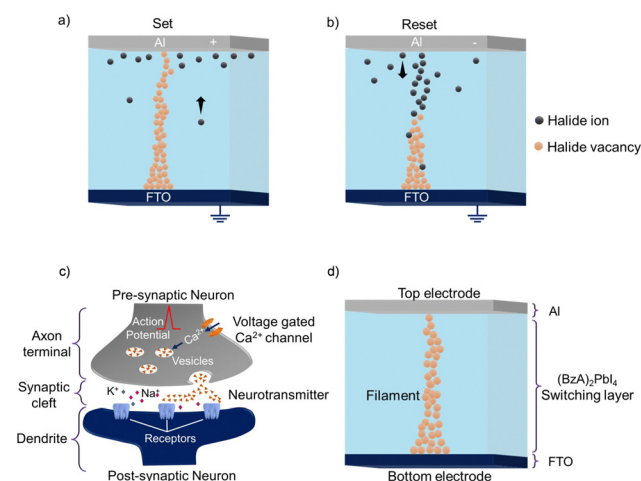


**Fig. 3** Bromide-based perovskite memory data storage and retrieval. (a) Write-read-erase-read (WRER) cycles. (b) Enlarged view of 1st WRER cycle indicating write (W), read (R), and erase (E) states with the corresponding voltage bias. (c) Cumulative probability for set and reset voltages based on 125 cycles. (d) Statistical distribution of set and reset voltages with Gaussian fitted curve. WRER cycles for the iodide-based system are detailed in Fig. S6 of the ESI<sup>†</sup>.

125 switching cycles (Fig. 3c and d). The statistical distribution of set and reset voltages indicated the voltage range where the switching occurs (Fig. 3c). The set voltage range was found to be between 0.69 V to 1.98 V with an average of 1.04 V, while the reset voltage was distributed between  $-0.81$  V and  $-2.38$  V with an average of  $-1.33$  V in  $(\text{BzA})_2\text{PbBr}_4$  (Fig. 3d). This voltage profile permits reliable and consistent switching in RRAM applications. The gradual switching behaviour in  $(\text{BzA})_2\text{PbI}_4$  devices, however, suggests a distinct operation mechanism that is more relevant in the design of artificial synapses (Fig. 4).

The differences in the resistance switching mechanism of  $(\text{BzA})_2\text{PbX}_4$  devices can be governed by the halide ion ( $X^-$ ) migration dynamics and the subsequent halide vacancy ( $V_X$ ) conductive filament formation under the influence of the applied electric field (Fig. 4a and b).<sup>22,23</sup> Specifically, when a positive voltage is applied, negatively charged halide ions migrate toward the top electrode. Such a migration of ions leads to the formation of halide vacancies, which are gradually accumulating and contributing to the formation of a halide vacancy conductive filament in the LRS (Fig. 4a). When a negative voltage is applied to the top electrode, halide ions migrate in the opposite direction, causing the 'rupture' of the filament, switching the device to the HRS (Fig. 4b). This halide ion dynamics can be related to the distinct digital and analog switching mechanisms, as analog switching can be associated with the gradual migration of iodide ions, whereas digital switching corresponds to the faster migration of bromide ions.<sup>30</sup> With lower activation energy, bromide ions exhibit faster migration, whereas higher activation energy of iodide vacancies hinders rapid migration,<sup>30</sup> leading to digital switching in  $(\text{BzA})_2\text{PbBr}_4$  and a more gradual movement of iodide ions and, consequently, analog switching behaviour in  $(\text{BzA})_2\text{PbI}_4$ .

To gain a better understanding of the conduction mechanism, current-voltage characteristics were assessed for both



**Fig. 4** Resistive switching mechanism in layered hybrid perovskites. Schematic of (a) set (LRS) and (b) reset (HRS) switching governed by ion migration. Illustration of a (c) biological synapse and (d)  $(\text{BzA})_2\text{PbI}_4$ -based artificial synapse mimicking the biological function.





showcasing their potential for non-volatile memory and neuromorphic computing applications. The study revealed a shift from digital to analog resistive switching through halide substitution, offering versatile functionality and reduced power consumption. The underlying mechanism was primarily attributed to halide ion migration and halide vacancy conductive filament formation. Both the bromide- and iodide-based systems exhibited high performances, with the former excelling in achieving a superior on/off ratio and the latter displaying excellent retention and endurance. The (BzA)<sub>2</sub>PbBr<sub>4</sub> established reliable random-access data storage and retrieval capabilities through write-read-erase-read (WRER) pulses, complemented by consistent switching voltages for uniform performance in practical random-access memory applications. In addition, (BzA)<sub>2</sub>PbI<sub>4</sub> demonstrated gradual (analog) set and reset processes, enhancing energy efficiency and emulating critical synaptic characteristics, such as potentiation, depression, and paired-pulse facilitation, showcasing its potential for neuromorphic computing. These resistive switching properties and synaptic behaviour position (BzA)<sub>2</sub>PbX<sub>4</sub> layered hybrid halide perovskites as compelling candidates for advanced non-volatile memory and neuromorphic computing. This further contributes to the development and understanding of hybrid halide perovskites in the realm of emerging memory technologies.

## Experimental

Experimental details, including materials and methods, are provided in the Electronic Supplementary Information (ESI<sup>†</sup>). Data presented here can be accessed at the DOI: 10.5281/zenodo.10392068, and it is available under the license CC-BY-4.0 (Creative Commons Attribution-ShareAlike 4.0 International).

## Author contributions

M.M.G. led the project and conducted the experiments with the support of S.S. and G.B., who (mechano)synthesized the layered hybrid perovskites and performed their structural and optoelectronic characterization in powders and thin films. The project was conceptualized by J.V.M. and M.K., who supervised and directed it. All authors contributed to the manuscript.

## Conflicts of interest

There are no conflicts to declare.

## Acknowledgements

G. B. and J. V. M. acknowledge the Swiss National Science Foundation project no. 193174. The authors are grateful to Prof. Ullrich Steiner (Adolphe Merkle Institute, Fribourg, Switzerland) and DFG-SPP 186453 project for support.

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