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Vertical Al₂Se₃/MoSe₂ heterojunction on sapphire synthesized using ion beam†

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The vertical Al₂Se₃/MoSe₂ heterojunction on sapphire was first fabricated *via* an ion beam-assisted process. The MoSe₂ was formed *via* Mo selenization, while Al₂Se₃ was formed *via* Se substitution for O in sapphire. The applications of this heterojunction will be developed in the future.

Introduction

Transition metal dichalcogenides (TMDs)^{1–4} undoubtedly surpass graphene⁵ in the application of channel and diode materials because they not only possess proper bandgaps transiting from indirect to direct as the monolayer is reached, but also are stacked by the interlayer van der Waals forces ideally being without dangling bonds. Initially, the single-layer MoS₂ transistors were fabricated successfully by Radisavljevic *et al.*⁶ and the back-gated MoSe₂ field-effect transistors (FETs) were demonstrated by Larentis *et al.*⁷ Afterwards, Georgiou *et al.*⁸ announced a WS₂-based FET on a SiO₂/Si substrate and Baugher *et al.*⁹ accomplished the in-plane p–n diode of ambipolar WSe₂ by electrostatic gating. However, all of the studies mentioned above utilized mechanically exfoliated TMDs for device fabrication, implying that the thickness of TMDs cannot be controlled precisely and the area is limited. The chemical vapor deposition (CVD) method^{10–12} was also developed for the growth of TMDs on SiO₂/Si substrates. Unfortunately, optical microscopy (OM) and/or atomic force microscopy (AFM) images of TMD triangular flakes obtained by CVD growth indicate that the TMD films are non-continuous, even though the dimensions are larger than 100 μm. The non-continuous film completely limits real applications in the semiconductor industry. On the other hand, S or Se corrosion would cause the SiO₂ to lose the insulating capability; hence the transfer step

seriously deteriorating the quality of TMD films is still inevitable. Another synthesis method¹³ based on the sulfurization or selenization of transition metal or transition metal oxide films can achieve wafer-scale continuous TMD films. Essentially, the quality of TMD films synthesized by this method is controversial because the crystallinity of e-beam evaporated metal or metal oxide thin films may not be good enough and they could not be uniformly sulfurized or selenized owing to the diffusion-limited reaction. As a result, these might be the main causes leading to the TMD FETs with a carrier mobility as low as 10 cm² V^{−1} s^{−1}.¹³ Zhang *et al.*¹⁴ grew MoSe₂ epitaxially on graphene/SiC template to observe the bandgap transition. In fact, the mass production of graphene/SiC template fabricated by the process¹⁵ is restricted because it employs an Omicron ultrahigh vacuum (UHV) scanning tunneling microscope (STM) combined with molecular beam epitaxy (MBE) system.

For device applications, heterojunctions are the basic units in the electronic device structure; hence the doping method becomes a crucial topic. In addition to electrical control of ambipolar WSe₂,⁹ chemical adsorption^{16,17} or substitution^{18,19} doping is the most common technique. Although the cost of chemical doping is relatively low, the adsorbed dopants are much easier to desorb due to the poor stability. The substitution doping is proceeded during the chemical vapor transport (CVT) synthesis of TMDs, requiring high temperature (~1000 °C) and quite a long period (48–500 h).^{18,19} Moreover, the large scale TMD films cannot be achieved *via* the CVT synthesis because the source materials should be sealed in a quartz tube under a vacuum state. Recently, the epitaxial growth of monolayer TMD heterojunctions was reported by several groups.^{20–23} They claimed that the so-called van der Waals epitaxy could overcome the issue of lattice mismatch, resulting in heteroepitaxial films with a limited area because the interaction between the epitaxial film and substrate is the van der Waals force rather than chemical bonding. Regrettably, the lateral scale of TMD films in these reports still cannot be enlarged, implying that the quality and scale of heteroepitaxial materials depend on several

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factors and not just the lattice mismatch. Herein, we developed an innovative process using a Se ion beam, as shown in Fig. 1 to directly fabricate a uniformly large scale vertical $\text{Al}_2\text{Se}_3/\text{MoSe}_2$ junction on sapphire for future applications. The MoSe_2 is a semiconductor with a ~ 1.55 eV bandgap, while the Al_2Se_3 is a semiconductor with a ~ 3.1 eV bandgap.^{24,25} The materials were explicitly identified and the formation mechanism could be exhaustively interpreted.

Experimental

Synthesis

A 2-inch sapphire (Al_2O_3) (0001) wafer was used as the template, in which the Al element was the source of the formation of Al_2Se_3 . A ~ 20 nm-Mo thin film was first deposited on the sapphire wafer by customized direct current (DC) sputtering at 25 mTorr. During deposition, the Ar gas with a flow rate of 10 sccm was infused to produce plasma for sputtering. The working voltage and current of the DC power were 277 V and 0.357 A, respectively. The deposition time was 40 s and the substrate was kept at room temperature. The Se ions (60 keV, 10^{16} ions per cm^2) formed from the Se pellets were introduced into the Mo/sapphire wafer by the medium current ion implanter (ULVAC, IMX3500). Finally, the cut samples were annealed at 800 °C with a temperature increasing rate of 20 °C min^{-1} in the N_2/H_2 (6/1, v/v) ambiance for 30 minutes. After furnace cooling, the samples are ready for characterization. The dose and energy of implantation were estimated by a SRIM (the stopping and range of ions in matter) simulation in advance.

Characterizations

Raman spectroscopy was implemented using a confocal micro-Raman spectrometer (HORIBA, LabRAM, HR800 UV) to be certain of the formation of MoSe_2 layers. The light source of the Raman spectrometer was a He-Ne laser with a 632.8 nm wavelength and a power of 35 mW. The surface composition was analyzed using XPS (Ulvac-PHI, Versaprobe II) to confirm the formation of Al_2Se_3 and MoSe_2 . The spherical-aberration corrected TEM (JEOL, JEM-ARM200FTH) with a 0.1 nm resolution of the lattice image and 200 kV accelerated voltage was employed to observe the nanoscale layer structure. The

distribution of depth concentration was monitored by EDS attached with TEM.

Results and discussion

After the conditions of ion implantation were well tuned, the thermal treatment was initially optimized by checking the Raman spectra. The Raman spectrum of the sample obtained by the ion beam-assisted process after optimization was compared with that of the as-implanted sample without annealing. Apparently, there were no Raman peaks that could be observed in the spectrum of the as-implanted sample (Fig. 2a). After the thermal treatment, three Raman peaks located at ~ 205 , ~ 228 , and ~ 242 cm^{-1} respectively emerged in the spectrum. The first two peaks were attributed to molybdenum oxide at the surface, resulting from inevitable oxidation during the thermal treatment. The last one was the A_{1g} mode of MoSe_2 caused by the out-of-plane vibration.²⁶ The ion implantation technique was chosen for the synthesis of MoSe_2 in this study because of its accurate dose and accelerated energy, which could achieve high uniformity and continuity for large-area surface modification. As shown in Fig. 2b, the Raman mapping of the A_{1g} peak of MoSe_2 revealed that the MoSe_2 film synthesized in this study was uniform and continuous. Thus far, we can confirm that the uniformly continuous MoSe_2 film with a large area could be synthesized stably by an ion beam-assisted process.

During the experimental design, we considered that the thickness of sputtered Mo film should not be too thin to be uniform. In addition, if the Se ions are implanted into a depth that is not deep enough, they may easily escape from the surface during the thermal treatment because the annealing temperature may be higher than the boiling point of Se. Therefore, we fixed the thickness of the sputtered Mo film to ~ 20 nm and set the acceleration energy of implantation at 60 keV, predicting that the maximum concentration of Se would be located at the depth around the Mo/sapphire interface. After Ar^+ sputtering through the upper part of Mo film, the X-ray photoelectron spectroscopy (XPS) was implemented to realize the chemical configuration of the interfacial region between the Mo and sapphire. In the Mo 3d spectrum of Fig. 3a, the Mo–Se peak splits into $3d_{5/2}$ and $3d_{3/2}$ bands, located at ~ 229 and ~ 232 eV, respectively. This indicates that the Mo near the Mo/sapphire interface has reacted with the Se ions introduced by the implanter. The Mo–O signal (~ 234.5 eV) caused by the

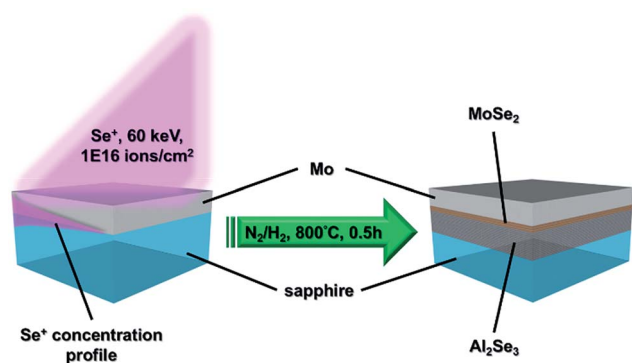


Fig. 1 Ion beam-assisted process.

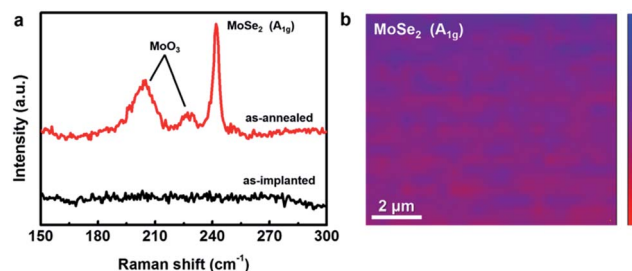


Fig. 2 (a) Raman spectra of the as-implanted and as-annealed samples. (b) Raman mapping of the A_{1g} peak of MoSe_2 .



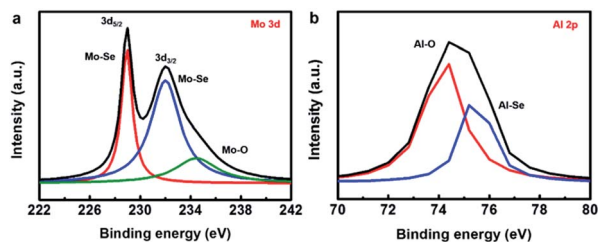


Fig. 3 (a) Mo 3d XPS spectrum of the sample obtained by the ion beam-assisted process. (b) Al 2p XPS spectrum of the sample obtained by the ion beam-assisted process.

inevitable oxidation during the thermal treatment was also detected in the Mo 3d spectrum. The signal for Al–O bonding (~ 74.4 eV) originates from the sapphire substrate, as shown in Fig. 3b. The Al–Se peak at ~ 75.2 eV infers the formation of an Al–Se compound, which might be Al_2Se_3 . XPS indicates that the Se ions near the Mo/sapphire interface might react with the Mo film and Al in the substrate to form MoSe_2 and Al_2Se_3 , respectively, during the thermal treatment.

Transmission electron spectroscopy (TEM) was used to monitor the cross-sectional layer structure at nanoscale after the ion beam-assisted process. In the low magnification image of Fig. 4a, the top SiO_2 layer deposited by an electron beam evaporator was capped on the surface to protect the thin film from damage during sample preparation using focus ion beam (FIB). In addition, there are two layers with similar thickness (~ 20 nm) covering the sapphire substrate. The upper layer should be the Mo film containing MoSe_2 layers, while the lower one should be the Al_2Se_3 . Only the lower part of the Mo film could react with the sufficient Se ions to form multilayer MoSe_2 with a layer spacing of ~ 0.66 nm, which is almost consistent with the theoretical value (~ 0.615 nm), as shown in Fig. 4b. In addition, the O atoms in the surface region of sapphire might be substituted with the sufficient Se ions to form an Al_2Se_3 layer because the maximum concentration of Se is located at the depth around the Mo/sapphire interface. The concentration depth profile was characterized by the energy dispersive spectrometer (EDS). The locations of multilayer MoSe_2 and the Al_2Se_3 layer could be clearly defined, as shown in Fig. S1.† The diffraction patterns of Mo, MoSe_2 , Al_2Se_3 , and sapphire, shown in the insets in Fig. 4a, were processed by fast Fourier transform

(FFT). In addition to MoSe_2 with the recognizable layer spacing, Al_2Se_3 can be identified using its diffraction pattern. The particular interplanar distances of plane groups were computed from two groups of reciprocal lattice points. These two calculated interplanar distances, which are equal to ~ 0.4478 and ~ 0.241 nm, correspond to the (011) and (400) interplanar distances (Fig. S2†) of Al_2Se_3 , respectively. Furthermore, the angle ($\sim 66^\circ$) between the two lines representing the (011) and (400) plane groups in the diffraction pattern is nearly equal to that between the (011) and (400) plane groups in the real lattice (Fig. 4b). Therefore, the layer below MoSe_2 could be identified as Al_2Se_3 , as shown in the high magnification image of Fig. 4a. The TEM results, which are in agreement with the EDS line scan results, indicate that the Mo selenization and Se substitution for O in sapphire occur simultaneously during the thermal treatment.

Conclusion

All the analytical results verify that the lower part of Mo film is selenized and the O atoms of sapphire near the surface are substituted by Se ions introduced by the implanter during the thermal treatment to form a vertical $\text{Al}_2\text{Se}_3/\text{MoSe}_2$ heterojunction on a sapphire substrate. This is the first study utilizing an ion implantation technique to directly synthesize the vertical heterojunction of TMD/III–VI semiconductor on a sapphire substrate. This simple ion beam-assisted process has great potential for fabricating other types of heterogeneous junctions. In future, we will attempt to develop devices based on this diode for suitable applications.

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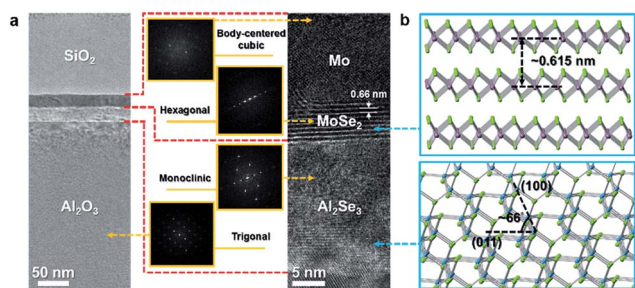


Fig. 4 (a) TEM image of the $\text{MoSe}_2/\text{Al}_2\text{Se}_3/\text{sapphire}$ layer structure. Insets: diffraction patterns of Mo, MoSe_2 , Al_2Se_3 , and sapphire. (b) Atomic models of MoSe_2 and Al_2Se_3 .



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