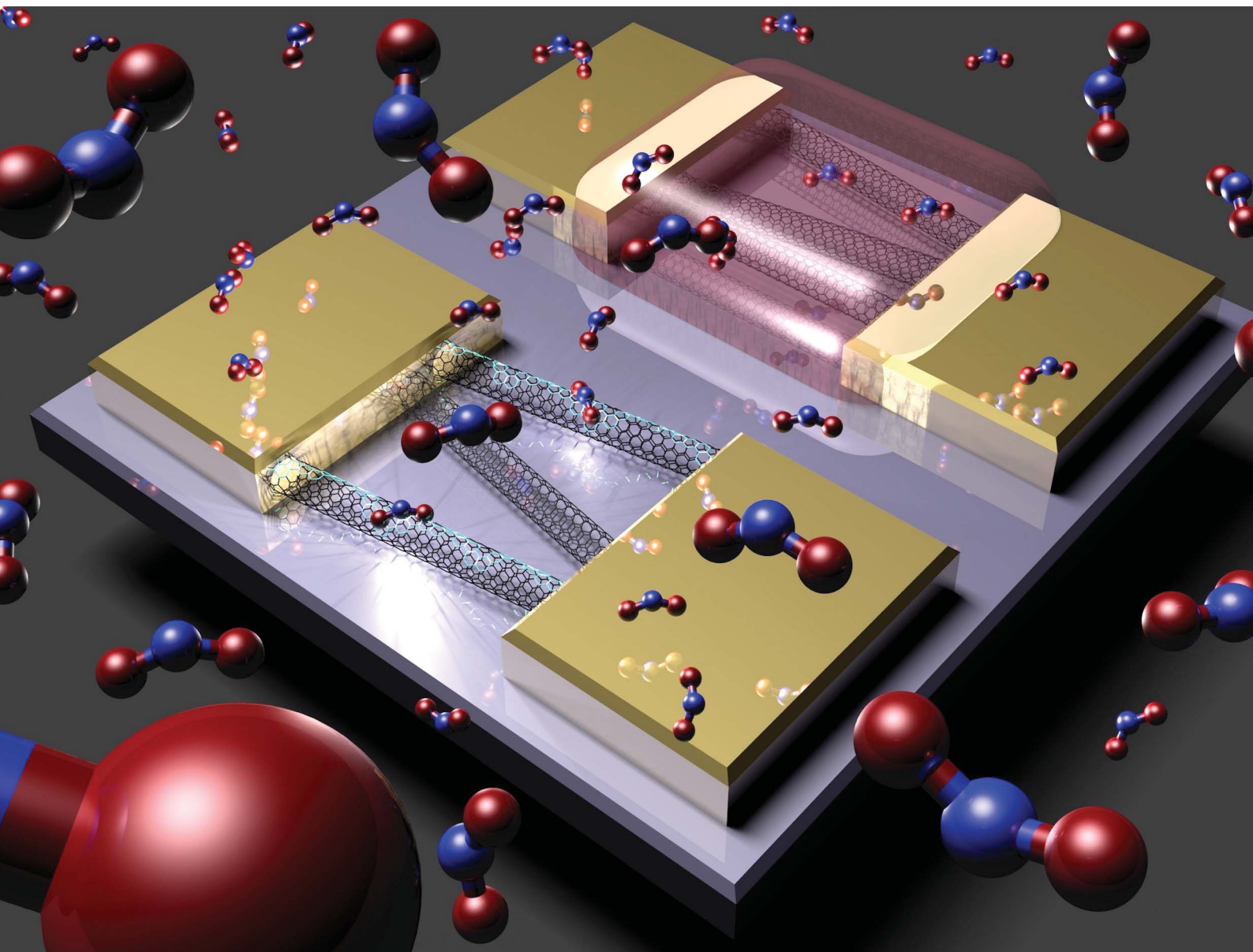


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# Simple and rapid gas sensing using a single-walled carbon nanotube field-effect transistor-based logic inverter†

Salomé Forel,<sup>‡</sup> Leandro Sacco,<sup>‡</sup> Alice Castan,<sup>‡</sup> Ileana Florea<sup>‡</sup>  
and Costel Sorin Cojocaru<sup>‡</sup>

Single-walled carbon nanotubes (SWCNTs) are promising candidates for gas sensing applications, providing an efficient solution to the device miniaturization challenge and allowing low power consumption. SWCNT gas sensors are mainly based on field-effect transistors (SWCNT-FETs) where the modification of the current flowing through the nanotube is used for gas detection. A major limitation of these SWCNT-FETs lies in the difficulty to measure their transfer curves, since the flowing current typically varies between  $10^{-12}$  and  $10^{-3}$  A. Thus, voluminous and energy consuming systems are necessary, severely limiting the miniaturization and low energy consumption. Here, we propose an inverter device that combines two SWCNT-FETs which brings a concrete solution to these limitations and simplifies data processing. In this innovative sensing configuration, the gas detection is based on the variation of an electric potential in the volt range instead of a current intensity variation in the microampere range. In this study, the proof of concept is performed using  $\text{NO}_2$  gas but can be easily extended to a wide range of gases.

sensitive and low-cost devices with fast responses operating at room temperature.<sup>1</sup> Currently, the most commonly used portable gas sensors are based on semiconducting metal oxides.<sup>2,3</sup> One of the major drawbacks with this sensor technology is its operating temperature, usually higher than 200 °C which leads to a high power consumption.<sup>4,5</sup> During the last decades, novel materials such as conductive polymers,<sup>6,7</sup> 2D layered transition metal dichalcogenides,<sup>8</sup> metal nanoparticles,<sup>9</sup> graphene<sup>10</sup> and carbon nanotubes<sup>11</sup> have been used to improve the key figures of gas sensors such as response, selectivity, stability, detection limit, and response/recovery times. Due to their remarkable electronic and mechanical properties, combined with their extreme sensitivity to their surrounding environment, single-walled carbon nanotubes (SWCNTs) represent a very promising alternative for the development of new sensors.<sup>12–19</sup> Usually, these gas sensors are in the form of SWCNT-FET devices and are based on gas exposure-triggered modifications of the SWCNT electrical response.<sup>15,17,20–26</sup>

## Introduction

Reliable, effective and sensitive detection of toxic or pollutant gases is a key point to provide appropriate responses to environmental and health issues. One of the major challenges for the sensor industry is the production of miniaturized, highly

The reactivity of pristine SWCNTs (*i.e.* non-functionalized SWCNTs) is usually described by two different phenomena: a modification of the height of the Schottky barrier at the SWCNT/electrode junction, or a doping of the SWCNT.<sup>27</sup> Pristine SWCNTs in classical SWCNT-FET devices have shown an enhanced reactivity to electron donor gases such as  $\text{NO}_2$  (ref. 17) and acceptor gases like  $\text{NH}_3$ .<sup>22</sup> In addition to the high sensitivity and low power consumption that characterizes the integration of nanomaterials as active elements in sensors, SWCNT-based devices offer a versatile platform to detect specific species by a functionalization of the SWCNTs<sup>5</sup> or proper engineering of the metal electrodes.<sup>28</sup> Furthermore, SWCNT-FETs on flexible substrates have been fabricated to promote applications compatible with wearable electronics.<sup>26</sup> However, one of the major disadvantages of usual SWCNT-FET based gas sensing devices lies in the difficulty to measure the characteristic transfer curve. Indeed, the current flowing through the SWCNT is usually in the range of  $10^{-12}$  A up to  $10^{-3}$  A,<sup>29</sup> requiring a complex, costly and energy intensive measurement chain

Laboratoire de Physique des Interfaces et des Couches Minces (LPICM), CNRS, Ecole Polytechnique, IP Paris, 91128, Palaiseau Cedex, France

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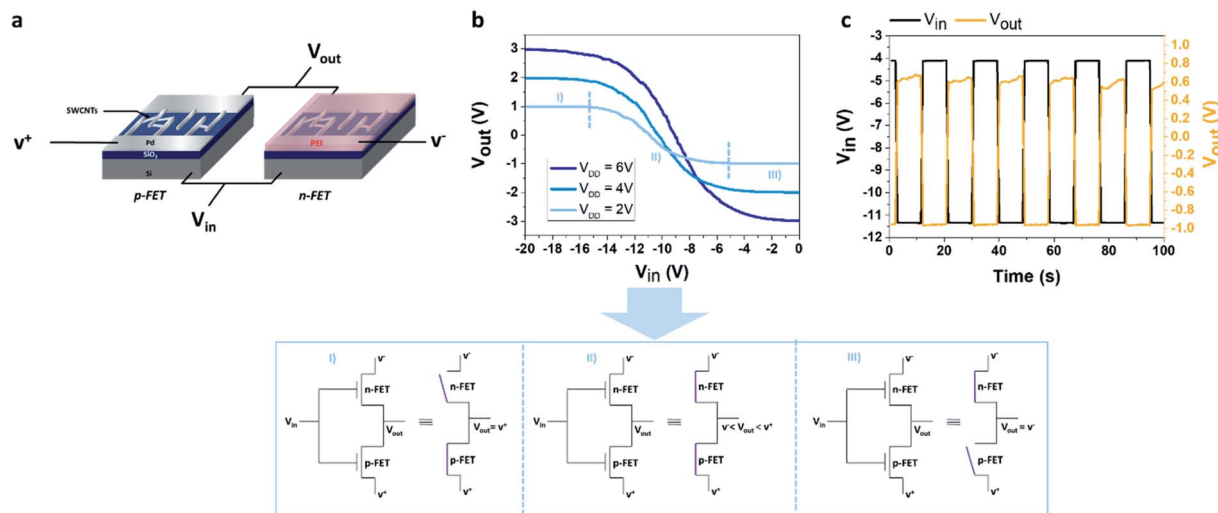
‡ These authors contributed equally.

§ Present address: Antwerp University, Physics Department, Experimental Condensed Matter Physics Laboratory, Universiteitsplein 1, 2610 Antwerp, Belgium. Email: salome.forel@uantwerpen.be

¶ Present address: Delft University of Technology, Faculty of Electrical Engineering, Mathematics and Computer Science, Department of Microelectronics, Delft, Feldmannweg 17, 2628 CT Delft, Netherlands. Email: l.n.sacco@tudelft.nl

|| Present address: Department of Physics and Astronomy, University of Pennsylvania, Philadelphia PA 19104, USA.





**Fig. 1** Logic inverter electrical characteristics under a  $N_2$  atmosphere. (a) Schematic view of the CMOS device. (b) Characteristic of the inverter for  $V_{DD} = 2$  V, 4 V and 6 V (i.e.  $V^+ = 1$  V, 2 V and 3 V, and  $V^- = -1$  V,  $-2$  V and  $-3$  V) inset: simplified ideal equivalent circuit corresponding to the regime I, II and III of the characteristic of the inverter for  $V_{DD} = 2$  V. (c) Characteristic of the inverter for a square input voltage ( $V_{in}$ ) of alternatively  $-4$  V and  $-11$  V and a  $V_{DD}$  fixed at 2 V (i.e.  $V^+ = 1$  V and  $V^- = -1$  V).

which severely limits device miniaturization and the expected energy savings. Another difficulty concerns the interpretation of the modification of the  $I_{ds}-V_g$  transfer curve after gas exposure. Since during gas exposure, many parameters can be modified simultaneously (such as on and off current values, the threshold voltage, and the shape/slope of the transfer curve), finding the proper parameters for a rapid and simple data treatment to determine gas concentration is challenging. These constraints remain a major problem towards the implementation of these sensors into usable products.

In order to overcome such drawbacks, it is necessary to shift from an analog to a digital conversion. Previous works have demonstrated the implementation of logic circuits for gas sensing applications.<sup>9,30-34</sup> Particularly, Shulaker *et al.*<sup>5</sup> used a 3D integrated circuit architecture, containing more than one million carbon nanotube FET-based gas sensors for inputs combined with silicon-based FET devices, all fabricated on overlapping vertical layers. These works mainly prove the concept of using logic circuits as gas sensors, however, they do not provide insight into the gas sensing response as a function of concentration.

Based on previous work done in our group, we focus here on the design and sensing performance of a gas sensor only based on SWCNT-FET technology, where an inverter configuration is used to simplify both the measurement chain and data analysis. Our device is based on the combination of an active p-type SWCNT-FET sensor with an n-type SWCNT-FET device to obtain a complementary metal-oxide-semiconductor (CMOS) logic inverter. The operation principle of SWCNT-based inverters is similar to that of conventional silicon devices,<sup>35,36</sup> but the implementation of a CNT as the conduction channel promises to outperform the existing MOSFETs since CNT-FET devices allow device miniaturization.<sup>37,38</sup> Interesting works have been done in order to compare the performances of

conventional MOSFETs and SWCNT-FET devices.<sup>39-41</sup> In the inverter configuration, when an adapted input gate voltage is applied, we obtain an output signal in the form of a voltage (of the order of one volt), much easier to measure than low current values. Moreover, by applying an alternating input signal presenting a square shape, we also limit the shape of the electrical response of the device under gas exposure to a modification of the amplitude of the square output signal. Here, the sensor is built for  $NO_2$  gas detection, taking advantage of the reactivity of pristine SWCNTs to this gas,<sup>17,42</sup> but the general developed method could be extended to the detection of other gas analytes using for instance specially functionalized SWCNTs.

## Results and discussion

SWCNTs were synthesized on  $SiO_2/Si$  wafers (200 nm thermal oxide layer), from an iron-ruthenium (Fe-Ru) catalyst through hot filament chemical vapor deposition (HF-CVD) following a previously described process.<sup>43</sup> 40 nm thick palladium electrodes were subsequently deposited on the as-synthesized SWCNTs using a standard UV lithography process<sup>44</sup> (see Fig. S1 and S2†). The characterization, stability and reproducibility of the response of our p-type SWCNT-FETs under  $NO_2$  exposure have been reported in a previous work,<sup>42</sup> nevertheless the stability and reproducibility of the response under gas exposure have been tested and are described in the ESI in Fig. S6.† After exposure to  $NO_2$ , the device was exposed to a constant flow of  $N_2$  to desorb the  $NO_2$  molecules until the recovery of its initial characteristic (see Fig. S4†), as shown previously.<sup>42</sup> In order to obtain n-type transistors, polyethyleneimine (PEI) was used to cover some of the transistors (see Fig. S2†), following the process reported by Shim and coworkers.<sup>45</sup>



The electrical measurements were performed using a semiconductor parametric Keithley 4200-SCS analyzer.

A fixed voltage bias  $V_{DD}$  was applied between the drain electrodes of the p-type and n-type transistors ( $V_{DD} = V^+ - V^-$ , see schematic in Fig. 1a).

In this study, three different values of  $V_{DD}$  are used: 2 V, 4 V and 6 V. A square input voltage ( $V_{in}$ ) of (-11 V; -4 V) was applied simultaneously to the gate of the two devices and the output voltage ( $V_{out}$ ) was measured.

Gas exposure was performed using a Owlstone gas generator (OVG-4) based on permeation tube technology in a homemade hermetic chamber previously described<sup>42</sup> (see also Fig. S3†). Two permeation tubes purchased from Owlstone (with permeation rates of 2425 ng min<sup>-1</sup> and 102 ng min<sup>-1</sup>) operating between 30 °C and 40 °C were used. The inserted gas was diluted in N<sub>2</sub> in order to set the concentration. The temperature of the inverter was set to 20 °C using a Huber mini-stat 125 and an atmospheric pressure was imposed in the homemade gas chamber.

We design a CMOS inverter based on SWCNT-FETs for gas detection. The CMOS inverter is based on the combination of a p-type FET device and an n-type FET device, the  $I_{ds}$ - $V_g$  curves of the two transistors used in the device are presented in Fig. S2.† In order to obtain a logic inverter, the two devices are associated following the schematic view detailed in Fig. 1a. Ideally, only one of the devices (n or p) is activated as a function of the applied gate voltage ( $V_{in}$ ). For instance, in the device presented in Fig. 1b, for an applied  $V_{DD}$  of 2 V (light blue curve), we observed that from -20 V to -15 V the n-type SWCNT-FET is blocked (Fig. 1b zone I), while from -5 V to 0 V the p-type SWCNT-FET is blocked (Fig. 1b zone III).

Preceding studies in the literature show that SWCNT-FET devices are extremely sensitive to the presence of NO<sub>2</sub>.<sup>14,16,17,22</sup> In the specific case of our p-type FET devices, we previously reported<sup>42</sup> that under NO<sub>2</sub> exposure (in comparison to N<sub>2</sub> exposure), a large increase of the current ( $I_{ds}$ ) flowing in the device was observed in the depletion regime (see Fig. 2a). Here, the n-type device is not sensitive to the presence of NO<sub>2</sub> within the concentration range that we used (0.6–10 ppm), thanks to the voluntarily thick PEI film which acts as a hermetic barrier preventing interaction between the environment and both the SWCNTs and the SCWNTs/metal contact junction (see Fig. 2b). Looking carefully at the  $I_{ds}$ - $V_g$  characteristic of our p-type device under NO<sub>2</sub> atmosphere (see Fig. 2a), a negligible change is observed in the accumulation regime while a huge modification of the flowing current is observed in the depletion regime ( $V_g$  between 5 and 20 V).

Then, to increase the response of the NO<sub>2</sub> detection in the inverter configuration, a working point where the p-type device is blocked must be chosen (*i.e.*  $V_{out} = V^-$ ). Also, to avoid charge accumulation in the transistors of the inverter, an optimized square voltage that forces the current to flow alternatively in the n-type and the p-type device needs to be applied. According to the technical limitation of the square voltage generator used in this study, we applied an input voltage ( $V_{in}$ ) of alternatively -4 V and -11 V. For the three  $V_{DD}$  used, at -4 V, the p-type FET is blocked, and at -11 V, the inverter is in an intermediary regime

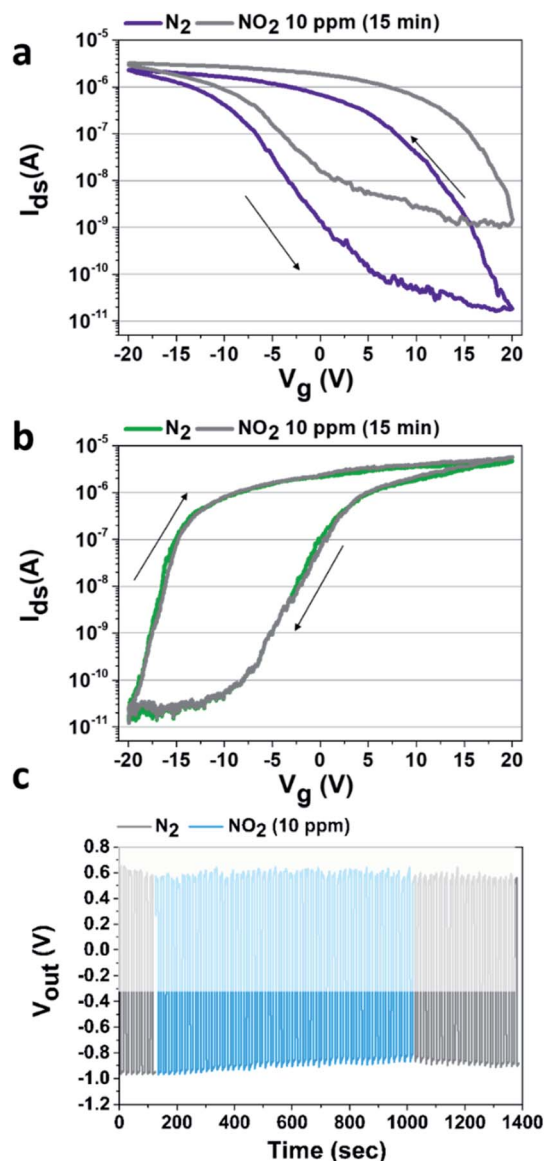


Fig. 2 (a)  $I_{ds}$ - $V_g$  characteristic of the p-type SWCNT-FET before and after 15 min of NO<sub>2</sub> exposure ( $V_{DS} = 1$  V). (b)  $I_{ds}$ - $V_g$  characteristic of the n-type SWCNT-FET before and after 15 min of NO<sub>2</sub> exposure ( $V_{DS} = -1$  V). (c) Output voltage as a function of time of the inverter under an exposure of NO<sub>2</sub> gas for a square input voltage for alternatively -4 V and -11 V ( $V_{DD} = 2$  V).

which is unstable as shown on Fig. 1b and c but allows to limit the charge accumulation. This latter regime will not be taken into consideration for the rest of the study. Another square generator could be used in order to reach the off regime of the n-type device, but this unstable state is not a limiting factor for this study. Consequently, as the NO<sub>2</sub> concentration increases, the p-type device becomes less and less effective to block the current, then at the chosen  $V_{in}$  (-4 V) the p-device is no longer blocked, leading to a modulation (here a decrease in absolute value) of the output voltage, depending on NO<sub>2</sub> concentration as shown in Fig. 2c where the response of the inverter (shown here for  $V_{DD} = 2$  V) has been recorded as a function of time during



the exposure to a constant flow of NO<sub>2</sub>. After the defined exposure (here 15 minutes), the device is exposed to a constant flow of pure N<sub>2</sub> leading to the desorption of the NO<sub>2</sub> molecules.

The desorption of the NO<sub>2</sub> molecules as a function of time is already visible on Fig. 2c, with the progressive decrease of  $V_{out}$  to reach again the initial  $-1$  V for a  $V_{in}$  of  $-4$  V. Before re-starting a new experiment, the complete desorption of the NO<sub>2</sub> molecules is controlled by recording the  $I_{ds}-V_g$  curve of the p-type SWCNT-FET device.

The desorption is considered complete when the  $I_{ds}-V_g$  curve recovers its initial state (*i.e.* before NO<sub>2</sub> exposure as shown in Fig. S4†). For the highest concentration used in this study (10 ppm), the complete desorption requires 5 hours. Of course for real-life applications, the time to recover the device to its initial state is too long. But this process can be sped-up as was previously reported, by heating the device with an integrated hot-plate<sup>46</sup> or using UV light<sup>47</sup> to induce molecule desorption.

Fig. 3a presents the output voltage of the inverter at different  $V_{DD}$  values, after an exposure to various concentrations of NO<sub>2</sub> gas, as a function of time. For all the tested concentrations (from 0.6 ppm to 10 ppm), we observe a decrease of the output voltage absolute value, indicative of gas detection (see Section 6 of the ESI† for the full curves).

These results are in agreement with previous results, where we showed that the particular design of our transistor device allows the detection of very low concentrations of gas.<sup>42</sup> Nevertheless, it is important to highlight that adopting the inverter configuration slightly hinders the sensitivity of the device in comparison to our previously reported single SWCNTs-FET devices.<sup>42</sup> However, the LOD for the inverted device is still

reasonably comparable to other SWCNT-FET based gas sensors<sup>21,24,26,48-52</sup> (see Table S4 in the ESI†). Moreover, the inverter configuration enables simpler data measurement and analysis, a possible miniaturization of the complete measurement chain of the sensor, and low power consumption. Then, for a further specific application a trade-off between the device ease of implementation has to be assessed against the sensitivity and LOD.

Following these results, we can safely claim that the use of an inverter configuration simplifies the sensing technology preserving the ability to detect low NO<sub>2</sub> gas molecules concentrations.

In order to obtain a calibration curve of our device, we calculate the response for each concentration after a continuous exposure to NO<sub>2</sub> for 15 min for three different  $V_{DD}$  values.

The response ( $R$ ) is defined by the following eqn (1):

$$R = \frac{(V_{out}[NO_2] - V_{out}[N_2])}{V_{out}[N_2]} \times 100\% \quad (1)$$

where  $V_{out}[N_2]$  represents the average value of the output voltage obtained for an input voltage at  $-4$  V under a N<sub>2</sub> constant flow, and  $V_{out}[NO_2]$  the average value of the output voltage obtained for the same input voltage after 15 min of exposure to a defined concentration of NO<sub>2</sub> (see Fig. S5†).

For both voltage values ( $V_{out}[N_2]$  and  $V_{out}[NO_2]$ ), the error is defined as the standard deviation of the average, the error on the response is then calculated from those errors by propagation.

As shown on Fig. 3b, the logic inverter response ( $R$ ) as a function of NO<sub>2</sub> concentration ( $C$ ) can be fit by a power law in

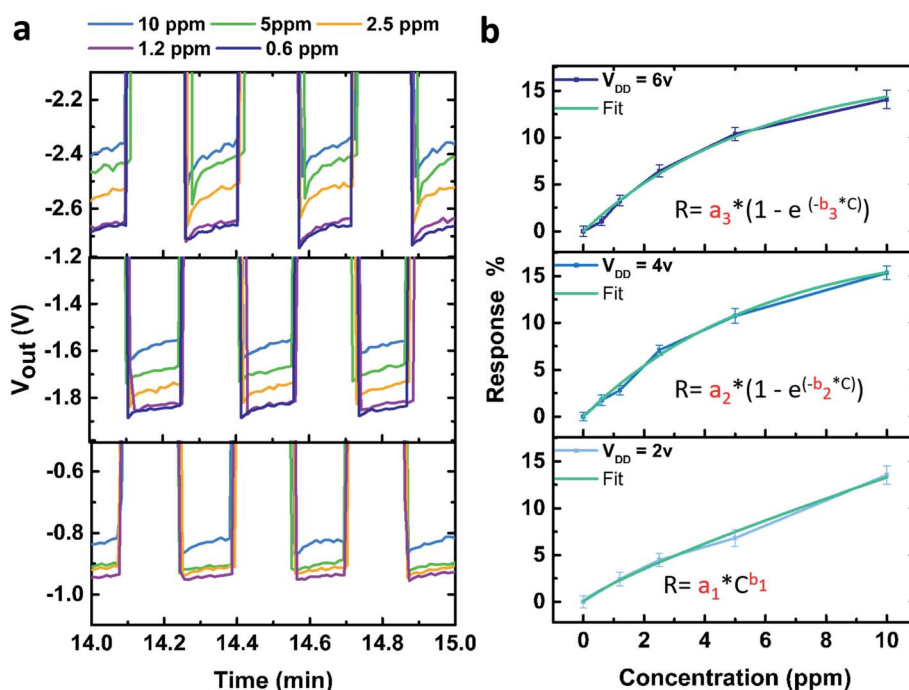


Fig. 3 (a) Output voltage of the inverter after exposure to various concentrations of NO<sub>2</sub> gas for a  $V_{in}$  at  $-4$  V and  $-1$  V alternatively for three different  $V_{DD}$  values (from top to bottom:  $V_{DD} = 6$  V, 4 V and 2 V). (b) Response of the inverter as a function of NO<sub>2</sub> concentration for the three different  $V_{DD}$  values (from top to bottom:  $V_{DD} = 6$  V, 4 V and 2 V).



**Table 1** Fit parameters for the fit  $R = f(C)$  where  $R$  is the response and  $C$  the  $\text{NO}_2$  concentration in ppm

$V_{\text{DD}}$ (V)	$a_i$	$b_i$	$r^2$
2 ( $i = 1$ )	$1.974 \pm 0.248$	$0.828 \pm 0.0632$	0.991
4 ( $i = 2$ )	$18.833 \pm 1.626$	$0.170 \pm 0.025$	0.994
6 ( $i = 3$ )	$17.473 \pm 1.968$	$0.173 \pm 0.032$	0.991

**Table 2** Limit of detection of the device for different  $V_{\text{DD}}$  values

$V_{\text{DD}}$ (V)	LOD (ppm)	Error (ppm)
2	0.92	0.20
4	0.57	0.14
6	0.56	0.21

the configuration  $V_{\text{DD}} = 2$  V, similar to other reported semiconductor based gas sensors that detect oxidizing gases,<sup>53</sup> on the other hand, for  $V_{\text{DD}} = 4$  and 6 V an exponential relation was chosen as a much better fit was obtained using this function. The results of the different fits are detailed in Table 1.

The LOD of the gas sensor device was also calculated for the three  $V_{\text{DD}}$  used, and are presented in Table 2, the details of the calculation can be found in the ESI† (Section 7). As shown in Table 2 for  $V_{\text{DD}} = 4$  and 6 V, the obtained LODs are  $0.57 \pm 0.14$  ppm and  $0.56 \pm 0.21$  ppm, respectively while for  $V_{\text{DD}} = 2$  V a LOD of  $0.92 \pm 0.20$  ppm is found. Then, for the detection of low concentrations, the inverter should be used at  $V_{\text{DD}} = 4$  V or 6 V where similarly low LODs were obtained.

According to the fits of the calibration curves shown in Fig. 3b for the three different working voltages, we expect rapidly occurring plateau for  $V_{\text{DD}} = 4$  V and  $V_{\text{DD}} = 6$  V (extrapolating the fits for these two curves leads to an asymptotic behaviour), whereas this is not the case for  $V_{\text{DD}} = 2$  V. Using the device at  $V_{\text{DD}} = 2$  V will therefore be more appropriate when detecting higher gas concentrations, however, further studies at higher concentrations must be performed to verify these assumptions.

## Conclusions

To summarize, we have demonstrated that a CMOS inverter fabricated by associating a p-type SWCNT-FET with an n-type SWCNT-FET could be used as an efficient sensor for  $\text{NO}_2$  gas. The main advantage of the system relies on a huge simplification of the measuring system as a simple voltmeter can be used to detect the presence of gas, even for very small concentrations. We also showed that by changing the applied voltage  $V_{\text{DD}}$  the sensitivity of the device can be tuned for measurement in different concentration ranges. Moreover, we show that a calibration of the device can be achieved, showing its potential for implementation in electronic readouts thanks to its compatibility with a further analog to digital conversion.

This generic process could also be generalized to detect other gases using for instance functionalized SWCNTs in order to increase the specificity of the response. The introduced

approach opens new frontiers in the SWCNT gas sensing field, the innovative core of the present work relies on simply measuring the output as a tension instead a low current. Moreover, here a logic inverter with two transistors has been explored, but the same principle can be expanded using a resistance device, or alternatively using other logic device configurations such as flip-flop memory cells or ring oscillators.<sup>35</sup>

## Conflicts of interest

There are no conflicts to declare.

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