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Purity of organic semiconductors as a key factor for the performance of organic electronic devices†

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Organic semiconductors offer great promise through their ease of synthesis in a multitude of derivatives, their low temperature processability and their amenability for fabrication of flexible, stretchable and conformable, even imperceptible devices. Nevertheless, the low charge mobility of organic semiconductors remains one of the limiting factors in delivering high performance for organic electronic devices comparable to their inorganic counterparts. In this study, the effects of purification of eight different organic semiconductors (*i.e.* n-type, p-type, and ambipolar) were determined by means of analyzing their performance improvement in organic field effect transistors. For this purpose, three purity grades of each organic semiconductor were investigated, and devices were fabricated in an identical fashion. It was found that temperature gradient sublimation improves considerably the quality of the organic semiconductors. The results presented here indicate that the purity of the organic semiconductor is a key parameter to be considered in order to achieve high performance for the field of organic field effect transistors.

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1. Introduction

In the last two decades, a large effort has been directed towards integration of consumer electronics into emergent “embedded” electronics such as wearable/implantable electronics and internet of things, in other words industry 4.0. The most recent developments include on-tissue sensors, implantable and ingestible medical devices, environmental monitoring, and disposable plastic electronics such as smart food packaging, RFID tags, plastic cards, e-tickets, *etc.*^{1–15} All these devices require abundant and inexpensive materials and fabrication technologies supporting their sustainable development. Organic semiconductors are interesting through their synthetic versatility in high throughput at low cost of production. For achieving the coveted goal of “green” and sustainable electronics development, one can tailor and make these materials biocompatible and biodegradable.^{1,2} The large-scale electronic waste produced in the world is creating trouble in the supply chain of rare earth materials as well as plastic waste contamination in oceans, due to packaging materials in general. Both these unfortunate events can be faced successfully if we introduce biodegradable organic semiconductors, substrates and plastics. However, these materials

have to be competitive to the performance requirements. Even though there are large areas of applications which do not need high frequency, high speed transistors and circuits, the mobility of organic semiconductor materials is still lower than their inorganic counterparts. Ultimately, the performance of the selected materials in the functional device will always be a competitive selection parameter parallel to their bio- and eco-friendliness.³

The design of new materials with required functional properties has always been a topical issue in Materials science.^{4–15} It includes both development of useful design rules and reproducible fabrication of new materials, which is particularly important for organic semiconductors to allow for a comprehensive understanding of their structure–property relationships. While studies have shown the great potential of organic materials in attaining excellent performance in optoelectronic devices, combining several functional properties in a single material able to match inorganic counterparts remains challenging.^{16–20} The organic field effect transistor (OFET) design is remaining still an active research area in organic electronics.^{19,21–26} In OFETs, every component layer, *i.e.* the substrate, dielectric, organic semiconductor, contact electrodes, and encapsulation layers, the choice of their materials as well as their geometries, is significant to the ultimate performance of the device.

Back in 2011 there were already crystalline OFETs with the mobility values exceeding those of amorphous silicon^{27–29} which was the main competitor in the past.²¹ Nevertheless, reaching

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† This paper is dedicated to Prof. Fred Wudl on the occasion of his oncoming 80th birthday.



values of field effect mobility in excess of $10 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ remains a tremendous challenge for the design and synthesis of novel organic semiconductors with increased functionalities,^{20,30} whereas attaining operation frequencies in advanced circuits design in the range of GHz seems possible, but rather difficult to materialize at the same time.³¹ We argue that all the benefits of inorganic semiconductors can be attributed to three major features of these materials: (i) their extremely high purity, (ii) their long-range order and (iii) their superior coupling through very strong (covalent) bonds. As a matter of fact, all the above beneficial characteristics that represent the cutting edge and the success of inorganic electronics are missing for the classic, *i.e.* weakly van der Waals bonded, organic semiconductors, where levels of purity of 99% or even lower are tacitly considered as acceptable for the fabrication of organic electronic devices. It is rather surprising that there is to the best of our knowledge no systematic study towards the elucidation of the role of purity of the organic semiconductors for the recorded performance of the fabricated thin film electronic devices. More often than not, organic electronic devices are fabricated with the “as received” materials carrying the declared purity by the chemical supplier,³² this event being obvious from the mere omission of the purification step of the organic semiconductor in the Experimental section of nearly all publications in the field.^{33–36} Nevertheless, when the effort of purification was invested, then the results improved impressively, with the recorded mobility of the organic semiconductor reaching record values in excess of $5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ for single crystal based OFETs with rubrene.³⁷ Importantly also, time of flight measurements at low temperature of ultra-pure oligomeric systems of organic semiconductors showed that mobilities of $10 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ at room temperature and of several hundred $\text{cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ at low temperature are possible to achieve.³⁸

In this work, we tackle the issue of the materials' purity and its influence on organic electronic devices systematically. We compared here the effect of different materials as well as different purification degrees respectively, on the performance of fabricated devices, to demonstrate systematically the influence of this purification *versus* performance relationship. We fabricated organic field effect transistors as a tool of evaluation of various purity grades of selected organic semiconductors. For this study a large pool of organic semiconductor materials, with n-, p-, and ambipolar-type charge transport were investigated. The selected semiconductors for this study were: fullerene (C_{60}), pentacene, copper phthalocyanine, indigo (vat blue 1), Tyrian purple, epindolidione, quinacridone and indanthrene blue RS (vat blue 4), the latter 5 molecules belonging to the group of hydrogen bonded (bio)-organic semiconductors.⁴⁶ We used for the experimental, three grades of purity for each organic semiconductor and fabricated OFETs with the respective materials under identical thin film deposition conditions. The method of purification of the organic semiconductors investigated in this study was the train sublimation method, described previously in detail in other studies.^{37,39–44} Although the benefit of purification of organic materials for the improvement of their performance was addressed elsewhere,^{37,39–44} the quantitative

results of various purification grades of organic semiconductors of this study clearly and systematically demonstrate that the mobility of charge carriers can be increased orders of magnitude alongside improving other device parameters.

2. Experimental

A photograph of one of the several vacuum sublimation ovens available in our laboratory is presented in Fig. 1. In the particular case displayed in Fig. 1, the sublimed material was quinacridone. The photograph shows by arrows the three important zones of the differential heating oven used for sublimation of the organic semiconductor. Several purification steps are necessary for obtaining a high-quality material, as will be demonstrated throughout this work.

For the scope of this study, each semiconductor material was analyzed in three purity grades: an unpurified grade (*i.e.* as-received grade, denoted here as $0\times$), a one-time purified grade (denoted as $1\times$ in this study) and a three-times purified grade (denoted as $3\times$). The only exception was Tyrian purple which was synthesized in-house through a previously reported method.⁴⁵ In this particular case, the entire extracted material was purified scrupulously *via* column chromatography extraction, and this starting material for our study is designated here as $1\times$. The successively purified grades employed in this study were $2\times$ and $3\times$ for Tyrian purple.

Sublimation purification of all the semiconductor materials used throughout this study was carried out using a quartz vacuum tube at a pressure below 1×10^{-6} mbar. A series of borosilicate glass tubes were inserted into the quartz tube and served both to confine the source material and to serve as a vehicle of retrieving the sublimed material. Each material sublimed in a 24 h period at an individual temperature ranging from 250°C to 400°C and has been purified by up to three such successive temperature gradient sublimations. Each “as-received” semiconductor was subjected to three successive train sublimation steps in a horizontal vacuum oven, and the purest material was collected after each purification step. The materials collected after the one time and three times sublimation were the ones used in this study, alongside the “as-received” grade.

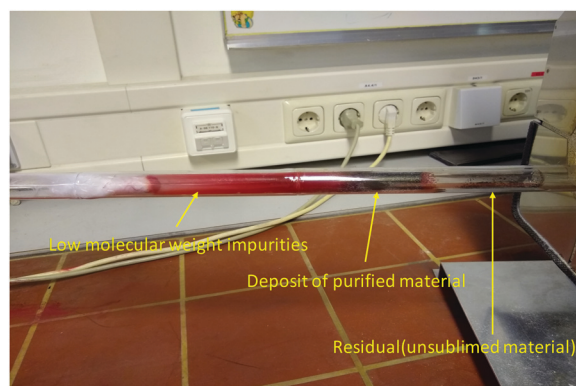


Fig. 1 Photograph of the train sublimation oven showing the sublimed material in the vacuum tube.



Fullerene (C_{60}) was purchased from Aldrich, product number MKBR 8207 V, having a labeled purity of 99.9%. Pentacene, was purchased from Aldrich, product number P-1802. Copper phthalocyanine was purchased from Sigma, product number 252980. Quinacridone was purchased from TCI Europe, product number Q0057. Epindolidione was purchased from 1-Materials, product number HW1020P1. Indanthrene Blue RS was purchased from TCI Europe, product number I0429. Indigo was purchased from Aldrich, product number 229296, having a labeled purity of 95%. Tyrian purple was synthesized “in-house” following the published synthetic routes.⁴⁵

When fabricating the devices with each particular grade of purity, we obsessively obeyed all the 9 prerequisites of the reproducibility of results detailed in ref. 46. In short, all devices in this work were fabricated in the same Physical Vapor Deposition (PVD) System, using identical recipes of deposition (vacuum level, temperature ramp, deposition rate, *etc.*), and in an identical transistor geometry by employing the same shadow masks for the semiconductor structuring, as well as for the gate, source and drain electrode depositions, respectively. Importantly also, there was in this study no attempt made to optimize the results in the sense of adapting the rate of semiconductor deposition (either lower or faster), or play with the substrate heating in order to improve the performance; we used a standard recipe for all eight materials, and fabricated one batch only of 24 transistors for each purity grade of each material in the study. Importantly also, we used an identical dielectric for all materials in this study, namely a bi-layer of inorganic–organic dielectric consisting of electrochemically grown aluminum oxide (AlOx) of 34 nm thickness and a capping layer of tetratetracontane ($C_{44}H_{90}$ or TTC as acronym) of 20 nm thickness. The anodization method is a standard method employed in our laboratories and was previously described elsewhere.^{47,48} The thickness of the aluminum gate electrodes that were subjected to anodization was 100 nm for all the devices employed in this study. The respective layer of aluminum was evaporated at a very fast rate of $3\text{--}5\text{ nm s}^{-1}$, which proved beneficial for generating a very smooth layer.⁴⁹ We used for anodization in this work a recipe that employed passing to the sample to be anodized a step voltage up to a maximum of 20 V, while maintaining in this process a steady current of 15 mA. After reaching the 20 V compliance, the sample was allowed to slowly continue the anodization until the final current read $4.5\text{ }\mu\text{A}$, which translated to a typical waiting time of about 6–7 min per sample, in our gate geometry. Using this method, as a rule of thumb, one obtains a typical thickness of the anodized layer as

$$d = a(V - V_{\text{ox}}),$$

where a is the oxide forming factor, which has a value of 1.6 for aluminum; V is the applied maximum voltage of anodization and V_{ox} is the voltage necessary to generate the native oxide (*i.e.* the layer that forms naturally when aluminum is exposed to ambient air), and its value is $V_{\text{ox}} = \sim -1.35\text{ V}$ for aluminum.⁵⁰ The measured bi-layer capacitance in a Novocontrol Impedance Analyzer instrument matched very well the calculated capacitance of the combo layer of $\sim 34\text{ nm AlOx}$ ($\sim 32\text{ nm}$

electrochemically grown plus $\sim 2\text{ nm}$ native) and 20 nm TTC , and the respective value of 77.77 nF cm^{-2} was used therefore for all the transistor related calculations in this work. For all transistors in this work, we evaporated the 20 nm tetratetracontane aliphatic capping layer at a rate of deposition between 0.7 and $1\text{ }\text{\AA s}^{-1}$, and performed an *in situ* annealing at $65\text{ }^{\circ}\text{C}$ for 30 min inside the PVD system prior to the semiconductor deposition. This annealing step proved necessary for smoothing the dielectric and allow for a better coverage of the aluminum oxide layer, as we demonstrated and reported before for several other dielectrics.^{51–54} All the semiconductors were evaporated with an identical recipe, at a rate of $0.2\text{--}0.3\text{ }\text{\AA s}^{-1}$ to account for a final thickness of 60 nm . Top contact electrodes (aluminum in the case of the devices with C_{60} and gold for all the others) were deposited at various deposition rates: 0.1 nm s^{-1} for the devices with C_{60} and pentacene, and between 0.7 and 1 nm s^{-1} for the rest of the semiconductors in this study. In this respect we showed previously that faster deposition rates of gold contact electrodes are desirable for the hydrogen bonded organic semiconductors.^{46,52,55–58} All the measurements of the fabricated OFETs were carried out with the aid of a probe station situated in a glove box under a nitrogen atmosphere.

3. Results

3.1 Organic field effect transistors

The typical device structure utilized in our laboratory employs a staggered bottom gate top contact structure, like the one presented in Fig. 2, with the component layers marked on the figure.

The investigated key parameters of organic field-effect transistors are: the operating voltage, the semiconductor charge carrier mobility, the on/off current ratio, the subthreshold swing and finally the threshold voltage and its stability during operation.⁵⁹ Fig. 3 presents an example of a field effect transistor of high mobility fabricated previously in our laboratories.⁶⁰ It is a device fabricated in a bottom gate-top contact geometry like the one schematically presented in Fig. 2, with adenine capped aluminum oxide as the dielectric layer and fullerene C_{60} as the organic semiconductor. The graph in Fig. 3(a) shows the recorded transfer characteristics of the

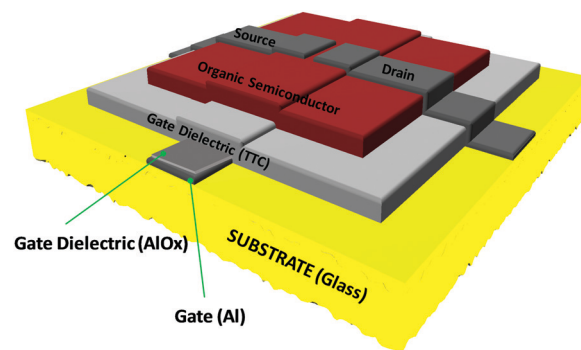


Fig. 2 OFET device architecture in the bottom gate-top contact configuration.



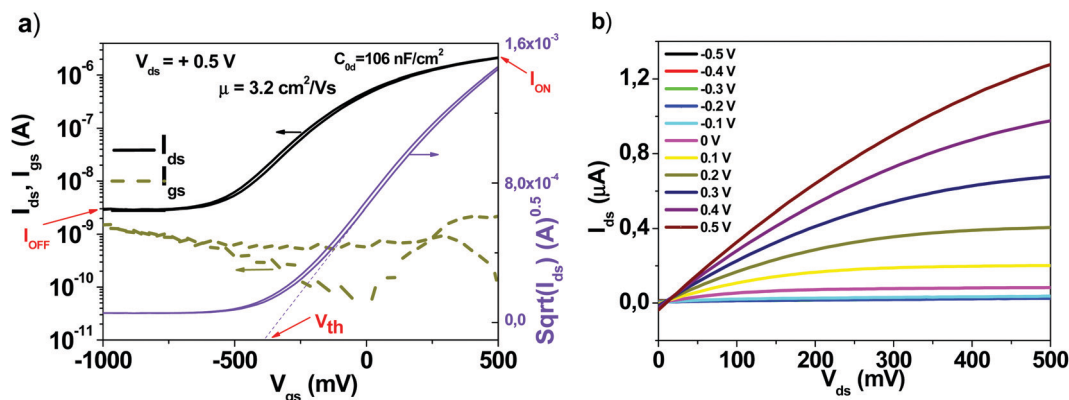


Fig. 3 (a) Transfer and (b) output characteristics of an organic field effect transistor which was part of the same batch as the one presented in ref. 60, as Fig. 4(c and d). Adapted with permission from ref. 60, G. Schwabegger, *et al.*, High Mobility, Low Voltage Operating C_{60} based n-type Organic Field Effect Transistors, *Synth. Met.*, 2011, **161**, 2058–2062.

OFET, *i.e.* the measured I_{ds} vs. V_{gs} at constant V_{ds} voltage, while the one in Fig. 3(b) shows the corresponding output characteristics of the OFET, *i.e.* the curves measured as I_{ds} vs. V_{ds} at constant V_{gs} voltages. The important parameters of the device, the ON/OFF ratio and the threshold voltage (V_{th}) are displayed in the transfer characteristics graph. The average charge mobility of the organic semiconductor in the fabricated batch of which the OFET displayed in Fig. 3 belongs was $3.2 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$.

The ON/OFF ratio is the ratio between the current in no conduction mode (marked as I_{OFF} on the graph) and the maximum current attained in the operation (conduction mode, marked as I_{ON} on the graph). In the particular example of the device presented in Fig. 3, this ratio is coming close to 1000, or 10^3 . The threshold voltage is determined as the intercept on the horizontal axis of the tangent drawn to the linear part of the square root of the I_{ds} vs. V_{gs} line in the saturation regime of the transistor (marked as V_{th} on the graph). For mobility calculations when a straight line is not an accurate fit to the $\sqrt{I_{ds}}$ we fitted the line at higher gate voltages. We determined the start and end points of the subthreshold swing from the best straight line fit to a gate voltage interval on the $\log(I_{ds})$ vs. V_{gs} plot for lower gate voltages than the threshold voltage as discussed in the review paper of Newman *et al.* (ref. 59).

The field effect mobility of the semiconductor in the saturation regime is extracted from the equation:⁵⁹

$$I_{ds,sat} = (W/2L)C_{od}\mu_{sat}(V_{gs} - V_{th})^2,$$

whereas the subthreshold swing (S_{sw}) is the inverse of the subthreshold slope (S_{sl}), and can be extracted from the equation:⁵⁹

$$S_{sw} = 1/S_{sl} = dV_{gs}/d(\log I_{ds}),$$

while the normalized subthreshold swing (S_{sn}) is given by the equation:

$$S_{sn} = S_{sw} \times C_{od}$$

In the above equations $I_{ds,sat}$ represents the drain current in the saturation regime, W and L are the geometric factors of the

channel (length, L and width, W , respectively), C_{od} is the specific capacitance of the dielectric, μ_{sat} is the field effect mobility of the organic semiconductor in the saturation regime, V_{gs} and V_{th} are the applied gate voltage and the threshold voltage, respectively, and S_{sw} is the subthreshold swing, expressed in volts per decade of current (*i.e.* V dec^{-1}). The subthreshold swing indicates how fast the OFET device switches from the OFF to the ON state. It is worth mentioning that the normalized subthreshold swing is a more convenient method to compare the performance of organic semiconductors in OFET devices, when deposited on various dielectrics or on dielectrics of different thicknesses.⁵⁹ A large value of the subthreshold swing indicates a large concentration of shallow traps, *i.e.* a slow and non-sharp turn-on region. Given the fact that we have in this study the same dielectric but different purity grades of organic semiconductors, the normalized subthreshold swing values are meaningful as a method of comparison of the performance of dielectric–semiconductor interface within the three purity grades of each semiconductor material.

The schematic of the semiconductor molecules employed in this work is presented in Fig. 4.

3.2 Fullerene, C_{60}

The schematic of the C_{60} molecule is presented in Fig. 4. We fabricated devices with fullerene (C_{60}) and compared the three purity grades mentioned above, *i.e.* $0\times$, $1\times$ and $3\times$ in OFET devices. It is interesting to note that although the original ($0\times$) material had a declared purity of 99.9% according to the label on the material package of the chemical supplier (Aldrich), the average mobility obtained for the 24 transistors fabricated was in the range of $5 \times 10^{-5} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ (black line in Fig. 5).

The one-time sublimed material showed a more decent performance, with mobility values already in the range of $1 \times 10^{-2} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. One sublimation of fullerene resulted therefore in a 200-fold increase in mobility, while three successive purification steps on the other hand counted for another 4-fold increase in the organic semiconductor mobility compared to the one-time purified grade (in total almost three orders of



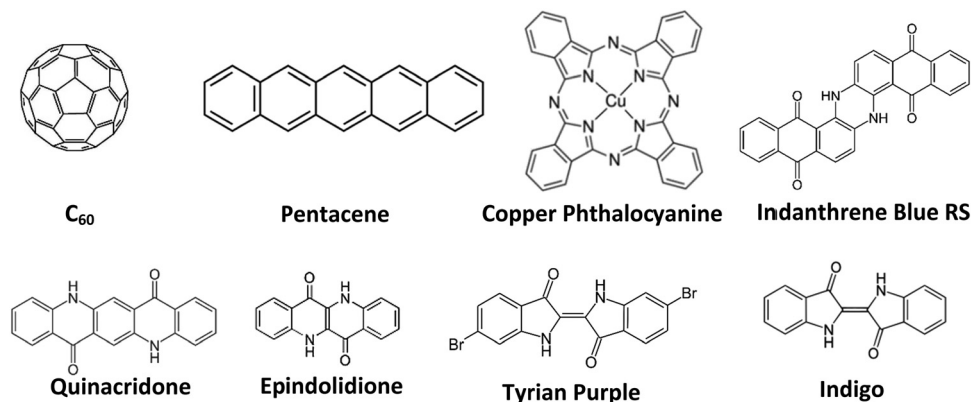


Fig. 4 Schematic of the molecules employed in this study.

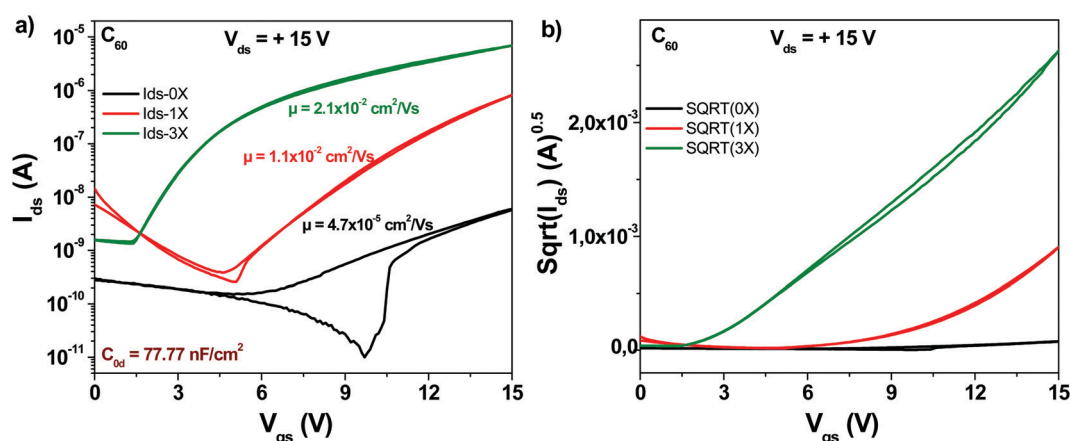


Fig. 5 (a) Comparison of OFET devices with three different grades of fullerene (C₆₀) semiconductor purity: unpurified (0× in graph legend), one time purified (1× in graph legend) and three times purified respectively (3× in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

magnitude compared to the unpurified grade). It is interesting to note also that three steps of material purification decreased the subthreshold swing of the transistor from 4.8 to 1.1 V dec⁻¹. Full details over the transistor's parameters (subthreshold swing, subthreshold slope, ON/OFF ratio and normalized subthreshold swing) are offered in Table 1.

3.3 Pentacene

The schematic of the pentacene molecule is displayed in Fig. 4. The OFET transistor characteristics are presented in Fig. 6. Different than C₆₀ though, the one-time purified grade does not show a major improvement in transistor mobility as compared to the unpurified grade, *i.e.* only a 4-fold increase, from 1.9×10^{-4} to 7.5×10^{-4} cm² V⁻¹ s⁻¹. Nevertheless, the three times purified grade of pentacene shows a marked increase in mobility that reached 2×10^{-2} cm² V⁻¹ s⁻¹ (100-fold compared to the unpurified grade). At the same time, the three times purified material showed a significant decrease of the subthreshold swing compared to the unpurified material, *i.e.* from 2.5 to 1.4 V dec⁻¹, with full details of the transistor measurements offered in Table 1.

3.4 Copper phthalocyanine

The schematic of the copper phthalocyanine molecule is displayed in Fig. 4. OFET devices employing the “as received” grade showed as in the case of fullerene and pentacene a modest mobility value, in the range of 2×10^{-5} cm² V⁻¹ s⁻¹ (see Fig. 7). However, one single step of purification resulted in a charge mobility increase of 35-fold, to 9×10^{-4} cm² V⁻¹ s⁻¹. Two more purification steps made possible attaining a mobility of 2×10^{-3} cm² V⁻¹ s⁻¹, in other words an 80-fold increase from the original, unpurified material. Notably, the subthreshold swing decreased from 4.1 V dec⁻¹ to 1.6 V dec⁻¹ after three sublimation steps. Full details of the subthreshold parameters as well as the ON/OFF ratios and threshold voltages for the OFETs featuring the three purity grades of copper phthalocyanine OFETs are presented in Table 1.

3.5 Indanthrene blue RS

Indanthrene blue RS (vat blue 4) employed in this study has the chemical structure presented in Fig. 4. Different from the previously described materials in this study, indanthrene blue



Table 1 OFET values for the semiconductors employed in this work. Legend acronyms are: e^- – electron; h^+ – hole; I_{on}/I_{off} – on–off ratio of the drain–source current; S_{sw} – subthreshold swing; S_n – normalized subthreshold swing; V_{th} – threshold voltage. The mobility values represent average values of 24 transistors in the respective batch. The departure from the average value of the batch is no greater than 10% for all transistors

Semiconductor	Purity grade	Mobility		I_{on}/I_{off}	S_{sw} (V dec $^{-1}$)	S_n (VnF cm $^{-2}$ dec $^{-1}$)	V_{th} (V)
		e^-	h^+				
Fullerene (C $_{60}$)	Unpurified	4.7×10^{-5}	—	18	4.9	381	8.7
	Purified*	1.1×10^{-2}	—	1.8×10^3	2.6	202.2	8.5
	Purified***	4.5×10^{-2}	—	5×10^3	1.2	93.3	0.75
Pentacene	Unpurified	—	1.9×10^{-4}	5.7×10^2	2.5	194.4	–4
	Purified*	—	7.5×10^{-4}	1.4×10^3	2.3	178.9	–3.5
	Purified***	—	2×10^{-2}	4.4×10^2	1.4	109	–1
Copper phthalocyanine	Unpurified	—	2.6×10^{-5}	39	4.1	318.5	–9.5
	Purified*	—	9.2×10^{-4}	1.1×10^3	1.9	147.8	–6.75
	Purified***	—	2.1×10^{-3}	3×10^3	1.7	132.2	–5
Indanthrene blue	Unpurified	1.5×10^{-4}	5.4×10^{-4}	79	3.7	287.7	–4.5
				37.2	2.7	210	–9.5
	Purified*	9.4×10^{-4}	5.9×10^{-3}	48	2.8	217.7	–4.5
				38	2.9	225.5	–5.5
	Purified***	2×10^{-3}	1.6×10^{-2}	55	2.4	186.6	–8.5
Quinacridone	Unpurified	—	4.7×10^{-5}	87.2	3.3	256.6	–9.5
	Purified*	—	1.3×10^{-3}	6×10^2	1.9	147.7	–8.75
	Purified***	—	9.8×10^{-3}	2.8×10^3	1.2	93.3	–6
Epindolidione	Unpurified	—	4.4×10^{-6}	3.5	11.3	878.8	–11
	Purified*	—	8.3×10^{-3}	1.3×10^4	2.2	171	–7.5
	Purified***	—	9.7×10^{-2}	3.5×10^4	1.9	147.8	–4.9
Tyrian purple	Purified*	6.5×10^{-5}	9.2×10^{-4}	13	4.6	357.7	–6
				303	2	155.5	–9.75
	Purified**	3.1×10^{-4}	2.1×10^{-3}	41	3.2	248.9	–7.5
				1.4×10^3	1.3	101.1	–9
	Purified***	6.8×10^{-4}	3.8×10^{-3}	63	2.8	217.7	–3
Indigo	Unpurified	2×10^{-3}	7×10^{-5}	2×10^3	1.1	85.5	–8
				3×10^3	2.7	235	8
	Purified*	3×10^{-3}	9×10^{-4}	7.8×10^3	1.7	148	5
				3.4×10^3	2.2	191.4	0.5
	Purified***	1.5×10^{-2}	1.2×10^{-2}	1.1×10^4	1.2	104.4	4.5
				5.9×10^3	1.3	113.1	–2

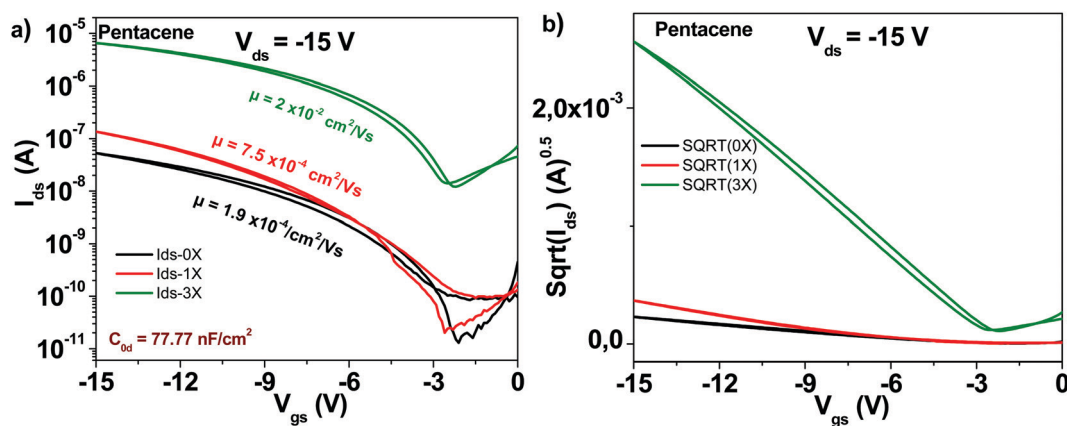


Fig. 6 (a) Comparison of OFET devices with three different purity grades of pentacene semiconductor: unpurified (0x in graph legend), one time purified (1x in graph legend) and three times purified respectively (3x in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

RS is an ambipolar semiconductor when it is deposited on top of aliphatic dielectrics (TTC in this study). Transistor characteristic results of OFETs with indanthrene blue RS semiconductor are displayed in Fig. 8. As in the case of other organic semiconductors employed in this study, three steps of sublimation proved beneficial for the generation of superior

transistor characteristics. Starting from an unpurified material with electron and hole mobilities in the range of $\sim 10^{-4} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, three purification steps increased the respective values to $\sim 2 \times 10^{-2} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ for the hole channel and $2 \times 10^{-3} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ for the electron channel, respectively. Notably, the subthreshold swing also decreased from 3.6 to



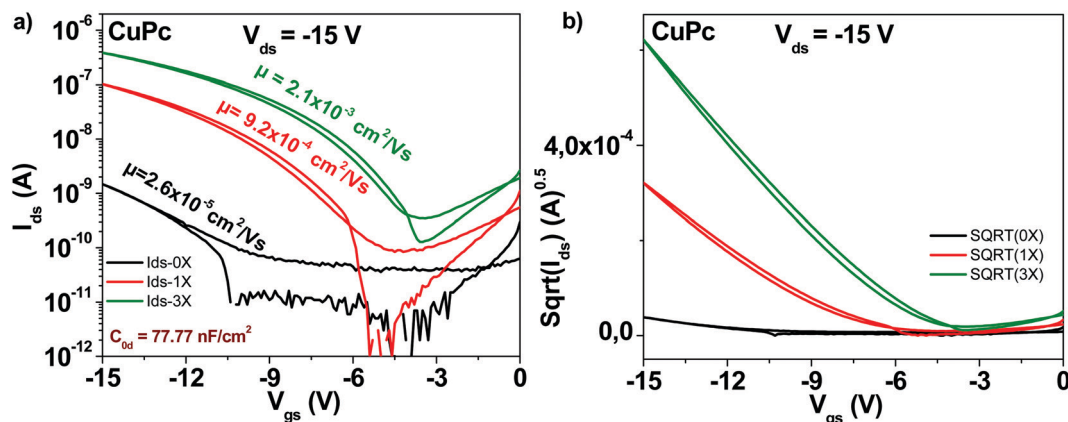


Fig. 7 (a) Comparison of OFET devices with three different purity grades of copper phthalocyanine semiconductor: unpurified (0× in graph legend), one time purified (1× in graph legend) and three times purified, respectively (3× in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

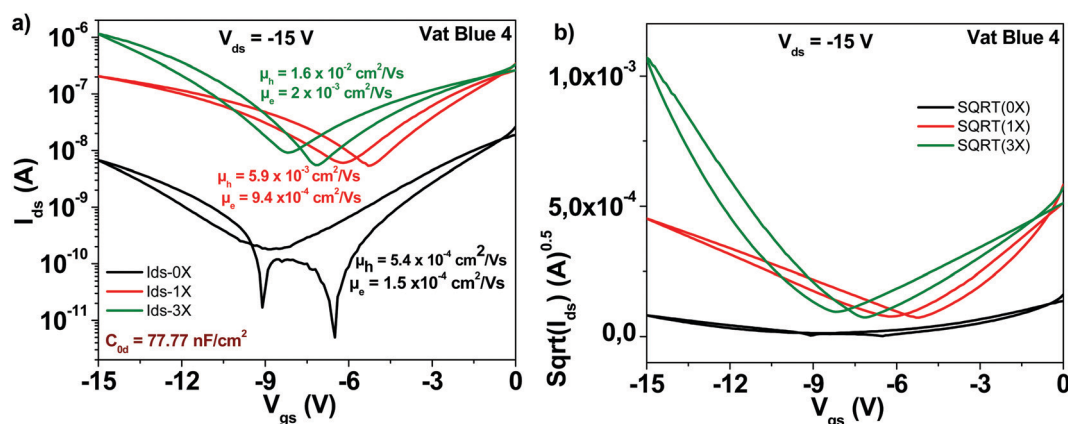


Fig. 8 (a) Comparison of OFET devices with three different purity grades of vat blue 4 semiconductor: unpurified (0× in graph legend), one time purified (1× in graph legend) and three times purified, respectively (3× in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

2.4 V dec⁻¹ for the electron channel and from 2.7 to 1.6 V dec⁻¹ for the hole channel, and full details of the indanthrene blue RS based OFET performance are presented in Table 1.

3.6 Quinacridone

The chemical structure of the quinacridone molecule is displayed in Fig. 4. In line with the findings for the previously described semiconductors, also quinacridone displayed a tremendous increase in mobility and decrease of subthreshold swing after three purification steps, see Fig. 9. Starting from a field effect mobility of $\sim 5 \times 10^{-5} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ for an unpurified, “as received” grade, quinacridone’s field effect mobility reached $\sim 1 \times 10^{-2} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ for the three times purified grade. At the same time, the subthreshold swing decreased from 3.2 to 1.2 V dec⁻¹ respectively, accompanied by an increase in the ON/OFF ratio from 87 to 2800. Full details of the transistor characteristic parameters are offered in Table 1.

3.7 Epindolidione

The chemical structure of the epindolidione molecule is displayed in Fig. 4. Among the studied materials in this work,

epindolidione was the one that produced the most remarkable improvement after being subjected to three steps of purification. Indeed, starting from very low field effect mobility of the “as received” material of $4.4 \times 10^{-6} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, epindolidione based OFETs almost reached the mobility of $0.1 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ after being subjected to three steps of scrupulous purification, or in other words an improvement in excess of 22 000. This tremendous mobility improvement was accompanied by a decrease of the subthreshold swing from 11.3 to 1.9 V dec⁻¹ and an increase of the ON/OFF ratio from 3.5 to 3.5×10^4 , literally a 10 000-fold improvement (Fig. 10). Full details of the transistor characteristics for epindolidione based OFETs with the three grades of analyzed purity are offered in Table 1.

3.8 Tyrian purple

A schematic of the Tyrian purple molecule (6,6′-Br-Indigo) is presented in Fig. 4. The results with OFETs carrying the three grades of pure Tyrian purple are displayed in Fig. 11. Given the good purity of Tyrian purple to start with (1×) in this study, it makes full sense that this material displayed the lowest improvement among all the other organic semiconductors



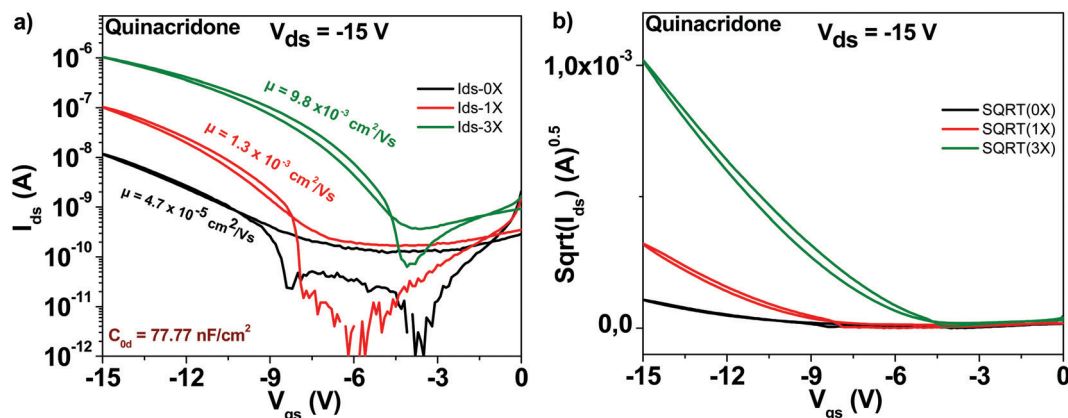


Fig. 9 (a) Comparison of OFET devices with three different purity grades of quinacridone semiconductor: unpurified (0× in graph legend), one time purified (1× in graph legend) and three times purified, respectively (3× in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

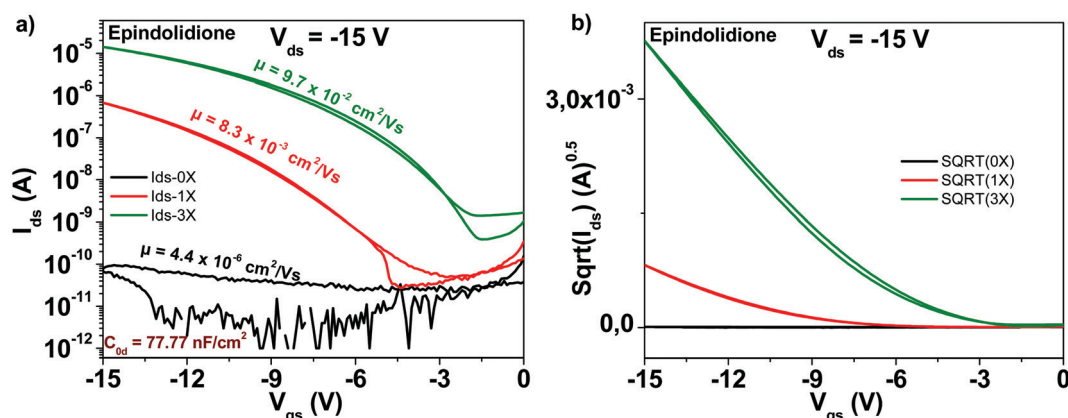


Fig. 10 (a) Comparison of OFET devices with three different purity grades of epindolidione semiconductor: unpurified (0× in graph legend), one time purified (1× in graph legend) and three times purified, respectively (3× in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

analyzed in this study. After three sublimation steps, the electron channel improved by a little bit over one order of magnitude in field effect mobility (*i.e.* $\sim 6 \times 10^{-5}$ to $\sim 6 \times 10^{-4} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$), whereas the hole channel improved by a factor of 4 only (*i.e.* from $\sim 9 \times 10^{-4}$ to $\sim 4 \times 10^{-3} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$). However, the successive purification steps did indeed improve the material performance, a fact that is obvious in the obtained transistor parameters displayed in Table 1, *i.e.* the subthreshold swing decreased for the electron channel from 4.6 to 2.8 V dec^{-1} , while for the hole channel from 2 to 1 V dec^{-1} , when comparing the 1× and 3× purity grades.

3.9 Indigo

A schematic of an indigo molecule is presented in Fig. 4. Indigo employed in this study was reported in our previous publication (ref. 46). In this work we present in addition, the tabulated values of the transistor parameters. It is obvious that performing three rounds of purification not only increases the mobility of the semiconductor but also decreases the subthreshold swing from ~ 2.7 to 1.2 V dec^{-1} for the electron channel and from ~ 1.9 to

1.3 V dec^{-1} for the hole channel respectively (see Fig. 12). Full details of the recorded/calculated values of the indigo transistor performance are presented in Table 1.

4. Discussion and conclusions

We showed here that scrupulous purification of the organic semiconductor is a critical parameter in improving the performance of the organic field effect transistors. We should point out here that each semiconductor material has itself a designated (specific) purification recipe, that differ from one another in the temperature ramping, the differential heating of the vacuum tube, and mostly the flow rate of the carrier gas and its type (Ar, N₂, or even no carrier gas, *etc.*). All these factors are material specific and cannot be generalized. In this work we demonstrated that by pursuing several purification steps starting from the pristine material, all the transistor parameters (apart from for some few exceptions, the threshold voltage, V_{th}) consistently improved alongside the purity of the organic semiconductors. In several cases (see Table 1) the mobility



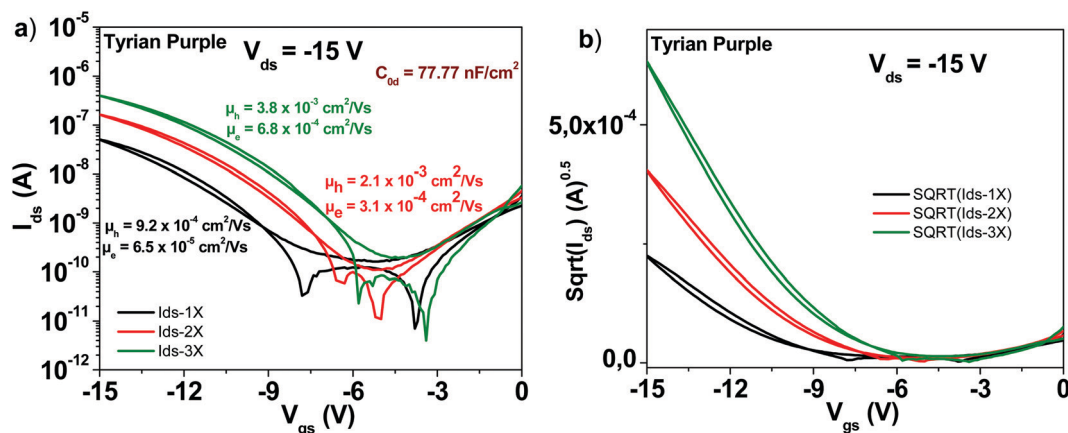


Fig. 11 (a) Comparison of OFET devices with three different purity grades of Tyrian purple semiconductor: one-time purified (1× in graph legend), two times purified (2× in graph legend) and three times purified, respectively (3× in graph legend); (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a).

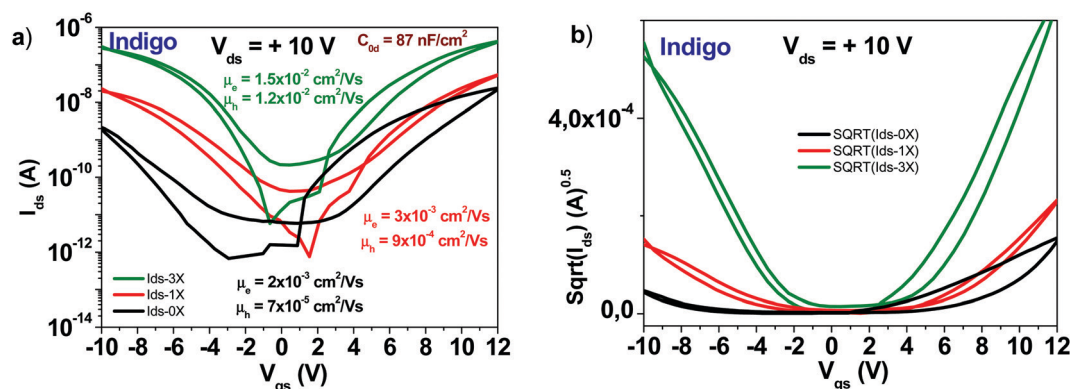


Fig. 12 (a) Comparison of OFET devices with three different purity grades of indigo semiconductor: an unpurified grade (0× in graph legend), a one-time purified grade (1× in graph legend) and a three times purified grade, respectively (3× in graph legend). The specific capacitance of Indigo OFETs is 87 nF cm⁻²; (b) the $\sqrt{I_{ds}}$ vs. V_{gs} of the three purity grades presented in panel (a). Adapted with permission from ref. 46, M. Irimia-Vladu, *et al.*, *Chem. Mater.*, 2019, **31**(17), 6315–6346.

improvement surpassed three orders of magnitude, whereas the subthreshold swing decreased to values close to 1 V dec⁻¹.

Inspecting Fig. 5–12 presenting transistor measurements in this manuscript, as well as Fig. 3 which displayed a high purity C₆₀-based OFET scrupulously optimized for high mobility values, it is interesting to note that for all the analyzed materials, always the highly purified semiconductor grade displays a much higher OFF level on the transfer characteristics as compared to the pristine, non-purified material. This is in a way counterintuitive, since a higher OFF level of the drain current is usually given by impurities (high doping levels). We consider that the change in morphology of the deposited semiconductor has also a significant influence when comparing various purity grades; and also the fact that we measured all the transistors in the accumulation regime (from zero V_{gs} to negative V_{gs} values for p-type materials and from zero V_{gs} to positive V_{gs} for the n-type semiconductors, C₆₀), a fact of significant influence on the OFF level of the drain current, observed also by Horowitz *et al.*⁶¹ We went on and measured our devices also in the depletion mode, and observed indeed a tendency of I_{off} to move towards lower values, as pointed out by

Horowitz *et al.* This trend is shown by all purity grades of each material and by all materials in this study. It seems plausible to assume then that the change of the morphology of the deposited film due to the increased purity level of the evaporated material generates a film of increased bulk conductivity, which more than offsets the decrease of the I_{off} level due to the lower impurity content of the film. More studies will be necessary to elucidate this observed event.

Also, it looks obvious that the identical recipe utilized in this work with respect to the semiconductor deposition is not beneficial for all of them in extracting the maximum performance (*i.e.* charge mobility value or charge mobility balance of the electron and hole channels, as it is the case for Tyrian purple or indanthrene blue RS). For all materials, a specific deposition method has to be implemented for rendering the high performance.^{52,56,57,60} In this respect, several factors need to be carefully considered and optimized for maximum performance of each material in OFETs, like for example: the deposition rate; the substrate temperature during deposition (ideal for the growth of larger crystals which minimizes the number of grain boundaries and the afferent charge trapping



sites); the distance from the crucible to the substrate holder, the semiconductor layer thickness, *etc.* As it is visible in Fig. 3 of our already published device,⁶⁰ a thicker layer of C₆₀ (300 nm in the respective case), carried out in a very deep vacuum of $\sim 10^{-9}$ bar, at a very low rate of deposition, significantly below 1 Å s^{-1} and a constant substrate temperature of 150°C seems to be an ideal recipe to obtain high semiconductor mobility performance, in spite of the poor transistor geometry of a large channel of $80 \text{ }\mu\text{m}$ utilized there. It is important to mention that this study focused on showing the trend of improvement in the OFET performance *via* increasing level of purity of organic semiconductors, and not to engineer record values of charge mobility. This is valid also in our choice of the organic dielectric capping layer, *i.e.* the thin TTC deposited on top of the aluminum oxide dielectric, which does not afford a substrate heating in excess of 65°C , whereas typical values of substrate heating in excess of 100°C are required for obtaining high mobility semiconductors, as we showed already in one of our previous studies.⁶⁰ Obviously, particular deposition recipes can be made available for each semiconductor to extract high charge carrier mobility, as we proved earlier in the case of epindolidione,⁵⁶ quinacridone,⁵⁶ indigo⁵⁸ and Tyrian purple.⁵² Nevertheless, even though all these factors shall be considered, one will always observe that record values are obtained when the level of cross contamination is minimized, *i.e.* the vacuum deposition instrument is dedicated for a single material only or cleaned rigorously in-between the change of materials and subsequently utilized for a single material for an extended time. This was indeed the case of C₆₀ deposition in our study in ref. 60, where the organic evaporator instrument was dedicated to handling one material only, *i.e.* C₆₀ for a very long period of time.

In conclusion, we showed through this study that the purification of organic semiconductors is a critical parameter, resulting in fabricated devices of superior performance. Therefore, we propose that setting a “materials’ purity standard” in different labs worldwide will make the results much more reproducible, comparable and competitive. This could bring a leap forward for organic electronics science and technology.

Conflicts of interest

There are no conflicts to declare.

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