

REVIEW

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Challenges and emerging opportunities in transistor-based ultrathin electronics: design and fabrication for healthcare applications

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Ultrathin healthcare electronics are gradually being employed for daily physiological monitoring and medical treatment tasks which have long been dominated by bulk and expensive devices. Recent advances in ultrathin devices have enabled more intimate interactions between humans and electronics in a highly accurate, comfortable, and even imperceptible manner. However, demands for new paradigms to establish long-term biosafe, robust, and anti-interference devices for healthcare management are still increasing, particularly amid the coronavirus pandemic. This review summarizes the recent progress in ultrathin transistor-based wearable devices ranging from implantable, skin-mounted devices to smart textiles. Emerging solutions and promising strategies for challenges encountered by high-performance systems have been summarized comprehensively from both mechanical (such as strain-insensitivity and permeability) and electronic (such as signal transduction ability and anti-interference) perspectives. In addition, the design and fabrication of ultrathin electronics with different topological categories have been discussed in detail. The prospective applications for ultrathin electronics to integrate with sensors, perception components, actuators, and drug delivery for clinical or healthcare management have been summarized.

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1. Introduction

Conventional bulky, rigid, and expensive clinical devices make continuous monitoring of the physiological parameters of outpatients very challenging. Thus, the development of lightweight and low-cost electronic devices with comfort has become an urgent need. To satisfy these requirements, ultrathin electronics (thickness of sub 300 μm) have been developed and have even ceaselessly advanced into wearable systems.^{1–6} The reduction in the thickness of devices could galvanize improvements in both flexibility and weight. The former delivers compliant mechanical properties that can match various surfaces of a 3D curved bio-morphology (e.g., examined pig skin with the roughness ranging from 1.9 to 90 nm to 240 μm^7) and the low Young's modulus of biological surfaces (e.g., elastic moduli of neural tissues are approximately in the 100 to 1500 kPa range⁸), and the latter facilitates adhesion and further integration with the human body as a tissue- and skin-attachable material and even as a woven cloth with an imperceptible feel during usage.⁹ Moreover, as implantable devices,

ultrathin electronic devices can also minimize the invasive reactions to realize long-term monitoring.^{10,11}

Unlike traditional silicon-based rigid electronics, ultrathin transistor-based electronics and devices are mild, bio-safe, low-cost, and consume low energy. Benefiting from the advances in plastic sensors and organic transistors since the 1980s,^{12–14} organic semiconductor materials,^{15–18} dielectric materials,^{19–22} thin-film logic/analog circuits,^{23–27} radio-frequency identification (RFID) tags,^{28–31} and displays^{32,33} have been extensively explored. This has resulted in exciting and remarkable research being conducted, such as the development of devices with biocompatibility,^{4,34} transparency,^{35–37} self-healing properties^{38–40} and stretchability^{41–43} using ultrathin transistors, which interact more intimately with our body.

The exploration of the structural design and fabrication of ultrathin electronics is ongoing; however, emerging design requirements of ultra-flexibility, durability, and anti-interference of robustness, particularly under the emerging mechanical and electrical requirements as a new confluence, are yet to be realized. Surface-compliant films with 1D fibers or 2/3D patterns are considered essential for structural design^{44–47} (Fig. 1). The major electrical requirements for semiconductor materials are good conductivity with stretchability and high semicrystalline with uniformity. However, several critical challenges for electrical principles have also attracted attention,

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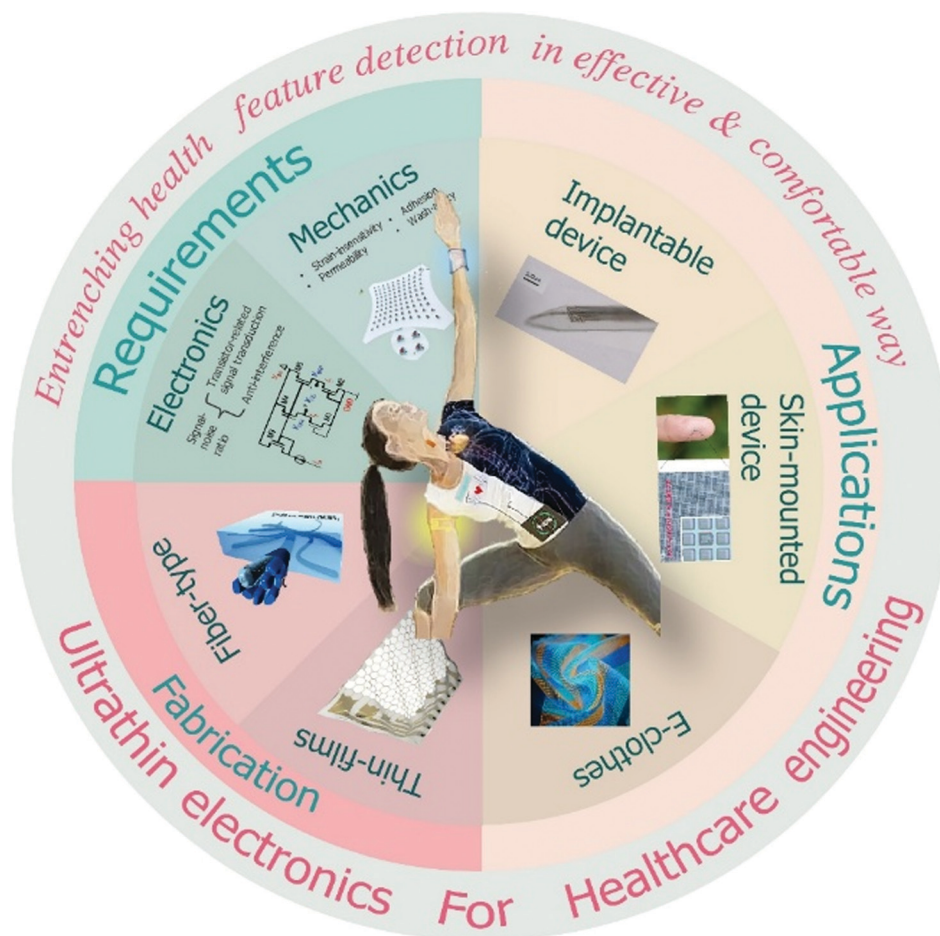


Fig. 1 General illustration of ultrathin electronics for a broad range of healthcare applications with a new paradigm and tendency. A stretching-insensitive ultrathin transistor-based temperature sensor with stretching-insensitive ability.⁴⁸ Copyright 2018, Nature Publishing Group. Inflammation-free implantable nerve-probe devices.⁴⁹ Copyright 2015, Wiley-VCH. A high-density skin-mounted sensor.⁵⁰ Copyright 2021, AAAS. Electronic clothes with large-area ZnS display textiles.⁵¹ Copyright 2021, Springer Nature. An organic electrochemical transistor array for tissue-attachable materials.⁵² Copyright 2018, AAAS. Semiconductor films with a fiber-type nanoconfinement structure.⁴² Copyright 2017, AAAS.

such as high signal-to-noise ratio (SNR) and anti-surrounding-interference performance (motion interruption, temperature, water, *etc.*). Moreover, to realize truly multifunctional sensing with motion recognition output, certain interdisciplinary tools, including classical analog amplification circuits, traditional machine learning algorithms, and deep learning networks, are in the process of collaborating toward high-level signal recording and processing.⁵³ These principles and advances have been discussed later. However, regarding wearable devices, the exploration of ultrathin electronic integration systems for closed-loop information circling and self-power is still quite limited.⁵⁴

In addition, there are several more emerging requirements for manufacturing at the system level of ultrathin devices.^{55,56} Furthermore, solution-based fabrication for large-scale and programmable processes has been proven to be effective.^{26,57,58} Moreover, room-temperature fabrication technology is in demand owing to the benefit of avoiding the shrinking issue of ultra-thin plastic substrates.⁵⁴ However, the fabrication and assembly process of an integrated platform

should be further discussed under the conditions of homogenous mechanical properties and electrical coupling.

Although there are many excellent reviews on healthcare devices,^{3,59–62} a comprehensive discussion of the recent paradigms and criteria is still lacking. This study provides a roadmap for the design and fabrication of ultrathin transistor-based electronics based on emerging confluences for long-term applications for healthcare monitoring as well as clinical diagnosis and therapeutics. In Section 2, we present the mechanical and electrical design strategies for the emerging requirements and properties of ultrathin electronics. In Section 3, we discuss and review the fabrication technology for various solution-based processes to address ultrathin structure considering 1-dimensional (1D) to multilayer geometric issues. In Section 4, to accommodate various surface and body dynamics, new principles and requirements for the design and fabrication of ultrathin electronics are gradually unfolded with different levels of close interactions between our body and a smart system, including e-fiber, smart cloth to e-skin, and implant devices. Furthermore, we describe the latest progress in

ultrathin electronics that are classified into three levels depending on the interaction with the human body: (1) implantable devices, (2) skin-attachable devices, and (3) fiber and woven electronics for e-textiles. Finally, we provide future prospects in Section 5 focusing on the challenges and requirements to satisfy the increasingly larger demands and higher criteria of healthcare wearable devices.

2. Emerging concepts for mechanical and electrical designs of ultrathin electronics

As mentioned above, ultrathin electronics offer excellent advantages such as better wearable comfort, portability, and topographical compliance. Consequently, a wearable healthcare device should consider more advanced factors, including strain insensitivity, gas permeability, wash-resistant ability, high SNR, anti-interference (fluctuating extra electrical field, motion, temperature, water, or moisture), and other requirements (Fig. 2).

2.1 New requirements for mechanical performance

2.1.1 Stretch-insensitivity. An ultrathin device must be compliant and robust. Several excellent reviews have referred to the topic of strain engineering. Good stretchability is vital for ultrathin devices for semiconducting components as well as conducting parts.^{63–69} It has been proven that a low Young's modulus results in much stronger ultrathin electronics under the conditions of flexible usage scenarios to achieve better fitness for complex bio-surfaces.^{70–73} However, a general survey on the principles of solutions is lacking. In fact, conventional manufacturing of flexible functional materials, including ionic materials, liquid metals, and conductive polymers doped with micro-/nanoparticles, inevitably results in fluctuations in their conductivity because of strain.^{79–86} Hence, a thorough

examination of strategies for stretch insensitivity is needed to better understand the aspects related to strain.

The reported strategies for stretch insensitivity can be classified into two categories: structural design including nano-patterning, micro-patterning, and electrical solutions, which have been reviewed in the next section.

First, an exciting example of a nano-patterned structure utilized composite materials based on vertically aligned conductive magnetic nanoparticles of an urchin shape (US-CMNPs)⁷⁴ (see Fig. 3A). It comprises magnetic silver-nickel NP cores and was shelled with gold nano-needles. Herein, the chemical reduction method was chosen to synthesize the piezoresistive composite. The specially aligned structure was formed by a guiding magnetic field applied to a polyurethane elastomer doped with magnetic nanoparticles. Hence, the anisotropically arranged nanoparticles contribute to a large difference in sensitivity along each axis. Therefore, it greatly minimizes the strain sensitivity of the substrate.

Second, another solution is a micro-/nano-patterned structure, which can be divided into three categories: 1D fiber-shape, 2D film-shape and 3D topologies. In the case of 1D patterns, the fiber-shaped devices are mostly established by mounting a spring-like conductive fiber around a stretchable polymer core^{75,87,88} (Fig. 3B). It works similar to an old telephone coil, which can easily be prolonged without sacrificing its conductivity. Regarding 2D and 3D micro-/nano-topologies, further options have been provided recently, including serpentine structures, wavy patterns and porous patterns^{63,66,71,89–95} (Fig. 3C). In general, this type of architecture can offer additional deformation without destroying the intertwined conductive composites.⁷⁶ However, to prevent cracks, delicate functional components are assembled with base materials to provide high mechanical stability.

The strain sensitivity of organic semiconducting materials is more complicated and depends on the degree of nucleation and its crystalline orientation as well as the alignment of

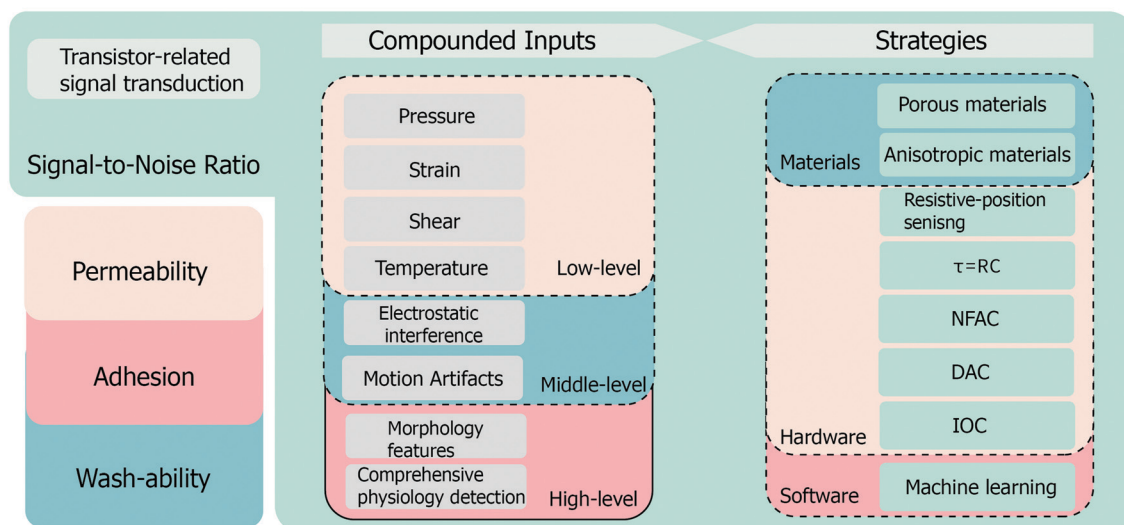


Fig. 2 Emerging challenges and corresponding strategies for ultrathin healthcare devices.

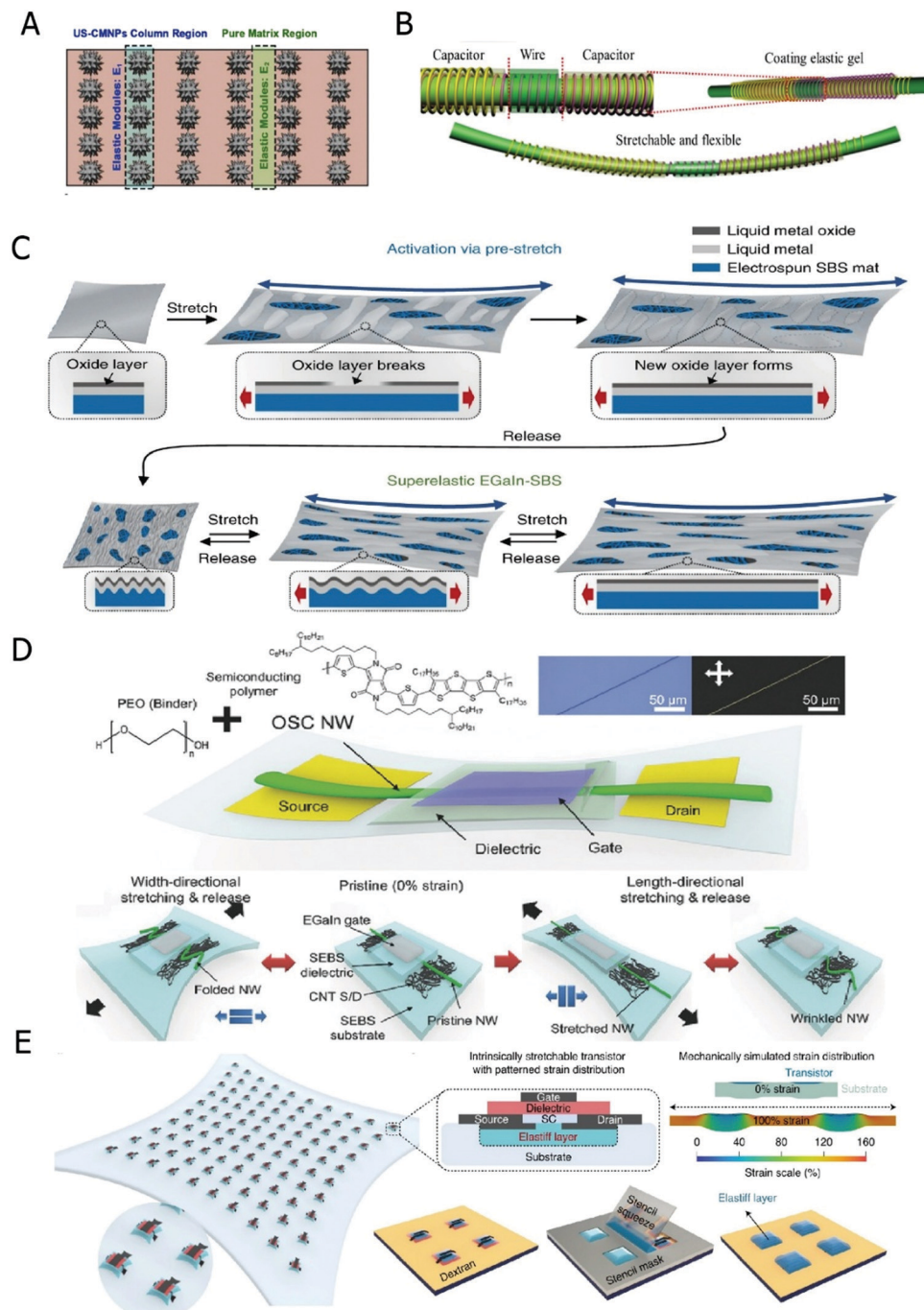


Fig. 3 Topological solutions for strain-insensitivity. (A) A stretchable tactile sensor with dynamic stability based on vertically aligned urchin-shaped nanoparticles.⁷⁴ Copyright 2020, Elsevier Ltd. (B) A one-dimensional fiber-shaped structure for a stretchable electronic tandem.⁷⁵ Copyright 2018, Springer Nature. (C) Monolithic stretchable electronics with a superelastic liquid-metal mat.⁷⁶ Copyright 2021, Springer Nature. (D) A deformable nanowire transistor.⁷⁷ Copyright 2018, Wiley-VCH. (E) An island-type train-insensitive intrinsically stretchable transistor array.⁷⁸ Copyright 2021, Springer Nature.

organic semiconductors.^{54,96} In general, the stretching of thin-film semiconductors has negative impacts on the semiconductor crystalline structure. Therefore, significant degradation of the field-effect mobility and current output may occur.

Recent reports have addressed this challenge. The first is based on a nanoconfinement morphology^{42,77,97} (Fig. 3D).

Nanoconfinement of polymers offers the advantages of a lower mechanical modulus and increased mechanical durability. In this system, a solution contains polystyrene-*block*-poly(ethylene-ran butylene)-*block*-polystyrene (SEBS) and poly(2,5-bis(2-octyldodecyl)-3,6-di(thiophen-2-yl)diketopyrrolo[3,4-*c*]pyrrole-1,4-dione-*alt*-thieno[3,2-*b*]thiophene) (DPPT-TT;1), each of which acts as an elastomer

substrate and active semiconductor, respectively.⁴² By forming nanofibrils inside a deformable elastomer, a nanoconfinement structure of the polymer semiconductor was achieved. Consequently, owing to their comparative surface energy, nanoscale phase separation is generated along with the formation of the nanoconfinement morphology. Using a fiber-net-like semiconductor, the output of the current was demonstrated to be relatively steady at an elongation of 75%. Another reported strategy involves following the design of the reorganization of crystalline orientation and alignment.⁹⁸ When combined with the strategies of nanoconfinement morphology and solution shearing by a microtrench blade array, the effect of semiconductor mobility increases three-fold with stable outputs while it is stretched by more than 100% of its length.

In addition, the “island-bridge” configuration serves as an alternative which is typically used to assemble silicon-based chips with a plastic substrate.^{99–102} However, owing to the difference in the mechanical modulus, a significant mismatch occurs between the rigid chip and compliant polymer. Furthermore, soft substrates suffer from most of the shear force and result in the formation of vulnerable points in the rigid-soft interface. Progress has been reported in the case of stretch-insensitive semiconductor circuits, based on following the “Island-bridge” principle, wherein the stiffness of semiconductor components was modified⁷⁸ (Fig. 3E). In addition, the difference in modulus between the passive substrate and active “island” can reduce the share-straining-deformation in the latter by approximately four-fold at 100% of the entire strain. Consequently, strain insensitivity was achieved by less than 5% variation up to 100% strain.

2.1.2 Permeability. Regarding skin-attachable electronics, gas/sweat permeability is a major factor that affects long-term continuous monitoring without causing inflammation and discomfort.^{71,103–105} Furthermore, it also enables durable attachment by suppressing the interruption of sweat within the skin-electronics interface.

To solve this issue, a high-porosity structure should be considered as a fundamental approach. The porous structure must be carefully designed, including the size and line width, to avoid the possibility of blocking sweat glands (approximately 100 μm in diameter)⁷¹ (see Fig. 4A). In addition, it also results in the reduction of the modulus, instinctive stretchability, and larger contact area for both the dielectric-active and active-passive interfaces of skin-attachable devices.

Recent progress in this regard comprises two approaches. One typical configuration is a stacked micro-/nano-fiber, which is generally obtained by spinning technology with active and passive polymer mixture solutions^{71,106,107} (see Fig. 4B). The other is 2D nanopatterning. Conductive nanomeshes can be obtained by photolithography, grain boundary lithography, and crack-patterned approaches *via* chemical vapor deposition (CVD) and other thin-film methods.^{64,66,69,108}

Hydrophilia of skin-mounted gas/sweat-permeable films has been reported as another key factor in the enhancement of water transportation through nano-/micro-meshes¹⁰³ (see Fig. 4C). Following a similar method with an additional monolayer of 1-thioglycerol molecules modified on the mesh surface,

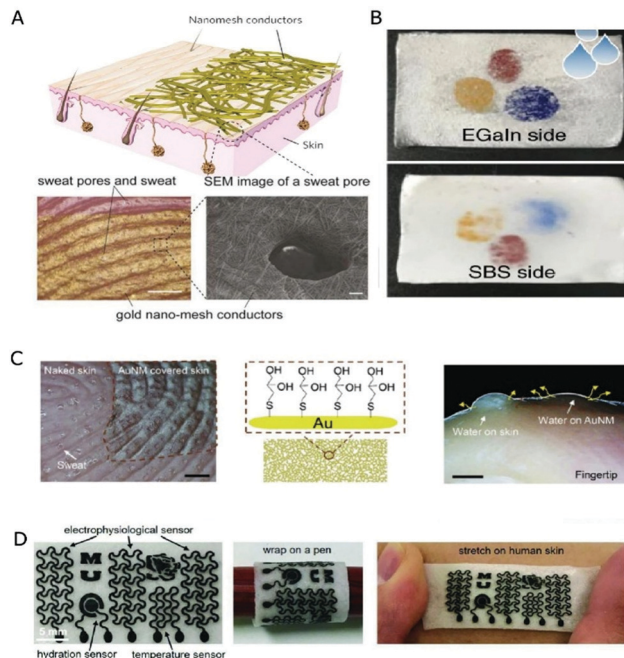


Fig. 4 Air-permeability for skin-attached electronics. (A) Nanomesh on-skin electronics.⁷¹ Copyright 2017, Springer Nature. (B) Demonstration of water permeability for a liquid–metal fiber mat.⁷⁶ Copyright 2021, Springer Nature. (C) Enhanced breathability of on-skin electronics.¹⁰³ Copyright 2020, Elsevier Ltd. (D) All-porous elastomer electronics.⁴⁴ Copyright 2018, Wiley-VCH.

the sweat evaporation from the substrate-free electrodes was improved by a factor of 3.6. Moreover, without sacrificing its thickness, it maintains long-term conformity and flexibility with the skin.

In addition to substrate-free electronics, structural materials, which commonly refer to the dielectrics of transistor-based devices and electrical package layers, are also required to be hollow patterned^{44,76,109,110} (see Fig. 4D). A typical example reported is a substrate material using a sugar-templated elastomer sponge to fabricate an all-gas-permeable device.⁴⁴

2.1.3 Adhesion. The adhesion between electronics and tissue, electronics, and skin, as well as the inside of electronics, is another challenge in the research of flexible electrodes. Strong and comfortable adhesion approaches could pave the way for a longer lifetime of the product in the future.^{111,117,118} The adhesion between the stacking interlayers is a serious consideration for minimizing the peeling failure and reliability of electronic devices.^{111,114,119} In this case, the ultrathin characteristics of thickness, biocompatibility, and reversibility are greatly appreciated in the adhesion of bio-electronics.

First, ultrathin electronics are highly appreciated for health-care monitoring because of their conformal ability and low Young's modulus to match 3D complicated biosurfaces. The thinner the device, the better it is.⁹ Tough adhesion of transistor-based electronics or organic photovoltaics with a thickness of sub-10 μm to tissue/skin without additional paste agent has been realized (Fig. 5A).^{120–122} In addition, sub-300 nm electronics are also described as imperceptible and

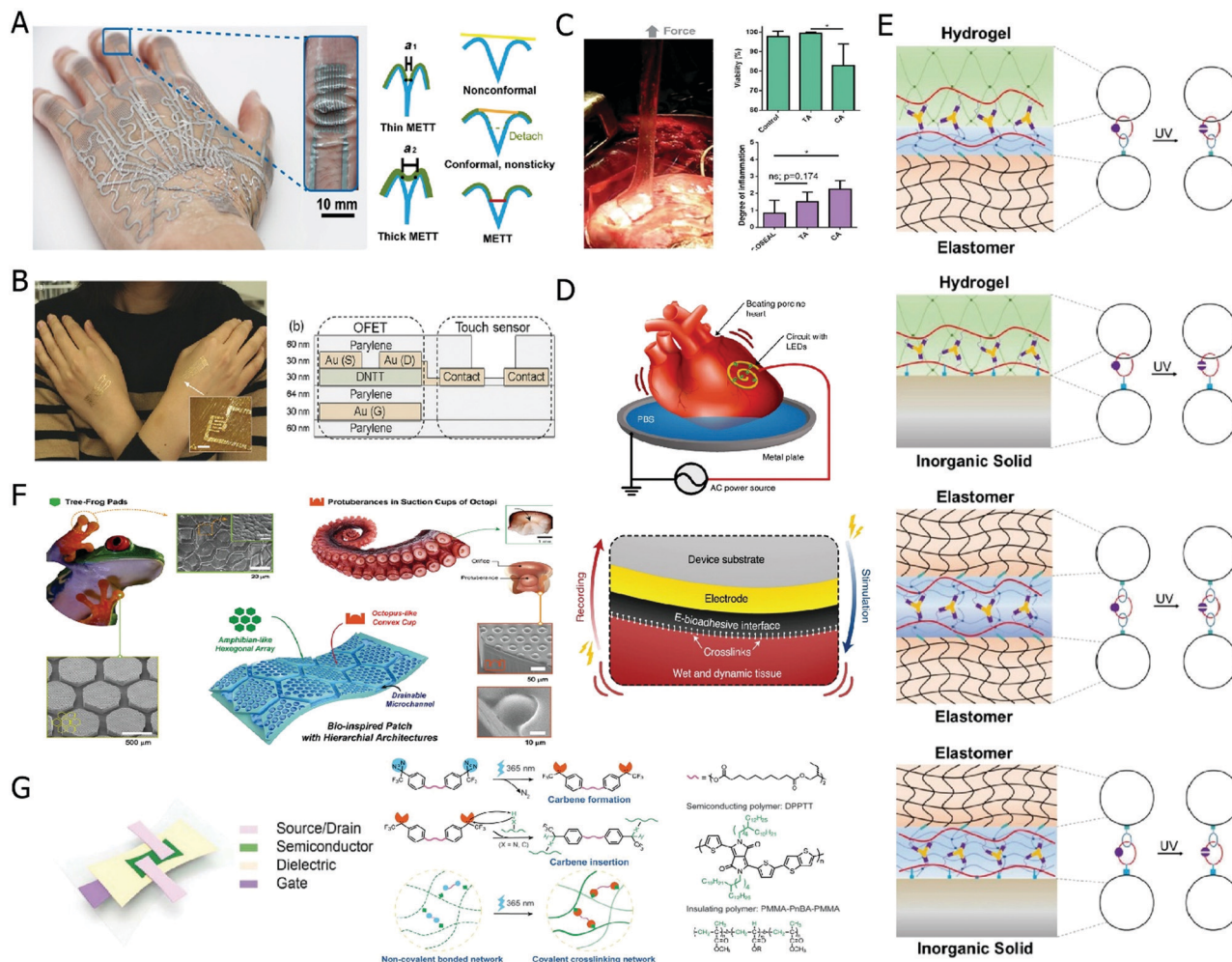


Fig. 5 Photograph of adhesion. (A) Multilayered electronic transfer tattoo.¹¹¹ Copyright 2021, AAAS. (B) 300-nm biocompatible e-skin.¹¹² Copyright 2016, Wiley-VCH. (C) Tough bio-compatible adhesives on wet surfaces.¹¹³ Copyright 2017, AAAS. (D) Electrical bioadhesive interface.¹¹⁴ Copyright 2021, Springer Nature. (E) Photodetachable adhesion.¹¹⁵ Copyright 2019, Wiley-VCH. (F) Amphibians and octopi for omnidirectionally enhanced wet adhesion.¹¹⁶ Copyright 2019, Wiley-VCH. (G) Microlithography of high-density elastic circuits.⁵⁰ Copyright 2021, AAAS.

demonstrate ultra-conformability to bio-surfaces^{9,71,112,123} (see Fig. 5B).

Second, the epidermis of the tissue is a type of wet surface and adhesion on wet surfaces has already become a rising hot issue. However, several other complex and dynamic factors must also be considered, such as blood coagulation, anti-bleeding interference, and inflammation.⁵² Natural materials are good candidates for solutions to these difficult problems. Several groups have employed dopamine, a type of mussel-inspired natural adhesive.¹¹⁹ An intertwined topology adhesion network is formed *via* branched structures that are delicately designed to have a higher molecular weight, such as poly(ethylene glycol) diacrylate ($M_w = 200$). Furthermore, a silk-based sealant is demonstrated as a tough adhesion agent on bloody tissue with instant homeostasis and good biocompatibility.¹²⁴

Moreover, an impressive report follows the principle of amidation between the amido and carboxyl groups.¹¹³ Owing to the abundant carboxyl groups in biological tissues, it greatly

enhances the robustness and toughness of the tissue-electronics interface (Fig. 5C). In addition, the dissipative matrix is designed to effectively share the dissipating energy. Following the same fundamental principle, a type of dry double-sided tape for adhesion was developed.¹²⁵ The mechanism of its quick adhesion ability (≤ 5 s) is due to the dry dissipative block to absorb and remove the interfacial water layer on the surface, resulting in the exposed dry surface subsequently forming a strong covalent bond.

Reverse adhesion is a useful concept in bioelectronics. An e-bioadhesive method with reversible wet-surface adhesion between tissues and electronics has been reported¹¹⁴ (Fig. 5D). Based on the above-mentioned dry substrate and amidation reactions, graphene oxide was introduced into a poly(vinyl alcohol) (PVA) hydrogel composite as the conductive component, and further grafted with *N*-hydroxysuccinimide ester (PAA-NHS ester). The combined working principles involved are covalent bonding, hydrogen bonding, and electrostatic interactions. Another group reported photodetachable

adhesion between hydrogels.^{115,126} The spreading long chains realized a strong entanglement on the adhesion interfaces. Furthermore, a third intertwined network *via* the Fe³⁺ complexation reaction facilitated reversible adhesion upon UV light exposure (Fig. 5E). Electrophoresis by electrically driven diffusion reactions has also induced a reversible effect on the adhesion of flexible materials. The introduction of external stimuli resulted in the formation of cationic–anionic bonds that follow electrically driven motions that can be disabled within a certain period of time.

Templated micro-patterned structures are also an efficient way of establishing adhesion for either dry or wet surfaces, termed dry adhesives. Many excellent studies have been reported following this approach as a biocompatible and bio-safe strategy.^{116,127–129} For example, based on the phenomenon of the Cassie state of a liquid droplet suspended on the head of micropillars, mushroom-shaped PDMS fibrillary arrays can effectively remove the water thin layer on the surface and accomplish adhesion to the contact interface.¹³⁰ This method can also be applied to surfaces with low-surface-tension liquids, oil, and other liquids. Another group created an octopus-inspired nanosucker array to achieve a tough contact with tissue surroundings¹³¹ (Fig. 5F). This templated PDMS structure also provides a promising solution for wound care.

Adhesion among electronic interlayers is another potential issue. Regarding thin-film electronics, the approaches commonly rely on van der Waals forces and electrostatic force.⁵⁴ However, with an increase in structural complexity, the number of interlayers and interconnections of circuits, as well as the thickness of the device, increases correspondingly.

Consequently, the possibilities of adhesion failure, such as peeling off, become possible. In addition, the additional thickness introduces a mismatch between the inadequate binding force and the mechanical modulus.⁹ Representative approaches have been demonstrated using optical microlithography (UV irradiation) with the help of benzophenone and a carbene photoinitiator.^{50,134,135} Furthermore, UV-triggered reactions have been proven to be efficient for crosslinking between polymer chains and the fabrication of all-transparent sensor devices⁵⁰ (see Fig. 5G).

2.1.4 Washability. E-textiles have evolved into an indispensable component of wearable devices.^{51,75,132,136,137} They provide multiple options to facilitate human–machine interfaces and additional daily monitoring of health, including body motions and physiological detections. The mechanical durability of woven e-textiles has recently garnered attention, particularly washability, which is found to be a natural property of traditional fabrics. However, this is still not a trivial issue for electronics.^{138,139} In addition, anti-abrasion must also be considered for E-cloth to facilitate its washability in washing machines.

Several groups have already attempted to overcome this challenge. Water-proof encapsulation was the first proposed idea. By using an ultrathin (1 μm) parylene passive film, the electrical performance of organic photovoltaics decreases by only 5.4% after dipping in water for 120 min (Fig. 6A).⁷⁰

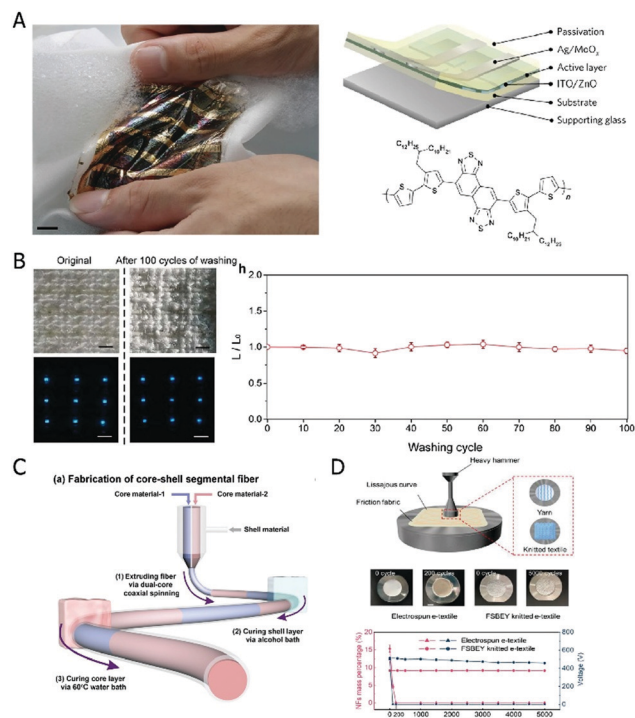


Fig. 6 Washable electronic textiles. (A) Elastomer-coated organic photovoltaics.⁷⁰ Copyright 2017, Springer Nature. (B) Large-area display textiles.⁵¹ Copyright 2021, Springer Nature. (C) Core-shell segmental hydrogel thermochromic elastomer hybrid fibers.¹³² Copyright 2020, American Chemical Society. (D) Waterproof triboelectric yarns based on Fermat's spirals.¹³³ Copyright 2021, Wiley-VCH.

Regarding e-fiber, a core-shell structure was reported as washable, wherein the hydrogel core made of poly(2-acrylamido-2-methyl-1-propanesulfonic acid-co-acrylamide) hydrogel doped with conductive reduced-graphene-oxide was encapsulated by a thermochromic silicon elastomer (Fig. 6C).¹³²

An excellent machine-washable large-area e-textile has recently been demonstrated for wearable display devices^{51,140} (Fig. 6B). It was fabricated by the encapsulation of ZnS phosphor slurry (with a diameter of approximately 0.3 mm), followed by drying at 120 °C. Consequently, the yarns remain flexible, with large improvements in both mechanical robustness and waterproofing capabilities.

In addition, a novel example of twisted electronic yarns has been reported.¹³³ Based on Fermat's spiral inspiration, excellent anti-abrasion properties and high output performance were simultaneously achieved. Twisted from a poly(vinylidene fluoride-trifluoroethylene) fiber mat, the triboelectric yarns have hydrophobic properties that could further protect the e-fiber from the penetration of water and laundry detergent (see Fig. 6D).

2.2 Emerging stratagems for electrical performance

Flexible transistor-based electronics have a wide range of applications, such as body gesture recognition, physiology, and electrophysiological signal detection. As a key property and optimizing principle, high SNR performance for healthcare

monitoring is focused upon establishing an effective and accurate dataset over long-time service. In general, the SNR issue for transistor electronics is closely related to two factors: signal transduction ability from the input to the output terminals, and the immunity level on the conditions of a variety of interferences, such as electromagnetic fields and abrupt mutation of power sources. The approaches to address this issue are discussed in detail in the following sections, including hardware/software working principles and mechanisms.

2.2.1 High signal transduction ability of organic transistor-based devices. Practical applications of healthcare monitoring services must be further integrated into a wearable platform that embraces sensors, signal processors, transmitters, and commutation modules. Ultra-thin transistors are essential elements in such systems as signal transducers, in part with multifunctions of amplifiers, transmitters, processors, filters, and sensors.

The key to signal transduction ability is related to three basics: carrier transportation, threshold and operating voltage, and cutoff frequency.^{96,141–144} First, to improve the output electronic performance, carrier mobility is a more prominent challenge to be overcome. The mechanism of carrier transportation is consequently related to the performance of current amplification and transconductance; that is, the higher mobility of carriers advances in both higher sensitivity to signal amplification and higher load power on the output terminals. The effective mobility μ_{eff} in the saturated mode can be expressed as follows:^{144,145}

$$\mu_{\text{eff}} = \mu_0 \left\{ 1 - \left[\frac{\mu_0 C_i W R_c (V_{\text{GS}} - V_{\text{TH}})}{L + \mu_0 C_i W R_c (V_{\text{GS}} - V_{\text{TH}})} \right]^2 \right\} \quad (1)$$

where μ_0 is the mobility eigenvalue of the materials, C_i is the gate dielectric capacitance per unit area, W and L represent the channel width and length, respectively, and R_c is the contact impedance at the metal–semiconductor interface. As mentioned above, μ_0 is determined by its crystalline degree along with the process of developing intra- and intermolecular interactions, that is, the formation of a conductor-related π -stack. However, a higher degree of crystallinity can lead to a rigid elastic modulus. Thus, there must be a trade-off between the mobility of the organic semiconductor and the compliance properties. In addition, the issue of metal–semiconductor impedance may lead to a steep decrease in μ_{eff} following this equation. Several approaches have been reported to address this issue, including self-assembling monolayers to minimize contact impedance.^{146–149}

Second, the performance on the input terminals of the device could benefit from the minimization of threshold and operating voltage with low energy consumption, low detection line, and high biosafety for bio-wearable applications. It has been reported that the gate-dielectric capacitance is essential to the threshold and operating voltage. In general, the dielectric capacitance can be approximated as:¹⁵⁰

$$C_0 = \frac{\varepsilon \varepsilon_0 S}{d} \quad (2)$$

where ε is the relative dielectric constant, ε_0 is the absolute dielectric constant, S is the cover area, and d is the thickness of the dielectric layer. Based on eqn (2), there are primarily two pathways to higher dielectric capacitance. One commonly begins with a higher relative dielectric constant ε , such as poly(1,1-difluoroethylene) and certain compact metal oxide films such as barium zirconate titanate¹⁹ and AlO_x .¹⁵¹ The other limitation is the thickness of the dielectric layer d , where large-scale uniformity still needs to be modified for the next manufacturing technique of ultrathin films. Recent progress in organic electrochemical transistors (OECTs) or electrolyte-gate transistors has provided effective ways to lower the threshold and operating voltage. Typically, the architecture of OECTS is established on a water-permeable gate electrode that directly contacts the active components.^{122,152–154} Consequently, without a dielectric layer, the threshold and operating voltage are significantly reduced. The other side of the coin is that the leakage current may also be elevated in this mode, that is, an increase in the dielectric capacitance results in more mobile charges and transconductance on the electrical dielectric interface, even under the same conditions of applied operating voltage.

The cutoff frequency is another challenge for transistor-based devices, which represents the charge–discharge frequency on both the gate–source and gate–drain interfaces. Improvements in cutoff frequency could concurrently boost the operating speed of the transistor and reduce the extra consumption. The analyses of cutoff frequency can be roughly illustrated by the following equation in the saturation state:¹⁵⁵

$$f_T \approx \frac{\mu_{\text{eff}} (V_{\text{GS}} - V_{\text{TH}})}{2\pi L (L + 2L_C)} \quad (3)$$

where f_T is the cutoff frequency and L_C is the contact length of the channel of the interlayers, which is closely related to undesired parasitic capacities.

2.2.2 Anti-interferences. To further improve the SNR by suppressing the noise, the abilities of anti-interference are considered as emerging landmarks for a proof-of-concept flexible device toward its market. Multifaceted challenges of noise sources generally originate from motion artifacts, external electrostatic fields, water/moisture influence, as well as multi-modal signal coupling (*e.g.*, temperature, heat of conduction, strain, compression, torsion, bending, and shape of the object). However, there are still gaps between multiple sensing modalities and a unified or single simplified device.⁵³ As a multi-disciplinary field, it is still lacking in terms of systematic retrospect from materials to analog electronics and software algorithms, such as machine learning (Fig. 2). In this section, we first summarize the architecture strategy to reduce motion artifacts. In addition, electronic analog circuits are also excellent candidates for solving this problem, which have been highlighted later. The fundamental electronic principles of analog circuits are discussed in detail. Finally, machine-learning and neural network achievements have been applied on this issue for decoupling multifunctional signals and achieving multisensation in a relatively simple design.

2.2.2.1 Architecture strategies to reduce motion artefacts. Most motion artifacts are mainly caused by unwanted shifting and vibrations along with body dynamics on the electronics–biosurface interface.^{7,8} In a relatively long period, electrophysiological signals originate from the receptor potentials inside the body. Hence, the impedance of biosurface electrodes is the main source of noise, hindering the recorded signals toward better SNR. The conventional approach usually involves minimizing the contact resistance *via* ionic electrodes^{117,156–158}—to maintain moisture and ion permeability on biosurface–electrode interfaces. However, the improvements in the SNR from such wet electrodes are not sufficient owing to the limited ionic concentrations and their naturally poor conductivity.

Fortunately, the performance of the SNR has been conspicuously enhanced with the evolution of ultrathin devices. Ultrathin films exhibit excellent mechanical compliance that is instinctively compatible with the topography of

skin.^{2,71,106,120,122,159–161} The roughness of pig skin was reported to be ranging from a minimum value of 1.9 nm to a maximum of 90–240 μm .⁷ Therefore, certain remarkable work of sub-300 nm electronics has been demonstrated to facilitate conformability to such 3D deformation to the best extent possible.⁹ Thus, compared with a range of objects with different thicknesses as well as a traditional wet electrode, perfect anti-motion disturbances have been demonstrated under a systematic check. The additional adhesive layer (such as acrylic pressure-sensitive adhesive $\sim 8\ \mu\text{m}$) was demonstrated as an alternative for thicker skin-attachable electronics to provide a sticky and stronger skin adhesion interface (Fig. 5D).¹¹¹ Following the same consideration to provide compliance for a larger contact area, the third approach is inspired by traditional commercial brain electrodes with pillar-like microstructures, which also improves interactions with the body surface.⁷

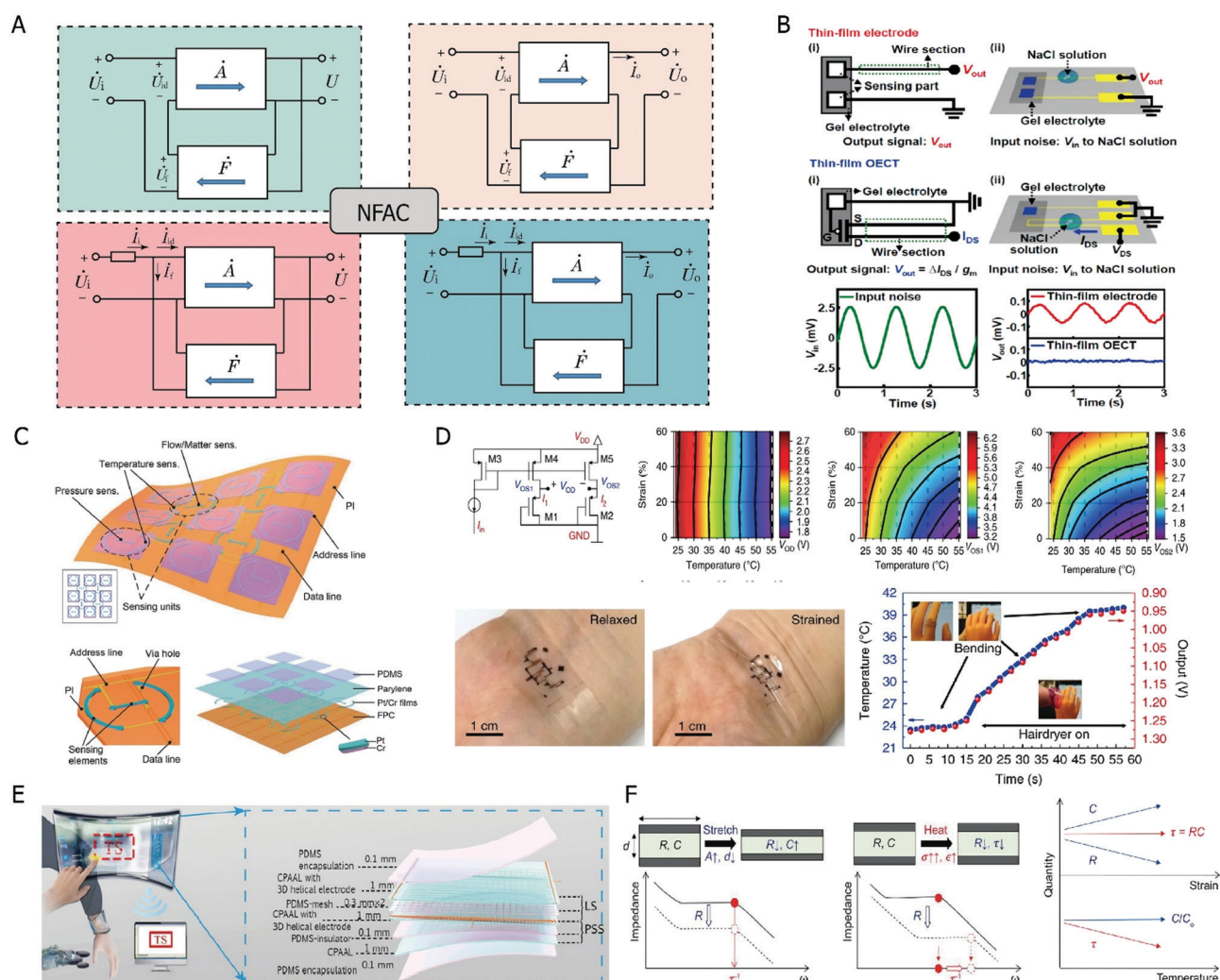


Fig. 7 Electrical solutions on analogue circuits configurations for anti-interference and multimodal recognition. (A) Schematic illustration of negative feedback amplifying circuits, clockwise corresponding photograph including voltage-series, current-series, voltage-parallel and current-parallel. (B) An ultrathin organic electrochemical transistor for electrophysiological monitoring.¹²² Copyright 2019, Wiley-VCH. (C) Multifunctional electronic skin based on thermosensation.¹⁶² Copyright 2017, Wiley VCH. (D) Stretchable temperature-sensing circuits with strain suppression.⁴⁸ Copyright 2018, Springer Nature. (E) Highly accurate and stretchable touchpad.¹⁶³ Copyright 2020, Wiley VCH. (F) Artificial multimodal receptors.¹⁶⁴ Copyright 2020, AAAS.

2.2.2.2 Fundamental principles of electronics and applications for anti-interference. Analog transistor-based electronics can potentially provide pathways for suppressing interference from the surroundings. There are three key elements that have recently been proven to be noise-resistant: negative feedback modules, differential amplification circuits, and integrated operational amplifier components. Their working principles can be summarized as follows.

First, negative feedback amplifying circuits (NFAC) are a type of closed-loop network that returns the reversed output to the input terminal. It stabilizes the static operating voltage of transistors and also counteracts various exogenous disturbances. For the elementary concepts of NFAC, there are four basic groups: voltage series, voltage parallel, current series, and current parallel (Fig. 7A). The voltage series is achieved by a feedback sub-module with the load potential in serials, which directly leads to the reduction of the input potential; voltage-parallel also provides a stable output by diverting part of the input current into the feedback network; current-series/parallel, with a sub-network of feedback connected in serial to the output, serves as a current signal stabilizer.

Second, for differential amplification circuits (DAC), the original design involved suppressing signal drifts as common-mode inputs, which is generally caused by the temperature-leakage current effect as well as unsteady transistor-related static potentials. The typical design of a DAC is composed of two reproductive amplification circuits in a symmetrical configuration. The final output is generated by the difference between the two sets of outputs, termed difference-mode signals. Thus, the drifts of common-mode input can be substantially reduced, which is usually termed as K_{CMR} as follows:^{165–167}

$$\begin{cases} K_{CMR} = \left| \frac{A_{ud}}{A_{uc}} \right| \\ A_{ud} = \frac{\Delta u_{Od}}{\Delta u_{Id}} = \frac{\Delta u_{Od1} - \Delta u_{Od2}}{\Delta u_{Id1} - \Delta u_{Id2}} = -\frac{\beta R_C}{R_B + r_{be}} \\ A_{uc} = \frac{\Delta u_{Oc}}{\Delta u_{Ic}} \approx 0 \end{cases} \quad (4)$$

where A_{ud} and A_{uc} are the difference- and common-mode amplifications, respectively. Δu_{Od} and Δu_{Id} are the difference-mode output and input voltages, calculated from the following subtraction between Δu_{Od1} and Δu_{Od2} , respectively. Correspondingly, Δu_{Oc} and Δu_{Ic} are the common-mode output and input voltages, which are almost zero in theory. It can be concluded that the amplification ability of the double-end DAC should be approximately infinity.

Third, integrated operational amplifiers (IOAs) are more complex. They are delicately designed and encapsulated by electric packaging, including three sub-networks of input, middle, and output stages with negative feedback as critical principles. It is notable that DAC usually serves as the input stage for its robustness to anti-common-mode noise and maximization of input resistance, which is important in reducing electrical couplings among neighboring feedback loops. One excellent example is ultrathin OECT as a key element in

voltage-parallel NFAC, which has been proven to successfully minimize the influence of external electrostatic noise¹²² (see Fig. 7B). A drop of 20 μL of 0.7 M NaCl solutions acted as a feedback module across the input/output sections. Under test conditions, the input noise of a 5 mV sine wave was suppressed without any output fluctuations observed.

Another report presents an ultrathin strain-insensitive temperature sensor based on a double-end DAC with selected strain-insensitive sections to compensate for the interference of strain⁴⁸ (Fig. 7D). The gate dielectric did not show any relationship with temperature fluctuation. Hence, its temperature sensing mechanism is directly related to the transistor charge transportation with an independent influence on the outputs. Therefore, the double-end DAC functions as a full compensation for the stretching interference in a common-mode input mode.

In the third example of a temperature sensor with strain compensation, researchers realized thermosensation on patterned Cr/Pt electrodes, which adopted an additional IOA with a voltage-parallel NFAC strategy¹⁶² (Fig. 7C). The mechanical load was fully applied to the sensor with increasing conductivity, which would be completely compensated by the common-mode effect. Herein, with the connection of a compensation thermistor and sensing thermistor to an IOA on “+” pin and “–” pin separately, only the varying temperature of the contact object can be transformed into output without the influence from the ambience.

In addition to analog circuit candidates, other electrical techniques have also been adopted to tackle this challenge. Certain achievements for distinguishing strain and pressure issues were established by virtue of a serpentine or spring structure to provide its functional components with anisotropic redundancies.^{76,97} For instance, the resistant position-sensing technique can be adopted to reduce the strain sensitivity in full along with a strict linear output (Fig. 7E).¹⁶³ Following resistive dividing principles, the output of position sensing is independently related to the distance ratio between the touching position and the total length of the sensor. Therefore, this solution is robust to a wide range of conductive materials without influencing the performance. Another thermal-mechanical sensing approach has been reported wherein a simple capacitive sensor architecture was employed¹⁶⁴ (Fig. 7F). The time constant τ , termed as $\tau = RC$, is common in the analysis of electric systems, where R is the resistance and C is the capacitance. The decoupled impedance capacitance information can be derived from the relationship of τ , R , and C . Obviously, only two of the three are independent varieties as two inputs, which are logically sufficient to decouple two independent outputs. Thus, either strain or temperature can be differentiated with further potential for use in real-time tactile analysis.

2.2.2.3 Pattern recognition algorithm for flexible electric multi-sensation. As mentioned above, wearable electronics for the next generation with a relatively simple structure are desirable and expected for a wide range of tasks, such as thermal

detection, electrophysiology transduction, mechanical pulse and heartbeat sensing, and motion differentiation. Since the 1980s, machine learning and neural network algorithms have been developed for wide applications, including pattern recognition and classification under intricate databases.^{173–175} Facing the challenge of such incredible tasks, recent research on machine learning and neural networks has been adopted to tackle the challenge of such incredible tasks with complex multi-model outputs.

(1) Classical machine learning algorithm (CMLA).

CMLA is a group of methods that are advantageous in small datasets. It can be studied with strict mathematical explainability, less time consumption, and lower costs. These characteristics facilitate its pioneering application in healthcare analysis. However, it is still limited by a specific mathematical model. Consequently, its portable ability for generalization and precise classification is greatly suppressed.

The typical categories of CMLA include support vector machine (SVM) classifiers, supervised clusters (*e.g.*, K nearest neighbor (KNN)), and non-supervised clusters (*e.g.*, K-means), among others. In addition, principal component analysis (PCA) is a type of linear mapping method that plays a role in data pre-processing and data dimensional reduction.

SVM classifiers have been utilized in linearly separable issues under the condition of convex optimization. Furthermore, nonlinear classification can also be realized by SVM *via* additional dimension or non-linear core functions. Recently, an ultrathin yarn-based stretchable array with an integrated flexible printed system served as an enhanced intelligent glove to realize sign-to-speech translation¹⁶⁸ (Fig. 8A). The PCA-SVM strategy proposes a recognition rate of 98.63% outcome with a dataset of 440 for training and 220 for random testing. Another research group designed two types of ultrathin triboelectric nanogenerator sensors (TENGs) integrated with a soft gripper to realize the proposed sorting¹⁶⁹ (Fig. 8B). Trained with SVMs, the soft gripper with self-powered sensors could continuously work *via* the designed sequential tooth-shaped integrated components in real-time detection. The accuracy of classification is displayed in the error matrix up to 98.1%.

kNN is a type of supervised cluster method based on a simple idea of ranking with similarity and sorting with a voting mechanism. As a result, the group that receives the most votes is selected and added with new members. Researchers have applied the kNN algorithm for ultra-compliant piezoelectric sensors to decode facial-deformation signatures¹⁷⁰ (Fig. 8C). Besides sensors attached to human faces record and establish a “motion library”—a collection of large facial feature datasets which is expected to be further interpreted into human language, the kNN algorithm is employed for real-time classification with continued calculation and comparison of the total

distance in $d = \sqrt{\sum_{i=1}^n (d_i)^2}$ as “similarity” mentioned above.

(2) Neural network with deep learning structure.

Before the breakthrough in deep learning, the neural network algorithm had stagnated for a long time. Traditional fully

connected deep learning (DNN) is further enhanced by the introduction of the ReLU activation function and dropout regularization principle. Compared with classical machine learning approaches, deep-learning networks are relatively simple iterating and transforming operations employed to tackle more complex datasets.¹⁷⁶ Under different frameworks published by tech giants, a rich source of code and instructions can be obtained freely. However, there is still a lack of proper mathematical theories to support its working principles, and it is a type of “black box.” Thus, reliable explanations of its outputs and rational improvements for the next step cannot be proposed. However, for advancement in big datasets and portable permission among different datasets, substantial healthcare devices based on DNNs have been approved by the US Food and Drug Administration (FDA).¹⁷⁷

One example combines sensing outputs with a DNN to render further perception and classification (Fig. 8D). A multi-layer flexible sensor was designed based on a patterned 180 nm Cr/Pt film as the top and bottom electrodes to recognize the interchange of heat among its hot electrodes, contact objects, and the ambience.¹⁷¹ Meanwhile, as a crack sensor, it also possessed the additional ability for mechanical force measurement. Under the DNN algorithm involving three hidden layers, the final outputs of the sorted features include the object's materials and shapes. The confusion matrix showed an accuracy for classification of 94% for seven types of objects. The other excellent work involved the use of optics, ionic liquid, and conductive fibers together to decode eight individual mechanical deformations with DNN (see Fig. 8E). The final accuracy was demonstrated to be higher than 95%.¹⁷²

3. Fabrication method

To make wearable electronics more compliant and portable, it is effective to reduce the thickness and stiffness of both passive and active components. Thickness reduction of the substrate results in difficulty in handling the total manufacturing process with a much lower mechanical modulus. Therefore, additional substrates for mechanical supports must be considered during fabrication.¹²¹ During the manufacturing process, it is vital to maintain it at room temperature and make the substrate smooth.⁵⁴ High temperature and energy processes can easily deform plastic thin films and induce wrinkling and shrinking. On the other hand, an additional surface-smooth technique is also important for the large-scale fabrication of high-performance transistor devices.^{29,151,178}

Solution-based approaches are highlighted here, which are attractive for low-cost and scalable methods with a wide range of options. To satisfy the emerging requirements mentioned in Section 2, the fabrication process for ultrathin devices is discussed in detail, followed by topology categories from 1D fiber-type materials to assembled thin-films.

3.1 Fiber-type materials

Recently, fiber-type structures are expected to be rapidly emerging candidates for ultrathin electronics. The advantages exist

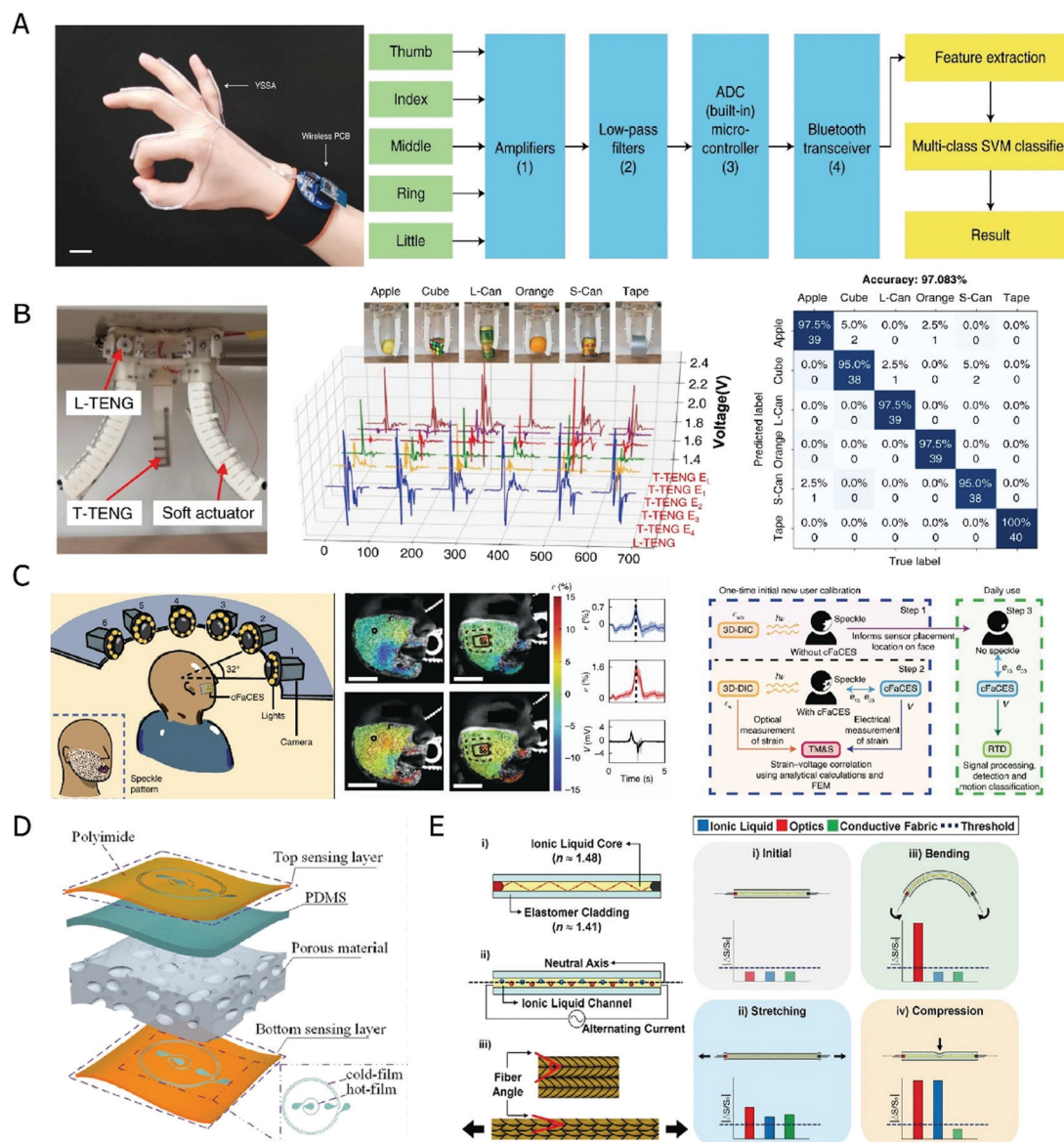


Fig. 8 Electrical solutions on machine-learning and other principles for anti-interference and multimodal recognition. (A) Machine-learning-assisted sign-to-speech translation sensor arrays.¹⁶⁸ Copyright 2020, Springer Nature. (B) Triboelectric sensors for soft robotics.¹⁶⁹ Copyright 2020, Springer Nature. (C) System for decoding of facial strains.¹⁷⁰ Copyright 2020, Springer Nature. (D) Skin-inspired quadruple tactile sensors enable object recognition.¹⁷¹ Copyright 2020, AAAS. (E) A multifunctional soft sensor for human-robot interfaces.¹⁷² Copyright 2020, AAAS.

not only in lower mechanical modulus and higher surface ratio and weight as presented in porous architectures, but also in remarkable gas/wet permeability to facilitate long-term usage and prevent inflammation.

The vapor deposition method is a conventional approach for the growth of 1D fiber-type and 2D thin-film materials, including physical vapor deposition (PVD),^{71,121,136} chemical vapor deposition (CVD),^{112,121} e-beam evaporation,⁶⁶ and RF magnetron sputtering.¹⁹⁴ ZnO thin films can grow along [0001] orientations cylindrically by PVD to form a thin fiber.¹³⁶ Au vacuum-deposited on PVA sacrifices fibers to form a nanowire as a non-substrate sensor.⁷¹ However, most of these approaches are hindered by high power consumption and cost for large-scale fabrication.

Solution-based approaches for nano-/micro-fibers have been extensively investigated with three key classifications: electrospinning,^{186,195–198} blow-spinning^{109,189,192,199,200} and wet-spinning^{75,181} (see Fig. 9A). All of these have been reported as multifarious outputs of more than 100 different categories. Either functional elements (see Table 1) or passive substrates (see Table 2) were fabricated into fiber shapes. First, the electrospinning technique has been studied since the 1930s and has produced several reports until the 2000s.²⁰¹ Typical working principles rely on the flow of a precursor jetting through the needle tip of a spring pump driven by a high voltage. Fibers at the early stage generally maintain a uniform shape. Subsequently, they are rapidly interrupted and whipped

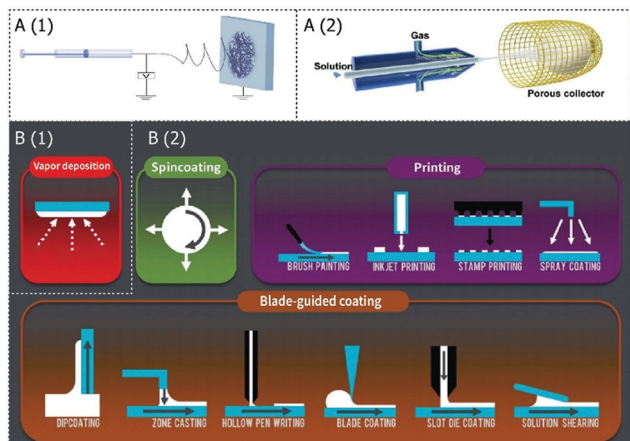


Fig. 9 (A) Schematic illustration of fabrication technique of fiber-type materials: (1) the basic electrospinning setup.¹⁹² Copyright 2019, Elsevier; (2) the basic blow-spinning setup.¹⁹² Copyright 2019, Elsevier. (B) Schematic illustration of fabrication technique of thin-film materials: (1) vapor deposition; (2) solution-based manner.¹⁹³ Copyright 2014, The Royal Society of Chemistry.

by an unstable airstream to make them prolonged and thinner. Ultimately, the collected fibers appear on an electrode pad connected to the ground terminals. Thus, fibers are aligned in an amorphous orientation to render a porous mat owing to van der Waals forces. Second, compared with electrospinning, blow-spinning is based on a relatively simple set of equipment, including a core-shell nozzle and a scalable collector. Fibers jet through the inner nozzle by the force of the pumping spring and are then accelerated by high-speed gas through the shell nozzle. With the proper viscosity of the precursors, a uniformly thinner fiber could be fabricated by the shear force of the high-speed gas. Furthermore, wet-spinning directly forces the solution of precursors through a narrow nozzle to form a flow in the thread, which is post-cured in a cooling water bath. In the absence of the additional straining force from the high-potential electric field of electrospinning or high-speed gas of blow-spinning, the thickness of wet-spinning fibers is relatively thicker than the former ones, and its speed of manufacturing is much lower, which limits its broad application.

There are several key issues that both electrospinning and blow-spinning are closely associated with. First, the viscosity of polymer solutions (e.g., molecular weight of the polymer used, concentration of the polymer in solution, and surface tension of the solution) has a significant impact on the fiber diameter and morphology. It has been reported that a 5% PLA solution in 2,2,2-trifluoroethanol (TFE) can produce fibers with diameters as low as 40 nm.¹⁸⁹ Second, mechanical forces between the pre-needle and on-needle sections, which are related to the injection rate and generated by the applied voltage in electrospinning or gas flow in blow-spinning, have a significant but relatively small impact on the fiber diameter. A higher pressure leads to a thinner fiber. Moreover, the diameter uniformity may be improved by a higher injection rate. Third, both the geometrical size of the nozzle and the distance between the tip and

collector have relatively small impacts on the morphology of its products.

3.2 Thin-films

The manufacture of transistor-based devices with multi-layered 2D thin-film structures has been widely studied *via* a variety of techniques, from vapor deposition to solution-based methods (see Fig. 9B). Consequently, advanced manufacturing has been developed considering scalability in large-area, high-speed for enhanced throughput, low cost, and energy consumption. High mobility is crucial to the evolution of semiconductor films. As mentioned above, it relies on the morphology in its inner crystalline (such as π -conjugation and π -stacking) and amorphous (such as chain orientation and alignment) domains. Therefore, further consideration and delicate control are required during the entire process.

Conventional fabrication methods for organic semiconductors rely mainly on vapor deposition. Various semiconductor materials have been successfully studied. Several vital factors have substantial influence on its output.^{96,141} First, the gas pressure in the tube furnace is directly related to the purity of the organic films, particularly in the initial accumulation layer. The quality of the crystalline thin film is determined by the first stage with only a few monolayers deposited. It has a further deep effect on the mobility and on-off ratio of transistor-based products. However, substrate temperature also has a close relationship with the morphology of crystalline and amorphous domains, which is demonstrated as a non-linear relationship. Above all, the basic equipment for a vapor deposition system involves a vacuum chamber, a sophisticated gas pressure controller, and a temperature manager in high-cost and relatively complex technical procedures. However, it is still an excellent candidate for making an encapsulation layer, such as depositing parylene *via* CVD, on thin-film devices.^{9,112,120,121,151} The reported thickness of the parylene ultrathin film can be set to 100 nm, which renders it an essential component in imperceptible bio-electronics.⁹

Solution-based methods are gradually becoming more dominant in advanced thin-film manufacturing. They have been widely utilized in ultrathin transistor-based systems, including substrates, semiconductors, dielectrics, and conductor layers. A general category of solution-based methods includes spin-coating, blade-guided coating, and printing techniques. While spin coating is the most common approach at the research scale to minimize the thickness of the substrate and is usually an effective procedure to fabricate ultra-thin electronics, its disadvantages are also noteworthy, which results in a ~90% waste of precursor solutions.⁹⁶ Furthermore, only a uniform distribution of shear force on the total film by such a simple approach can render its crystalline morphology with specific shapes difficult to control. To form thin films on silicon slices, the boiling point of solvents and hydrophilic treatment of the surfaces should also be considered. Finally, its small-scale productivity, only in serial mode, largely limits its practical applications.

Table 1 Functional fiber-type materials

Functional materials	Solvent	Concentration	Manufacturing type	Manufacturing factor	Diameter	Type	Ref.
Te-PEDOT:PSS nanowires	L-Ascorbic acid (2 wt%)	78 mg mL ⁻¹	Reduction and centrifugation	90 °C	10 nm/—	Conductor	161
P(VDF-TrFE)/(poly(vinylidene fluoride-co-trifluoroethylene))	Dimethylformamide/acetone (DMF/acetone: 3 : 2)	21 wt% (polymer/solvent)	e-Spinning	30 kV, 1 mL h ⁻¹	260 nm/10–40 µm	Piezoelectrics	179
Silver nanowire/polyvinyl butyral (PVB)/melamine	Ethyl alcohol (EG) and additional 1.6 mL of CuCl ₂ ·2H ₂ O (3.3 mM) EG solution	AgNO ₃ 1 wt%; polyvinylpyrrolidone 0.8 wt%; PVB 0.8 wt%	Dispersion in commercial melamine sponge	—	Ag nano-wires dispersion with stirring	Conductor	109
PEDOT:PSS	Distilled water	2 wt%	Wet-spinning	0.5–2 mL min ⁻¹ ; and dried at 120 °C for 10 min	44–71 µm	Conductor	75
Carbon fibers-zinc oxide	—	—	Physical vapor deposition method with carrier gas made of oxygen and nitrogen gas (1 : 4) e-Spinning	40 sccm; temperature: 960 °C	10 µm (carbon fiber)	Piezoelectrics	136
P(VDF-TrFE) and PU (polyurethane)	DMF-acetone 3 : 2 in mass ratio	10 wt%	e-Spinning	30 kV, 0.015 mL h ⁻¹	—	Piezoelectrics	133
CNTs-graphene polymer	4-Methyl-2-pentanone (3 g); 2 g fluorinated copolymer, vinylidene fluoride-tetrafluoroethylene-hexafluoropropylene (Daikin, G912), 0.2 g ionic liquid (1-butyl-3-methylimidazolium bis(trifluoromethanesulfonyl)imide) DMF and DMSO (tetrahydrofuran)	Graphene (in 4-methyl-2-pentanone at 10 wt%); CNTs (in the same solvent at 0.1 wt%)	e-Spinning	15 kV, 2 µL min ⁻¹	300–700 nm	Conductor	180
PEDOT:PSS-PU	—	1 : 1 or 2 : 1 v/v ratio (PEDOT : PSS/PU)	Wet-spinning	5 mL h ⁻¹	20 µm	Conductor	181

Table 2 Passive fiber-type materials

Passive materials	Solvent	Concentration	Manufacturing type	Manufacturing factor	Assembly method with active component	Diameter (aver.)/thickness	Ref.
SBS (poly(styrene- <i>block</i> -butadiene- <i>block</i> -styrene))	1,2-Dichloroethane	16.7 wt%	e-Spinning	12 kV, 5 mL h ⁻¹	EGaIn coating with squeegee guided	2.7 μm/320 μm	76
PVDF	DMF	19 wt%	e-Spinning	20 kV, 10 μL min ⁻¹	Silver flaker-polymer mixture coating with glass guided	300–500 nm/—	182
PVA (poly-vinyl-alcohol)	Distilled water	10 wt%	e-Spinning	25 kV, 10 μL min ⁻¹	Vacuum deposition to deposit Au (100 nm)	—/1.8 μm	106
PU (with PVA sacrificed layer)	DMF: MEK (7:3)	15 wt%	e-Spinning	20 kV, 10 μL min ⁻¹	Vacuum deposition to deposit Au (100 nm)	—/10.5 μm	120
Polyurethane (PU SG 85 A)	DMF-acetone 1:1	15 wt%	e-Spinning	13 kV, 0.5 mL h ⁻¹	PDMS was spin-coated on the stacked nanofiber mat	200–250 nm	183
ZrO ₂ -Al ₂ O ₃ (zirconium acetate (Zr: 15–16 wt%), 1.269 g of AlCl ₃ ·6H ₂ O, 2.665 g aluminum isopropoxide)	Water/ethanol/acetic acid 1:1:0.375	Al/Zr = 5, 10, 15, 25, and 35 mol%	e-Spinning	25 kV, 4 mL h ⁻¹ , humidity and temperature: 30%, 22 °C, respectively	—	280–740 nm/150 μm	184
Polyacrylonitrile (PAN), AlCl ₃	DMF	12 wt% (PAN), 3 wt% (AlCl ₃)	e-Spinning, pre-oxidation, carbonization	e-Spinning at 15 kV, 0.6 mL h ⁻¹ ; pre-oxidation at 210 °C; carbonization at 1050 °C	—	180–200 nm/150 μm	185
PVDF-HFP (poly(vinylidene fluoride-co-hexafluoropropylene))	Methyl isobutyl ketone	15 wt%	e-Spinning	—kV, 2 mL h ⁻¹	Ag nanowires (35–50 nm)/ethanol solution dispersion by vacuum filtered	710–890 nm	186
Titanium dioxide (TiO ₂)	Anhydrous ethanol and acetic acid (3:1)	Poly(vinylpyrrolidone) (MW = 1 300 000) and tetrabutyl titanate (Ti(OBu) ₄) with mass ratio 1:1	e-Spinning	15 kV, 15 μL min ⁻¹ , humidity 40%	Ag nanowires spray-coated by a sliver jet compressor	120 nm (after sintering)/25–114 μm	187
SiO ₂ -Al ₂ O ₃	10% PVA-224 water solution	Added with different mass yielding sponges with Al and Si molar ratio of 0.1:1–0.8:1	Blow-spinning	50 kPa gas pressure	—	2.7 μm (after sintering)/—	188
PMMA	Chloroform	10 wt% (Mw = 120 000 g mol ⁻¹)	Blow-spinning	276 kPa gas pressure	—	0.84–3.46 μm	189
Cellulose	Lithium chloride/ <i>N,N</i> -dimethylacetamide	8 wt%	Blow-spinning	Over 45 °C, feeding rate of 0.3–0.8 mL min ⁻¹	—	160–960 nm	190
Cellulose	Potassium hydroxide (2 wt%) and sodium chloride (2 wt%)	1 wt%	Dispersion by high-pressure homogenizer	1500 bar	Mixture with Te-PEDOT nanowires	10–20 nm/<1 μm	161
Degummed silk	Sodium hydroxide (NaOH)/urea solution	3 wt%	Dissolved	–12 °C	—	Ribbon with width of 25 nm and height 0.5 nm/—	191

Blade-guided coating primarily refers to solution-based fabrication *via* a blade or similar objects by dipping into precursor solutions and applying extrusion into coating by viscous forces.^{76,96,193,202} Compared with spin-coating, the pre-aggregation and pre-nucleation of the polymer are amenable to the blade-guided orientation as inner aligned domains. In addition, it provides feasible solutions for scalable and high-throughput manufacturing. Different shapes of guiding blades,

such as bar-, pillar-, and slot-die-guide with different patterns, have approximate effects. Examples of transistor-based electronics with blade-guided nucleation and polymer orientations have been displayed.^{203,204} The size and density of the nucleation are closely related to the speed of the applied shear force. The higher the speed, the smaller and more dense the nucleation forms. Furthermore, the temperature of glass transition was also demonstrated to have an impact on the two-phase

interactions during the post-annealing period. Meanwhile, the wettability and boiling point of the solvent should also be considered.

Without complex patterning processes, printed electronics have become a promising candidate for solution-based manufacturing techniques, including inkjet,^{205–208} gravure,^{209,210} and screen printing.^{211,212} However, the resolution of the printed technique, always termed as “line/space” is still limited to tens or hundreds of micrometers. Both transfer printing *via* an elastomer stamp and nanoimprint lithography are relatively novel approaches that boast printing techniques for high-resolution products. In general, there are several key parameters including resolution, accuracy, uniformity either in a single unit or larger area, solubility and compatibility of inks, and wettability of the substrate for transfer printing.¹⁴⁵ Recent reports have demonstrated 3–30 μm poly(styrene-butadiene-styrene) (SBS) substrates using spin coating.¹¹¹ To produce transparent and ultrathin (typically less than 1 μm) electronics, substrates and conductive lines were further demonstrated by the screen printing technique. However, the thinness of printed circuits leads to considerable parasitic resistance. It has been reported that a printed line of 1 μm in width, 100 nm in thickness, and 1 mm in length from a polymer conductive ink with a square-resistivity of $10^{-3} \Omega \text{ cm}^{-1}$ outputs a tremendous total resistance of 100 k Ω .¹⁴⁵ Moreover, the issue of uniformity for either a printed conductor or semiconductor could be more complicated under the condition of an ultrathin device. Furthermore, more parasitic capacitance and leakage current should also be considered during the fabrication process.

4. Application

Recently, intensive research has been conducted to implement the reduction of both thickness and weight, generating higher conformity along with various non-zero Gaussian-curvature bio-surfaces. The range of applications includes implantable devices such as tissue-mountable electronics, skin-mountable devices, wearable e-fibers, and e-textiles. Ultrathin electronic devices with high-performance properties and impressive mechanical durability have been reported and largely enhanced recently as well.

4.1 Implantable devices

Implantable electronic devices for biosignal monitoring and medical invasive treatments have attracted considerable attention. For biosignals, *in vitro* diagnosis is not precise and sufficiently valid in different physical conditions, particularly for long-term health management of chronic diseases and neural signal recognition. Medical invasive treatment, which is a traditional topic in miscellaneous diseases, has suffered from inflammation reactions, possibilities of displacement, and limited spatial distribution for a long time. Thus, an implantable device for the next generation is required with sterilizability, biocompatibility, and ultrathin-thickness with

miniaturized shapes, yielding an upmost degree of compliance and flexibility.

As mentioned before, recently, implantable bio-monitoring devices have benefited from OECT technology for electrophysiology monitoring and depth probes of neurological signals.^{153,217–220}

First, for electrophysiology recording, solutions turn to flexible electrodes that are usually manufactured with a broad range of thicknesses from centimeters to hundreds of nanometers.^{3,109,118,120,162,221–223} To achieve higher spatial resolutions and high SNRs with multi-pixelated arrays, active components have been successfully introduced for local amplifications. Among various transistor-based candidates, the OECT is a competitive one with excellent transconductivity that improves the SNR with a low threshold and operation voltage to match the corresponding magnitude of physiological signals. For example, an ultra-thin transistor-based multielectrode has been reported as a 4×4 array with a total thickness of only 2.6 μm .⁵² Its transconductance and SNR reached as high as 1 mS and 52 dB, respectively. In addition, the challenges of tissue-attachable devices are more complex in dynamic physiological changes *in vivo*. Tissue bleeding, clotting, inflammation triggering, rejection reaction, wound healing, and stromal fibrous proliferation results in displacement of both thin-film devices and other unexpected body injuries. In this report, an anti-clotting layer made of 100 nm-thick poly(3-methoxypropyl acrylate) was coated on the entire device to serve as an antithrombotic component. Thus, it realizes relatively high long-term service. For healthcare electronic systems, the other leading example is an implantable OECT sensor with integrated ultrathin organic photovoltaics to accomplish a self-powered cardiac signal recording system. The SNR of integrated system is reported as 25.9 dB which is threefold higher than previous reports, owing to its photovoltaic power component using stable light illumination without additional noise influx.

Second, for neurological signals, recent remarkable efforts have concentrated on depth probes of neurological signals as well as artificial sensory nerves applied on neural interfaces.^{49,152,219,224–227} Compared with electrodes, the OECT has demonstrated superior performance in recording neural activity (see Fig. 10E). To support medical treatment for epilepsy, back pain, and Parkinson's disease, implanting depth probes to stimulate individual or local neurons using ultrathin electrodes for minimally invasive effects (Fig. 10B) has been found to be effective. One prominent report featured a mechanical detachable component to guide the OECT device while penetrating the brain cortex (Fig. 10A). With a total thickness of only 4 μm , the OECT has shown excellent performance for local neuron monitoring and stimulation eliciting minimal glial scarring for 1 month *in vivo*.⁴⁹

Third, as an integrated platform, an artificial sensory nerve is a type of bio-inspired transistor-based electronic system that realizes human-like sensing processing abilities such as self-learning, memorizing, and multi-level perception. Although functionalities such as self-learning and memorizing have

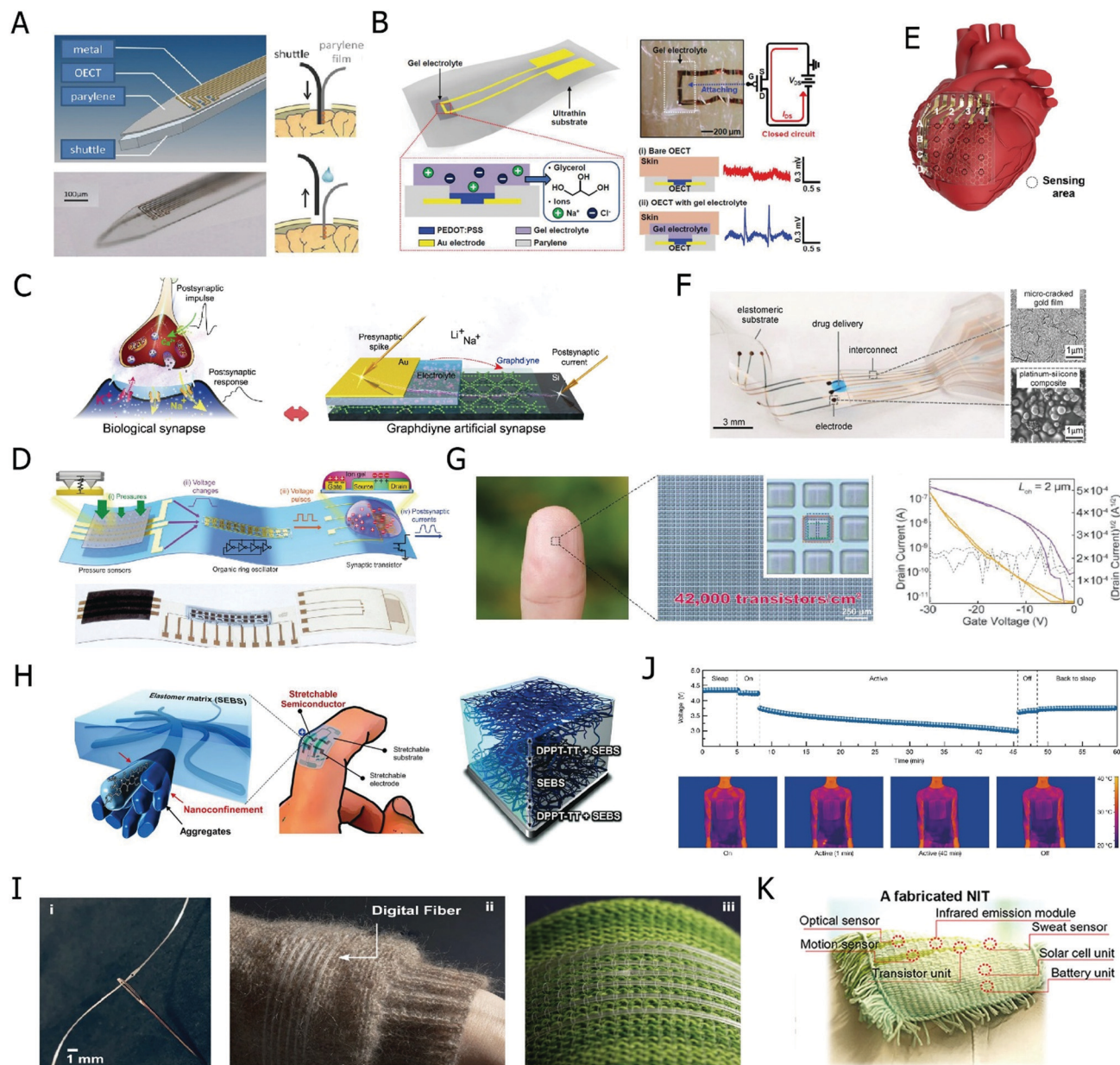


Fig. 10 (A) Localized neuron depth probes.⁴⁹ Copyright 2015, Wiley-VCH. (B) An ultrathin organic electrochemical transistor for electrophysiological monitoring.¹²² Copyright 2019, Wiley-VCH. (C) Mimicking efferent nerves by a graphdiyne-based artificial synapse.²¹³ Copyright 2021, Springer Nature. (D) Organic artificial afferent nerves.²¹⁴ Copyright 2018, AAAS. (E) Multielectrode array for electroanatomical mapping.⁵² Copyright 2018, AAAS. (F) Electronic dura mater.⁸ Copyright 2015, AAAS. (G) Optical microlithography of high-density elastic circuits.⁵⁰ Copyright 2021, AAAS. (H) Semiconductor films through the nanoconfinement effect.⁴² Copyright 2017, AAAS. (I) Digital electronics in fibers enable fabric-based machine-learning inference.²¹⁵ Copyright 2021, Springer Nature. (J) Woven lithium-ion fiber batteries.⁵⁶ Copyright 2021, Springer Nature. (K) Integrated-circuit textile for wireless theranostics.²¹⁶ Copyright 2021, Springer Nature.

recently been developed and discussed, here we focused on artificial neural subnetworks generating action potentials and applying feedback to nerves as bidirectional information approaches, which is the state-of-the-art emulation of neural activities to our knowledge (Fig. 10C and D). An outstanding example is the all-organic-plastic artificial afferent nerve.²¹⁴ The artificial afferent nerve comprises three components: pressure sensors, organic ring oscillators, and synaptic transistors, which represent bioinspired mechanoreceptors, nerves, and

biological synapses (Fig. 10D). Its activating signal is generated from force detection, transforming into the frequency and magnitude features of the organic ring oscillators. Emulating our biological heminode, the ring oscillator can combine a group of sensors with recorded and processed inputs in parallel via the following synaptic transistors. The other example originates from an ultra-flexible electronic dura mater to galvanize muscles on the leg of moving mats and deliver drugs concurrently⁸ (Fig. 10F). It has been demonstrated as a

high-resolution neural recorder and electronic and chemical treatment for long-term use without inflammation. However, the degree of anticipation of flexible electronic neural systems directly into bio-activities or health treatment is limited by our limited knowledge of neuroscience and brain science. The development of more biocompatible and accurate ultra-flexible electronics is still required by multidisciplinary researchers to address this issue.

4.2 Skin-mountable devices

A considerable number of reports and progress have been focused on skin-mountable devices to monitor human health-care and enhance human abilities, such as the control of prostheses, robots, and other domestic electronic devices. In conclusion, it is desirable for wearable electronics in the next generation with a higher level of wearable comfort that is inflammation-free under long-term service and has highly integratable systems with ultra-lightness architecture.

Electronic skin (e-skin) is a good candidate as a promising sensing component, for example, pressure sensors, strain sensors, temperature sensors, and chemical detection sensors. In addition, sensors based on a triboelectric mechanism with a relatively simple architecture could serve as an important option for realizing self-power wearable devices.^{228,229} An ultra-thin organic photovoltaic device,²³⁰ with an extremely light weight of 4 g m^{-2} without sacrificing high performance, is also an effective power alternative to be considered.⁷² Organic based ultra-flexible processors and short-/long-term memory modules, such as artificial synapses and flexible memristors, are also desirable substitutes for silicon-based processors, opening opportunities to start a new era based on the advances in the traditional von Neumann architecture^{42,50,213,231–234} (see Fig. 10G and H). Although most integrated reports are still incorporated with silicon electronics, the possibility of realizing a skin-mounted electronics system with all-organic components should be explored.

4.3 Wearable e-clothes

Wearable electronic clothes (e-clothes) have been intensively studied for healthcare purposes. A cluster of various topologies of different fiber shapes is emerging, for example, segmental connection, core-shell, coil, and telephone wire structures. Without direct attachment to the human body or tissues, it is much safer with a much lower possibility of causing inflammation. Additionally, with porous characteristics, it is naturally bendable, rollable, and stretchable, enabling good conformity to match the body dynamics. Gas/sweat permeability is also directly related to the porosity of the woven substrates, while washability and durability need to be further concentrated upon during the prototype stages of structural design. As a type of portable device, thinner and lighter ones need to be appreciated, as well as mass production at low cost.

As a technology in the lab scale, the concept of healthcare service *via* ultrathin electronics is gradually replenished with bioelectronics recording, motion detection and signal processing^{51,70,132,235} (see Fig. 10I). Flexible woven displays

and power components are key drivers in this research field. For flexible displays, the traditional solution is an OLED that relies on the mechanism of carrier injection. However, woven structures with narrow contact areas such as warps-wards fail to provide sufficient interactions for carriers, thereby limiting their further application. Recent advances have focused on large-scale display textiles that depend on ZnS phosphor coatings to fabricate luminescent fibers.⁵¹ Combined with the decoding of the electroencephalogram in the test, the translated human language can be displayed on the woven e-cloth as an emergency warning. Similar to skin-mountable devices, the power source of e-textiles is another focus. In addition to the textile of solar cells, triboelectric yarns have been gradually explored recently.^{70,236,237} Although the assembly of all e-textile systems, containing power sources and displays, is still poorly understood, pioneer reports have already demonstrated e-textile systems^{56,216} (Fig. 10J and K). Examples include photovoltaic cells, batteries, wire-type infrared emission modules, transistors as active components, tensile stress sensors, pH sensors, and optical sensors within non-printed wire- and fabric-types.²¹⁶ The principle of this research could serve as a general design for application in a wider range of wearable devices.

5. Conclusions

As reviewed above, extensive efforts have been made in the research of transistor-based flexible devices for healthcare monitoring and medical treatments. Approaches to obtain high SNR, wearing comfortability, and minimally invasive solutions are highlighted for ultrathin devices with thicknesses ranging from 300 to the sub-micron scale. Thinner devices could render more conformal and closer interactions with the 3D complex bio-surface. Furthermore, mechanical robustness has also been greatly improved by a specifically designed structure.

As core components in electronic wearable systems, a cluster of various organic transistors is actively applied in sensors, signal processors, transmitters, and communication modules. Apparently, they possess great potential to address a range of requirements and challenges, yielding a new paradigm for wearable healthcare devices. We have concentrated comprehensively not only on the mechanical requirements such as stratagems of strain-insensitivity and permeability, but also the electronic challenges of SNR, including high sensitivity and anti-interference in detail. These basic principles and pioneering studies can be very helpful for rational selection and design with optimum performance. Moreover, there are a range of new applications of such devices for the long-term monitoring of physiological features as well as medical treatments that interact intimately with the human body. Many bio-inspirations have been made to realize neural imaging and optogenetic electrophysiology *via* transparent micro-electrodes or transistor arrays. Furthermore, ultrathin and stretchable organic synaptic transistors have been demonstrated with sensing applications as low-/high-level signal processors. Artificial skin perception

components shift the data-processing from the centralized processing unit to local and instant modes, which is an important step toward the authentic “Internet of Things” (IoT).

However, there is still much work to be done at the integration level. To date, the higher integration level of all ultrathin and ultraflexible sensors, processors, power sources, memorizers, and communication components within a common platform has rarely been reported. For practical implementation of ultrathin healthcare electronic systems, certain engineering issues and challenges still exist and need to be solved.

First, the issue of mechanical mismatch is more prominent on a multilayer architecture with vertically aligned components interconnected with each other. The “Achilles heel” of mechanical robustness always lies amid the rigid and flexible interfaces, as well as the different mechanical properties of the selected materials. Worse still, mechanical breakage of such interconnections often occurs with dynamic movement. In addition, reliable adhesion between the rigid and flexible interface is still urgently needed.

Second, a complete brain-inspired sensing-processing system requires distributed sections with a relatively large number of sensing arrays, multi-modes, ultra-flexible, and resilient mechanical properties. With a steep increase in the number of pixelated arrays, real-time sensing and processing are thus not trivial questions. Compared with parallel processing speed of our neural system, there is still a huge gap within wearable electronic devices toward realizing the “Internet of Things.” In addition, flexible analog and digital circuits for wireless data transmission also need to be explored for efficient sending and receiving.²³⁸

Third, pattern recognition technologies are promising candidates for high-level signal processing with sufficient support from the developed theory. With multidisciplinary background of wearable healthcare electronics, the influx of compound approaches from different backgrounds have been emergency to satisfy the challenges of the next generation. Furthermore, artificial synapses with ultrathin transistors for flexible processors are crucial for the development of all-flexible electronics.

Conflicts of interest

There are no conflicts to declare.

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