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17.6% stabilized efficiency in low-temperature processed planar perovskite solar cells†

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We present here a planar perovskite solar cell with a stabilized power conversion efficiency (PCE) of 17.6% at the maximum power point and a PCE of 17% extracted from quasi-static $J-V$ with an open-circuit voltage of 1.11 V. Such excellent figures of merit can be achieved by engineering a solution-processed electron buffer layer that does not require high temperature steps. A compact thin film of perovskite absorber is grown onto a PCBM-based electron extraction layer by implementing a novel two-step procedure which preserves the soluble organic interlayer during the deposition of successive layers. We demonstrate that efficient charge extraction is the key for high steady state efficiency in perovskite solar cells with a highly integrated architecture.

Hybrid lead halide perovskite solar cells (PSCs) are experiencing a drastic boost in solar energy conversion performances, with reported efficiencies approaching crystalline silicon solar cells.¹ Methylammonium lead iodide $\text{CH}_3\text{NH}_3\text{PbI}_3$ or its analogue, chlorine-doped $\text{CH}_3\text{NH}_3\text{PbI}_3$, is typically employed in order to achieve good light harvesting, with an absorption onset close to 800 nm and an open circuit voltage close to 1 V. Stemming from the field of dye-sensitized solar cells, the first device architecture deployed $\text{CH}_3\text{NH}_3\text{PbX}_3$ ($\text{X} = \text{Br}, \text{I}$) as sensitizers on a mesoscopic TiO_2 layer.² Since then, the demonstration of ambipolar charge transport within the perovskite layer,³ with diffusion lengths of photo-carriers exceeding 1 μm ,^{4,5} has led to the development of planar structures. The mesoscopic TiO_2 has been replaced only by a flat TiO_2 layer, working as an electron-extracting layer (EEL),

Broader context

One of the most promising approaches to boost efficiency in photovoltaics is to make tandem solar cells. The challenge is to find a way to make this approach economically viable, by reducing the production costs. Over the past few years, organic-inorganic lead halide perovskites have emerged in the PV technology scenario. The rapid progress in the field has led to certified solar energy conversion efficiencies above 20%, which brings this technology to the cohort of highly performing solar cells, while keeping the promise of low cost energy production. All-perovskite-based tandem solar cells may represent the ultimate goal considering the relatively easy tunability of the semiconductor's bandgap. However, on a shorter term, the realization of hybrid systems which couple perovskite devices with market-leading technologies is highly attractive. For achieving these targets we need to obtain perovskite solar cells with high steady state efficiency (and thus reliable) and to deliver a device architecture and fabrication process that enables multi-layered structures.

on top of which the perovskite semiconductor is deposited. Finally an organic semiconductor is generally used as a p-type hole-extracting layer (HEL) [*e.g.*, 2,2',7,7'-tetrakis(*N,N*-di-*p*-methoxyphenyl-amine)-9-9'-spirobifluorene (spiro-MeOTAD), poly(triarylamine) (PTAA), poly(3-hexylthiophene) (P3HT)].⁶ This architecture, where the perovskite is grown on the EEL, *i.e.* the transparent electrode works as an anode, is indicated as a standard structure. The planar architecture is definitely advantageous from a fabrication point of view. This is particularly true when aiming at the promising implementation of perovskite-based solar cells in tandem structures.⁷ However, the TiO_2 -based flat device, though showing efficiencies surpassing 19%,⁸ is also affected by important shortcomings. One primary limitation is the necessity of a 500 °C sintering step to form a crystalline TiO_2 layer, precluding its application on temperature sensitive substrates, including plastic foil, silicon solar cells, and second perovskite solar cells. Although low-temperature solution-processed TiO_2 , ZnO nanoparticles and cesium carbonate have been occasionally reported as EELs,^{8–10} the notorious hysteretic behaviour has so far prevented the correct evaluation of their potential.^{11,12}

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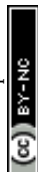
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To address these issues, indium tin oxide (ITO)/poly(3,4-ethylenedioxythiophene)-poly(styrenesulfonate) (PEDOT:PSS)/perovskite/fullerene derivative/aluminium devices (indicated as an inverted structure) have been developed with simple low temperature processing. Efficiencies of up to $\sim 16\%$ have been reported for low-temperature processed polycrystalline films.^{13–15} The degradation of the underlying perovskite layer in such a structure is prevented by processing fullerene derivatives, *i.e.*, [6,6]-phenyl-C₆₁-butyric acid methyl ester (60-PCBM), from orthogonal solvents. On the other hand, the realization of a structure where the electron selective layer is used as a bottom layer for PSCs with low-temperature processing would make the device architecture much more versatile, both because it would be opened to a large library of p-type layers that could be used on top of the perovskite layer and because it would simplify the realization of tandem perovskite structures.¹⁶ Wojciechowski *et al.* employed a self-assembled fullerene monolayer to modify the TiO₂ surface.¹⁷ This solution leads to an improvement of the steady state operation of the devices (a power conversion efficiency of approximately 13% is demonstrated with a scan rate of 0.0015 V s^{−1}). However, it still needs high temperature processing. Ryu *et al.* succeeded in the fabrication of a standard architecture at low temperature ($\sim 100^\circ\text{C}$), *i.e.*, FTO/PEI/60-PCBM/CH₃NH₃PbI₃/PTAA/Au, but such a device still suffers from large hysteresis.¹⁸

In this communication we propose the use of a low-temperature solution-processed titanium oxide (TiO_x)/60-PCBM layer that conformably covers the FTO as an EEL on top of which the hybrid perovskite can be directly deposited. Moreover, we demonstrate that by implementing a novel two-step procedure for growing the perovskite films we are able to form a compact, uniform layer that preserves the soluble 60-PCBM interlayer even upon spin coating of the organic hole-extracting component spiro-MeOTAD. This has a broader impact on the design and optimization of future EELs as their choice will not be limited anymore by the processing

of the top layers. Remarkably, we demonstrate a planar junction with a standard architecture that is able to achieve a 17.6% stabilized power conversion efficiency.

TiO_x is synthesized starting from titanium(IV) isopropoxide and deposited directly through spin coating onto FTO substrates in order to get a nearly conformal coverage of the rough FTO (see the ESI† for details of the synthetic procedure). An extremely thin layer of 60-PCBM is subsequently spin cast on top of TiO_x. The TiO_x/60-PCBM layer serves as an EEL (see the ESI† for the characterization of its surface work function, Table S1 (ESI†), and morphology, Fig. S1, ESI†), which shows comparable optical transmittance as TiO₂ deposited according to the procedures used in state-of-the-art planar heterojunction perovskite solar cells (see Fig. S2 in the ESI†).^{17,19–23} To fabricate the CH₃NH₃PbI₃ film the traditional one-step method is unsuccessful, since any combinations of dimethylformamide (DMF), dimethyl sulfoxide (DMSO), or γ -butyrolactone (GBL) are detrimental to the very thin underlying 60-PCBM layer. Similarly, the direct application of known two-step methods using such solvents is prohibitive. Thus, we developed a new two-step process compatible with 60-PCBM that combines a thermally evaporated PbI₂ layer followed by MAI deposition by spin coating from 2-propanol, a bad solvent for 60-PCBM. Following MAI deposition, the light yellow PbI₂ films turn slightly darker, indicating the interfacial reaction between PbI₂ and MAI, hence the formation of a very thin CH₃NH₃PbI₃ layer. The remaining dense PbI₂ layer together with the already transformed CH₃NH₃PbI₃ layer would prevent further MAI penetration at room temperature.²⁴ However, upon thermal annealing at 100 $^\circ\text{C}$ the film turns totally dark, indicating an extended transformation of PbI₂ into CH₃NH₃PbI₃ perovskite crystals. To check the quality of the polycrystalline thin film and the presence of any unconverted PbI₂ precursor, we measured the XRD patterns of the hybrid perovskite deposited on both TiO_x/60-PCBM and TiO₂ layers, spun cast onto FTO substrates. Fig. 1a shows, in the

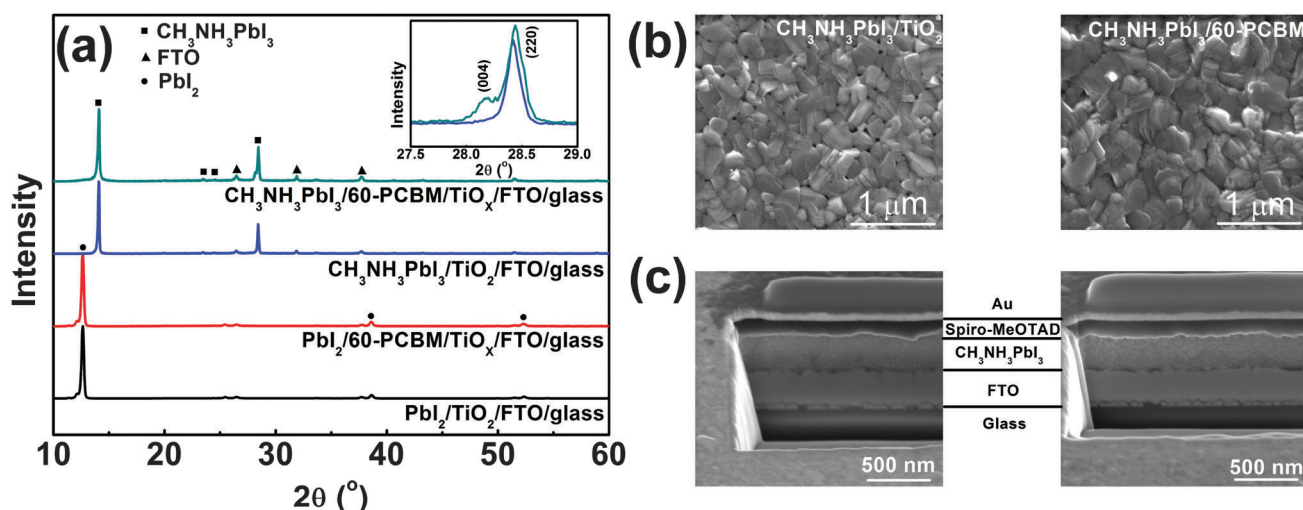


Fig. 1 (a) XRD patterns of PbI₂ on glass/FTO/TiO₂ and glass/FTO/TiO_x/60-PCBM before and after the deposition. The inset shows the enlarged CH₃NH₃PbI₃ XRD pattern between 25° and 30° on two substrates. (b) SEM top-view of CH₃NH₃PbI₃ grown on TiO₂/FTO/glass (left) and 60-PCBM/TiO_x/FTO/glass (right). (c) SEM cross section of complete photovoltaic devices: glass/FTO/TiO₂/CH₃NH₃PbI₃/spiro-MeOTAD/Au (left) and glass/FTO/TiO_x/60-PCBM/CH₃NH₃PbI₃/spiro-MeOTAD/Au (right). Note that the thin TiO₂ and TiO_x/60-PCBM layers present between the FTO and CH₃NH₃PbI₃ are not resolved in the SEM images.



bottom panel, the XRD patterns of thin layers of PbI_2 evaporated on the amorphous ($\text{TiO}_x/60\text{-PCBM}$) and the crystalline (TiO_2) substrates. They both look identical to the one obtained from solution-processed films,²⁴ exhibiting a preferential orientation along the c axis accompanied by the appearance of four diffraction peaks, corresponding to the (001), (002), (003), and (004) lattice planes. The top panel of Fig. 1a shows the XRD patterns of the hybrid perovskite films grown on these two substrates. The absence of any PbI_2 diffraction peaks indicates the full conversion in both cases. The perovskite shows the typical peaks of the tetragonal phase. However, it is interesting to note that while the crystalline TiO_2 substrate induces a preferential orientation of the perovskite grains mainly along the [220] crystallographic direction,²⁵ the amorphous substrate induces the preferential orientation of the grains also in the [004] crystallographic direction [see the inset in Fig. 1(a)]. In Fig. 1(b) we show a top-view SEM image of the perovskite films grown on these two substrates. Generally, the uncontrolled precipitation of perovskite using one-step spin casting from a mixture of PbX_2 and $\text{CH}_3\text{NH}_3\text{X}$ ($\text{X} = \text{Cl}, \text{I}$) in a common solvent produces large morphological variations, in particular numerous pinholes in planar PSCs. We show that our novel two-step hybrid procedure allows for better control of the thin film uniformity. A closer inspection of the surface images suggests that the presence of the amorphous substrate allows for an even better coverage. This may be induced by the growth of the crystals along different planes, possibly allowing denser packing.

To complete the device, we deposited by spin coating a spiro-MeOTAD layer as HELs followed by gold *via* thermal evaporation. The ability to create a uniform and compact $\text{CH}_3\text{NH}_3\text{PbI}_3$ film is essential for the deposition of spiro-MeOTAD that is dissolved in chlorobenzene (CB). The presence of pin holes would leave free paths for CB penetration through the perovskite films, re-dissolving the 60-PCBM layer. In Fig. 1(c) we show a cross section of the full device employing extremely thin layers of crystalline TiO_2 and $\text{TiO}_x/60\text{-PCBM}$ EELs. The total thickness of these two devices is almost the same, with very uniform perovskite and HELs. The image also confirms that the EELs, hardly distinguishable, simply form a conformable layer that covers the FTO surface.

Fig. 2(a) and (b) show representative current density *versus* voltage (J - V) characteristics of optimized planar devices with TiO_2 ^{17,19–23} and $\text{TiO}_x/60\text{-PCBM}$ as EELs, measured under air mass 1.5 global (AM 1.5G) conditions. To check for possible hysteresis phenomena, which are known to strongly influence the device testing for many perovskite solar cells reported so far in the literature,^{11,12} we show the J - V curves as a function of the scan rate, in forward and reverse scan directions. Table 1 summarizes the main figures of merit of the tested samples, *i.e.*, short-circuit current density (J_{SC}), open-circuit voltage (V_{OC}), fill factor (FF) and power conversion efficiency (PCE). Fig. 2(c) shows the transient behaviour of J_{SC} for both devices. The differences are striking and clear. The TiO_2 -based device shows performances which are strongly dependent on the scan rate and direction. As already reported in ref. 12, high PCE (14.4%) can be achieved only upon a fast scanning of the device from open-circuit to short-circuit conditions (reverse scan).

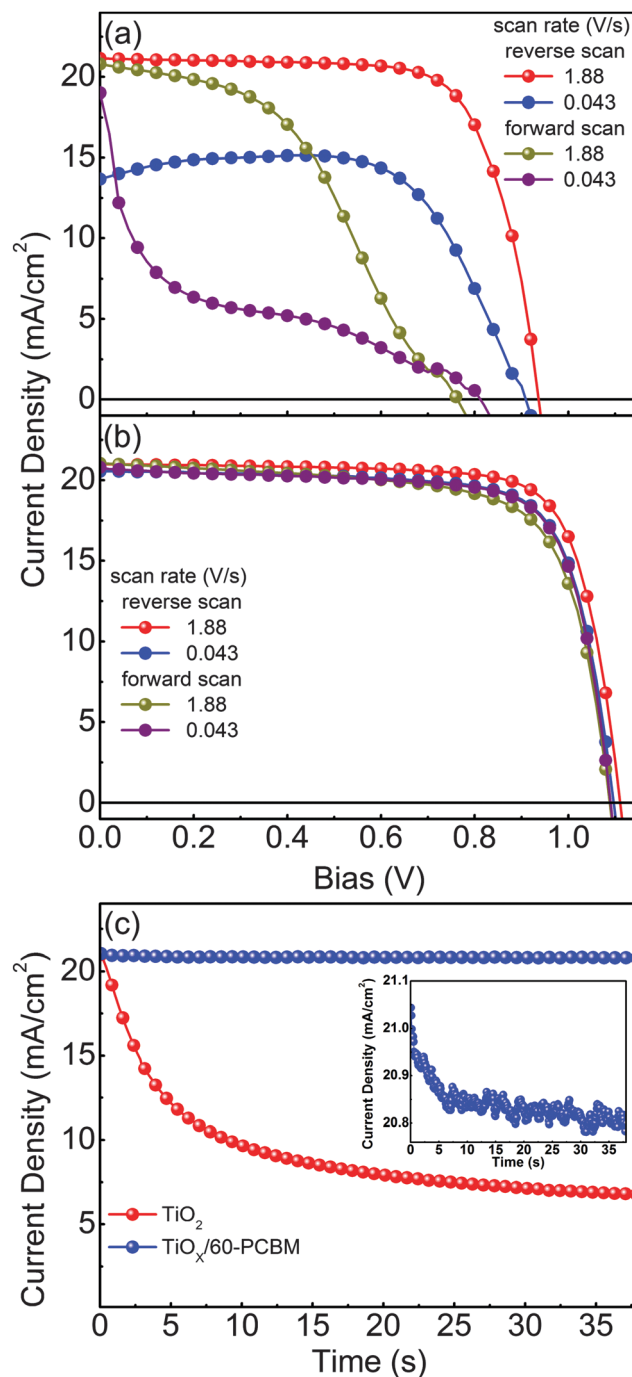


Fig. 2 J - V curves for photovoltaic devices $\text{FTO}/\text{TiO}_2/\text{CH}_3\text{NH}_3\text{PbI}_3/\text{spiro-MeOTAD}/\text{Au}$ (a) and $\text{FTO}/\text{TiO}_x/60\text{-PCBM}/\text{CH}_3\text{NH}_3\text{PbI}_3/\text{spiro-MeOTAD}/\text{Au}$ (b) measured with different scan rates (V s^{-1}) and different scan directions under AM 1.5G white light illumination with an intensity of approximately 100 mW cm^{-2} . Note that the devices were exposed for 10 s to white light before testing. No pre-polarization has been performed. (c) Corresponding short-circuit current density transients during illumination.

However, such apparent PCE drastically drops when the scan rate slows down (8.8%), and is even lower when the scan direction is inverted (6.9% and 2.3% for fast and slow scan rates, respectively). It should be noted that no pre-polarization conditioning has been performed, differently from what is generally done in

Table 1 Summary of the performances of the devices

	Device	Scan rate (V s ⁻¹)	V _{OC} (V)	J _{SC} (mA cm ⁻²)	FF (%)	PCE (%)
TiO ₂	Reverse scan	1.88	0.94	21.1	72.5	14.4
		0.043	0.91	13.8	69.9	8.8
	Forward scan	1.88	0.76	20.8	43.9	6.9
		0.043	0.81	19.0	14.7	2.3
TiO _x /60-PCBM	Reverse scan	1.88	1.11	21.0	76.6	17.9
		0.043	1.10	20.6	75.2	17.0
	Forward scan	1.88	1.09	21.1	70.8	16.2
		0.043	1.09	20.7	74.6	16.9

most of the published work; this may explain the lower figures of merit with respect to the literature value only when scanning the device from short-circuit to open-circuit conditions (forward scan).^{17,19–23} This seems to indicate that the TiO₂-based device needs a sort of a latent, pre-charging treatment, which may also lead to erroneous PCE estimation if *J*-*V* characteristics are not judiciously reported. In agreement, *J*_{SC} shows a drastic reduction over time, dropping from 21.0 mA cm⁻² to 6.8 mA cm⁻² within 38.5 s. This indeed leads to a very poor steady state (0.043 V s⁻¹ scan rate) PCE of about 2%.

The TiO_x/60-PCBM device delivers instead a PCE under steady state conditions of 17.0%, with V_{OC} = 1.10 V, *J*_{SC} = 20.6 mA cm⁻², FF = 75.2% (see Table 1). The integrated EQE current densities also agree within 10% of the measured white light *J*_{SC} (see Fig. S3, ESI†). The high V_{OC} demonstrates that the presence of the amorphous TiO_x does not open any detrimental recombination/trapping paths as the thin layer of 60-PCBM is enough to drive the optimal device behaviour (note that we also find a linear dependence of the *J*_{SC} as a function of the incident light intensity, see Fig. S7 in the ESI†). We also tested planar devices with only amorphous TiO_x as an EEL, and TiO₂/60-PCBM-based solar cells. The TiO_x-only devices show very poor performances (see Fig. S4 in the ESI†). The TiO₂/60-PCBM-based devices behave similarly to those embodying TiO_x/60-PCBM (see Fig. S5 in the ESI†), highlighting the role of the thin-60-PCBM/perovskite interface.

The steady state operation of the TiO_x/60-PCBM-based devices gives full credit for the actual implementation of this architecture. However, it is fair to notice that the TiO_x/60-PCBM-based devices still show a transient behaviour. The *J*_{SC} transient shows that there is a loss of ca. 0.2 mA cm⁻² (from 21 to 20.8 mA cm⁻², see the inset in Fig. 2(c)). A very mild variation in V_{OC} and/or FF is also found depending on the scan direction. In fact, an enhancement of the PCE can be observed slowing down the scan rate and operating the device with a forward scan, *i.e.*, from 16.2% at a scan rate of 1.88 V s⁻¹ to 16.9% at a scan rate of 0.043 V s⁻¹. On the other hand, a reduction of the PCE can be observed when slowing down the scan rate and operating the device with a reverse scan, *i.e.*, from 17.9% at a scan rate of 1.88 V s⁻¹ to 17.0% at a scan rate of 0.043 V s⁻¹. We have measured the dependence of the transient photocurrent density on the applied bias (Fig. S8 of the ESI†) and we can safely say that a scan rate 0.043 V s⁻¹ is enough to test the steady state behaviour of a solar cell (see also Fig. S6, S7 and S9 of the ESI† for statistics on devices' performances

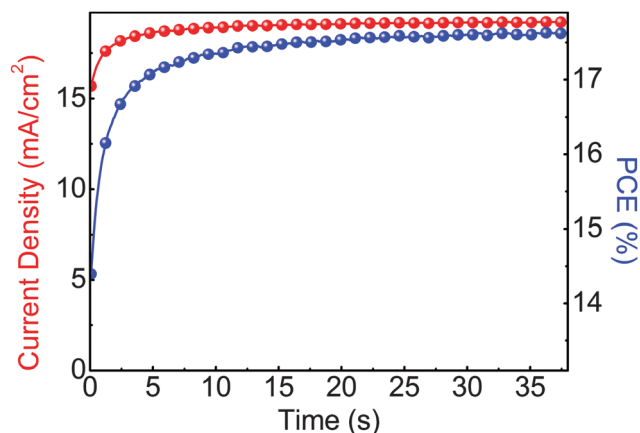
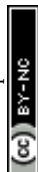


Fig. 3 Stabilized current density and power output measured close to the maximum power point (~ 0.92 V) for a photovoltaic device of glass/FTO/TiO_x/60-PCBM/CH₃NH₃PbI₃/spiro-MeOTAD/Au.

and stability studies). For a direct comparison with the literature values (ref. 6 and 17), we present the stabilized power output close to the maximum power point in Fig. 3, showing that the PCE reaches a stable value as high as 17.6%.

Clearly such behaviours are the result of a transient internal field formation in a time scale of seconds^{26,27} and that the presence of 60-PCBM has a particularly beneficial role in keeping the steady state performances of the device extremely high. Perovskite grain boundaries passivation by 60-PCBM has been recently suggested for hysteresis mitigation in 60-PCBM-based devices with an inverted architecture.²⁸ However, the extremely thin thickness of the 60-PCBM bottom layer with respect to the perovskite layer (a few tens *versus* a few hundreds) makes it difficult to consider the passivation as the only dominant mechanism. Liang *et al.* have also recently highlighted an interesting correlation between the device performances and the EEL electron mobilities when different fullerene derivatives are used as a top contact in inverted planar devices.²⁸ Again, this cannot be directly transferred to our system as the extremely thin thickness of the amorphous oxide, covered by a much thin 60-PCBM layer, is not expected to present carrier mobilities higher than those from the same flat, crystalline TiO₂.^{29,30}

To get a deeper insight into the working mechanism of these systems we performed femtosecond (fs)-resolved transient absorption spectroscopy on working devices. In Fig. 4 we show the dynamics of the photo-bleaching (PB) bands (see Fig. S10 in the ESI† for the entire spectral analysis) belonging to the photo-excited perovskite in FTO/TiO₂/CH₃NH₃PbI₃/spiro-MeOTAD/Au and FTO/TiO_x/60-PCBM/CH₃NH₃PbI₃/spiro-MeOTAD/Au devices when working under short-circuit conditions. Such device architectures allow us, for the first time, to directly compare electron extraction at the TiO₂/perovskite and 60-PCBM/perovskite interface. We recall that the PB band dynamics will follow the electron and hole population dynamics in the hybrid perovskite semiconductor as it originates from the transparency induced at the onset of the optical absorption after population of the bottom of the conduction band and top of the valence band by photogenerated electrons and holes, respectively.⁴ A faster decay of the PB band occurs in TiO_x/60-PCBM-based devices with a



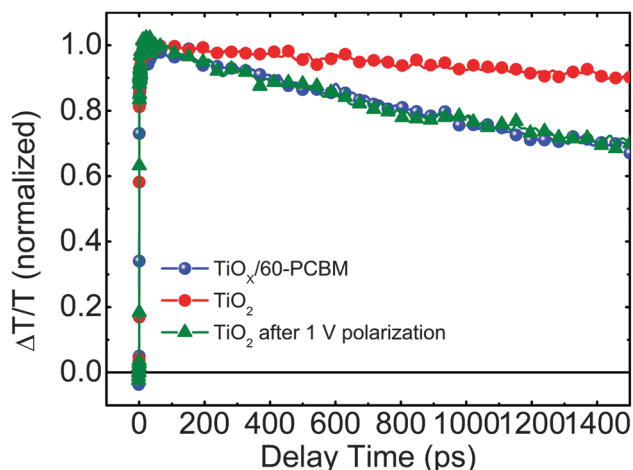


Fig. 4 The dynamics of the photo-bleaching bands for the photo-excited perovskites in working devices under short-circuit conditions.

lifetime of approximately 1.3 ns. Since both devices employ the same HEL – spiro-MeOTAD, we assign this to a more efficient charge transfer at the 60-PCBM/ $\text{CH}_3\text{NH}_3\text{PbI}_3$ interface. Interestingly, when the TiO_2 -based device is kept at 1 V for a few seconds, and then the perovskite PB dynamic is probed again under short-circuit conditions, its lifetime becomes shorter and matches the lifetime of the PB band in $\text{TiO}_x/60\text{-PCBM}$ -based devices (see Fig. 4), suggesting that the electron transfer is suddenly activated. On the other hand, electron injection at the 60-PCBM interface is hardly affected (see Fig. S11 in the ESI†). This indicates that, upon polarization, the TiO_2 /perovskite interface is modified and such a modification is needed to allow for an efficient charge transfer (note that during the revision of this manuscript a theoretical work has been published by Jon Azpiroz *et al.*, which suggests a similar scenario).³¹ This is in full agreement with the fact that the TiO_2 -based devices generally show a much higher J_{SC} value when the device is pre-polarized.¹² In particular, we highlight that the $\text{TiO}_x/60\text{-PCBM}$ -based device shows a J_{SC} of approximately 21 mA cm^{-2} under short-circuit conditions, with virtually no dependence on the device polarization history (Fig. 2b). On the other hand, the TiO_2 -based device shows a lower J_{SC} value, unless it is reversely scanned with a very fast scan rate. In such a case the J_{SC} is again $\sim 21 \text{ mA cm}^{-2}$. This matches the trend observed by TA spectroscopy on the electron transfer dynamics. Recently there has been various experimental evidence for the formation of n and p regions in the perovskite films upon ion migration.³² Importantly, this is independent of the charge extracting layers used.^{26,27} If there is already an optimal energy alignment, such phenomena will have a minimal effect on the steady state performances of the device, as we suggest in the presence of 60-PCBM. On the other hand, the pre-biasing history can temporarily improve the extraction in devices with poor contact quality. Our results suggest that the compact TiO_2 falls in the latter case.

Conclusions

We presented the integration of a low-temperature, solution-processed, transparent EEL, the $\text{TiO}_x/60\text{-PCBM}$, for planar

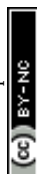
perovskite solar cells with a standard structure. We have also introduced a novel, hybrid deposition method for hybrid perovskites, which produces good quality polycrystalline thin film independently of the nature of beneath layers, thus opening the solar cell design to a large library of EELs. The $\text{TiO}_x/60\text{-PCBM}$ -based devices reach a stabilized PCE of 17.6% close to the maximum power point and a PCE of 17% extracted from quasi-static J - V . Such solar cells exhibit negligible hysteresis, which has been a huge obstacle in developing conventional planar perovskite solar cells so far. We have evidenced the role of charge extraction in reducing the detrimental effects related to slow transient phenomena. Efficient charge extraction achieved with the use of 60-PCBM as EEL makes the device much less sensitive to the device polarization, thus producing an inherently more stable device. On the other hand, in the presence of a flat TiO_2 layer, the electron extraction is strongly dependent on the pre-polarization conditions, thus undermining device stability. Overall, the solution we here propose will foster great advancements in the establishment of a hybrid perovskite-based PV technology thanks to its high level of integrability in both single junction and tandem structures.

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