



Cite this: *Phys. Chem. Chem. Phys.*,  
2021, **23**, 26806

# Synthesis of 2D semiconducting single crystalline $\text{Bi}_2\text{S}_3$ for high performance electronics

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2-Dimensional (2D) semiconducting materials are attractive candidates for future electronic device applications due to the tunable bandgap, transparency, flexibility, and downscaling to the atomic level in material size and thickness. However, 2D materials have critical issues regarding van der Waals contact, interface instability and power consumption. In particular, the development of semiconducting electronics based on 2D materials is significantly hindered by a low charge-carrier mobility. In order to improve the critical shortcoming, diverse efforts have been made in synthesis and device engineering. Here, we propose a synthesis method of single crystalline 2D  $\text{Bi}_2\text{S}_3$  by chemical vapor deposition for high performance electronic device applications. The ion-gel gated field effect transistor with the as-grown  $\text{Bi}_2\text{S}_3$  on the  $\text{SiO}_2$  substrate exhibits a high mobility of  $100.4 \text{ cm}^2 \text{ V}^{-1} \text{ S}^{-1}$  and an on-off current ratio of 104 under a low gate voltage below 4 V at room temperature without chemical doping and surface engineering. The superior performance is attributed to the high crystal quality of  $\text{Bi}_2\text{S}_3$  that shows low sulfur vacancies and atomic ratio close to the ideal value (2 : 3) under a rich sulfur growth process using  $\text{H}_2\text{S}$  gas instead of sulfur powder. The synthesis method will provide a platform to realize high performance electronics and optoelectronics based on 2D semiconductors.

Received 19th August 2021,  
Accepted 4th November 2021

DOI: 10.1039/d1cp03815j

rsc.li/pccp

## 1. Introduction

Field effect transistors (FETs) based on silicon are one of the main components of modern electronic integrated circuit and high-speed electronic devices due to their high charge-carrier mobility and low power consumption. Silicon-based FETs have been steadily improved and dramatically developed in fabrication processes for decrease of cost and improvement of electronic performance to date. Recently, the channel size of FETs has reached its theoretical limitation of scaling of less than 10 nm. The short channel of FETs results in critical issues such as high-power consumption, heat dissipation, high contact resistance and leakage current. Alternative materials are required to overcome the limitations and problems in conventional silicon-based transistors.

Two-dimensional (2D) materials such as graphene, BN, and  $\text{MoS}_2$  have been attracting great attention due to their unique physical and chemical properties.<sup>1–5</sup> Among these materials, 2D semiconducting materials such as  $\text{MoS}_2$ ,  $\text{WSe}_2$  and phosphorene have high potential in future electronic device

development because of their atomic-scale thickness, high flexibility, and controllable band gap.<sup>5–10</sup>

However, common 2D semiconducting materials have critical issues such as high contact resistance, interface instability and high power consumption.<sup>11–15</sup> In particular, they exhibit low intrinsic charge-carrier mobilities ( $0.1\text{--}10 \text{ cm}^2 \text{ V}^{-1} \text{ S}^{-1}$ ) at room temperature that critically impedes their application in electronic devices.<sup>16–18,29</sup> Although phosphorene exhibits outstanding electrical mobility ( $1000 \text{ cm}^2 \text{ V}^{-1} \text{ S}^{-1}$ ), it is not an appropriate candidate for next-generation electronic devices due to significant instability in the air.<sup>19,20</sup>

$\text{Bi}_2\text{S}_3$  (bismuth sulfide) is an anisotropic orthorhombic structured 2D semiconducting material.<sup>21,22</sup> It exhibits superior optoelectronic properties due to unique physical properties such as strong spin-orbit coupling and a band gap of 1.3 eV.<sup>23–25</sup> It also possesses excellent thermoelectric properties because of small effective carrier masses, a high Seebeck coefficient and low thermal conductivity.<sup>21,26</sup> Above all,  $\text{Bi}_2\text{S}_3$  presents theoretically a high electron mobility of  $200 \text{ cm}^2 \text{ V}^{-1} \text{ S}^{-1}$  and a hole mobility of  $1100 \text{ cm}^2 \text{ V}^{-1} \text{ S}^{-1}$  that can be applied in high performance electronic devices.<sup>27</sup> However, electronic devices based on synthesized  $\text{Bi}_2\text{S}_3$  have shown a relatively low mobility like other 2D semiconducting materials owing to low crystal quality and unintentional impurities and vacancy defects.<sup>28,29</sup>

We have developed a synthesis method for single crystalline  $\text{Bi}_2\text{S}_3$  by chemical vapor deposition (CVD).  $\text{Bi}_2\text{S}_3$  was grown

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**Table 1** Comparisons of mobility and current on/off ratio between the synthesized Bi<sub>2</sub>S<sub>3</sub> with other conventional 2D materials using the chemical vapor deposition (CVD) method

2D materials (CVD)	Progress	Layer	Mobilities (cm <sup>2</sup> V <sup>-1</sup> S <sup>-1</sup> )	On/off ratio	Ref.
MoS <sub>2</sub>	MoO <sub>3</sub> + S (powder)	Monolayer	~4	~10 <sup>7</sup>	Van Der Zande <i>et al.</i> , Nature materials, (2013) <sup>41</sup>
WS <sub>2</sub>	H <sub>2</sub> WO <sub>4</sub> + NaCl + S (powder)	Monolayer	~28	—	F. Reale <i>et al.</i> , Scientific reports, (2017) <sup>42</sup>
ReS <sub>2</sub>	Re + S (powder)NH <sub>4</sub> ReO <sub>4</sub> + S (powder)	Monolayer bilayer	~9 ~7.2 × 10 <sup>-2</sup>	~10 <sup>6</sup> ~10 <sup>3</sup>	X. He <i>et al.</i> , Small, (2015) <sup>43</sup> K. Keyshar <i>et al.</i> , Advanced Materials, (2015) <sup>44</sup>
Bi <sub>2</sub> S <sub>3</sub> (nano wire)	Bi <sub>2</sub> O <sub>3</sub> + S (powder)	—	~0.563	~10 <sup>3</sup>	F. Lu <i>et al.</i> , ChemPhysChem, (2015) <sup>45</sup>
Bi <sub>2</sub> S <sub>3</sub> (nano sheet)	Bi + S (powder)	—	~28	~10 <sup>3</sup>	K. A. Messalea <i>et al.</i> , Advanced Materials Interfaces, (2020) <sup>46</sup>
Bi <sub>2</sub> S <sub>3</sub> (single crystal)	Bi <sub>2</sub> O <sub>3</sub> + H <sub>2</sub> S (gas)	Few layer	~100.4	~10 <sup>4</sup>	This work

directly on a SiO<sub>2</sub> substrate using Bi<sub>2</sub>O<sub>3</sub> powder and H<sub>2</sub>S gas instead of conventional sulfur powder. Sulfur powder is broadly used to synthesize 2D materials such as MoS<sub>2</sub>, WS<sub>2</sub> and ReS<sub>2</sub> (Table 1). However, it is difficult to provide a steady density of sulfur during the growth process due to a solid state material. Moreover, the CVD method using sulfur powder can result in high sulfur vacancies which is the main reason for the low crystallinity of 2D materials such as MoS<sub>2</sub>, WS<sub>2</sub> and ReS<sub>2</sub>.

Previous reports have demonstrated that an annealing process under sulfur supply can improve the quality of crystals by lowering the ratio of sulfur vacancies in the crystal structures.<sup>30,31</sup> H<sub>2</sub>S is the ideal precursor for the CVD method because of the gas phase material that can exactly control the density and supply duration of the precursor. H<sub>2</sub>S was used to improve the crystallinity and solve the critical problem of sulfur vacancies of 2D materials such as MoS<sub>2</sub> and WS<sub>2</sub>, because it can provide a steady and rich supply of sulfur during the process of growth. X-ray photoelectron spectroscopy (XPS) proved an atomic ratio of ideal value (2:3) that suggests high crystal quality with few sulfur defects. The single crystallinity and high crystal quality of Bi<sub>2</sub>S<sub>3</sub> were confirmed by high-resolution

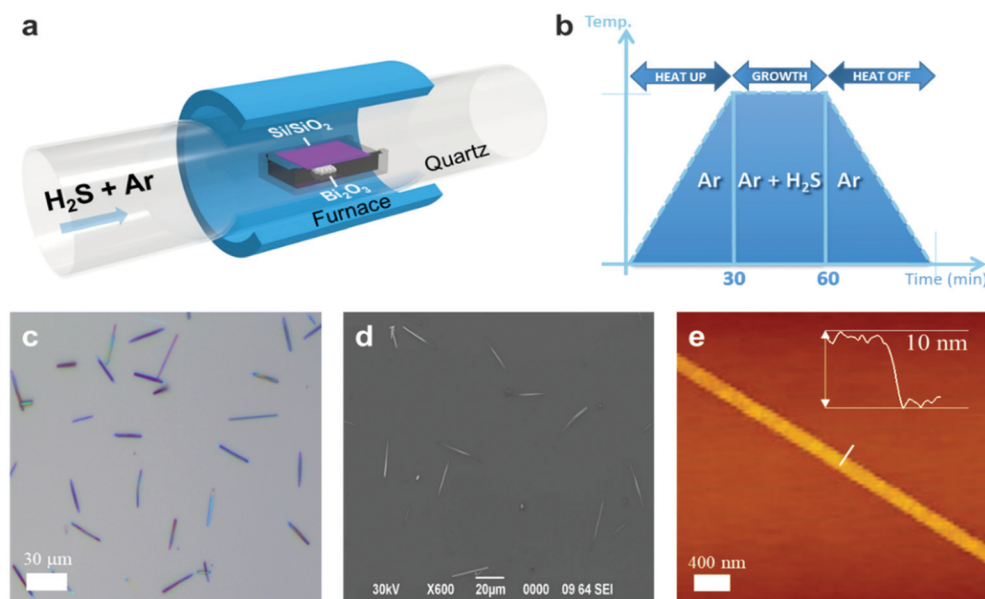
transmission microscopy (HR-TEM). The smooth surface properties without impurities were revealed by atomic force microscopy (AFM). Above all, an ion-gel gated transistor based on as-grown Bi<sub>2</sub>S<sub>3</sub> exhibited a high charge-carrier mobility of 100.4 cm<sup>2</sup> V<sup>-1</sup> S<sup>-1</sup> and an on-off ratio of 104 under a low gate voltage below 4 V at room temperature without chemical treatments and surface engineering that demonstrate high crystal quality of Bi<sub>2</sub>S<sub>3</sub>. These results demonstrate the feasibility of application of synthesized Bi<sub>2</sub>S<sub>3</sub> in high performance and low voltage electronics and optoelectronics.

## 2. Results and discussion

### 2.1. Synthesis and improvement of the crystallinity of Bi<sub>2</sub>S<sub>3</sub> using the LPCVD system

Fig. 1(a) shows a schematic diagram of the experimental set-up for synthesis.

As depicted in the figure, 2D Bi<sub>2</sub>S<sub>3</sub> multilayer flakes were synthesized in a vacuum quartz tube in a furnace chamber under low pressure (~10<sup>-3</sup> torr). CVD method was carried out

**Fig. 1** The synthesis method of Bi<sub>2</sub>S<sub>3</sub> and its characterization. (a) Schematic illustration of experimental set-up of Bi<sub>2</sub>S<sub>3</sub>, (b) diagram of synthesis process of Bi<sub>2</sub>S<sub>3</sub>, (c) OM image of as-grown Bi<sub>2</sub>S<sub>3</sub>, (d) SEM image of as-grown Bi<sub>2</sub>S<sub>3</sub>, and (e) AFM image and height profile of as-grown Bi<sub>2</sub>S<sub>3</sub>.

using  $\text{H}_2\text{S}$  and  $\text{Bi}_2\text{O}_3$  powder as precursors and Ar gas was used as a transporting material in whole CVD process.  $\text{Bi}_2\text{S}_3$  was directly grown on a  $\text{SiO}_2/\text{Si}$  substrate (300 nm  $\text{SiO}_2$ ) by sulfurization of  $\text{Bi}_2\text{O}_3$  after injection of  $\text{H}_2\text{S}$  gas (Fig. 1(a and b)). During the whole growth process, the density and supply duration of  $\text{H}_2\text{S}$  gas were exactly adjusted using a mass flow controller and CVD system. While sulfur powder is rapidly vaporised within several min,  $\text{H}_2\text{S}$  can provide a rich and steady sulfur environment until the end of the duration of growth. Although  $\text{Bi}_2\text{S}_3$  can be synthesized within 10 min, the duration of  $\text{H}_2\text{S}$  gas supply increased up to 30 min including annealing process to reduce the sulfur vacancies and improve crystal quality.

## 2.2. Characterization and structure of $\text{Bi}_2\text{S}_3$

Fig. 1(c) and (d) show optical and electron microscopy images of  $\text{Bi}_2\text{S}_3$  multilayer crystals which were grown on the  $\text{SiO}_2/\text{Si}$  substrate. The optical microscope image exhibits the contrast of multilayer  $\text{Bi}_2\text{S}_3$  with the size of 10–50 micrometers. Also, the shape of synthesized  $\text{Bi}_2\text{S}_3$  presents a highly anisotropically structured 1D nanowire. AFM measurements were performed to observe surface states and thicknesses as shown in Fig. 1(e).

Fig. 1(e) reveals the highly uniform and clean surface of  $\text{Bi}_2\text{S}_3$  crystals without chemical residue and critical defects. The synthesized  $\text{Bi}_2\text{S}_3$  samples show a thickness of about 10 nm and a width of about 200 nm.

Raman spectroscopy was used to characterize qualitative physical properties of  $\text{Bi}_2\text{S}_3$  samples. As shown in Fig. 2(a), the Raman spectrum was obtained from the sample with approximately 10 nm thickness under a 532 nm wavelength laser and excitation power of 1 mW. The Raman system was

calibrated using the Si peak at  $520\text{ cm}^{-1}$ . The Raman spectrum shows well-defined main peaks at  $185$  and  $235\text{ cm}^{-1}$  of  $\text{A}_g$  phonon mode and  $260\text{ cm}^{-1}$  of  $\text{B}_{1g}$  phonon mode.<sup>32</sup> The  $\text{A}_g$  and  $\text{B}_{1g}$  vibration modes are associated with transverse and longitudinal optical phonons respectively. These peak positions match well with those values reported in both of previous theoretical works and experimental results.<sup>33</sup> PL spectroscopy was also performed using a 532 nm excitation source. To avoid thermal damage from laser irradiation, 1 mW of power was used for this work. The relatively weak signal of the PL spectrum results from a smaller width of the  $\text{Bi}_2\text{S}_3$  sample ( $\sim 200\text{ nm}$ ) than the spot size of the Raman laser ( $\sim 1\text{ }\mu\text{m}$ ). As shown in Fig. 2(b), the PL spectrum has the main peak at 917 nm (1.33 eV) which corresponds well to the expected value of the optical band gap.<sup>34,35</sup>

XPS measurements were carried out to characterize the chemical stoichiometry of the synthesized  $\text{Bi}_2\text{S}_3$  crystals and prove low sulfur vacancies of  $\text{Bi}_2\text{S}_3$  crystals (Fig. 2(c) and (d)). To obtain exact peak values, the experimental data were fitted with 4 peaks as shown in Fig. 2(c). The 4f spectrum of  $\text{Bi}^{3+}$  presents dominant peaks at 163.7 and 158.4 related to  $4f_{5/2}$  and  $4f_{7/2}$  respectively.

Similarly, Fig. 2(c) reveals two peaks at 162.3 and 161.1 eV representing S  $2p_{1/2}$  and S  $2p_{3/2}$ . In Fig. 2(d), the 2s spectrum of S exhibits the peak at 225.3 eV. These results correspond well to the chemical bond between sulfur and bismuth of  $\text{Bi}_2\text{S}_3$ .<sup>33</sup> The Bi 4f orbitals, which result from inherent oxide in  $\text{Bi}_2\text{O}_3$  can appear at high binding energy levels.<sup>36</sup>

The Bi 4f spectrum shows no signal arising from the  $\text{Bi}_x\text{O}_y$  radicals or  $\text{Bi}_2\text{O}_3$  residues. The atomic ratio of Bi to S is about 0.67 (Fig. 2(c) and (d)), which closely corresponds to the ideal stoichiometry of  $\text{Bi}_2\text{S}_3$  and proves the presence of a few sulfur vacancies that result in critical low crystal quality. Furthermore, this result shows lower ratio of sulfur vacancies than CVD-grown  $\text{Bi}_2\text{S}_3$  using sulfur powder.<sup>37</sup>

The electronic band structure of bulk  $\text{Bi}_2\text{S}_3$  is plotted in Fig. 3. It displays a semiconducting character with a bandgap of 1.29 eV, in excellent agreement with the previously reported value of 1.3 eV as well as our PL peak value of 1.33 eV.<sup>47</sup> The carrier effective mass is calculated by a parabolic fit to the band edge. The hole and electron effective mass from VBM and CBM are  $0.39 m_0$  and  $0.25 m_0$ , respectively, where  $m_0$  is the free electron mass. Compared with the value of  $\text{MoS}_2$  ( $\approx 0.5 m_0$ ), such a smaller electron effective mass suggests a comparable or higher electron mobility than that of  $\text{MoS}_2$  ( $10\text{--}50\text{ cm}^2\text{ V}^{-1}\text{ S}^{-1}$ ).<sup>48</sup>

HR-TEM was used to analyze the crystal structure and evaluate the crystalline quality of a synthesized  $\text{Bi}_2\text{S}_3$  sample. The  $\text{Bi}_2\text{S}_3$  flakes synthesized on the  $\text{SiO}_2/\text{Si}$  substrate were coated with polymethylmethacrylate (PMMA) in a spin-coater. The PMMA coating with the flakes was removed from the substrate in a buffered oxide etchant (BOE) and rinsed in deionized water (DIW) several times.

The PMMA-supported  $\text{Bi}_2\text{S}_3$  was transferred onto a TEM copper grid. PMMA was removed in acetone solution and the grid was rinsed in IPA several times. Fig. 4(a) shows the image of HR-TEM of a  $\text{Bi}_2\text{S}_3$  flake transferred onto the copper TEM



Fig. 2 Characterization of optical properties and stoichiometry analysis of  $\text{Bi}_2\text{S}_3$ . (a) Raman spectrum of  $\text{Bi}_2\text{S}_3$ , (b) PL spectrum of  $\text{Bi}_2\text{S}_3$ , (c) XPS spectrum of Bi 4f peaks and S 2p peaks, and (d) XPS spectrum of S 2s peaks.



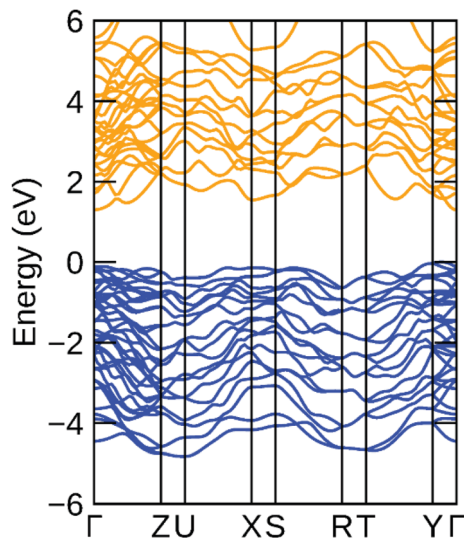


Fig. 3 Electronic band structure of bulk  $\text{Bi}_2\text{S}_3$ . Blue/orange lines for valence/conduction band. The VBM is set to be  $E = 0$ .

grid. Fig. 4(b) and (c) show the selected area electron diffraction (SAED) pattern and the HR-TEM image of the  $\text{Bi}_2\text{S}_3$  flake, respectively. The SAED pattern in Fig. 4(b) presents a periodic orthogonal feature that is associated with a classical anisotropic orthorhombic structure of  $\text{Bi}_2\text{S}_3$ .

HR-TEM image shows no impurities and defects in the  $\text{Bi}_2\text{S}_3$  crystal. Above all, the SAED pattern reveals a high-quality and perfect single crystal of a synthesized  $\text{Bi}_2\text{S}_3$  flake. The diffraction points indicate the lattice planes in the SAED pattern and the (001) plane is perpendicular to the (020) plane.<sup>38</sup> This result is in good agreement with the lattice structure of  $\text{Bi}_2\text{S}_3$  which is the orthorhombic crystal system with the lattice parameters of

$a = 0.112 \text{ nm}$ ,  $b = 1.125 \text{ nm}$ ,  $c = 0.397 \text{ nm}$ ,  $\alpha = 90^\circ$ ,  $\beta = 90^\circ$ ,  $\gamma = 90^\circ$  as shown in Fig. 4(d).<sup>39,40</sup> Also, the low magnification image of HR-TEM in Fig. 4(C) demonstrates the anisotropic growth of  $\text{Bi}_2\text{S}_3$  along a main axis of the [001] direction which is perpendicular to the [010] direction due to the anisotropic orthorhombic structure of  $\text{Bi}_2\text{S}_3$  (Fig. 4(d)).

### 2.3. Electrical performance of a $\text{Bi}_2\text{S}_3$ FET

A FET was fabricated to evaluate the electronic properties of the synthesized  $\text{Bi}_2\text{S}_3$  sample.

In this work, an as-grown multilayer  $\text{Bi}_2\text{S}_3$  flake was used for fabrication as shown in Fig. 5(a) and (b). As shown in Fig. 5(a), 5 nm Cr and 50 nm Au as the electrode were deposited using an e-beam evaporator. The gate insulator was made by drop-casting an ionic liquid onto the  $\text{Bi}_2\text{S}_3$  flake (Fig. 5(b) and (c)). The ion-gel solution consists of a poly(ethylene glycol) diacrylate (PEG-DA) monomer, a 2-hydroxy-2-methylpropiophenone (HOMPP) initiator, and a 1-ethyl-3-methylimidazolium bis(trifluoromethylsulfonyl)imide (EMIM : TFSI) ionic liquid with a weight ratio of 2 : 1 : 22. In Fig. 5(d), the transfer characteristics of the ion-gel gated FETs exhibited n-type behavior, which is consistent with previous reports. The field effect mobility can be calculated using the equation  $\mu = (L/WV_D C_{\text{ion-gel}}) (\Delta I_D / \Delta V_G)$ , where  $L$ ,  $W$ , and  $C_{\text{ion-gel}}$  are the channel length, the channel width, and capacitance of the ion-gel gate dielectric.

The value of  $\Delta I_D / \Delta V_G$  was obtained from the slope of the transfer characteristic curve plotted in Fig. 5(d). The FET exhibited a high electron mobility of  $100.4 \text{ cm}^2 \text{ V}^{-1} \text{ S}^{-1}$ , and a high current on/off ratio of  $\sim 10^4$  at  $V_D = 1 \text{ V}$ , which are superior to those of other conventional 2D materials such as

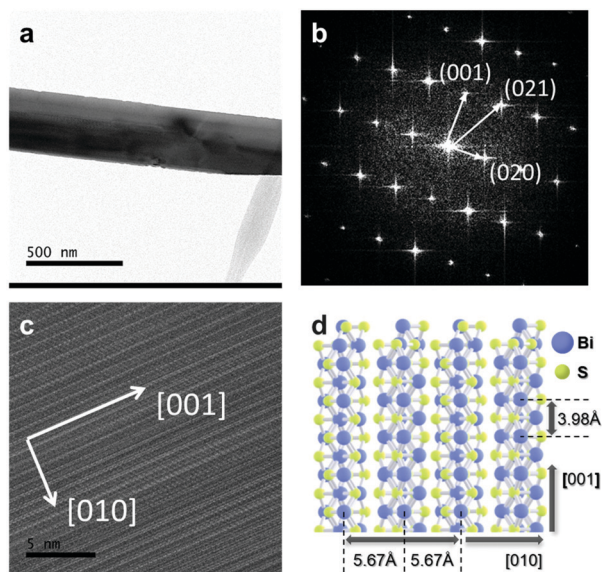


Fig. 4 Structure analysis of  $\text{Bi}_2\text{S}_3$ . (a) TEM image of  $\text{Bi}_2\text{S}_3$  on a TEM grid, (b) SAED patterns of single crystalline  $\text{Bi}_2\text{S}_3$ , (c) HRTEM image of anisotropic  $\text{Bi}_2\text{S}_3$  structure, and (d) schematic illustration of  $\text{Bi}_2\text{S}_3$  crystal structure.

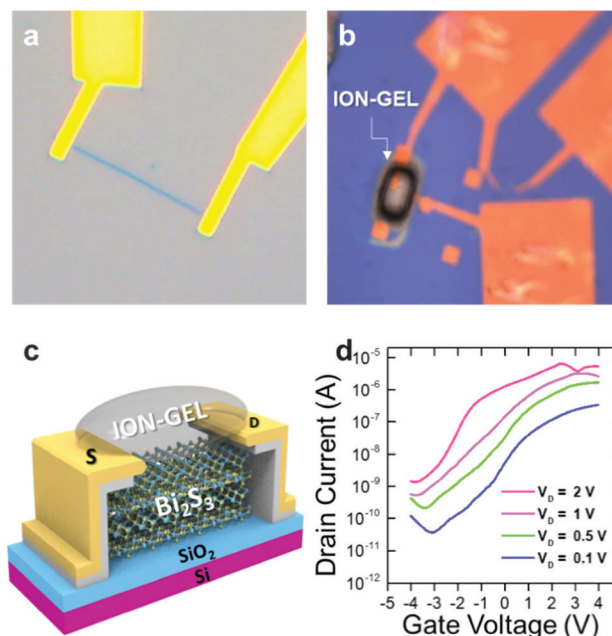


Fig. 5 Electronic properties of  $\text{Bi}_2\text{S}_3$ : (a) OM image of FET based on as-grown  $\text{Bi}_2\text{S}_3$ , (b) OM image of the ion-gel gated transistor of  $\text{Bi}_2\text{S}_3$ , (c) schematic diagram of ion-gel gated  $\text{Bi}_2\text{S}_3$  FET, (d) transfer characteristics of  $\text{Bi}_2\text{S}_3$  transistor.

MoS<sub>2</sub>, WS<sub>2</sub> and ReS<sub>2</sub> as well as those of the previously reported Bi<sub>2</sub>S<sub>3</sub> (Table 1). The excellent electronic device performance again proves the high crystallinity of the synthesized flakes and demonstrates the feasibility of practical and high-performance electronic device applications.

## 3. Experimental

### 3.1. Synthesis of Bi<sub>2</sub>S<sub>3</sub>

Bi<sub>2</sub>O<sub>3</sub> powder (99.9%, Sigma Aldrich) was used as the precursor for the synthesis of Bi<sub>2</sub>S<sub>3</sub>. Bi<sub>2</sub>S<sub>3</sub> was grown in a vacuum quartz tube in a furnace under low pressure (~103 torr). Before synthesis, SiO<sub>2</sub>/Si (300 nm SiO<sub>2</sub>) wafer was cleaned in acetone and isopropyl alcohol (IPA) and then rinsed in DIW several times. A mass of Bi<sub>2</sub>O<sub>3</sub> 5 mg powder in a quartz boat was put onto the edge of the furnace and covered by SiO<sub>2</sub>/Si wafer. The furnace was heated up to 550 °C for 30 min and then H<sub>2</sub>S gas was injected for sulfurization of Bi<sub>2</sub>O<sub>3</sub> for 30 min. The furnace chamber was cooled down to room temperature. Ar gas was injected over the whole process.

### 3.2. Characterization of synthesized Bi<sub>2</sub>S<sub>3</sub>

Raman and PL spectroscopy was carried out at 532 nm laser wavelength, and 1 mW laser power, which avoids thermal damage to samples. The Raman instrument (NTMDT AFM-Raman spectroscope) was calibrated using a Si peak of 520 cm<sup>-1</sup> before measurement. XPS was performed to determine the chemical stoichiometry and bonding states of Bi<sub>2</sub>S<sub>3</sub> with an Al K $\alpha$  X-ray source (ESCALAB 220i-XL, VG Scientific Instruments). AFM was used for surface analysis and measurement of the thickness of the samples. AFM measurements (Seiko instrument) were taken with a Si tip (NT-MDT). To make the TEM sample, a Bi<sub>2</sub>S<sub>3</sub> on SiO<sub>2</sub>/Si wafer was coated with polymethylmethacrylate (PMMA) in a spin-coater. It was immersed in BOE solution to remove SiO<sub>2</sub> and then was rinsed in DIW several times. The PMMA-supported Bi<sub>2</sub>S<sub>3</sub> was transferred onto a TEM copper grid and the PMMA was removed in acetone for 30 min and rinsed in IPA several times. The TEM images and SAED patterns were obtained using a JEM-2100F, Cs corrector.

### 3.3. Structure relaxation and electronic band structure calculations

All the DFT calculations were performed using the Vienna *ab initio* Simulation Package (VASP).<sup>49</sup> For the ion–electron interaction, projector augmented wave (PAW) pseudopotentials were used. For the exchange–correlation functional, the Perdew–Burke–Ernzerhof (PBE) type generalized gradient approximation (GGA) was adopted.<sup>50</sup> The van der Waals interactions were described by the DFT-D3 Grimme method.<sup>51</sup> The  $\Gamma$ -centered  $k$ -mesh of  $4 \times 12 \times 5$  ( $9 \times 25 \times 9$ ) and the plane-wave energy cutoff of 600 eV were set for the structural optimization (electronic band structure calculation). The calculated lattice constants of the relaxed bulk Bi<sub>2</sub>S<sub>3</sub> are  $a = 11.12$  Å,  $b = 11.29$  Å and  $c = 4.00$  Å, in very good agreement with the experimental values ( $a = 11.2$  Å,  $b = 11.25$  Å and  $c = 3.97$  Å).

### 3.4. Device fabrication

A FET based on as-grown Bi<sub>2</sub>S<sub>3</sub> was fabricated using e-beam lithography. The metal electrodes of Cr/Au (5/50 nm) were deposited by using an e-beam evaporator. To make the gate insulator, the ionic liquid was drop-cast onto the FET. The gate dielectric layer was formed by drop-casting an ion-gel solution comprising poly(ethyleneglycol) diacrylate (PEGDA, Sigma-Aldrich), 2-hydroxy-2-methylpropiophenone (HOMPP, Sigma Aldrich), and 1-ethyl-3-methylimidazolium bis(trifluoromethanesulfonyl) imide ([EMIM][TFSI]) ionic liquid (Merck) in a weight ratio of 2 : 1 : 22. After drying, the ion gel layer in a patterned photomask was exposed to UV light (365 nm, 100 mW cm<sup>-2</sup>) for five seconds. The area exposed to UV light became chemically crosslinked, while the unexposed region was removed by rinsing in DIW.

## 4. Conclusions

In conclusion, we have developed the CVD method for the synthesis of single crystalline Bi<sub>2</sub>S<sub>3</sub>. The crystals have the shapes of 1-dimensional nanowires or nanorods with a width of ~200 nm and a thickness of tens of nanometers. TEM and SAED patterns revealed anisotropic properties and high crystallinity in the orthorhombic structure of Bi<sub>2</sub>S<sub>3</sub>. The optical band gap of Bi<sub>2</sub>S<sub>3</sub> (~1.33 eV) was confirmed by PL spectroscopy. AFM and SEM images showed clean and uniform surfaces of Bi<sub>2</sub>S<sub>3</sub>. Above all, ion-gel gated FETs based on as-grown Bi<sub>2</sub>S<sub>3</sub> displayed superior electronic properties with a charge carrier mobility of 100.4 cm<sup>2</sup> V<sup>-1</sup> S<sup>-1</sup>, a current on/off ratio of ~10<sup>4</sup> and a low voltage operation below 4 V. The CVD method to grow Bi<sub>2</sub>S<sub>3</sub> single crystals paves a new way for high performance electronics with 2D semiconducting materials.

## Author contributions

Y. K. developed the synthesis method of Bi<sub>2</sub>S<sub>3</sub> and carried out the analysis of CVD-grown Bi<sub>2</sub>S<sub>3</sub>, M. J. conducted the electronic band structure calculation of Bi<sub>2</sub>S<sub>3</sub> and E. J. fabricated the device of Bi<sub>2</sub>S<sub>3</sub> and conducted the electrical test. All authors contributed the discussion and preparation of the manuscript.

## Conflicts of interest

There are no conflicts to declare.

## Acknowledgements

This study was supported by the Basic Science Research Program (2021R1F1A1051987, 2016K1A1A2912707, 2018R1A6A3A11047867, 2020R1A2C2014687, and 2019R1I1A1A01061466) through the National Research Foundation of Korea (NRF) and the KISTI grant (KSC-2020-CRE-0325).

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