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Control of Active Semiconducting Layer Packing in Organic Thin Film Transistors through Synthetic Tailoring of Dielectric Materials

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Apart from the development of new dielectric and semiconductor materials, the semiconductor-dielectric interface study is also very important for the optimum performance of organic thin film transistors (OTFTs). Herein, we have reported the detailed synthesis of a whole new family of dielectric materials which are 1,3,5,7-tetrabromoadamantane; 1,3,5,7-tetrachloroadamanatane; 1,3,5,7-tetraiodoadamantane and 1,3,5,7-tetrauraciladamantane (AdUr4). The unique ability of these molecules to undergo supramolecular thin film formation at low temperature, was analysed for their potential use as an insulator in organic electronic devices. Owing to the good leakage current density property shown by AdUr4 dielectric material it was further employed as a gate dielectric in regioregular poly(3-hexylthiophene), (P3HT) based OTFT. This OTFT device which was fabricated on flexible PI plastic substrate have showed good on/off current ratio (e.g., 2.18×10^4) and high mobility (e.g., 0.15 cm^2 V^{-1} s^{-1}). The semiconductor–dielectric interface study, has revealed that AdUr4 gate dielectric layer has guided the P3HT molecular chain domains to undergo edge-on orientation, which is the charge transport direction in OTFTs. In this process, the grazing incidence X-ray diffraction (GI-XRD) analysis and AFM study was also employed.

1. Introduction

Organic thin film transistors (OTFTs) have been emerging as a new class of organic electronic devices, with a potential to replace the traditional silicon based transistors.[1] They are expected to play a major role in future for the development of low cost electronic devices which will be tough, light weight and flexible.[2] They have been the focus of research for the development of flexible electronics because of their cost-effective production, solution based processability and compatibility with variety of flexible substrates.[3] Instead of so much merits associated with them, there are lot of challenges needs to be met before their commercialization for practical uses such as in the field of displays, sensors, invertors and radio frequency identification tags.[4] The major hurdle coming in the commercialization of OTFTs is mainly due to their low output current and high operating voltage, as resulting from low charge carrier mobility of the active semiconducting layer employed respectively in them.[5] In general, OTFTs are composed of three components namely an electrode, semiconducting layer and a

dielectric insulating layer separating the active semiconducting material from a gate electrode. Thus the performance of OTFTs can be improved either by employing an organic semiconducting with better charge carrier mobility or with the use of dielectric material having low leakage current and high dielectric constant.[6] In past various research groups have successfully achieved the high charge carrier mobility, $\mu_{FE} \approx 10$ cm^2 V^{-1} s^{-1} by employing new organic semiconductor materials in OTFTs.[7] However, the dielectric layer which also plays an important role in working of OTFTs, has been less explored as compared to semiconductor materials.[8] The mobility and threshold voltage of OTFTs in the linear regime are related by the equation,[9]

$$I_{DS} = \frac{W C_i}{L} \mu_{FE} (V_G - V_{th}) V_{DS} \qquad (1)$$

where, I_{DS} and V_{DS} are the current and voltage bias between source and drain, respectively, V_G denotes the gate voltage at which the current starts to rise. Further, W and L denotes the

source-drain width and length, respectively; μ_{FE} is the field effect mobility; C_i is the capacitance per unit area of the insulator, and V_{th} is the threshold voltage. Thus, the proper choice of the dielectric material having low leakage current and high capacitance is essential for the low operating voltage OTFTs.[10] The field induced charge carriers are confined to the thin region *i.e* interface between semiconductor and dielectric layer in OTFTs.[11] Hence, in combination with the development of new dielectric and semiconductor materials, the semiconductor-dielectric interface study is also pivotal for the optimum performance of OTFTs.[12] Kim *et.al* have also reported[12a,] the effect of dielectric surface modification on the field-effect mobility of regioregular poly(3-hexylthiophene)(P3HT) based transistor. When the silicon dioxide SiO_2 insulator substrate was modified with –OH terminated self-assembled monolayers (SAM), μ_{FE} was 0.08 cm^2 V^{-1} s^{-1}. However, upon SiO_2 surface modification with –NH$_2$ terminated SAM, the mobility increased by several orders of magnitude of 0.28 cm^2 V^{-1} s^{-1} for the same P3HT semiconductor. This was due to the difference in orientation (perpendicular and parallel) of P3HT chains with respect to the modified SiO_2 insulator substrate which lead to the difference in behaviour of device performance. Recently, Jenn-Chang Hwang *et.al.* have reported[13a] the use of silk fibroin as a dielectric on the flexible poly(ethylene terephthalate) (PET) plastic substrate for the fabrication of pentacene OTFTs. The planner structure of the silk fibroin dielectric has allowed the pentacene molecules to self-assemble in the planner form which resulted in a very high μ_{FE} value of 23.2 cm^2 V^{-1} s^{-1} in the saturation regime and a low operating voltage of –3 V. Hence, the proper choice of dielectric material with a good compatibility with the active semiconducting layer is also essential for the optimum performance of OTFTs.

Organic and polymeric materials have been extensively explored in recent years for their potential use as a gate dielectric in OTFTs. Nowadays, they are widely pursued over traditional SiO_2 dielectric because of their low cost and their ability to form high quality thin films from them can be prepared by simple processing methods. These are spin coating, ink-jet printing and sol-gel process.[14] Some of the most extensively explored polymers in this field are poly(methyl methacrylate) (PMMA), poly(styrene) (PS), poly(4-vinyl phenol) (PVP), poly(acrylic acid) (PAA), and poly(perfluorobutenylvinylether) (CYTOP).[15] These polymer gate dielectrics are known to have low leakage current which leads to the performance enhancement of OTFTs. However, due to their solubility in common organic solvents these polymer dielectrics get washed away during the deposition of organic semiconducting material by dip coating or spin coating methods.[16] Some research groups have achieved the dielectric thin film of thickness < 50 nm with high breakdown voltages by thermally crosslinking of the gate dielectric material.[17] However, the curing temperature as needed for this process is higher than the glass transition temperature of flexible substrate.[18] This leads to the poor performance of device. Therefore, there is a strong desire to design new organic dielectric materials which should be processable into thin film at low temperature from their solution in specific solvents.

Diamond is also a good insulator material[19], however its high cost, restricts its use as a gate dielectric in low cost OTFTs. On the other hand, the adamantane, (chemical formula $C_{(4n+6)}H_{(4n+12)}$, where n = 0, 1, 2, 3…), which is the smallest cage structure of diamond, is cheap and can be easily obtained from oil and natural gas condensates after purification.[20] This structurally rigid, thermally stable (pyrolysis temperature > 660 ºC) and easy to be chemically modified[21] diamondoid molecule has recently been reviewed by Schwertfeger *et al.*[22] The spatial arrangement of carbon atoms in adamantane is similar to that in diamond lattice.[23] It has been broadly studied for its numerous applications in biology and chemistry.[24]

Motivated by so much merits associated with adamantane, we modified adamantane surface structure, chemically to synthesize its four derivatives namely; 1,3,5,7-tetrabromoadamantane (AdBr$_4$); 1,3,5,7-tetrachloroadamanatane (AdCl$_4$); 1,3,5,7-tetraiodoadamantane (AdI$_4$) and 1,3,5,7-tetrauraciladamantane (AdUr$_4$). The solution of these functionalized molecules has the ability to undergo smooth thin film formation at low temperature through supramolecular self-assembly (STF) (Scheme 1). The halogen-halogen interactions, van der Waals force of attractions and electrostatic force of attractions have played an important role in supramolecular polymeric thin films formation of AdBr$_4$, AdCl$_4$, AdI$_4$.This unique ability of these small nanodiamond molecules to undergo thin film formation, with network structure similar to that of diamond lattice has prompted our group to explore their potential application as an insulator in organic electronics. This was analysed by employing these materials as insulating layer in MIM devices, as fabricated on flexible polyimide (PI) plastic substrate. The scanning electron microscope (SEM) study was employed to study morphology of organic dielectric thin films and further its effect was explored on MIM device performances as based on them. The difference in morphology of AdBr$_4$, AdCl$_4$, AdI$_4$ STF has prevailed because of the different type of intermolecular forces involved in their formation. This was determinant in MIM device performance based on them. To further explore the effect of intermolecular forces on dielectric STF and its subsequent effect on the electrical performance of device we synthesised another unique derivative of adamantane, *i.e* AdUr$_4$. The uracil substituents on the surface of AdUr$_4$ molecules has allowed them to adsorb on the gold coated flexible PI plastic substrate. Moreover, with Fourier transform infrared (FTIR) spectroscopy, it has been found that these AdUr$_4$ molecules have undergone lateral interaction with each other through uracil-uracil (U---U) intermolecular hydrogen bonding that has led to the formation of their STF. Because of this the MIM devices as fabricated from AdUr$_4$ STF showed better insulator properties as compared to that of AdBr$_4$, AdCl$_4$, AdI$_4$ STF. The thermal stability of AdUr$_4$ STF based MIM devices, was further analysed by heating them to various temperature and then measuring the leakage current at that corresponding temperature. The variable temperature FTIR spectroscopy was performed to find out the effect of heat on intermolecular hydrogen bonding involved in AdUr$_4$ dielectric thin film. After this detailed analysis study of AdUr$_4$ dielectric thin film, we

employed it as a gate dielectric material in the P3HT based OTFTs with bottom gate and top contact. Through semiconductor-dielectric interface study, it has been found, that the unique rigid structure of AdUr$_4$ has guided the regioregular P3HT chain domains to crystallize in edge-on orientation. The GI-XRD and Atomic force microscopy study (AFM) was performed to successfully explore this phenomenon. The edge-on conformation has increased the charge carrier transfer to in-plane direction of P3HT chains which has led to the success of AdUr$_4$ as a gate dielectric in P3HT based OTFTs with good on/off current ratio.

2. Experimental Section

2.1 Reagents and Materials.

1-Bromoadamantane (99 %), bromine (99.5%) and iodomethane was procured from Sigma Aldrich and used as received. Aluminum chloride and aluminum bromide anhydrous, powder of 99.999% trace metal basis was supplied by Alfa Aesar. The dichloromethane, chloroform and tetrahydrofuran (THF) obtained from Alfa Aesar were dried and distilled from sodium/benzophenone prior to their use. Chromium shots (Cr, 99.999%, Admat Inc.) 3-5 mm in size, aluminium shots (Al, 99.999%, Admat Inc.) 3-5 mm in size, and gold shots (Au, 99.999%, Admat Inc.) 1-2 mm in size, were purchased from Gredmann Taiwan Ltd. The organic semiconductor regioregular poly(3-hexylthiophene) (P3HT catalog number 698997 electronic grade, purchased from Aldrich, average molecular weight M_n 54000-75000, >98% head-to-tail regioregular) was adopted for this research. DuPont Kapton Polyimide film, 38 μm in thickness and from PV9100 series was used as substrate to fabricate the device. All other reagents and solvents were obtained from commercial suppliers and used as such, unless specified. It should be noted that all experiments were performed under dry nitrogen atmosphere and in standard fume hood.

2.2 DIELECTRIC THIN FILM PREPARATION AND FABRICATION OF THE MIM AND OTFT DEVICES.

Scheme 1 presents the general method[26] for the preparation of the respective dielectric thin films of AdBr$_4$, AdCl$_4$, AdI$_4$, and AdUr$_4$. In a typical thin film preparation experiment AdUr$_4$ (0.05 mg) was dissolved in (10 ml) of THF under static conditions at 50 ºC for 30 minutes. It was filtered with micro-syringe before drop casting on PI substrate. The dielectric thin film from tetrahedral molecules of adamantane derivatives were produced by their slowly self-assembling from a solution (0.05 mg/mL) under a certain solvent pressure in a closed jar, as depicted in Scheme 1. It should be noted, that the solvent in the bottom of the jar was very crucial for the formation of well-defined smooth thin film from adamantane derivatives, which could play two important roles: (1) slow evaporation of the solvent guaranteed the small tetrahedral molecules had sufficient time to adjust themselves and come closer together with strong intermolecular interactions so that they can further self-assemble into smooth thin film on the substrate; (2) certain solvent vapour pressure in the upper space of the jar could guarantee the free movement of these molecules and then induced them to assemble together leading to the growth of polymer like film on the substrate. Apart from AdUr$_4$ which have good solubility in THF the other halogen derivatives of adamantane as reported here, have good solubility in all organic solvents such as toluene, chloroform, chlorobenzene. However, the good solubility of all adamantane derivatives in THF and low vapor pressure of THF has prompted us to finally choose THF as a solvent of choice for the respective STF preparation of all the adamantane derivatives in this work.

A DuPont Kapton plastic PI plastic substrate with a thickness of 38 μm was used as the flexible substrate for the fabrication of MIM capacitor and OTFT devices. The PI plastic substrate was ultrasonically cleaned using ethanol (Fluka; water content: < 0.1%) for 30 min and then rinsed with DI water. A high-pressure stream of N$_2$ gas was then used to remove the
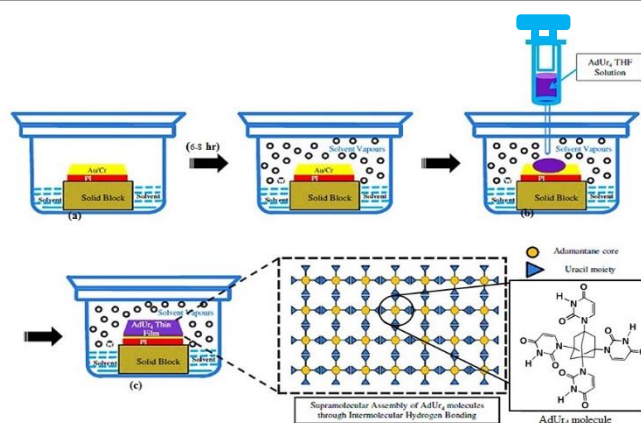
Scheme 1 Schematic representation of the AdUr$_4$ insulator supramolecular thin film formation on flexible PI plastic substrate: (a) flexible PI coated with Au/Cr layer is placed on a rigid support (solid block) in a beaker containing the solvent (THF) (b) injecting the drops of AdUr$_4$ solution on this PI plastic substrate after 3-4 hours (c) the small rigid AdUr$_4$ molecules self-assemble through U---U intermolecular hydrogen bonding into the form of a thin film. This last process takes 5-6 hours.

water and any remaining particles from the PI surface. After being cleaned, the PI plastic substrate was annealed at 200 °C for 1 h under vacuum to achieve relative thermal stability and to enhance the adhesion strength. Next, Cr (20 nm thick) and Au (80 nm thick) were sequentially deposited onto the PI plastic substrate using a thermal coater. The Cr layer was used as the adhesion layer between the PI plastic substrate and the Au thin film.[27] Au was deposited as a gate electrode over the Cr layer on the PI plastic substrate. This PI was then placed in a jar containing the THF solvent and the jar was covered for 6-8 h to maintain a suitable vapour pressure inside it as shown in Scheme 1. Finally, the filtered THF solution of AdUr$_4$ was drop casted over this PI plastic substrate using a syringe in such a way that it should cover the whole PI surface. After, the complete evaporation of solvent from the PI plastic substrate as evaporated; it was heated at 40 ºC for 10 min to remove any

solvent trapped inside the dielectric film, formed by self-assembly of molecules. At the end of this experiment, Al films with a thickness of 300 nm were patterned as the top electrode using a shadow mask and a thermal coater.

The electrical insulating properties of the halogen and uracil adamantane derivatives respective thin films have been optimized for OTFT on the PI plastic substrate. For OTFT device fabrication Cr and Au, which had thickness of 20 and 80 nm, respectively, were sequentially deposited through a shadow mask using a thermal coater to function as gate electrodes. Then, gate dielectric $AdUr_4$ thin film was prepared on it, as described in Scheme 1. The P3HT film was then deposited as a channel layer using a spin casting method, at room temperature and annealed at 40ºC for 30 minutes. Finally, source (S) and drain (D) Al electrodes with a thickness of 100 nm were deposited onto the $Al/P3HT/AdUr_4/Au/Cr/PI$ through a shadow mask, which yielded the top-contact electrode OTFTs. The channel length (L) and width (W) were 82 μm and 2000 μm, respectively.

2.3 THIN FILM CHARACTERIZATIONS AND ELECTRICAL MEASUREMENTS.

The surface morphology of the $AdBr_4$, $AdCl_4$, AdI_4 and $AdUr_4$ respective self-assembled thin films on the PI plastic substrate was evaluated using Scanning electron microscope (SEM, JEOL). The thicknesses of these films were estimated by optical ellipsometry techniques. The surface morphology of the P3HT film that was spin casted over insulator substrate on the PI plastic substrate was evaluated using atomic force microscopy (AFM, Digital Instruments Nanoscope, D−5000) with a scan size of 2 μm × 2 μm and a scan rate of 1 Hz. The FTIR spectrum was recorded using the FTIR spectrometer (model 580, Perkin-Elmer) with a resolution of 4.00 cm^{-1}. An infrared spectrum was recorded under nitrogen atmosphere in the range of 500−4000 cm^{-1} to determine the functional groups in the molecular structure. The samples were prepared on PI substrate (1 × 1 cm^2) for FTIR measurement. Further, variable temperature FTIR experiments were performed on $AdUr_4$ STF. The $AdUr_4$ thin film was prepared on PI plastic substrate (1 × 1 cm^2) by following the procedure as shown in Scheme 1. This PI plastic substrate was then *in-situ* heated from 40 ºC–120 ºC under nitrogen flow (20 ml/min) in a FTIR instrument equipped with temperature controller function. Corresponding to each temperature, FTIR spectrums were recorded for this $AdUr_4$ STF. The XRD pattern for the organic P3HT film was recorded using a Rigaku D/max−IIIB diffractometer with Cu Kα radiation (λ = 1.5406 Å). The sample for XRD study was prepared by spin casting the THF solution of P3HT on the $AdUr_4$ dielectric STF with PI as the substrate.

MIM CAPACITOR: To investigate, the electrical characterization of all the four adamantane derivatives dielectric thin films, the capacitance–voltage (C−V) and the leakage current − voltage (I−V) were measured using a HP−4284A C−V analyser and Agilent−4156 probe station, respectively. The real part of the dielectric constant (k) was calculated using the following formula

$$k = \frac{Ct}{\varepsilon_o A_m} \tag{2}$$

in which C is capacitance, t is the thickness of the dielectric film (measured by ellipsometery technique), A_m the capacitance area, and ε_o the permittivity of vacuum. The leakage current was measured in direct current on the capacitors in the range $0 − 100$ V with a holding time of 100 ms (Agilent 4156). A quasi-static capacitance versus voltage curve was taken at a sweep rate of 0.25 V s^{-1} using a Keithley 595 quasi-static C-V meter. We examined an additional property for $AdUr_4$ dielectric based MIM device, to check its stability against the temperature. This was done by heating the MIM device to various temperatures and measuring the leakage current at that respective temperature.

OTFT DEVICE: The electrical characteristics of the transistors were measured in a nitrogen glove box on an Agilent−4156 probe station. The transistor parameters, such as charge carrier mobility, were calculated using the standard formalism of field-effect transistors in the linear and saturation regimes respectively. The P3HT based OTFTs exhibits output characteristics with pinch off and current saturation. The electrical characteristics of the P3HT based OTFTs are very stable during operation. The μ_{FE} value in the linear region ($\mu_{FE, lin}$) is obtained from the transfer characteristics using equation 1. The C_i value for the P3HT thin film transistor was measured using MIM structure fabricated next to the P3HT based OTFT device.

3. Results and Discussion

The adamantane, a tricyclic saturated hydrocarbon (tricyclo [3.3.1.1]decane) is the smallest "nanodiamond" molecule.[26] Its bulky molecular volume, rigidity and highly symmetrical tetrahedral shape makes it an ideal building block for nanofabrication.[21,24] Our research group have took the advantage of this unique rigid structure of adamantane for its application, as an insulator in flexible organic electronic devices, first in MIM devices and then finally in OTFTs. The ability of adamantane molecule to undergo substitution reaction readily at its 1,3,5,7 position allowed us to prepare its four derivatives.[20e] These are $AdBr_4$, $AdCl_4$, AdI_4, $AdUr_4$, and was synthesized by following the Scheme 2. The STF of these compounds were prepared on flexible PI plastic substrate, as illustrated in Scheme 1 for their application as an insulator in MIM and OTFTs devices. It has been found that the supramolecular interactions, has guided these individual small rigid molecules to self-assemble[28] in form of their rigid and flexible thin films. The STF of $AdBr_4$, $AdCl_4$, AdI_4 molecules have occurred through halogen-halogen interactions *i.e* Br---Br, Cl---Cl, I---I respectively.[29,30,31] However, the $AdUr_4$ molecules have knitted up together in the form of STF through U---U intermolecular hydrogen bonding.[32] The Figure 1 shows that in $AdUr_4$ STF each $AdUr_4$ molecule is involved in intermolecular hydrogen bonding with nearby $AdUr_4$ molecules.
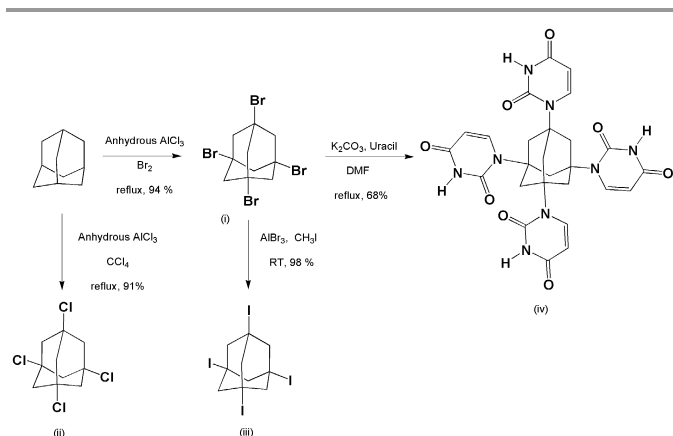
Scheme 2 Synthetic route[27] of dielectric materials based on adamantane (AdBr$_4$, AdCl$_4$, AdI$_4$, AdUr$_4$)

Furthermore, through FTIR study we also confirmed the presence of some free –NH (not involved in U---U intermolecular hydrogen bonding) of uracil in AdUr$_4$ STF.
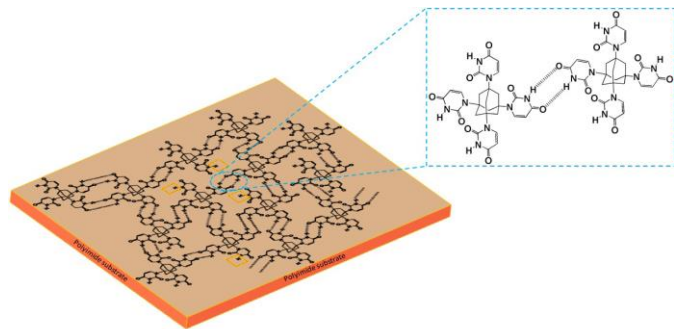

Figure 1 U---U Intermolecular hydrogen bonding in AdUr$_4$ molecules leading to their STF formation on the flexible PI plastic substrate. Free –NH (not involved in U---U intermolecular hydrogen bonding) are shown in yellow square box.

The difference in the respective STF of AdBr$_4$, AdCl$_4$, AdI$_4$, AdUr$_4$ is because of the different type of intermolecular forces involved in their formation. Due to this they have exhibited the different leakage current density, when employed as an insulating layer, in MIM devices. The I-V characteristics of these respective MIM devices are shown in supplementary information (Figure S5-S8). In Figure 3, we have compared the leakage current density (capacitor area = 0.01 mm^2) of these MIM devices and corresponding results has been shown in table 1. The leakage current density of AdUr$_4$, MIM device is 2.28×10^{-9} A cm^{-2} while the leakage current density of MIM devices based on AdCl$_4$, AdBr$_4$, AdI$_4$ insulating materials are 1.74×10^{-6} A cm^{-2}, 2.74×10^{-7} A cm^{-2} and 6.01×10^{-8} A cm^{-2} respectively. From this it can be concluded that the AdUr$_4$ STF is an excellent insulator material followed by that of AdI$_4$, AdBr$_4$, and AdCl$_4$. The good leakage current density of AdUr$_4$ STF has motivated us to carry our further study to explore its potential use as a gate dielectric in OTFTs. Moreover, we have also measured the capacitance for these films and found the low dielectric

constants, which are 2.1 for AdCl$_4$, 2.09 for AdBr$_4$ and 2.4 for AdI$_4$. But, the better capacitance (209.0 nF cm^{-2}) was measured for AdUr$_4$ STF with better dielectric constant (2.6) than the other three films. Moreover, the thicknesses of AdCl$_4$, AdBr$_4$, AdI$_4$ and AdUr$_4$ films as estimated by ellipsometery are 10.2, 10.7, 11.5 and 11.0 nm respectively.
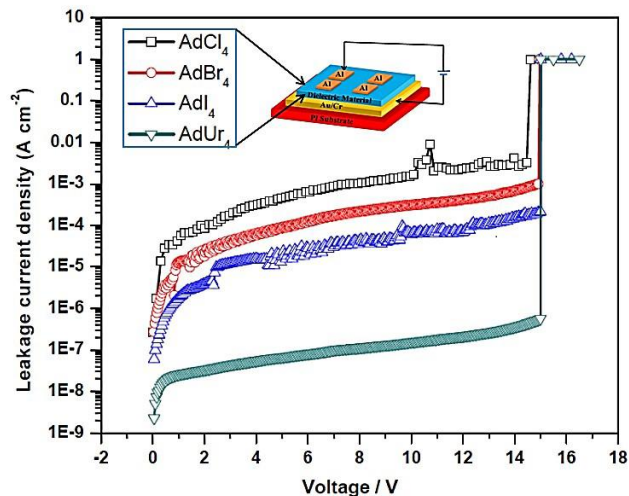

Figure 2. Comparison of leakage current behaviour of AdCl$_4$, AdBr$_4$, AdI$_4$ and AdUr$_4$.

It has been found that the difference in morphology of STF formed by these respective materials on PI plastic substrate has led to this difference in their electrical performance of MIM devices.[33] The AdBr$_4$, AdCl$_4$, AdI$_4$ are well known to form plastic crystals through X---X interactions, as do other symmetrical adamantane derivatives. Desiraju and Parthasarathy have also reported[30] that X---X (where X = Cl, Br, I) interactions can be represented as specific attractive forces. The attractive nature of X---X contacts depend on atomic polarization of respective halogen atoms, which varies as follow.[34]

$$Cl---Cl < Br---Br < I---I$$

The atomic size of chlorine, bromine and iodine atoms are 0.99 Å, 1.15 Å and 1.40 Å respectively.[35] The I---I attractive contacts appeared because of electron cloud distortion of iodine atoms further leading to the formation of partial positive and negative charge on them. Thus, the I---I contacts that have prevailed in AdI$_4$ STF are because of electrostatic forces of attractions among iodine atoms.[30] Owing, to the larger atomic size, this distortion of electron clouds is much more pronounced in case of iodine atoms as compared to that of bromine and chlorine atoms. The X---X (where X = Cl, Br,) that are present respectively in AdCl$_4$, AdBr$_4$ STF are because of van der Waals (vdW) interactions (attractive). Each molecule of AdBr$_4$ has been interacted with the nearby two or three molecules of AdBr$_4$ through these Br---Br vdW interactions.[36] The vdW interactions are known to be of weaker strength as compared to that of electrostatic forces of attractions and intermolecular hydrogen bondings.[37] Hence, due to weak intermolecular forces, AdBr$_4$ molecules in its STF are

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Table 1. Summary of all the characteristic properties of AdCl₄, AdBr₄, AdI₄ and AdUr₄ when incorporated as insulator layer in MIM devices.

Dielectric material	Thickness (nm)	Dielectric constant	Capacitance density (nF cm⁻²)	Leakage current density (A cm⁻²)
AdCl₄	10.2	2.1	182.0	1.74×10^{-6}
AdBr₄	10.7	2.09	172.0	2.74×10^{-7}
AdI₄	11.5	2.4	185.4	6.01×10^{-8}
AdUr₄	11.0	2.6	209.0	2.28×10^{-9}

not packed as closely as that of AdI₄ molecules in AdI₄ STF. Further, the smaller atomic size of chlorine atoms has resulted into more weaker X---X (X= Cl) vdW interactions among AdCl₄ molecules resulting into the more loose packing of the AdCl₄ mol -cules in their STF.[34b,31]The effect of this intermolecular interactions on molecular packing on surface morphology of STF was further analysed by SEM. It has been found that the AdI₄ STF has smaller cracks followed by that of AdBr₄ and AdCl₄ (SEM images of these STF are shown in supporting information S1, S2 and S3). Due to this difference in surface quality of AdX₄ (X= Cl, Br, I) STF, the MIM devices based on them has showed the difference in their electrical performance.[38] The halogen-halogen interactions are the weaker intermolecular forces of interactions as compared to that of intermolecular hydrogen bonds.[39] Therefore, when AdUr₄ STF were analysed by SEM, they appeared to be smooth with no pinholes or cracks, as shown in Figure 3b. The SEM image in Figure 3a shows the AdUr₄ STF when prepared from its low concentration solution on PI plastic substrate (without following Scheme 1) appeared as cluster of AdUr₄ molecules. In this case, the THF solution of AdUr₄ was drop casted on PI plastic substrate and allowed to dry it in open atmosphere.
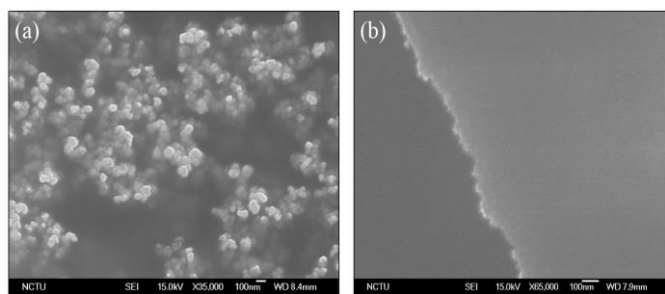

Figure 3. SEM image of (a) AdUr₄ STF when prepared from THF solution of AdUr₄ without following Scheme 1, (b) supramolecular thin film formed from THF solution of AdUr₄ by following the Scheme 1

We found that, for preparation of AdUr₄ STF on PI plastic substrate by Scheme 1, has played a significant role in the formation of its smooth STF. In this method, the slow evaporation of solvent gives sufficient time to AdUr₄ molecules to move closer to each other and then self-assemble in the form

of their STF through U---U intermolecular hydrogen bonds.[32] The AdUr₄ molecules undergoes adsorption on gold coated surface layer (bottom electrode for MIM and OTFTs devices) on PI plastic substrate through uracil groups. This adsorption holds the individual AdUr₄ molecules strongly against the gold coated PI plastic substrate.[40] These molecules further interacts laterally with each other through intermolecular hydrogen bonding between uracil groups as shown in Figure 1. In this process, –NH and –CO present in imide functional group of uracil acts as the donor and acceptor respectively, leading to the formation of rigid STF of AdUr₄ on gold coated PI plastic substrate.

The role played by U---U intermolecular hydrogen bonds on stability of the AdUr₄ STF was further characterized by variable temperature FTIR spectrometer.[41] The AdUr₄ STF on PI plastic substrate was heated to various temperatures and corresponding to each temperature the leakage current property of MIM capacitor device was analysed as shown in Figure 4.
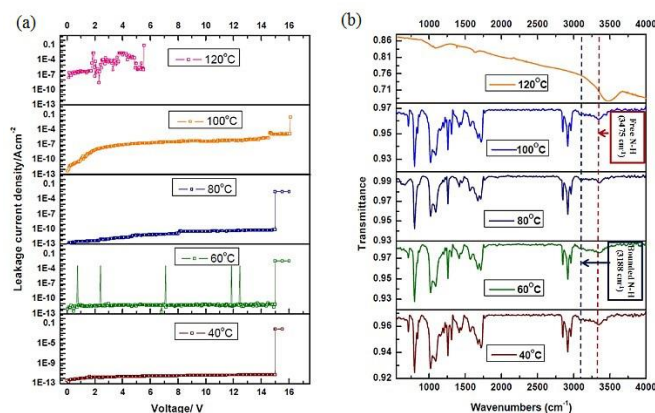
Figure 4. (a) I-V characteristic property under different temperature of flexible 5 × 5 cm² MIM device incorporated with AdUr₄ insulator layer (b) Corresponding variable temperature FTIR spectra of the AdUr₄ STF as prepared on flexible PI plastic substrate.

It has been found that MIM devices, incorporated with AdUr₄ STF as an insulator has shown good electrical performance upto 100 ºC. This can be seen in terms of its high leakage current density, 3.30×10^{-3} A cm⁻² at 120 ºC as calculated from its I-V characteristics curve in Figure 4a. This has been attributed to breakage of U---U intermolecular hydrogen bonds at 120 ºC, which has prevailed in AdUr₄ STF. The variation in FT-IR

spectra of AdUr$_4$ thin film at 120 ^oC, in Figure 4b shows this change. Hence, the excellent electrical performance shown by MIM devices incorporated with AdUr$_4$ STF is due to the U---U intermolecular hydrogen bonds in them. The FTIR spectra of AdUr$_4$ STF in Figure 4b, shows the characteristic peaks corresponding to those of the free amide NH groups at 3475 cm^{-1} and that of –NH involved in U---U interactions at 3188 cm^{-1}.[41c] Thus, the U---U intermolecular hydrogen bonds has played a significant role in success of the AdUr$_4$ STF as new dielectric layer in flexible MIM device at high temperature. The success of AdUr$_4$ STF based MIM devices, have prompted us to incorporate it as a gate dielectric layer in OTFTs.
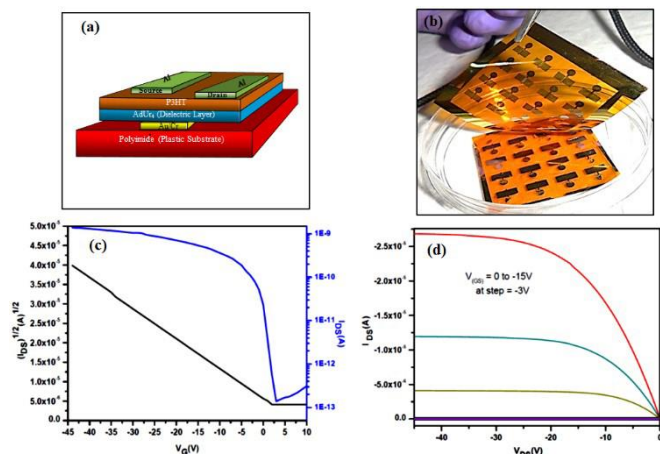

Figure 5. (a) Schematic diagram of bottom gate OTFT device that features AdUr$_4$ as a gate insulator and P3HT as a semiconducting layer (b) photograph of the OTFT devices on a 5 × 5 cm^2 area on a flexible PI plastic substrate and plots for OTFT ; (c) transfer characteristic (I_{DS}-V_{GS}), when V_{DS} = -2 V, and (d) output characteristic (I_{DS} -V_{DS}), where V_{GS} ranges from 0 to -15 V at -3 V step.

In order to study the potential of the AdUr$_4$ STF as a dielectric layer, we fabricated a P3HT based OTFTs with a bottom gate and top contact having a channel width of 2000 μm and a channel length of 82 μm (Figure 5a). The Figure 6b shows the photographic optical image of OTFTs as fabricated on flexible PI plastic substrate with its corresponding configuration shown in Figure 5a. The Figure 5c, shows the transfer characteristics for OTFTs. The Figure 5d, presents the drain current-drain voltage (I_{DS}-V_{DS}) output curve as obtained from our OTFT with a 11.0 nm thick AdUr$_4$ dielectric layer, which is prepared according to scheme 1. The device demonstrated, desirable OTFT characteristics at an operating voltage lower than –45 V. Maximum saturation current of 0.26 μA was achieved. The observed OTFT characteristics confirmed closely to conventional transistor models in both the linear and saturation regimes with I_{DS} increasing linearly at low drainage voltage, and clear saturation behaviour at high drain voltage. According to the drain current-gate voltage (I_{DS}-V_{GS}) transfer curve of Figure 5c, the OTFT with the AdUr$_4$ dielectric layer displayed an average field mobility of 0.15 cm^2 V^{-1} s^{-1} at –45 V, threshold voltages (V_{th}) of –3 V, an on/off current ratio of 2.18 × 10^4. Regioregular, P3HT is a material of choice for research OTFTs

because of its high field effect mobility, stability, and solution processability. The orientation of P3HT molecular chains *i.e* parallel (face-on structure) or perpendicular (edge-on structure) to the insulator substrate is an important phenomenon which need to be explored for the optimum performance of OTFTs. This phenomenon is much dependent on the chemical nature of the gate insulator material as it is the penultimate layer on which semiconducting layer is spin-casted. To study the structural ordering of spin coated films of P3HT on the insulator surface (AdUr$_4$), we used synchrotron grazing-incidence X-ray diffraction (GI-XRD). The grazing incidence angle of 0.18^o was employed and in order to isolate the scattering data from the P3HT polymer film only, the scan from a bare insulator substrate was used to remove background.[42] The GI-XRD result is shown in Figure 6a which demonstrates that the nanocrytalline regioregular P3HT molecular domains has mainly prefered to crystallize in edge-on orientation over the AdUr$_4$ gate insulator as compared to their possible face-on orientation. These two different chain orientations of nanocrytalline regioregular P3HT domains with respect to the AdUr$_4$ STF are evident from the different intensity distribution of the (100) reflections due to the lamellar layer structure (16.4 Å) and the (010) reflections due to interchain stacking (3.8 Å) through out-of-plane and in-plane geometric mode.[12a,b] The intense (100) reflections in the XRD spectra with respect to the (010) reflections shows that on AdUr$_4$ STF the P3HT molecular chain domains has preferred to crystallize in edge on orientations, as shown in Figure 6b. The GI-XRD has revealed this type of nanocrystalline structural order that has prevailed throughout the P3HT film, while the AFM observations was further done to provide images of the top surface. Figure 6c, shows AFM topographic image of regioregular P3HT thin films on insulator substrate (AdUr$_4$). The wormlike morphology of P3HT molecular chains in AFM image, has arisen because of their perpendicular orientations over insulator substrate (AdUr$_4$).[12a] This edge on orientation of P3HT chains has led to their enhanced π-π stacking in the direction of charge transfer in OTFT.

The presence of uracil moiety at the 1,3,5,7 position of the adamantane, in AdUr$_4$ STF has played an important role in the appearance of edge on orientation of P3HT molecular chain domains. The AdUr$_4$ molecules first undergoes adsorption on gold coated PI plastic substrate and then form AdUr$_4$ STF through U---U intermolecular hydrogen bonds. The FTIR spectra of AdUr$_4$ STF in Figure 4b, shows that, their exist two type of –NH protons in uracil substructure. The one which are involved in intermolecular hydrogen bonding shows their characteristic peak at 3188 cm^{-1} while the other, free –NH protons which are not involved in intermolecular hydrogen bonding appears at 3475 cm^{-1}. The free polar –NH protons have undergone π–H interaction (attractive) with thienyl backbone of the regioregular P3HT molecular chains. However, the lone pair of electrons on other type of (-NH)$^{..}$protons in AdUr$_4$ STF were involved in
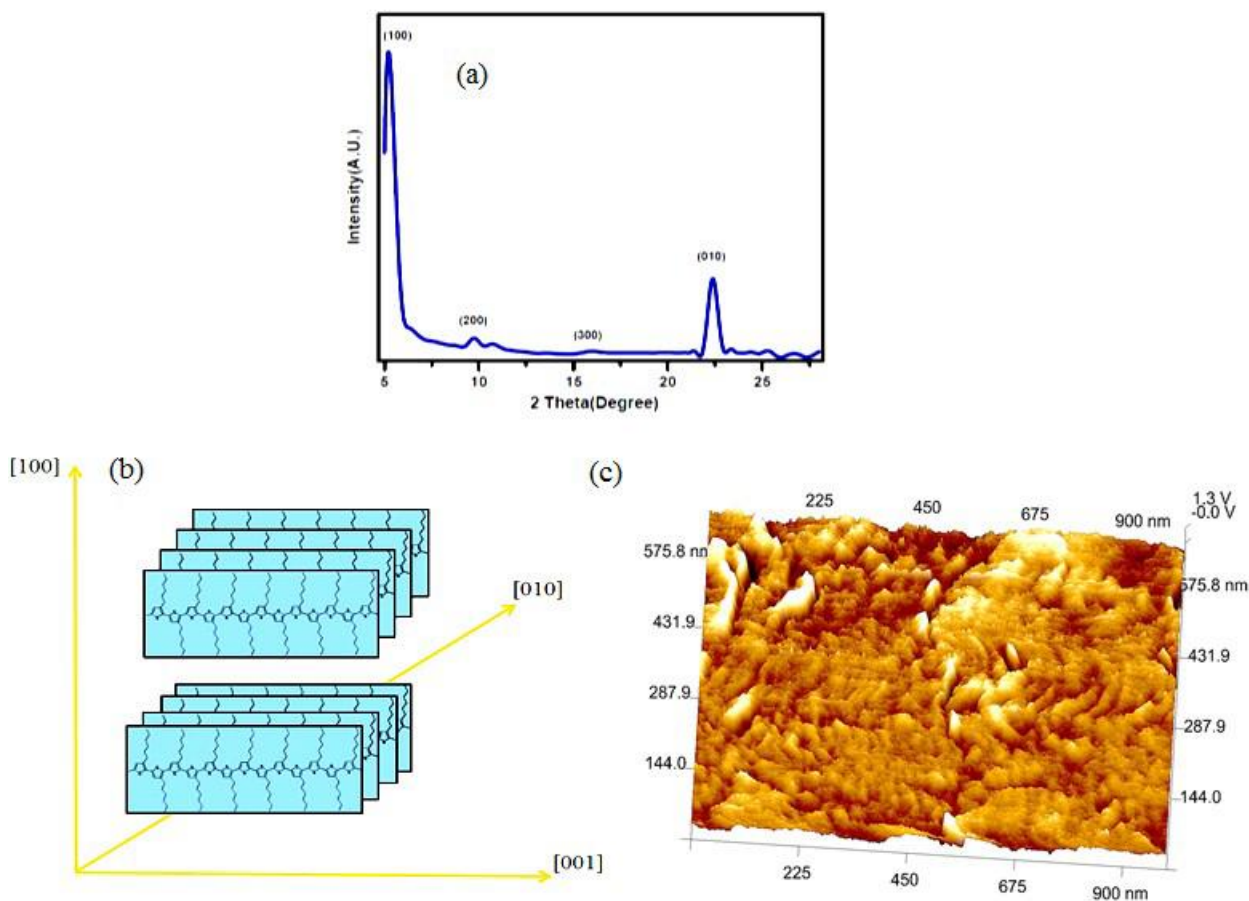
Figure 6. (a) Out of plane grazing-incidence angle X-Ray diffraction intensities as a function of the scattering angle 2θ for regioregular P3HT thin films crystallized against insulator material AdUr$_4$ on silicon wafer. (b) The insets show schematically the crystallography of the P3HT nano-crystallites with respect to the insulator material, AdUr$_4$ (c) tapping phase mode AFM image of a regioregular P3HT film against the AdUr$_4$ insulator on flexible PI substrates

repulsion force with the π-electron cloud of the P3HT molecular chains. Therefore, the P3HT molecular chain domains experiences two opposite force of interactions, over the AdUr$_4$ gate insulator thin film. These two counter forces i.e. force of attraction and repulsion makes the regioregular P3HT molecular chains to pack into the edge-on orientation, *i.e.* perpendicular to the insulator substrate.[12a,b]

Figure 7 shows this whole mechanism of formation of edge-on orientation of P3HT molecular chains over AdUr$_4$ STF dielectric layer. Further, the adamantane is a rigid tetrahedral molecule[21] hence during molecular packing of the AdUr$_4$ mole-

Figure 7. Bottom gate flexible thin film transistor configuration with schematic representation of P3HT polymer chains undergoing edge-on orientation against the AdUr$_4$ insulator surface.

-cules in the form of STF there may exist some voids in them. In these voids the hexane chains on the backbone of P3HT stucks, which further helps the regioregular P3HT molecular chains to crystallize in edge-on orientations. These all factors has led to the success of our P3HT based OTFT device as fabricated with new gate insulator layer material AdUr$_4$.

4. Conclusion

The recent advances in flexible OTFTs have highlighted the use of low cost technology and materials to replace the most commonly used semiconductor and dielectric materials. We have successfully synthesized new dielectric materials AdCl$_4$, AdBr$_4$, AdI$_4$ and AdUr$_4$ through easy to follow synthesis process. Their potential to act as a dielectric material was analysed by fabrication of MIM devices respectively from them. The comparisons of I–V characteristics study of these respective MIM devices have showed that AdUr$_4$ is a better insulator material for plastic electronics. The ability of AdUr$_4$ molecules to undergo supramolecular thin film formation through intermolecular hydrogen bonding has been successfully explored here. Its use as a gate electrode in electronic devices, through fabrication of a new and fully flexible P3HT, a p-type OTFT on the flexible PI plastic substrate has also been demonstrated in this
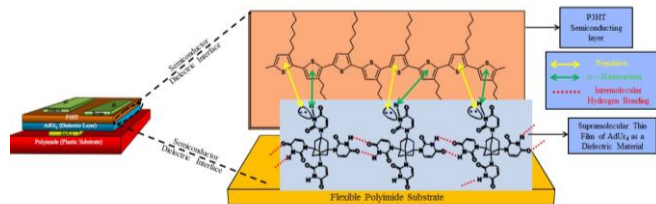
work. This P3HT based OTFT device incorporated with AdUr$_4$ as a gate electrode showed good on/off current ratio (2.18×10^4) and high charge mobility (0.15 cm^2 V^{-1} s^{-1}). The detailed semiconductor-dielectric interface study, lead us to conclude that the P3HT molecular chain domains has undergone edge-on crystallization (perpendicular to the insulator substrate), *i.e.* the charge transport direction in OTFTs. This study has explored a new efficient pathway for development of flexible OTFTs to their full potential on commercial scale, by exploiting the self-assembling ability of small molecules into thin films as a dielectric material in them.

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TOC: Graphical Abstract: text and image

The solid state structure packing of active semiconducting layer in OTFTs has been carefully studied by employing a whole new family of dielectric materials based on adamantane, a smallest cage structure of diamond.