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Composition-Dependent Structural and Electrical Properties of P-type SnO_x Thin Films Prepared by Reactive DC Magnetron Sputtering: Effects of Oxygen Pressure and Heat Treatment

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ABSTRACT

The composition-dependent structural and electrical properties of SnO_x films prepared by means of reactive DC sputtering at various oxygen partial pressure (P_0) and post-heat treatment temperature (T_A) were investigated, toward these films' potential use in p-channel oxide thinfilm transistors (TFTs). A SnOx film fabricated under the lowest studied Po of 4 % and heattreated at 210 °C consisted of dendritic phases and irregular protrusions of metallic Sn. The resulting p-channel SnO_x thin-film transistors suffered from marginal field effect mobility (μ_{FE}) and low on/off current ratio (ION/OFF), suggesting that the imperfect phases caused by oxygendeficient stoichiometry hinder hole carrier conduction and act as bulk trap states. The heterogeneous structures observed in SnO_x films annealed at 210 °C could be eliminated by increasing P₀ during fabrication. The resulting TFTs based on p-type SnO_x films prepared at the high P_O of 8 % showed high mobilities up to 2.8 cm² V⁻¹ s⁻¹ and reasonable $I_{ON/OFF}$ of approximately 10³, demonstrating the critical role of these films' homogeneous ordered aggregates without any imperfect phases such as dendritic phase or irregular protrusions of metallic Sn. Among TFTs based on the films fabricated under 8 % P_O, the $\mu_{\rm FE}$ and $I_{\rm ON/OFF}$ performance metrics degraded with increasing T_A from 210 to 300 °C, which was mainly related to the $2SnO \rightarrow SnO_2 + Sn$ disproportionation reaction.

1. Introduction

Metal oxide semiconductors (MOSs) have received remarkable attention as backplane materials for flat panel displays such as those based on liquid crystals (LCs) and organic light emitting diodes (OLEDs), due to their cost-effective, low-temperature processability and high carrier mobility. Recently, MOSs have been studied extensively as candidate materials for emerging applications such as flexible and transparent electronics, owing to their outstanding characteristics including high optical transparency and electrical conductivity.¹⁻³

N-type InGaZnO-based TFTs have been commercialized as backplane components of flat panel displays, yielding electron mobilities over 10 cm² V⁻¹ s^{-1.4} However, p-type MOSs still need to be improved. This discrepancy between types arises mainly from the difference in their band structures: in n-type oxide semiconductors, electrons are the majority carrier and are transported through a conduction band (CB) composed of delocalized s orbitals of metal ions, whereas in corresponding p-type materials, the valence band maximum (VBM) for transport of the majority hole carriers is composed of localized p orbitals of oxygen ions. Due to these characteristics of MOSs, electrons injected into MOS thin films are easily transferred along the conducting path and show better mobility regardless of structure ordering, relative to holes. Nevertheless, researchers have persistently studied p-type MOSs because they are necessary to fabricate inverters and/or logic circuits for low power consumption, application to transparent devices, etc.³

Stannous oxide (SnO) as a p-type MOS has a relatively large optical band gap of 2.7 eV, and thus has high visible transmittance of above 80 %.^{4, 5} Recently, it has been reported that in a SnO film the delocalized 5s orbitals of Sn²⁺ at the edge of the VB could be controlled to have energy levels similar to that of the O 2p orbitals, thereby yielding high hole mobility in the resulting

TFTs.⁶ It is known that Sn has the dual valency of 2+ and 4+. N-type SnO₂ (Sn⁴⁺) has a rutile structure with a tetragonal unit cell; in this structure, the Sn and O atoms have coordination numbers of 6 and 3, respectively. In contrast, p-type SnO (Sn²⁺) has a layered litharge structure having a Sn_{1/2}–O–Sn_{1/2} sequence in the [001] direction, and wherein each Sn and O atom form a tetragonal unit.^{7, 8}

Due to competition arising from the dual valency of Sn, Sn-based oxide films sputtered under an oxygen partial pressure (P_0) contain three phases: metallic Sn, SnO, and SnO₂. In accordance with this, it has been recently reported that the different oxidized states of Sn in the corresponding thin films yield discernible differences in electrical, optical and micro structural properties of SnO_x films prepared by magnetron sputtering.^{4, 9-12} In particular, in the previous researches on the p-type SnO_x based devices, the morphological evolution and related electrical properties in the SnO_x thin films have been overlooked even though the morphological property can influence substantially the resulting electrical properties in other transparent oxide semiconductor TFTs.^{13, 14} Only a couple of studies have mentioned on the microstructural evolutions by the heat treatment conditions where the clear explanations for the differences in the electrical properties in the SnO_x thin films and resulting TFTs were not explicitly discussed.^{15, 16} Accordingly, to achieve high performance in the p-type SnO_x based field-effect transistors, systematic studies are still required to reveal out the oxidation state and/or microstructure dependency of their electrical properties.

In the present work we investigated the microstructural, morphological evolutions, phase transitions, and related electrical properties of SnO_x thin films and their TFTs sputtered under conditions of various P_O , before and after heat treatment at various temperatures (T_A). The SnO_x film prepared at the lowest P_O of 4 % and subsequently heat-treated at 210 °C consisted of

4

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dendritic phases and irregular protrusions of metallic Sn, which were facilitated by the oxygendeficient stoichiometry in the films. These imperfect phases in the SnO_x thin films could be eliminated by increasing P₀ conditions up to 8 % during fabrication. The resulting p-channel SnO_x TFTs which were fabricated under P₀ of 4 % and subsequently heat-treated at 210 °C showed poor field effect mobility (μ_{FE}) of 0.73 cm² V⁻¹ s⁻¹ and low on and off current ratio ($I_{ON/OFF}$) less than 10. Whereas the resulting TFTs prepared at the higher P₀ of 8 % showed much improved mobilities up to 2.8 cm² V⁻¹ s⁻¹ and $I_{ON/OFF}$ of above 10³, which underscores the critical role of the homogeneous ordered aggregates without dendritic phases and irregular protrusions of metallic Sn hindering hole carrier conduction and acting as bulk trap states in this material. By increasing T_A from 210 to 300 °C, the μ_{FE} and $I_{ON/OFF}$ of p-channel SnO_x TFTs fabricated at P₀ under 8 % were much degraded, which was attributed by the disproportionation reaction of SnO_x thin films (2SnO \rightarrow SnO₂ + Sn).

2. Experimental Methods

2.1 Materials and Device Preparation: A 100-nm-thick SiO₂ layer thermally grown on a highly doped Si wafer was used as a gate dielectric. SnO_x thin films were deposited on a SiO₂/Si substrate at room temperature by means of reactive DC magnetron sputtering with a Sn target (99.999 %, 3 inch diameter), under a processing pressure of about 5×10^{-3} Torr (0.67 Pa); the target-to-substrate distance was approximately 15 cm and the plasma power of 50 W was applied. Various oxygen partial pressures (P₀ = O₂ / (Ar + O₂), vol.%) from 4 to 12 % were applied to prepare various film samples. As-deposited SnO_x thin films were then heat-treated for 1 h inside an Ar-purged tube furnace (above 5 Torr) in order to minimize external oxygen

participation in film formation; various heat treatment temperatures (T_A) from 150 to 300 °C were applied. For the SnO TFTs, 70-nm-thick Pt electrodes as top contact source/drain (*S/D*) electrodes were deposited on the SnO_x/SiO₂/Si substrate by means of e-gun evaporation. The isolated SnO channel and S/D electrodes were patterned through a shadow mask during each layer deposition. The channel width (*W*) and length (*L*) of the fabricated SnO TFTs were 300 μ m and 1000 μ m, respectively.

2.2 Characterization: The thickness of each SnO_x thin film sample was measured by means of spectroscopic ellipsometry (SE, ESM-300, J.A. Woollam). Each sample's chemical composition was analyzed by means of X-ray photoelectron spectroscopy (XPS, VG Thermo Scientific, Sigma Probe) under illumination by a monochromatic Al K_a source (\geq 15 keV). Synchrotron-based grazing-incidence X-ray diffraction (GIXD) analysis of sample films on their SiO₂/Si substrates was conducted at the 9A and 6D beamlines of the Pohang Accelerator Laboratory in Korea. Crystalline phases in these SnO_x films were characterized by using transmission electron microscopy (TEM, Tecnai F20, FEI). Additionally, film morphologies were observed by means of scanning electron microscopy (SEM, Sigma, Carl Zeiss) and atomic force microscopy (AFM, Multimode 8, Bruker).

Resistivity and carrier concentration of the SnO_x thin films were evaluated using a four-point probe (CMT-SR1000N, Advanced Instrument Technology), and van der Pauw measurements of Hall mobility were conducted using a Hall effect measurement system (HL 5500PC, Bio-Rad).¹⁷ Electrical characteristics of the SnO_x TFTs were measured in a room-temperature dark chamber, using a semiconductor parameter analyzer (HP4155A, Hewlett-Packard).

3. Results and Discussion

6

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3.1. Chemical composition variations among the SnO_x thin films

The chemical compositions of the SnO_x films fabricated at various P₀ conditions were evaluated based on deconvoluted XPS spectra. To minimize the contributions of native oxide layers on the topmost layers of the SnO_x films, each sample was subjected to Ar sputtering (2 kV, 3 mA) for 2 s prior to XPS analysis. **Figure 1** shows Sn $3d_{5/2}$ and O 1s XPS core-level spectra of 15-nm-thick SnO_x films fabricated under various P₀ from 4 to 12 %, before and after heat treatment. Here, the heat treatment was performed at 210 °C for 1h, which resulted in the best electrical properties. Note that all binding energies were calibrated by setting the C 1s peak position at 284.8 eV. Hereafter, SnO_x film samples fabricated under the P₀ condition of 4 % are referred to as "4 % P₀ samples", and so forth.

The binding energy states of the Sn $3d_{5/2}$ core level yielded XPS peaks around 484.4, 485.9, and 486.6 eV, originating from oxidized states of Sn with three different oxidation numbers (i.e., Sn⁰, Sn²⁺, and Sn⁴⁺, respectively).^{18, 19} XPS analysis of the Sn $3d_{5/2}$ core level in the SnO_x sample prepared at the lowest P₀ of 4 % indicated large portions of Sn⁰ and Sn²⁺: 52.2 and 45.0 at.%, respectively. With increasing P₀, the as-deposited films contained lesser portions of metallic Sn (Sn⁰), and no Sn⁰ was observed for the 12 % P₀ sample: in this case, the Sn⁴⁺ state was also observed (see red curves around 486.6 eV in **Figure 1**). After the films were heat-treated at 210 °C for 1 h, most of the Sn⁰ in the films was found to have oxidized to form SnO. Additionally, XPS spectra of the O 1s core levels in these SnO_x films showed discernible oxygen states, depending on P₀ and heat treatment conditions. The binding energies of O–Sn²⁺ and O–Sn⁴⁺ were assigned as 529.8 and 530.4 eV, respectively.⁴, ¹⁸⁻²²



Figure 1. XPS spectra of (a, b) Sn $3d_{5/2}$ and (c, d) O 1s core levels in 15-nm-thick SnO_x thin films deposited on SiO₂ dielectrics under various P₀ conditions (4 to 12 %), (a, c) before and (b, d) after heat treatment at 210 °C for 1 h: open black circles represent the measured spectra; blue, red, and green lines respectively represent the deconvoluted Sn²⁺ (or O-Sn²⁺), Sn⁴⁺ (or O-Sn⁴⁺), and Sn⁰ (or O_{chem}, chemisorbed oxygen) spectra, and the black line represents the sum of these deconvoluted spectra.

Figure 2 is based on the XPS results and represents variations in the oxygen content and Sn oxidization states (Sn⁰, Sn²⁺, Sn⁴⁺) among SnO_x films fabricated under various P₀ conditions, before and after heat treatment at 210 °C for 1 h. The detailed compositional variations were summarized at Table S1 in the SI. The oxygen content in as-deposited SnO_x thin films increased

monotonically from 24.3 to 43.5 at.% with increasing P_0 from 4 to 12 %. As P_0 was increased, the resulting portions of Sn^0 decreased from 52.2 to 3.3 % and the portions of Sn^{2+} and Sn^{4+} respectively increased from 45.0 to 84.7 at.% and from 2.8 to 12.1 at.%. During heat treatment, the metallic Sn atoms in the as-deposited films seemed to be mostly oxidized to Sn^{2+} instead of to Sn^{4+} (**Figure 2b**): the portions of Sn^{4+} in the SnO_x films were almost the same before and after heat treatment.



Figure 2. Chemical compositions of SnO_x films fabricated under various P_O conditions before and after heat treatment at 210 °C for 1 h: (a) O, (b) Sn^0 , Sn^{2+} , and Sn^{4+} . Open and closed marks respectively represent compositions before and after heat treatment at 210 °C for 1 h.

3.2. Structural and morphological characteristics

Film structure was characterized by means of synchrotron-based GIXD and TEM analyses. First, 15-nm-thick SnO_x films were subjected to two-dimensional (2D) GIXD, which revealed

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Sn, SnO, and SnO₂ phases of different portions, depending on their P₀ and heat treatment conditions. Among the as-deposited SnO_x films on SiO₂/Si substrates, the 4 and 6 % P₀ samples showed weak X-ray reflections at Q of 2.155, 2.250, and 3.048 Å⁻¹ (**Figures S1a** and **b** in the SI), originating from (200), (101), and (220) crystal planes in β phases of Sn (JCPDS No. 01-086-2264),²³⁻²⁵ whereas films fabricated under higher P₀ showed only a hollow ring at Q = 2.064 Å⁻¹ in the 2D GIXD patterns (**Figures S1c** and **d** in the SI), indicating amorphous structure.

After heat treatment at 210 °C for 1 h, except for the 10% P_O sample, all SnO_x films showed intense X-ray reflections in 2D GIXD patterns that indicated ordered phases of metallic or oxidized Sn (Figure 3). For the 4, 6, and 8 % Po samples, intense X-ray reflections were observed that corresponded to polycrystalline SnO (JCPDS No. 01-085-0712) as well as β -phase Sn residue (see arrow-marked peaks in Figure 3). For heat-treated 4, 6 and 8 % Po samples, typical X-ray reflections were observed at Q of 1.297, 2.101, 2.334, and 2.594 Å⁻¹, originating from the (001), (101), (110), and (002) planes in SnO crystallites with tetragonal unit cells: a = b= 3.796 Å, c = 4.816 Å.⁸ The X-ray reflections were anisotropically distributed along the X-ray Debye rings (Figure 3), suggesting that the SnO aggregates were preferentially oriented with respect to the substrates. Interestingly, the heat-treated 8 % Po SnOx film showed quite discernible orientations of (101), (110), and (200) planes in comparison to those in the 4 and 6 % P_O samples (this will be discussed later). However, the SnO_x film samples with the highest O content (the 10 and 12 % $P_{\rm O}$ samples) retained amorphous-like structures even after heat treatment, suggesting that the presence of the secondary SnO₂ phase strongly prevented crystallization and ordering of the primary SnO phase (approximately 76 at.%, determined by XPS analysis). The XPS and GIXD results strongly supported the conclusion that the order of

structures in the as-deposited and heat-treated SnO_x films was degraded severely with increasing SnO_2 composition.⁴



Figure 3. (a) 2D GIXD patterns of SnO_x films fabricated under P_0 of 4, 6, 8, and 10 % and heattreated at 210 °C for 1 h. (b–e) 1D in-plane and out-of-plane X-ray profiles extracted from the patterns shown in (a): (b) 4, (c) 6, and (d) 8 % P_0 samples; (e) unit cell of the SnO lattice.^{3, 7} Weak X-ray reflections marked with red arrows marked in (c) correspond to the β phase of Sn.

Figure 4 shows in-plane TEM micrographs of 15-nm-thick SnO_x films fabricated under the P_O of 4 and 8 % and then heat-treated at 210 °C for 1 h. The 4 % P_O sample consisted mostly of

polycrystalline SnO in which the (001) and (101) crystal planes were oriented normal to the dielectric surface (**Figure 4a** inset). Additionally, less ordered grain boundaries were observed having 5–10 nm gaps (see broken line–marked region in **Figure 4a**). In contrast, the SnO crystallites in the heat-treated 8 % P₀ sample did not show any visible grain boundary: the (110) and (101) crystal planes were preferentially oriented normal to the dielectric surface (**Figure 4b**). In this case, the angle between (110) and (101) planes was found to be about 64°, close to calculated value of 64.04° for the previously reported typical tetragonal structure of SnO.⁸ It was also found that the [001] direction (c-axis) in these crystallites was tilted from the surface normal, as evidenced by the 2D GIXD pattern (**Figure 3a**). During reactive DC magnetron sputtering with a Sn target, fine control of P₀ is important to achieve preferential development of p-type SnO crystallites on dielectric surfaces; this leads to a discernible difference in the electrical properties of the resulting TFTs, as will be discussed below.²⁶



Figure 4. High-resolution TEM micrographs (plan-view) of 15-nm-thick SnO_x films fabricated under P_O of (a) 4 and (b) 8 %, and both subsequently heat-treated at 210 °C for 1 h. Insets depict fast Fourier transform patterns corresponding to the micrographs.

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Additional SEM and AFM analyses were carried out to investigate the morphologies of the SnO_x thin films. All the as-deposited films showed very smooth and featureless morphologies, regardless of the P₀ during their fabrication (**Figure S2**). However, the heat-treated films all showed discernible structures that depended on the processing parameters of P₀ and T_A. An SEM micrograph of the heat-treated 4 % P₀ film showed a percolated layer including dendritic phases of widths ranging from 20 to 100 nm and vertically grown irregular protrusions with diameters on the order of several tens of nanometers. The dendritic grains and aggregates caused considerable inhomogeneity in this SnO_x film, specifically, yielding severe lateral disconnection (**Figure 5a and Figure S5 in SI**). The irregular protrusions were determined by means of energy dispersive X-ray spectroscopy (EDS) analysis to be metallic Sn aggregates (**Figure S3**). The crystallites and aggregates in the 210 °C-heat-treated SnO_x films became denser and less numerous with increasing P₀ (Figure 5b, c). In contrast, the 10 % P₀ samples showed featureless and smooth morphologies both before and after heat treatment (Figure 5d).

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Figure 5. SEM micrographs of SnO_x thin films fabricated under various P_O and subsequently heat-treated at 210 °C for 1 h: (a) 4, (b) 6, (c) 8, and (d) 10 % P_O samples.

Additionally, AFM topography analyses of the corresponding SnO_x films showed similar morphologies as those indicated by the SEM results: increased oxygen content in the heat-treated SnO_x films reduced the phase inhomogeneity and film surface roughness (R_q) (**Figure 6**). R_q of the heat-treated films decreased drastically from 20.5 nm (at $P_0 = 4$ %) to 0.4 nm (at $P_0 = 10$ %) with increasing P_0 during their fabrication. Particularly, the R_q of 10 % P_0 SnO_x film was not changed after heat treatment at T_A of 210 °C. The SEM and AFM results strongly supported the conclusion that the SnO_x phase formation was very sensitive to oxygen stoichiometry and an equivalent oxygen stoichiometry was required to form the homogeneous p-type SnO phase from the as-deposited films.



Figure 6. AFM phase images of SnO_x films fabricated under various P_O and subsequently heattreated at 210 °C: (a) 4, (b) 6, (c) 8, and (d) 10 % samples.

Several studies have shown that the evolution of dendritic structures is enhanced when crystal growth rate exceeds the mass transport rate of ions and molecules that are necessary for crystal growth.^{27, 28} Especially, Boggs et al.²⁹ suggested reasonable mechanisms for the dendritic crystal growth of polycrystalline SnO phases during crystallization of SnO_x thin films under oxygen-deficient conditions. Namely, they suggested that the areas adjacent to the crystallized SnO lattices become depleted in oxygen as the oxygen is captured and integrated into SnO lattices;

consequently, further growth of SnO crystals must be directed toward areas that are richer in oxygen.

As shown in **Figure 2a**, the SnO_x thin films in this work were all deposited under oxygendeficient conditions and then heat-treated in an oxygen-deficient atmosphere. Accordingly, the observation in the present work of the evolution of dendritic structures on surfaces of heattreated SnO_x thin films can be attributed to dendritic crystal growth of polycrystalline SnO phases in oxygen-deficient environments. Consequently, metallic Sn compositions in the oxygen-deficient areas adjacent to the dendritic structures would steadily increase until they exceeded their solubility limit, leading to phase separation and the formation of metallic Sn irregular protrusions. This explanation is supported well by the gradual disappearance of dendritic structures and irregular protrusions of metallic Sn observed with increasing P₀ from 4 to 8 %. The reason why the heat-treated 10 % P₀ sample showed a uniform surface is that the film had not yet crystallized.

3.3. Electrical properties and functionalities

The electrical resistivity (ρ), net free carrier concentration, and Hall mobility (μ_{Hall}) of the SnO_x thin films were evaluated. **Figure 7a** shows the ρ values of 15-nm-thick SnO_x thin films prepared under various P₀ (4–12 %) and T_A (165–300 °C) conditions. The as-deposited 4 % P₀ film had ρ of 10⁻¹ Ω cm, suggesting its feasibility for use as a semiconductor. Increasing the P₀ used during film preparation increased the resistivity of the resulting SnO_x films. Thus, the as-deposited 10 % P₀ sample showed the increased ρ value of 10² Ω cm. In contrast, the 12 % P₀ sample exhibited insulator-like properties whereby it was difficult to measure ρ because of the high contact resistance. The films' ρ values could be tailored by controlling T_A. Heat treatment at

 $T_A = 165$ °C caused the resistivities to increase by 1 to 3 orders of magnitude relative to those of the as-deposited films. When the as-deposited films were heat-treated at T_A of 210 °C or greater, the SnO_x films fabricated under P₀ ranging from 4 to 8 % exhibited T_A -independent ρ values ranging from 10⁰ to 10¹ Ω cm. On the other hand, those films fabricated under P₀ of 10 and 12 % showed insulator-like behaviors. Considering the previously discussed evolution of chemical states and crystal structures, the observation of increased ρ with increasing P₀ for the asdeposited films is well consistent with the fact that the portion of metallic Sn decreased with increasing P₀. Oxidative deposition would decrease the metal Sn fraction, leading to enhanced electrical resistance. On the other hand, the strong T_A dependency of ρ observed for the SnO_x films deposited with P₀ of 4 to 8 % seems to have been associated with the microstructural transition from amorphous SnO_x to polycrystalline SnO. Note that the amorphous phase of the insulator-like 10 and 12 % P₀ films was maintained after heat treatment at various T_A.

Heat-treated semiconducting SnO_x films ($T_A = 210$ °C) were subjected to Hall effect measurements, in which they exhibited relatively high thermal stability. **Figure 7b** shows net carrier concentration and μ_{Hall} values for the SnO_x thin films versus the P₀ used during their fabrication. The dominant carrier conduction for the 4, 6, and 8% P₀ films was determined to be p-type. The net hole concentration (N_h) for the SnO_x films monotonically decreased from 4.35 × 10^{18} to 1.81×10^{18} cm⁻³ with increasing P₀. This can be attributed to a reduction in free hole carriers in the overall SnO_x film as the n-type SnO₂ phase present in the film increases with increasing P₀ (see **Table S1 in the SI**).⁴ Conversely, the films' μ_{Hall} values increased from 1.1 to 5.5 cm² V⁻¹ s⁻¹ with increasing P₀. The differences in crystallographic preferential orientation and topological evolution of the SnO_x film depending on P₀ during deposition can be considered the most plausible origin for the observed variation in μ_{Hall} . The electronic structure reported by

Togo el al.⁶ suggests that the stannous oxide (SnO) has an anisotropic band structure in its Brillouin zone. The curvature of the *E-k* diagram in the [001] direction (Γ -Z) is larger than those in the [100] direction (Γ -X) and [110] direction (Γ -M) near the VBM. This means that the effective hole mass (m_{h}^{*}) in the [001] direction is the smallest among the various directions. From this, one would predict that 4 % P₀ films showed higher mobility than 8 % P₀ films because the volume faction of [001] oriented parallel to the in-plane direction was substantially higher in the former, however, as shown in **Figure 7b**, this is not the case. In general, the carrier mobility is proportional to the product of the mean scattering time and inversely proportional to the effective mass. Therefore, it can be inferred that the carrier scattering mechanism plays a critical role in determining the carrier mobility. Indeed, the 4 % P₀ film suffered from microstructural inhomogeneities such as metallic Sn aggregates and dendritic structure. These imperfections are likely to act as strong scattering centers, and thus to be responsible for this material's inferior mobility.



Figure 7. (a) Resistivity of SnO_x thin films (15-nm-thick) deposited under various P_O (4 to 12 %) before and after heat treatment (165–300 °C). (b) Net hole concentration (N_h) and Hall mobility (μ_{Hall}) of SnO_x thin films heat-treated at 210 °C, as acquired during Hall effect measurements.

The electrical functionality of these p-type semiconducting SnO_x films was further evaluated in thin-film transistors (TFTs) made from the films. **Figure 8a** shows representative transfer characteristics for bottom gate SnO_x TFTs with 15-nm-thick channels made using the 4, 6, and 8 % P_O films. The field-effect mobility (μ_{FE}) and subthreshold gate swing (*SS*) were respectively calculated using equations [1] and [2], given below. The maximum bulk trap density ($N_{SS,max}$) and interfacial trap density ($D_{it,max}$) were evaluated using equation [3], for which the value of $N_{SS,max}$ ($D_{it,max}$) was estimated by setting the $D_{it,max}$ ($N_{SS,max}$) term to zero.

$$u_{\rm FE} = \frac{L \times g_{\rm m}}{W \times C_{\rm i} \times V_{\rm DS}}$$
, where $g_{\rm m} = \frac{\partial I_{\rm DS}}{\partial V_{\rm GS}}|_{V_{\rm DS} = \rm constant}$ [1]

$$SS = \left(\frac{\partial \log(l_{\rm DS})}{\partial V_{\rm GS}}\right)^{-1} |_{\rm max}$$
^[2]

$$SS = qk_{\rm B}T(N_{\rm SS,max}t_{\rm ch} + D_{\rm it,max})/[C_{\rm i}\log(e)]$$
[3]

Here, *L* is the channel length (300 μ m), *W* is the channel width (1000 μ m), *C*_i is the capacitance per unit area of the gate dielectric (34.5 nF cm⁻²), *V*_{DS} is the drain voltage, *I*_{DS} is the drain current, *V*_{GS} is the gate bias, *q* is the electron charge, k_B is the Boltzmann constant, *T* is the absolute temperature, and *t*_{ch} is the channel layer thickness (15 nm).³⁰

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All the SnO_x TFTs exhibited p-type conduction irrespective of the P_O values. The SnO_x TFT prepared at P_O = 4 % showed the marginal μ_{FE} of 0.73 cm² V⁻¹ s⁻¹, and yielded the low $I_{ON/OFF}$ ratio of 6.8, representing poor current modulation capability. The device performance of the SnO_x TFTs was improved for films deposited under greater P_O. The high μ_{FE} of 2.8 cm² V⁻¹ s⁻¹ and reasonable $I_{ON/OFF}$ ratio of 1.0 × 10³ were achieved by the SnO_x TFT based on the 8 % P_O film underscores the importance of its homogenous ordered aggregate state and its absence of Sn metal aggregate and abrupt dendritic structure.

It is interesting to discuss why the SnO_x TFT based on the 4 % P_O film yielded the poor $I_{ON/OFF}$ ratio of 6.8. The observation of high off-state I_{DS} indicated the difficulty in fully depleting the bulk semiconducting SnO_x film. In other words, the quasi Fermi level (E_F) in the SnO_x film near the gate dielectric/semiconductor interface was strongly pinned due to the very large N_{SS} because E_F cannot move upward without trap filling.³¹ Indeed, the extracted $N_{SS,max}$ value for the TFTs based on 4 % P_O film was the largest measured ($1.5 \times 10^{20} \text{ eV}^{-1} \text{ cm}^{-3}$). Contrastingly, $N_{SS,max}$ for the TFTs based on 8 % P_O film was $2.0 \times 10^{19} \text{ eV}^{-1} \text{ cm}^{-3}$. The superior transporting property of these TFTs was clearly reflected in the higher I_{DS} level of their output characteristics (see **Figure 7b**).

A qualitatively similar trend for the p-channel SnO_x TFTs based on the 4, 6, and 8 % P₀ films heat-treated at T_A of 210 °C was also observed for higher T_A (240, 270, and 300 °C; **Figure S4**). Interestingly, the μ_{FE} value and $I_{ON/OFF}$ ratio observed for a SnO_x TFT at a given P₀ condition were degraded by increasing T_A from 210 to 300 °C. For example, the TFTs based on the 8 % P₀ film and heat-treated at 300 °C yielded μ_{FE} of 1.0 cm² V⁻¹ s⁻¹ and the $I_{ON/OFF}$ ratio of 1.6 × 10². This behavior can also be attributed to a structural transition involving the chemical states of Sn

ions and microstructural inhomogeneity, namely, phase separation from 2SnO to SnO₂ plus metallic Sn; this transition is thermodynamically driven above 250 °C.^{32, 33}

Finally it would be meaningful to compare the performance of SnO TFTs in this study with other works. The μ_{FE} of 2.8 cm² V⁻¹ s⁻¹ and $I_{ON/OFF}$ ratio of 1.0×10^3 for the SnO_x TFT based on the 8 % P₀ film are comparable to those (μ_{FE} : 1.7 ~ 6.8 cm² V⁻¹ s⁻¹, $I_{ON/OFF}$ ratios of $10^3 \sim 10^4$) reported in the literature for other p-channel oxide TFTs.^{4, 11, 15, 34-38} It should be noted that higher mobility ($\geq 3.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$) can be achieved further by adopting the high permittivity dielectric films such as HfO₂,¹¹ AlTiO_x,³⁶ and/or reducing the gate insulator thickness.^{34, 35}



Figure 8. (a) Transfer and (b) output characteristics of bottom gate structure TFTs based on thin SnO_x films (15-nm-thick) fabricated under various P_O (4, 6, and 8 %) and heat-treated at 210 °C.

Table 1. Summary of electrical characteristics of the SnO_x thin films and TFTs: net carrier concentration (N_h), Hall mobility (μ_{Hall}), $I_{ON/OFF}$ ratio, field effect mobility (μ_{FE}), subthreshold

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swing (SS), maximum bulk trap density ($N_{SS,max}$), and interface trap density ($D_{it,max}$). The SnO_x thin films and TFTs were fabricated at various oxygen partial pressures (P₀) from 4 to 12 % and were heat-treated at 210 °C.

P _O (%)	$N_{\rm h}$ (cm ⁻³)	$(\mathrm{cm}^2 \mathrm{V}^{-1} \mathrm{s}^{-1})$	I _{ON/OFF}	$(\mathrm{cm}^2 \mathrm{V}^{-1} \mathrm{s}^{-1})$	SS (V/de c.)	$N_{\rm SS,max}$ (eV ⁻¹ cm ⁻³)	$(eV^{-1}cm^{-2})$
4	4.35×10^{18}	1.14	6.8 imes 1 0^0	0.73	62.9	1.52×10^{20}	2.28×10^{14}
6	2.64×10^{18}	2.84	2.9×1 0^1	1.21	27.6	6.64×10^{19}	9.96×10^{13}
8	1.81×10^{18}	5.49	1.0×1 0^3	2.80	8.47	2.03×10^{19}	3.05×10^{13}

Note: The electrical properties of SnO_x thin films fabricated under P_0 of 10 and 12 %, and TFTs fabricated based on these films, were not measurable due to their excessive resistivity.

4. Conclusions

The chemical, structural, and electrical properties of SnO_x thin films deposited by means of reactive DC sputtering were tailored by controlling the oxygen partial pressure used during sputtering ($P_0 = 4-12$ %) and the post heat treatment temperature ($T_A = 150-300$ °C). The asdeposited SnO_x thin films were found to be oxygen-deficient irrespective of the P_0 during deposition whereas the chemical state of Sn ions in the heat-treated SnO_x films strongly depended on P_0 . Higher P_0 conditions, within the range between 4 and 8 %, favored the formation of SnO/SnO_2 phases and the suppression of metallic Sn phase in the resulting heat-treated films. Simultaneously, the preferential orientation of SnO_x films changed from the (001), (101) face to the (101), (110) face with increasing P_0 . The SnO_x film fabricated under the lowest P_0 studied of 4% consisted of dendritic phases and irregular protrusions of metallic Sn, which are enhanced by the oxygen-deficient stoichiometry. This structural inhomogeneity was weakened with increasing P_0 and finally disappeared in the 10 % P_0 sample. The P_0 - and T_A -

dependent chemical and structural properties of the SnO_x films were reflected in the electrical performance of the resulting TFTs. TFTs based on a SnO_x channel prepared at P₀ of 4 % and T_A of 210 °C exhibited the marginal μ_{FE} of 0.73 cm² V⁻¹ s⁻¹ and the low I_{ON/OFF} ratio of 6.8, which can be attributed to structural imperfections of the film, namely its rough dendritic structure and the presence of metallic Sn clusters. Contrastingly, the 8 % P₀ film heat-treated at 210 °C showed a homogeneous ordered aggregates without any dendritic phase or irregular protrusions of metallic Sn, and p-type TFTs based on this film achieved the high μ_{FE} of 2.8 cm² V⁻¹ s⁻¹ and $I_{ON/OFF}$ ratio of ~10³, underscoring the critical role of this material's superior structure. The μ_{FE} and $I_{ON/OFF}$ ratio performance metrics of the TFTs based on the 8 % P₀ films deteriorated as T_A was increased from 210 to 300 °C, indicating that the disproportionation reaction [2SnO \rightarrow SnO₂ + Sn] should be avoided to ensure optimal transistor performance. Comprehensive understanding of the P₀ and T_A dependency of SnO_x films' chemical, structural, and electrical properties would enable useful design concepts for high performance TFTs based on p-channel SnO_x.

ASSOCIATED CONTENT

Supporting Information

Summarizations of the chemical compositions of Sn (Sn⁰, Sn²⁺, and Sn⁴⁺) and O content in SnO_x films fabricated under various P_O conditions, Additional details on the 2D GIXD patterns of asdeposited SnO_x films, SEM images of as-deposited SnO_x thin films, Energy dispersive X-ray spectroscopy (EDS) analysis of atomic composition in the irregular protrusions of SnO_x thin films, TFT performance metrics extracted from transfer characteristics of bottom gate TFTs

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prepared using SnO_x thin films, SEM micrographs of 45-nm-thick SnO_x films annealed at 210 °C for 1 h. This material is available free of charge via the Internet at http://www.rsc.org.

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Author Contributions

The manuscript was written through contributions of all authors. All authors have given approval to the final version of the manuscript.

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ABBREVIATIONS

DC, direct current; MOSs, metal oxide semiconductors; LCs, liquid crystals; OLEDs, organic light emitting diodes; TFTs, thin-film transistors; CB, conduction band; VBM, valence band maximum; *S/D*, source/drain; SE, spectroscopic ellipsometry; XPS, X-ray photoelectron spectroscopy; GIXD, grazing-incidence X-ray diffraction; TEM, transmission electron microscopy; EDS, energy dispersive x-ray spectroscopy; SEM, scanning electron microscopy; AFM, atomic force microscopy.

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Table of Contents Graphic



as-deposition Annealed at 210 °C