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ARTICLE

Flexible Supercapacitor Based on Vertically Oriented 'Graphene Forest' Electrodes†

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Vertically-grown graphene electrodes are developed for flexible electric double layer capacitors (EDLC). Solid-state electrolytes and large area flexible electrodes are essential parts for wearable applications. In this work the vertically-grown graphene electrodes were fabricated by a plasma-enhanced chemical vapor deposition process and applied to flexible electrodes of EDLC. The areal capacitance of the capacitor based on vertical graphene is 2.45 mF/cm², which is much better even with solid electrolytes when compared to other reported vertical graphene capacitors. The capacitance also shows good flexibility and it remains unaltered even after 100,000 times of bending or 180 degree folding. These unique features of the capacitor could be ascribed to a discrete 'tree-like' morphology of the vertical graphene, which has not been known before.

Introduction

Graphene, a single layer of graphite, has widely been studied as a carbon-based electrode material for transparent electrode,^{1,2} solar cells,^{3,4} supercapacitors⁵⁻⁷ and secondary lithium ion batteries^{8,9} due to its high electrical conductivity,¹⁰ high surface area,¹¹ and excellent mechanical strength.¹² Many processes have been developed to produce graphene for various applications.¹³⁻¹⁷

For supercapacitors, reduced graphene oxide (rGO) obtained from graphene oxide (GO) has mostly been utilized.^{8,16,18} The horizontally aligned rGO films, typically used for electrode, however, do not allow easy diffusion of electrolyte ions through the film because of close stacking of rGO sheets.^{5,7} To remedy the problem, spacers such as carbon black nanoparticles, carbon spheres, or carbon nanotubes have been inserted into rGO inter-layers.^{16,18}

The advent of vertical graphene grown by plasma deposition^{19,20} allowed easy and fast access of ions to the electrode without any spacers. This feature of vertical graphene as an electrode, together with the non-porous nature of graphene, made it possible to usher capacitors into the high frequency filtering arena with fast response time.²¹⁻²³

Wearable devices, an emerging technology and a burgeoning trend, require capacitors to power the devices. Because they are worn by human beings, the capacitor has to be safe and furthermore, endure countless number of bending for it to be useful. To satisfy the requirements, a solid electrolyte instead of the usual liquid electrolyte is used. A unique morphological feature of the vertical graphene developed here is utilized to make it utterly unaffected by bending even after 100,000 times of bending.

In this work, we present a wear-tolerant and safe capacitor based on vertical graphene and solid electrolyte. The areal capacitance of the flexible capacitor is higher than that of other vertical-graphene capacitors by an order of magnitude.²³⁻²⁵ Forest of 'tree-like' vertical graphene grown on quartz substrates is utilized for the electrode, which is termed here vertically oriented graphene forest or simply graphene forest (GF).

Experimental

Preparation of vertically oriented graphene forest:

A 5 × 5 cm² quartz substrate was used. It was first cleaned in deionized water and acetone, and blow-dried with N₂ gas. The quartz square was placed in the heating zone of a radio-frequency (RF) plasma enhancement chemical vapor deposition (PECVD) system. The chamber was evacuated to a pressure less than 1 mTorr and then hydrogen gas was introduced at a rate of 8 sccm. After heating the chamber to 850 °C and staying at that temperature for 5 min, plasma was generated for 2 min with RF power of 200 W to further remove contaminants on the quartz substrate. Graphene was then grown by introducing C₂H₂ and H₂ at flow rates of 3 sccm and 1 sccm, respectively, for 2 hours with a plasma power of 280 W at a total pressure of 60 mTorr.

Preparation of polymer electrolyte:

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Polyvinyl alcohol (PVA) was utilized to prepare polymer electrolyte. In detail, PVA (98-99% hydrolyzed, medium molecular weight, Alfa Aesar) powder was mixed with water (2 g PVA/20 mL H₂O) and subsequently heated under stirring to around 90 °C in an oil-bath until the solution became clear. After cooling to room temperature, 1.6 g of concentrated phosphoric acid was added to the solution with thorough stirring.

Supercapacitor fabrication and characterization:

The PVA solution thus prepared, 3 ml in volume, was coated on the surface of GF on quartz directly. The coated GF was allowed to dry to form a PVA film. The PVA/GF film was then carefully peeled away from the quartz substrate. The PVA surface is quite sticky and therefore, two pieces of the PVA/GF film can be pasted together easily to form GF/PVA/PVA/GF structure. After GF is peeled away from the quartz, the substrate can be recycled to grow graphene on it again.

The fabricated capacitor was characterized by cyclic voltammetry (CV), galvanostatic charge-discharge (GCD) and electrochemical impedance spectroscopy (EIS). Average specific capacitance can be obtained from CV, GCD, and EIS. For comparison with literature data, the average specific capacitance was calculated from these three characterization results.

The average specific capacitance of GF supercapacitor from the galvanostatic discharge curve was calculated as follows:

$$C = \frac{I}{\Delta V / \Delta t \times a}$$

while the capacity from CV was obtained from,

$$C = \frac{A/2}{s \times \Delta V \times a}$$

where I , ΔV , Δt , a , s and A are the discharge current, the voltage drop upon discharging, the discharge time, the

electrode area, the scan rate, and the integration of positive and negative sweep in cyclic voltammetry curves, respectively. The specific capacitance of GF supercapacitor from electrochemical impedance spectroscopy was obtained from the following equation:

$$C = \frac{-1}{2 \times \pi \times f \times Z'' \times a}$$

where f is the frequency, Z'' is the corresponding imaginary part of the impedance, and a is the electrode area.

Characterization of materials:

The surface and cross-section morphology of the vertically oriented graphene was examined with field emission scanning electron microscopy (FE-SEM, JEOL, JSM7500F). High-resolution transmission electron microscopy (HRTEM, JEM-2100F JEOL) images were also taken to show successful growth of graphene. Carbon bonding structure was analyzed by Raman spectroscopy (Renishaw, RM-1000 Invia) with a wavelength of 532 nm (Ar⁺ ion laser). The sheet resistance of all samples was measured using four-point probe method (Keithley 2420I-V) at room temperature. Cyclic voltammetry (CV), galvanostatic charge-discharge and electrochemical impedance spectroscopy (EIS) were carried out with multichannel potentiostat and galvanostat (Bio-Logic Science Instruments, VMP3). Focused monochromatized Al K α radiation ($h\nu = 1486.6$ eV) was utilized for X-ray photoelectron spectroscopy (XPS, QUANTUM 2000, Physical electronics, USA).

Results and discussion

The graphene was grown in a radio-frequency (RF) plasma-enhanced chemical vapor deposition (PECVD) chamber directly on quartz substrate. For the growth, C₂H₂ and H₂ were introduced to the chamber under vacuum with a RF plasma power applied.

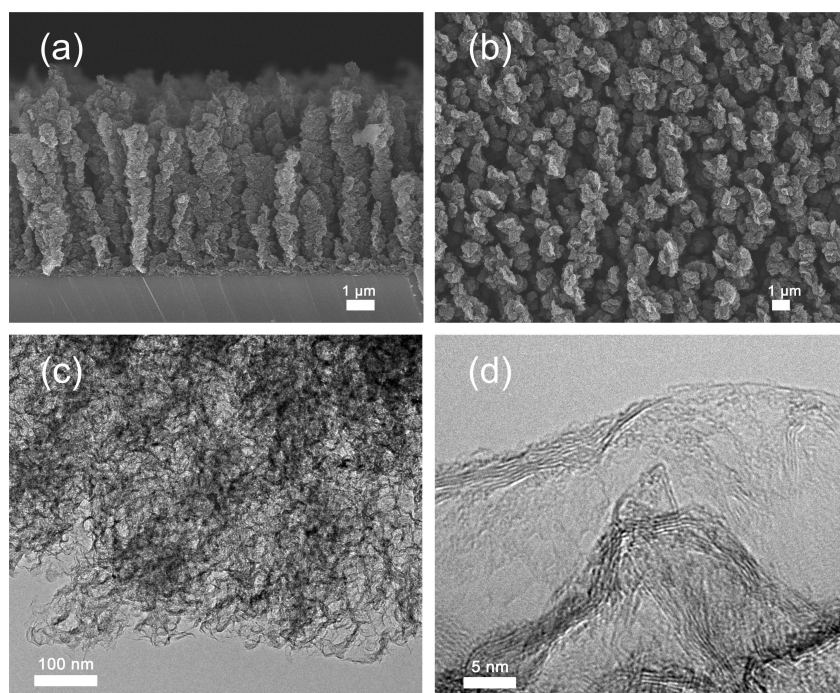


Fig. 1. Morphology of graphene forest (GF): scanning electron microscopy (SEM) images of cross-section (a) and surface (b) on quartz substrate, and detailed structure of GF (c-d) by transmission electron microscopy (TEM) in various magnifications.

The vertical graphene thus grown exhibited a morphology that is different from any vertical graphene reported so far. The SEM images show detailed morphology of the vertically grown graphene in Fig. 1. From Fig. 1a and 1b, the micrometer scale graphene structure resembles a tree and the packed graphene film looks like a thick forest. The graphene tree is 6.4 μm high on the average and the inter-tree spacing is hundreds of nanometers. Each graphene tree is composed of graphene nanosheets and graphene sheets are seen to be randomly stacked, as confirmed by high-resolution transmission electron microscopy (HR-TEM) images in Fig 1c and 1d. Furthermore, each nanosheet consists of multi-layer graphene instead of single layer graphene (Fig. 1d).

The as-produced GF on quartz substrate shows excellent low sheet resistance of $110 \Omega/\square$. In order to further decrease the resistance, nitric acid treatment is applied, which is one of the effective ways of doping graphene film to increase electric conductivity.²⁶ The average sheet resistance of GF was decreased by about 12 % to $96 \Omega/\square$ from $110 \Omega/\square$, after doping with concentrated HNO_3 (Table S1). Because of the morphology of GF, the four point probe method only reveals the sheet resistance of bottom horizontal connection for GF, unlike the measurement for mono-layer graphene and other 2D graphene films. However, the doping process takes place to the whole GF film, and therefore, the resistance of the upper part of the film is expected to be decreased. Meanwhile, the SEM images in Fig S2-S3 show that the doping process does not obviously change the vertical nanostructure of GF.

The relation between bonding structure and doping was investigated with Raman spectra as shown in Fig. 2. The vertical graphene in this study is grown on quartz substrates at relatively low temperature of 850°C without any metal catalyst (Cu, Ni, etc.). As a result, the amount of defects is much higher compared to the graphene grown at higher temperature with metal catalyst.^{2,26,27} The I_D/I_G ratio increased after the HNO_3 treatment,²⁸⁻³⁰ indicating that the doping process induces defects. This result also agrees with XPS results (refer to Fig. S1) and the concentrations of both oxygen and nitrogen increase (Table S1) as a result of the doping.

Fabrication of electric double layer capacitor (EDLC) is rather straightforward when the graphene electrodes are ready as illustrated in Fig. 3. In this work, a generic solid gel electrolyte of $\text{PVA}/\text{H}_3\text{PO}_4$ was used in fabricating electric double layer capacitors. The method reported by Kaempgen et al.³¹ was utilized to prepare the polymer electrolyte. Use of solid

electrolyte was motivated by the safety it provides that is needed for wearable devices. Unlike the liquid state electrolyte EDLC, the potential leakage and strict encapsulation can be avoided with the electrolyte bound within the polymer matrix. In this work, the solid electrolyte functions both as a separator and as electrolyte with a single layer structure.

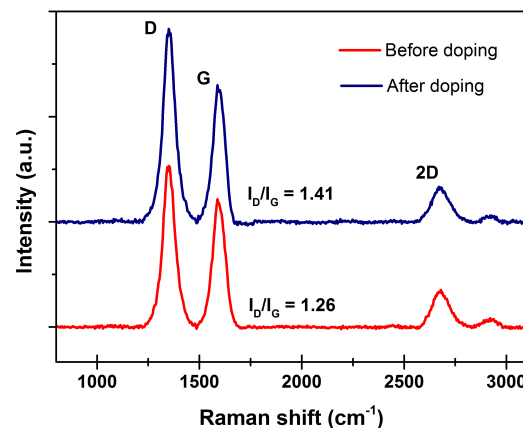


Fig. 2. Raman spectra of GF before (red line) and after (blue line) HNO_3 doping.

After coating the GF with the prepared PVA solution, the coated GF film is allowed to dry to form a PVA/GF film. The PVA/GF film is then carefully peeled away from the quartz substrate. The PVA/GF film is a rubbery flexible film and it also serves as a support film. Owing to the unique rubbery film property, any arbitrary shape of supercapacitor can be realized by simply cutting the film (Fig. S8). The PVA surface is quite sticky and therefore, two pieces of the PVA/GF film can be pasted together to form GF/PVA/PVA/GF structure as shown in Fig. 3d. This structure by itself is an electric double layer capacitor when the contact wires are attached. After GF film is peeled from the quartz, the substrate can be recycled to grow graphene on it again. A current collector can be used to enhance the performance of the capacitor (Fig. 3f). One major role of current collector in a capacitor is to collect/transport charge carriers effectively to/from active material during charge and discharge processes.⁵ Copper foil was used as the current collector in this work.

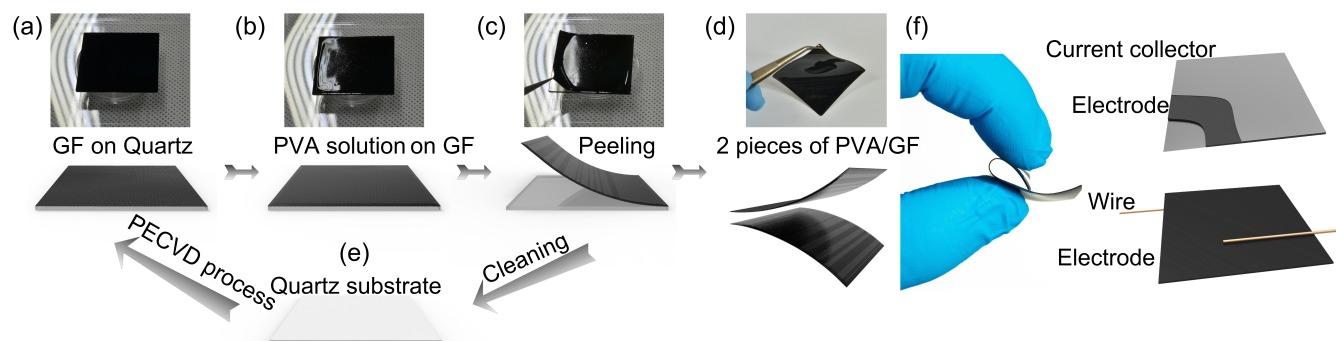


Fig. 3. Schematic illustration of fabrication of flexible GF electrode and that of EDLC. (a) Vertically oriented graphene forest (GF) is grown on quartz substrate. (b) Polyvinyl alcohol (PVA) solution is coated on GF. (c) After PVA film is formed, the PVA/GF is carefully peeled from the substrate. (d) 2 pieces of PVA/GF film are pasted together. (e) The quartz substrate can be repeatedly used for GF growth. (f) EDLCs with and without current collector

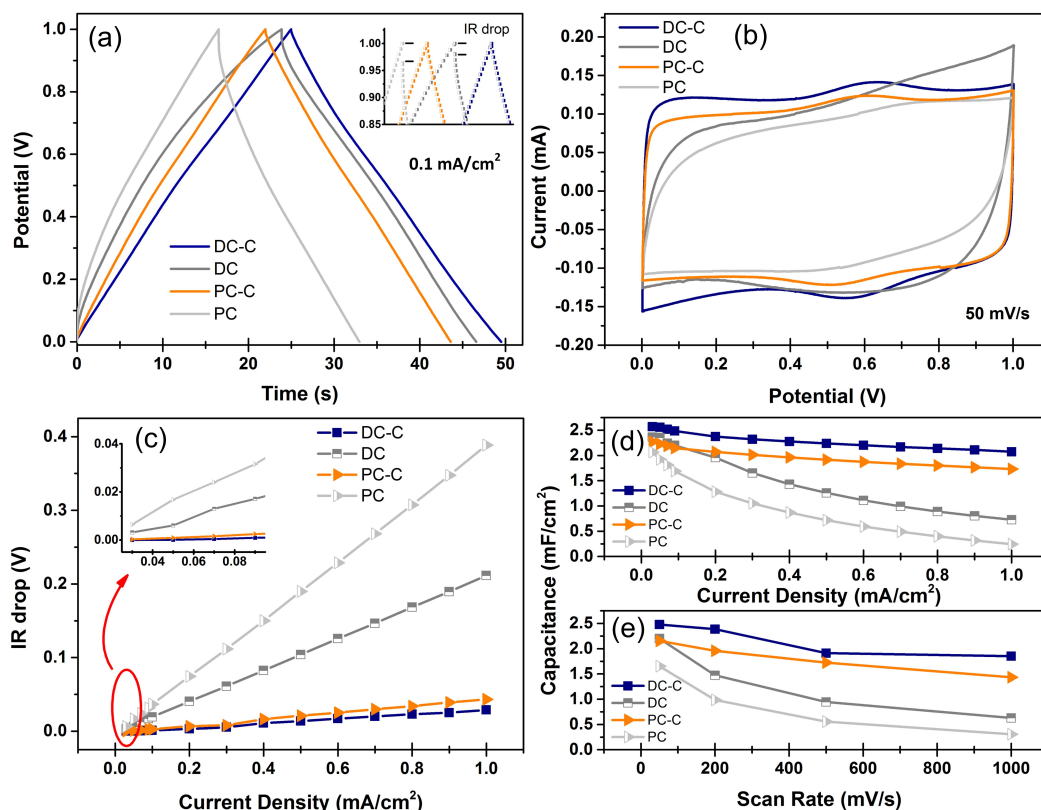


Fig. 4. Electrochemical performance of four types of EDLCs: pristine EDLC (PC), doped EDLC (DC), PC with current collector (PC-C), DC with current collector (DC-C). (a) Galvanostatic charge/discharge plots at current density of 0.1 mA/cm^2 . The inset image of (a) shows magnified tips of charge/discharge plots to reveal IR drop. (b) Cyclic voltammetry at 50 mV/s . (c) IR drop of GF-EDLCs as a function of current density. (d) Specific capacitance of GF-EDLCs as affected by current density, and (e) by scan rate.

To ascertain the positive effects of doping and use of current collector, four different types of capacitor were fabricated: pristine GF-EDLC (PC), doped GF-EDLC (DC), PC with current collector (PC-C), and DC with current collector (DC-C). The results of galvanostatic charge/discharge at 0.1 mA/cm^2 and cyclic voltammetry (CV) at 50 mV/s are shown in Fig. 4a and 4b, respectively. The capacitances obtained from Fig. 4a for the four types of capacitor are: 2.45 mF/cm^2 for DC-C, 2.30 mF/cm^2 for PC-C, 2.21 mF/cm^2 for DC, and 1.67 mF/cm^2 for PC. Similar results were obtained for the capacitance calculated from CV (Fig. 4b).

Two findings emerge from the results obtained. While the capacitance is of the same order of magnitude for all the four types, the doped capacitor with current collector (DC-C) yielded the highest capacitance. The other finding that stands out is the fact that the doped capacitor without current collector yields a capacitance fairly close to that of the doped capacitor with current collector, i.e., 2.21 mF/cm^2 for DC vs. 2.45 mF/cm^2 for DC-C. This finding signifies that the GF-EDLC does not necessarily need a current collector, a distinct advantage over the one with metal collector film in terms of cost and flexibility, at least in the low current density region below 0.1 mA/cm^2 . It is notable that the beneficial effect of doping is greater for the capacitor without the current collector than with the collector.

The dynamic voltage drop or IR drop given in Fig. 4c for various current densities indicates that the drop in general increases with increasing current density. The IR drop for the EDLCs with current collector (PC-C and DC-C) remains very low

even at high current density owing to the low resistance of current collector. However, the IR drop of the EDLCs without current collector (PC and DC) increases quickly when current density increases due to the sheet resistance of GF film that is higher compared to metal. As a consequence, a large amount of energy will be consumed when discharging the EDLCs without current collector at high current density. Nevertheless, for the current density lower than 0.1 mA/cm^2 , the IR drop of PC and DC is low enough (less than 0.04 V , as shown in Fig. 4c and inset image) to be useful for low current devices.

Dependence of the capacitance of the EDLCs on current density and scan rate is shown in Fig. 4d and 4e (Fig. S5 and S6 for more detail information), respectively. It is notable that the capacitance of the EDLCs with current collector (PC-C and DC-C) shows a relatively slow decrease with an increase in current density as well as scan rate. However, the EDLCs without the current collector show a sharp decrease in the capacitance because of the amplified resistance of GF film to capacitance at high current density and scan rate. It is notable that the IR drop is low at low current density (less than 0.1 mA/cm^2). So the effect of current collector is insignificant. Therefore, the doped GF based EDLC has a higher capacitance than that of pristine GF based EDLC. As the current density increases, the capacitance of DC decreases faster than that of PC-C due to the higher IR drop of DC (Fig. 4c), resulting in crossover of capacitances of DC and PC-C.

To investigate the kinetic features of ion diffusion, electrochemical impedance spectroscopy (EIS) was carried out. Nyquist plots obtained for the four types of EDLCs in the

frequency range between 300 kHz and 1 MHz are shown in Fig. 5a with a magnified view of the high-frequency range in Fig. 5b for the capacitors with current collector. The plots show nearly vertical lines for the capacitor with current collector, revealing excellent ion diffusion behavior, whereas the lines for those without the collector are not vertical. The perfect semicircles for the EDLCs with current collector in the high frequency region of the Nyquist plot (Fig. 5b) imply porous characteristics of GF.³² The diameter of semicircle for DC-C is shorter than for PC-C, indicating a lower charge transfer resistance²² due to doping by HNO₃. The GF-EDLCs with current collector show lower equivalent series resistance (19 Ω for DC-C and 23 Ω for PC-C) in comparison with those without current collector (90 Ω for DC and 130 Ω for PC).

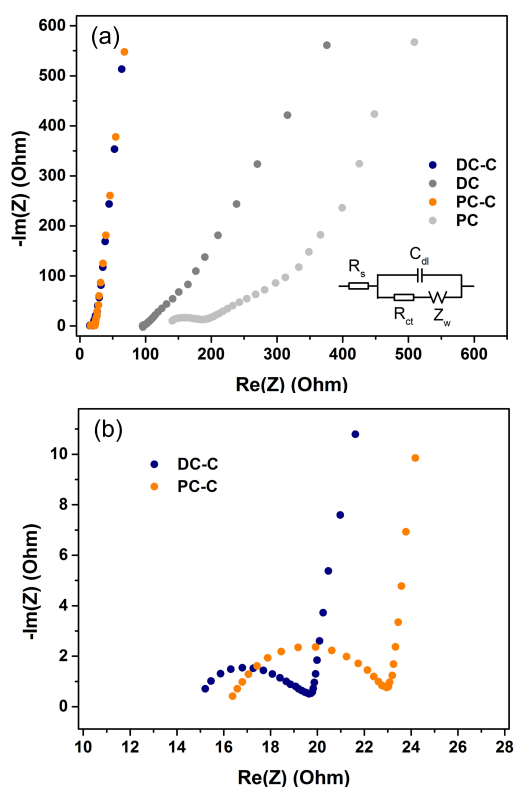


Fig. 5. (a) Nyquist plots of four types of GF-EDLCs and (b) magnified high frequency region plots for GF-EDLCs with current collector. The inset in (a) shows equivalent circuit

Most of the data on the specific capacitance reported for vertical graphene capacitors are at 120 Hz, attainable from EIS, because of interests in high frequency applications. The highest specific capacitance in this work at the frequency was obtained from DC-C, yielding a capacitance of 1.36 mF/cm² with 6.4 μ m high graphene (Fig. S7). In comparison, Sheng et al.²⁴ reported that electrochemically reduced graphite oxide electrodes gave a specific capacitance of 0.283 mF/cm² with 20 μ m thick film. Ren et al.²³ fabricated a capacitor with 2 μ m thick vertical graphene on a 200 μ m Ni foam, producing a specific capacitance of 0.360 mF/cm² at 120 Hz. Cai et al.²⁵ reported a capacitance of 0.265 mF/cm² at 120 Hz for the capacitor fabricated with 2 μ m thick vertical graphene nanosheets. Liquid electrolytes were used for all these capacitors. While the capacitance depends on the frequency, and applications could be different, the comparison does

indicate that the capacity of the flexible capacitor presented here, even with solid electrolyte, is almost an order of magnitude greater compared to the others.

Stability of the device when subjected to repeated charge/discharge cycles is a measure of interest for any capacitor. This stability was tested at the current density of 0.1 mA/cm² (Fig. S4) for the four types of the EDLC. The figure shows that there is no significant change during the 10,000 cycles for the EDLCs with current collector. The EDLCs without current collector also show a good capacitance retention but with a little undulation. The undulation could be ascribed to the change of ambient temperature that affects the internal resistance of GF during the measurement, which can be neglected for the ones with current collector.

One aspect of bendability is the extent to which the device can be bent. Another has to do with bending durability. The latter is a measure of device lifetime since repeated bending usually leads to loss of capacitance. As a power supply of wearable devices, this aspect becomes a critical factor because these devices are subject to countless number of bending during their lifetime. For the bendability tests, HNO₃ doped capacitors (DC and DC-C) were used. Fig. 6a shows the retention of the capacitance as a function of bending radius, with the value for the flat capacitor as the reference. The result for DC-C, which is the doped capacitor with current collector, shows that the capacitance improved slightly for bending radii of 12.95 mm, 11.13 mm and 7.59 mm.

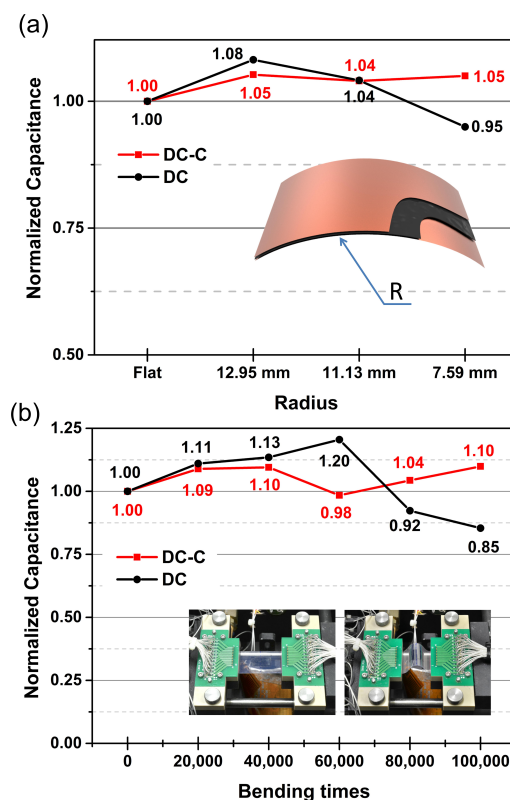


Fig. 6. Flexibility tests of doped GF-EDLC with current collector (DC-C): (a) Capacitance as affected by bending radius. (b) Retention rate of capacitance as affected by times of bending at bending radius of 10 mm.

Durability of the device when subjected to repeated bending/flattening was tested with a home-made bending machine, as shown in Fig. 6b. After the capacitor is bent to a radius of 10 mm repeatedly for a specified number of times, the capacitor was flattened and the capacitance was measured. The results in the figure reveal that the capacitor without the current collector does not lose its capacitance even after 80,000 times of bending but then the capacitance deteriorates rather rapidly thereafter on. All the bending experiments reported in the literature involved bending times less than 10,000. Huang et al.³³ and Kou et al.,³⁴ for instance, reported bending test results only after 5,000 and 1,000 times of bending, respectively.

The finding here indicates that the bending test should perhaps be extended to 100,000 times rather than less than 10,000 times for wearable applications, considering countless number of times of bending such a device has to endure over its lifetime when worn on a person. A unique feature of the capacitor presented here is the device with copper foil current collector shown in Fig. 6b. No deterioration of the capacitance occurred even after 100,000 times of bending (slight decrease of capacitance for DC-C occurred at bending times of 60,000 because of the room temperature change during the long time bending test³⁵). This superb bending durability could be ascribed to the nature of the graphene forest in which each graphene tree stands on its own, rather than connected as in the usual vertical graphene structure. Therefore, the bending occurs in between trees rather than against the connected layer of graphene, which would leave the graphene trees intact.

Folding test was performed to investigate the bending limitation as shown in Fig. 7a. The DC-C does not show any loss of capacitance even with the 180 degree folding. In order to simulate dynamic operation situation, bending and flattening motion was repeated with the DC-C attached on the finger joint of a glove. The bending frequency is about 1 time per second. Fig. 7b shows that the dynamic bending and flattening does not decrease the capacitance of DC-C. The folding and bending test show excellent flexibility of the GF based EDLC.

Conclusions

The flexible capacitor presented here should be suitable for wearable devices in terms of safety and bending durability. The unique morphological feature of the vertical graphene in the form of graphene trees, which is introduced here for the first time, has made it possible to produce a capacitor that can withstand 100,000 times of bending or 180 degree folding without any loss of capacity and to provide an areal capacitance that is an order of magnitude larger than any capacitor made with vertical graphene. With the exception of graphene forest, only generic materials are used for the fabrication of the capacitor, and as such the fabrication is cost-effective. The capacitor with graphene forest as the electrode can be used as such without metal current collector, almost without loss of capacitance and IR drop, for low current density applications.

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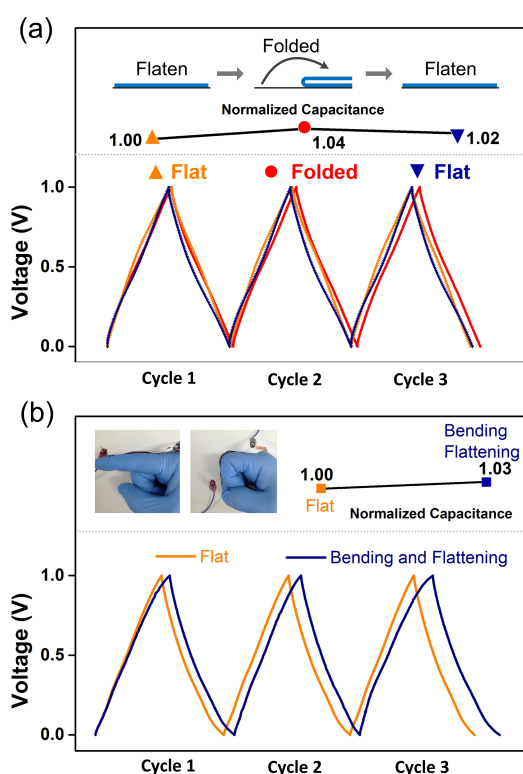


Fig. 7. Plots of normalized capacitance and GCD curves of 180 degree folded DC-C (a) and dynamically bended DC-C (b).

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A flexible supercapacitor was demonstrated with 'graphene forest' as electrodes. No capacitance loss was observed even with 100,000 time bending.

