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Synthesis of β-SiC nanowires via a facile CVD method and their photoluminescence properties

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β-SiC nanowires with large scale have been synthesized successfully via a chemical vapour deposition (CVD) method at 1300 °C without using catalysts, templates or extra gases. X-ray diffraction (XRD), field emission scanning electron microscopy (FESEM), Fourier transform infrared spectroscopy (FT-IR) and energy-dispersive X-ray spectroscopy (EDS) were taken to characterize the phase and the morphology of the as-prepared products. Characterization of the as-synthesized nanowires indicated that they were made up of a crystalline SiC core coating with an amorphous SiO₂ shell. A vapor-solid (VS) growth mechanism was proposed for the growth mode of the as-grown SiC/SiO₂ nanowires. The photoluminescence (PL) spectrum of as-obtained nanowires exhibited a significant blue shift compared with the bulk SiC. When the reaction temperature was changed (1200 °C, 1400 °C, 1500 °C), low production or irregular morphology of the nanowires was obtained. The diameter of nanowires synthesized with catalyst showed narrower between 20-40 nm while without catalyst about 50 nm. The as-synthesized products have the potential in future blue-green emitting device applications. This research will also be helpful in simplifying the synthesis of SiC-related nanostructure.

Introduction

One-dimensional (1D) nanostructured materials, such as nanowires, nanotubes, nanobelts, nanochains and nanocables, have attracted lots of attention for their excellent properties. [1-5] Among these nanostructures, nanowires become the main research objects due to their potential applications in electronic, optical and optoelectronic devices. [6-8] The past decades have witnessed many efforts devoted to the growth of nanowires, for example, Ag nanowires [9], Ge nanowires [10], Silicon carbide (SiC) nanowires [11], Si nanowires [12] and so on.

SiC is a significant wide band gap (2.4 eV at room temperature) semiconductor [13]. Two hundred crystalline forms have been found of SiC. Only one type of the forms belongs to cubic system, named 3C-SiC or β-SiC. The others are all hexagon, named α-SiC. β-SiC nanowires show imaging electronic, physical, and chemical properties causing them suitable for high temperature, high power, high frequency and high thermal conductivity environments. [14-18] So far, lots of methods have been used to synthesize β-SiC nanowires. Fu et al. [10] synthesized β-SiC nanowires using CH₃SiCl₃ as raw materials, H₂ and Ar gases as protective gas and via a chemical vapour deposition (CVD) method at 1050-1150 °C without using metallic catalyst. Liang et al. [19] prepared mesoporous silica containing Fe nanoparticles by the sol-gel process previously and heated the sol-gel solution at 1400 °C to get the β-SiC nanowires. Dai et al. [20] synthesized β-SiC nanowires using Si-SiO₂ powders as raw materials via a simple CVD method, and during the experiment, Ar gas was used to remove residual oxygen and the reaction temperature was 1460 °C. Nevertheless, most of these methods involve complicated equipment and processes with high temperature and spend more in raw materials or removing the metal catalyst when put the nanowires in application.

In this paper, large scale β-SiC nanowires are synthesized using Si-SiO₂ powders as raw materials through a relatively facile CVD method at 1300 °C without using any gases (Ar, N₂, CH₄ etc.) or metallic catalysts. It may reduce costs in experiment or application.

Experiment

In our previous researches, a simplified CVD method was reported to synthesize β-SiC nanostructure. [21-22] However, the output of the as-grown nanowires was little and the reaction temperature was high (1500 °C). In order to synthesize a large scale of β-SiC nanowires at lower cost, the reaction temperature was changed at 1300 °C. At the same time, the influence of temperature and catalyst to the products was also investigated. In this research, Si (200 mesh, Sinopharm Chemical Reagent Co., Ltd.) and SiO₂ (200 mesh, Sinopharm Chemical Reagent Co., Ltd.) powders were used as raw
Scheme 1 Schematic experimental setup for β-SiC nanowires synthesis.

According to the chemical reaction equation (Si(s) + SiO₂(s) → 2SiO(g)), 7 g Si and 15 g SiO₂ powders were put into nylon ball milling tank with corundum ball (material: corundum ball milling = 1:2). The Si-SiO₂ mixture powders were gotten after 2 hours milling. 500 mg such mixture powders were put into a small cleaned graphite crucible and covered with the cleaned graphite felt (The surface of the graphite felt was ultrasonically cleaned with ethanol for 10 min and then dried in air.). Afterwards, the cleaned system was embedded in a larger enclosed corundum crucible filling with graphite powders as shown in Scheme 1. At the same time, a bit of holes were made by a needle in the clean graphite felt to keep the atmospheric pressure balance between the big enclosed corundum and the small graphite crucibles. After that, the above-mentioned big enclosed corundum crucible was heated in air atmosphere in a resistance furnace. The furnace temperature was initially raised to 1000 °C with a rate of 10 °C/min, and then raising the temperature to 1300 °C at a heating rate of 3 °C/min and holding for 3 hours. When the temperature was naturally cooled down to the room temperature, a blue colored layer was gotten on the surface of the graphite felt.

X-ray diffraction (XRD) with Cu Kα radiation (λ = 1.5406 Å) was conducted to identify the phase composition of the as-prepared products. Field emission scanning electron microscope (FESEM, LEO-1530, Germany) equipped with energy-dispersive X-ray spectroscopy (EDS) and transmission electron microscope (TEM/HRTEM, FEI-TECNAI G², America) were used to characterize their morphology, chemical composition and microstructure. Fourier transform infrared (FT-IR) data was gained with a Nicolet IR100/200 spectrophotometer over the wavelength between 400 and 1300 cm⁻¹. The photoluminescence (PL) property was performed at room temperature by using a fluorescence spectrophotometer (Hitachi F-4600, Japan) with Xe lamp excitation.

Results and discussion

Fig. 1a shows the digital camera picture of the as-prepared products. As shown in Fig. 1a, a light-blue layer of products is clearly seen on the graphite felt. The as-grown products show a large scale compared with previous experimental results. [24-25]

Fig. 1b-c show the FESEM images of as-grown products. It can clearly be seen, the products are shown as one dimensional nanowire, which have long straight length (from twenty to hundreds of micrometres) and narrow diameter about 50 nm. The nanowires are irregular in direction, showing highly curved and good flexibility. Furthermore, as shown in Fig. 1c, the high magnification FESEM image illustrates these nanowires with high aspect ratio have a smooth surface. As can be observed, no metallic droplets are found at the top of the as-grown nanowires. Fig. 1d shows the EDS spectrum of the as-grown nanowires. The EDS analysis reveals that the nanowires are composed of Si, C and O. The atomic percentages of Si, C and O are about 26.82%, 28.05% and 45.13%.

XRD pattern, as shown in Fig. 1e, identifies the products are β-SiC compared with the JCPDS Card No. 29-1129. In addition, the strong diffraction peaks at 26.6° and 54.79° are due to the graphite felt substrate.

At the same time, FT-IR is also used to identify the phase composition of the products. Fig. 1f shows the FT-IR absorption spectrum of as-synthesized products. The absorption peak at 780 cm⁻¹ is related to the transversal optic (TO) Si-C stretching vibration and the peak at 946 cm⁻¹ corresponds to the longitudinal optic (LO) vibration mode [1, 23].

Fig. 1 (a) Digital camera picture of blue wool-like products grown on the surface of graphite felt, (b) Low magnification and (c) High magnification FESEM images of the as-grown product, (d) EDS pattern of the nanowires, (e) XRD pattern of as-synthesized products, (f) FT-IR absorption spectrum of as-synthesized products.
Fig. 2 (a) and (b) typical low and high magnification TEM images of as-grown SiC nanowires, (c) and (d) HRTEM images of the nanowires, the inset in (d) is the corresponding electron diffraction pattern of the SiC core.

Fig. 3 (a) Low magnification FESEM image of the nanowires synthesized at 1400 °C reaction temperature, and (b) at 1500 °C.

In addition, the three absorption peaks arrange at around 450-500 cm⁻¹ and 1000-1200 cm⁻¹ correspond to Si-O-Si stretching vibration of amorphous SiO₂.[23,26,27]

The detailed microstructure of the as-obtained nanowires is characterized by TEM/HRTEM. Fig. 2a-b and Fig. S1 show the typical TEM images of the nanowires. It is clearly seen the nanowires consist of a crystal-core with the diameter of 20 nm and an amorphous-sheath with there exists stacking faults in the core. Combining with the data of XRD and SEM, it can be inferred that the core of the nanowires is crystalline SiC and the sheath is an amorphous SiO₂ layer. Fig. 2c and d show the HRTEM images of the as-obtained nanowires at different magnification. The lattice fringes shown in Fig. 2d is 0.25 nm corresponded to the (111) plane spacing of β-SiC. The axis of the (111) crystal plane is consistent with the longitudinal direction of SiC nanowires, implying that the nanowires grow along the [111] direction. Combining with the XRD, FT-IR, TEM and EDS analyses, we roughly consider the nanostructure as-prepared is SiC nanowires coating with amorphous SiO₂.

Furthermore, in order to characterize the influence of temperature in the process of preparing SiC nanowires, the same reaction setup is also heated at 1200 °C, 1400 °C and 1500 °C, respectively.

The digital camera pictures of the as-synthesized nanowires are shown in Fig. S2a-c. No product is appeared in the graphite felt when heating at 1200 °C (as shown in Fig. S2a). As shown in Fig. S2b-c, when heating at 1400 °C or 1500 °C, few scattered products can be observed. Fig. 3 display the FESEM images of the as-grown products at 1400 °C and 1500 °C. It can be observed that the morphology of the products prepared at 1400 °C and 1500 °C are not as uniform as the products synthesizing at 1300 °C. The nanowires collected at 1500 °C have a larger diameter and lower aspect ratio compared with the products synthesized at 1300 °C. Therefore, we roughly consider that 1300 °C is an appropriate temperature in preparing of SiC nanowires via this facile CVD method.

The traditional nanowires growth mechanism is vapor-liquid-solid (VLS) mechanism. In this mechanism, metallic catalyst will be used to assist the reaction. When the experiment finished, metallic catalyst droplets will be found in the top (VLS-tip growth mechanism) or base (VLS-base growth mechanism) of the nanowires. In our research, no metallic catalyst is introduced into the raw material. According to the TEM images, no catalyst droplet is found. At the same time, the existence of metal elements has not been found in the energy spectrum diagram. Thus completely rule out the possibility of VLS mechanism. In view of our experimental results, a vapour-solid (VS) growth mechanism (shown in Scheme 2) is inferred to explain the main growth mechanism for as-synthesized SiC/SiO₂ nanowires.

In our setup, graphite is used to supply carbon source and prevented the oxygen in the air from entering the corundum crucible, thus, forming an enclosed system. So, the source of the oxygen in this process is contributed to the SiO₂ powders and O₂ inside the furnace (detailed discussions). The O₂ will react with the graphite powders to yield CO vapour straight by reaction (1).

\[ 2C(s) + O_2(g) \rightarrow 2CO(g) \]  

(1)

When the temperature rises gradually, SiO vapour is generated by the reaction of the SiO₂ with Si powders according to the reaction (2).

\[ Si(s) + SiO_2(s) \rightarrow 2SiO(g) \]  

(2)

With the temperature increasing, the graphite powders are vaporized to C vapour.[28] Then, according to the research of Chen et al.[29] through the reaction (3) and (4), SiO also can

\[ SiO(g) + C(g) \rightarrow SiC(s) + CO(g) \]  

Scheme 2 Schematic illustration of the possible growth mode for SiC/SiO₂ nanowires.
be gotten.

\[
\text{SiO}_2 (s) + C (g) \rightarrow \text{SiO} (g) + \text{CO} (g) \tag{3}
\]

\[
\text{SiO}_2 (s) + \text{CO} (g) \rightarrow \text{SiO} (g) + \text{CO}_2 (g) \tag{4}
\]

Afterward, the SiO vapour obtained in the above-mentioned reactions further reacts with the graphite powders or CO vapour, according to the reaction (5) and (6) mentioned in [29].

\[
\text{SiO} (g) + 2\text{C} (g) \rightarrow \text{SiC} (s) + \text{CO} (g) \tag{5}
\]

\[
\text{SiO} (g) + 3\text{CO} (g) \rightarrow \text{SiC} (s) + 2\text{CO}_2 (g) \tag{6}
\]

As interpreted in reaction (4) and (6), CO₂ vapour can be generated in this enclosed system. The as-formed CO₂ could re-react with the graphite powder or C vapour, and then regenerate CO gas. It can be inferred that the presence of CO₂ is beneficial to the transfer of carbon source. According to the above reactions, SiC nuclei are obtained on the surface of the graphite substrate.

Fig. 4a shows a typical FESEM image, which displays the root of the nanowires (the place where the nuclei generated). Figure 4b-d show the EDS spectra recorded at A, B and C marked in Fig. 4a, respectively. As shown in Fig. 4, the three locations all possess Si, C and O elements. Together with the above discussions, we believe that SiC nuclei are generated on the graphite, and then SiC nanowires are grown along [111] direction.

TEM observations indicated that the as-synthesized nanowires possess a SiC-SiO₂ core-shell structure. On account of the higher melting point of SiC than that of SiO₂, the solidification of the SiC core happens faster than that of the viscous SiO₂ layer. Hence, the amorphous SiO₂ layer will be formed and wraps the crystalline SiC nanowires during the cooling period. As previously reported [23], the generation of SiO₂ according to the reaction (7) and (8).

\[
2\text{SiO} (g) + \text{C} (g) \rightarrow \text{SiC} (s) + \text{SiO}_2 (s) \tag{7}
\]

\[
3\text{SiO} (g) + \text{CO} (g) \rightarrow \text{SiC} (s) + 2\text{SiO}_2 (s) \tag{8}
\]

The PL spectrum (shown in Fig. 5) of as-obtained SiC/SiO₂ nanowires is measured at room temperature under excitation at 254 nm from Xe source. The SiC/SiO₂ nanowires show a broad emission peak in the blue region of the spectrum. Compared with the band gap of bulk β-SiC 2.4 eV (at room temperature) [13], the emission peaks are apparently blue-shifted in this research. Similar emission peak at about 416 nm was reported by Zhang et al. [26] for the SiC/SiO₂ nanowires, which may be attributed to the existence of oxygen defects in the amorphous layer, the special rough core-shell interface, and the morphology defects including stacking faults within the SiC nanostructures. SiC/SiO₂ nanochain synthesized by Meng et al. [17] showed a broad emission peak at 408 nm. They proposed that the wide PL peak may be owing to altering diameter of the nanostructures, stress at the SiC/SiO₂ interface boundary, the size confinement effect and high density of defects including stacking faults within the SiC nanostructures. Therefore, it is presented that the blue-shift in our research might result from the stacking faults and the influence of the amorphous SiO₂ layer.

In previous studies, low melting point metals were often used as the catalyst in preparing of various 1D structure, such as Si nanowires [33], Si₃N₄ nanobelts [34], sialon nanobelts [35], etc.

Therefore, the influence of catalyst in our self-designed
The as-obtained nanowires were composed of a crystalline SiC core and an amorphous SiO$_2$ shell. The average diameter of the nanowires was about 50 nm and their length was about hundreds of microns. The SiC-core nanowires grew along [111] direction and stacking faults were existed along their growth direction. The as-synthesized nanowires without using catalysts were grown via V5 mechanism. Due to the stacking faults and the presence of the amorphous SiO$_2$ layer, the SiC/SiO$_2$ nanowires showed a blue-shift PL spectrum. The SiC/SiO$_2$ core-shell nanowires as reported in this research have potential application in future blue-green emitting devices.

**Conclusions**

In summary, SiC/SiO$_2$ nanowires were synthesized by a facile chemical vapour deposition method at 1300 °C using Si and SiO$_2$ mixture powders as raw materials without any catalysts. The as-obtained nanowires were composed of a crystalline SiC core and an amorphous SiO$_2$ shell. The average diameter of the nanowires was about 50 nm and their length was about hundreds of microns. The SiC-core nanowires grew along [111] direction.

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**Notes and references**
