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1	Fabrication of resistive switching gallium oxide thin film with tailoring gallium
2	valence state and oxygen deficiency by rf cosputtering process
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1 Abstract

Resistive switching gallium oxide base thin films with tailoring oxygen deficiency were $\mathbf{2}$ 3 fabricated by rf cosputtering of Ga₂O₃ and Cr. XPS and STEM-EDX analysis disclosed that the resultant film was made of homogeneous oxide glass layer with mixed valance 4 states of Ga(III)-Ga(I). The amount of Ga(I) and the corresponding oxygen deficiency $\mathbf{5}$ were precisely controlled because the following redox reaction subsequently progresses 6 in the deposited films: $3Ga(III) + 2Cr(0) \rightarrow 3Ga(I) + 2Cr(III)$. The on/off resistance 7 8 ratio was largely varied by changing Ga(I) fraction in relation to the oxide ion 9 conductivity, and $Ga_{0.82}Cr_{0.18}O_{1.2}$ thin film was found to exhibit optimal switching 10performance. The film resistance state was tunable by a 100's µs pulse biasing and was incrementally changed by increasing applied pulse numbers. The strongly 11 12time-dependent switching events and area dependent current level of $Cr-GaO_x$ films was distinct from the abrupt switching behavior of filamentary mechanism TiO_x thin film 1314devices. It was demonstrated that rf cosputtering of the metal oxides and the corresponding oxygen scavenging metals was a powerful technique to design the bulk 15state resistive switching devices based on nonstoichiometric metal oxide thin film. 16

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18 Key words: resistive switching, gallium oxide, gallium suboxide, rf sputtering

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1 Introduction

As the potential next generation for nonvolatile memory, resistive random access $\mathbf{2}$ memory (RRAM) with a simple metal/oxide/metal sandwich structure has been studied 3 intensively during the past decades.¹ In case of the well-established Pt/TiO₂/Pt 4 memristor devices, the switching between high resistance state (HRS) and low $\mathbf{5}$ resistance state (LRS) is triggered by the modification of the tunnel barrier width 6 between a top electrode (TE) and the preformed conductive filament due to the ionic $\overline{7}$ drift within a few nanometre-thick space charge layer in vicinity of the interface with 8 electrode.2-4 9

10Gallium oxide devices are considered as one of ideal candidates for RRAM because of its intrinsic high resistance characteristic and extraordinarily sensitive conductivity to 11 the oxygen concentration.⁵⁻⁹ In a previous study, we found resistive switching in 12highly-nonstoichiometric, amorphous gallium oxide thin films, a-GaO₁₁ was originated 13from the bulk mixed oxide ion-electron conductivity.⁸ The homogeneous migration of 14oxygen vacancy donors modify the electronic carrier distribution across films and thus 15resistance states of film bulk varies by applied fields.⁸ Such a bulk-conduction 16mechanism memristor could realize multilevel states of resistance and a large on/off 17current ratio due to the continuous tunability of the internal state variable, and therefore 18is expected to be a dynamical memristor system to emulate synaptic memory 19behaviour.^{10,11} To develop feasible homogeneous resistive switching devices for future 20

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neuromorphic application, it is benefical to design amorphous gallium oxides with 1 enhanced ion conductivity. The oxide ion conductivity of a-GaO_x must be related with $\mathbf{2}$ interstitial spaces and local Ga environments of the mixed valence state Ga(III)-Ga(I) 3 oxide glasses. Therefore, it is of fundamental and of technological importance to 4 develop the precise fabrication process with tailoring the oxygen deficiency, namely, $\mathbf{5}$ Ga(I) fractions in the oxide matrices in order to tune the resistive switching properties 6 of film devices. $\overline{7}$ Highly-nonstoichiometric amorphous GaO_x thin films have been fabricated by pulse 8 laser deposition (PLD) with a Ga₂O₃ target under low p_{O2} conditions.^{5,8,9} Unfortunately, 9 10it is difficult to control the yields of Ga(I) suboxides by the method because Ga_2O_3 is not easily reduced under extremely low O_2 pressure at elevated temperature.¹² Lee et al

11 12reported the fabrication of oxygen deficient gallium oxide films by high temperature vacuum annealing of Ga₂O₃/Cr/Ga₂O₃ three laver laminates.⁶ Gallium oxide is 13efficiently reduced through the interdiffusion reaction because of relatively large 14negative energy of Cr-O bonding (-1053 kJ mol⁻¹) in comparison to Ga-O bonding 15(-998.3 kJ mol⁻¹).¹³ Such a redox reaction must be useful to prepare the Ga(I)-Ga(III) 16mixed valence gallium oxide. Herein, we successfully fabricated Cr-doped gallium 17oxide films with a controlled amount of Ga(I) and oxygen deficiency by depositing a 18homogeneous mixture of Ga(III) oxide moieties and Cr(0) metal atoms. The chromium 19atoms scavenge the oxygen atoms of Ga(III) oxide moieties and thus Ga(I)-Ga(III) 20

mixed valence state homogeneous oxide glass was prepared. The films with optimal amount of Ga(I) fractions revealed remarkable bulk resistive switching with large on/off ratio according to the efficient oxide ion conductivity, which enables incremental change of the bulk resistance state by increasing bias duration or applied short pulse numbers. The switching behavior was compared to the well-established TiO_x thin film base filamentary mechanism switching devices.

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8 **Experimental**

9 A 20 nm-thick Pt deposited Si wafer (100) was used as a bottom electrode (BE). Nonstoichiometric TiO_x and Cr-doped GaO_x thin films were deposited on a Pt BE by rf 10sputtering at room temperature. TiO_x thin films were deposited by using a Ti target 11 12(purity: 99.9%) in a mixed argon and oxygen atmosphere (the ratio of Ar/O₂ was 5:1) at a total pressure of 0.4 Pa at substrate temperature of 150°C. Cr-doped GaO_x films 1314 $(Cr-GaO_x)$ were deposited by cosputtering with Ga_2O_3 and Cr targets in pure argon atmosphere at a pressure of 1 Pa. The sputtering power of Ga_2O_3 was fixed at 50 W and 15that of Cr was adjusted at 30, 40, 60 and 70 W in order to change Ga/Cr molar ration in 16the resultant film. The substrate was kept at ambient temperature. Circular metal top 17electrodes (TE) with a diameter of 100-800 µm and a thickness of 50 nm were 18sputter-deposited on the films through a metal shadow mask. The *I-V* characteristics of 19the devices were measured at room temperature using a source-measure units 20

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(KEITHLEY 2601B). In all cases, Pt TE is firstly swept from 0 V to anodic region (+ 1 1 V), swept back to cathodic region (-1 V) and returned to 0 V so as to complete one $\mathbf{2}$ 3 cycle of voltage sweep. The voltage steps are fixed at 0.1 V. The chemical composition of Cr-GaO_x films was examined by wave-length dispersive 4 X-ray analysis (WDX) with a JEOL JXA-8530F. For WDX measurements, 800 $\mathbf{5}$ nm-thick films were deposited on the Pt/Si wafer by sputtering under the same 6 atmospheric, rf power and temperature conditions as those for the corresponding 7 switching device films. The cross-sectional transmission electron microscopy (TEM) 8 9 and energy dispersive X-ray fluorescent analysis (EDX) were carried out in a HITACHI 10HD-2000. The specimens for TEM observation were prepared by a focused ion beam microfabrication (FIB; HITACHI FB-2100). X-ray photoelectron spectroscopy (XPS) 11 12was carried out in JEOL JPS-9010MC with Mg K α radiation. The depth profile was applied by *in situ* Ar^+ ion sputtering. The curve fitting were carried out with 1314Lorenz-Gaussian function. 15Results 16Firstly, GaO_r films are fabricated by sputtering a single Ga_2O_3 target. The resulting 17films are transparent, indicating amount of oxygen deficiency in the film is quite small. 18

20 of Cr and Ga₂O₃. The chemical composition of the films deposited with a Cr sputtering

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On the other hand, the uniform, dark-colored thin film can be obtained by cosputtering

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1	power of 30, 40, 60 and 70 W is $Ga_{0.96\pm0.02}Cr_{0.04\pm0.02}O_{1.4\pm0.1}$, $Ga_{0.91\pm0.04}Cr_{0.09\pm0.05}O_{1.3\pm0.1}$,
2	$Ga_{0.82\pm0.2}Cr_{0.18\pm0.02}O_{1.2\pm0.1}$ and $Ga_{0.78\pm0.06}Cr_{0.22\pm0.03}O_{1.2\pm0.1}$ respectively, as determined by
3	WDX measurements (Table 1). These films are hereafter called as Ga96Cr4, Ga91Cr9,
4	Ga82Cr18 and Ga78Cr22, respectively, based on the average metal composition. Figure
5	1(a) shows cross-sectional TEM of Ga82Cr18 thin films. The densely-packed films (120
6	nm thickness) are uniformly formed over a wide area of a Pt/Si wafer substrate.
7	Apparent pinholes and clacks are not observed. STEM-EDX shows chromium is
8	homogeneously distributed throughout the film bulk (Figs. 1(c) and (d)) without
9	segregation, indicating homogeneous mixtures of chromium and gallium oxide are
10	prepared by the cosputtering. The Ga82Cr18 thin films show only a halo ring, indicating
11	an amorphous phase (Fig. 1(b)).
12	Figure 2 shows Ga 3d and Cr 2p XPS spectra of 120 nm-thick $Cr-GaO_x$ thin films.
13	In all cases, Ga 3d peak is deconvoluted into two peaks; a peak at 20.7 eV due to the
14	Ga(III) state and a peak at 19.6 eV due to Ga(I) state (Fig. 2a). ^{8,14} Ga(III)/Ga(I) molar
15	ratios in Ga96Cr4, Ga91Cr9, Ga82Cr18 and Ga78Cr22 are 90/10, 77/23, 69/31 and
16	64/36, respectively (Table 1). These values of the molar ratios are consistent with ones
17	calculated from chemical composition by accounting charge compensation between
18	cations (Ga ³⁺ , Ga ⁺ and Cr ³⁺) and anion (O ²⁻). On the other hand, Cr 2p XPS spectral

20 spectral feature of Cr(III) with Cr $2p_{1/2}$ and $2p_{3/2}$ peaks at 587.5 eV and 577 eV,

features are not changed by chemical composition (Fig. 2(b)). All films reveal apparent

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1	respectively ¹⁵ while the features of lower valence state Cr, such as Cr(0) (574.2 eV of
2	$(2p_{3/2})^{16}$ and Cr(I) (576.3 eV of $(2P_{3/2})^{16}$ does not appear (Fig. 2(c)).
3	Ga 3d XPS spectra of Ga82Cr18 were also measured in the depth of about 50 nm
4	(middle) and 100 nm (near bottom edge) by Ar ⁺ ion milling (Fig. 2(c)). The spectra
5	show similar features at any depths (Fig. 2(b)), indicating Ga(I) states are
6	homogeneously distributed through film.
7	Guo et al reported the switching properties of a -GaO _{1.2} thin film device are strongly
8	dependent on the electrode materials. ⁵ Pt is used as a top electrode material in this study
9	in order to avoid oxidation of electrode by contacting with the oxide films.
10	Resistive switching behavior of $Cr-GaO_x$ films is strongly dependent on the
11	composition (Fig. 3). The Ga96Cr4 thin films give two orders of magnitude smaller
12	current than those of other composition and the hysteretic $I-V$ curves do not appear at
13	any sweep rates (Fig. 3(a)). The others, Ga91Cr9, Ga82Cr18 and Ga78Cr22, involves
14	resistive switching at a sweep rate of 1 and 0.01 V s ⁻¹ (Figs. 3(b), (c) and (d)), revealing
15	a pinched hysteresis I-V loop in counter eight-figure clockwised polarity when the
16	voltage on TE was swept in the way of $0 \rightarrow +1 \rightarrow 0 \rightarrow -1 \rightarrow 0$ V in every composition (Fig.
17	3). A positive voltage RESET the film from the low resistance state (LRS) to the high
18	resistance state (HRS), and with negative bias the film SET to the LRS, indicating
19	bipolar resistive switching behavior of the films. Electroforming process by applying
20	high voltage is not required to initiate the switching. These results are consistent with

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1	the previous	studies of a	$a-GaO_{11}$	thin film	devices. ⁵ ,
T	the previous	studies of t	<i>i</i> -OaO[]]		ucvices.

2	The hysteretic shape of <i>I-V</i> curves is strongly dependent on the voltage sweep rate,
3	see e.g. Figs. 3(b), (c) and (d). The films do not reveal hysteresis loops at fast sweep rate
4	(100 V s ⁻¹), but tend to reveal wider hysteretic loops at slower rate. The switching
5	behavior is stationary and is not decayed for a few 10's voltage sweep cycles.
6	The Ga82Cr18 thin film is distinct from other composition films. The films reveal the
7	large hysteresis loop at a sweep rate of 1 and 0.01 V s ⁻¹ and possess the highest
8	HRS/LRS resistance ratio (on/off ratio) which is about 20 at 0.7 V (Fig. 4(a)). The ratios
9	decrease to about 2 if Cr dopant concentrations, namely, Ga(I) fractions are decreased or
10	increased as Ga91Cr9 or Ga78Cr22, respectively.
11	The area dependence of current level in Ga82Cr18 film shows linear scaling (Fig. 4b).
12	This feature discloses that the resistance state of the film below TE is homogeneously
13	changed by responding to applied field or a number of conducting filaments are
14	uniformly distributed over the electrode area.
15	In a separate experiment, we fabricated $Pt/TiO_x/Pt$ thin film devices by the similar
16	sputtering deposition processes as reported elsewhere. ¹⁷ Rutile-type TiO_x thin films with
17	a 100 nm thickness was uniformly formed on Pt electrode, as confirmed by TEM and
18	ED patterns (Fig. S1a). The TiO_x thin film prepared here reveal the similar switching
19	behavior as the filamentary mechanism switching TiO_x devices reported previously. ^{2,4}

20 *I-V* hysteretic shape is not varied by decreasing the voltage sweep rate (Fig. S1b) in the

range of less than 100 V s⁻¹. The current level is independent on Pt TE area (Fig. 4b).

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 $\mathbf{2}$ This is peculiar to the resistive switching triggered by variation of local states such as 3 formation and deformation of the conductive filament. It is evident that resistive switching of $Cr-GaO_x$ is quite different from TiO_x . The 4 strongly time-dependent switching events and area dependent current level is $\mathbf{5}$ incompatible with the filamentary mechanism as operating in TiO_x devices. It is 6 concluded the resistive switching of $Cr-GaO_x$ films is driven by variation of bulk $\overline{7}$ states.^{8,18} 8 9 The bulk switching mechanism suggests the film resistance can be incrementally adjusted by tuning the duration and sequence of the applied bias.¹⁹ Unlike the devices 10with abrupt switching characteristics where the programming bias control the final 11 device state,^{20,21} each programming pulse controls the relative change of the film 1213resistance in case of $Cr-GaO_x$ devices because donor concentration profiles across film 14can be varied sequentially by the migration at each programing bias. Figure 5 shows the resistance state change of Ga82Cr18 films when a series of 70 identical negative (-0.95 15V, 500 μ s) pulses was applied on the films, followed by a series of 70 identical positive 16voltage pulses (0.95 V, 500 μ s). The large pulse bias (namely -0.95 V or 0.95 V) 17programs the resistance state of the films and a small read bias of 0.1 V measures a 18response current to represent the resistance state. As expected from the DC 19

20 characteristics of the device, the application of negative voltage pulses (-0.95 V)

1incrementally decreases the film resistance, and the application of positive voltage2pulses (+0.95 V) incrementally increases the resistance.3**Discussion**5It is clear from the results of XPS and STEM-EDX measurements (Figs. 1 and 2)6that co-sputtered Cr(0) metallic atoms scavenge oxygen from adjacent Ga(III) oxide7moieties by the following redox reaction8
$$2Cr(0) + 3Ga(III) \rightarrow 2Cr(III) + 3Ga(I)$$
 (1)9The chemical composition of the resultant film can be given by10 $(1-x)Ga^{III}O_{1.5} + xCr^0 \leftrightarrows (Ga^{III}_{1-y}Ga^I_y)_{1,x}Cr^{III}_{x}O_{1.5(1-x)}$ (2)11Then the charge balance yields:12 $y = 1.5x (1-x)^{-1}$ (3)13The reaction (3) clarify the amount of oxygen deficiency $(1.5x)$ is related to Ga(I)14fraction (y). The molar fractions of Ga(I) of the films prepared here are in agreement15with the values calculated by reaction (3) (Table 1). It is apparent that oxygen16deficiency is involved by the reaction (2) during cosputtering.17The switching performance of Cr-GaO_x thin films is drastically changed by Ga(I)18fractions (Fig. 3). The schematic model of bulk mechanism switching of gallium oxide19base thin films is shown in Fig. 6. The positive bias on small area TE accumulates20negatively-charged oxide ions in vicinity of TE blocking electrode by extinguishing

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(Fig. 6(a)).

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electron carriers so as to form highly-resistive, oxygen-rich layer near TE as follows $Ga(I)O_{0.5} + 2e' + O \leftrightarrows Ga(III)O_{1.5}$ (4)

The opposite bias can enrich the electron carriers in the layer again by driving oxide 4 ions towards large area BE (Fig. 6(b)). At fast voltage sweep rate, the resistance ratio $\mathbf{5}$ between HRS and LRS, namely the hysteretic width of I-V loop tends to be small 6 $\overline{7}$ because the shift of oxygen vacancy donor distribution cannot be largely changed due to the relatively short bias duration. However, the hysteresis becomes wider at slower 8 9 sweep rate due to the large shift in donor distribution by longer bias duration.

10Based on the mechanism, the poor resistive switching performance of low Ga(I) fraction films, such as Ga96Cr4, can be related to the poor oxide ion conductivity 11 12because the low oxygen deficiency may not afford sufficient space for the pathway of 13oxide ion migration. The Cr(III) contents is not beneficial for the oxide ion conduction, 14since the dissociation of Cr-O bonding require relatively large energy. This might be one of the reasons for the deteriorated switching performance of the high chromium 15content films, Ga78Cr22 (Fig. 3d). The current results suggest that the optimal molar 16fraction of Cr/Ga is nearly 18/82 in the relation to fast oxide ion conduction in highly 17oxygen deficient glass matrices. 18

19The resistive switching performance of 120 nm-thick Ga82Cr18 thin films is superior to that of the GaO_x films (x = 1.1-1.2) given by PLD in previous studies.^{5,8} The 20

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former exhibits the on/off ratio of 20 at 0.7 V, however, the ratios of the later is about 2

at the same voltage. These results reveal the oxide glass network of the cosputtered $\mathbf{2}$ 3 Ga82Cr18 films retain the efficient oxide ion conductivity as much as the PLD films. The Ga82Cr18 film exhibits remarkable multiple resistance states as responding to short 4 pulse biasing (Fig. 5), indicating that the significant shift of oxygen vacancy donors for $\mathbf{5}$ resistance change can be induced by the bias duration of 100's µs. 6 The oxide ion mobility in M-doped GaO_x thin films may be largely varied by choice 7 of dopant cations as is the case with various ceramic oxide ion conductors.²² Except for 8 9 chromium, various metals such as Mg, Ti, Al etc. are potential candidates as oxygen 10scavenging dopant of Ga₂O₃ from the view point of M-O bond formation energy. Recently, Chang et al reported oxygen-deficient, amorphous WO_x thin film also shows 11 multiple resistive switching probably due to oxide ion conduction.²³ This suggests that 12not only the gallium oxide but also other nonstoichiometric metal oxide systems can be 1314considered as a possible candidate of bulk oxide ion conducting thin films. Most of metals and binary metal oxides can be used as a target material of rf sputtering 15techniques. It is concluded that the cosputtering with a pair of resistive switching oxides 16and oxygen scavenging metal dopant affords a strong tool to tune the oxide ion 17conductivity of the highly-nonstoichiometric, resistive switching metal oxide thin films. 18

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20 Conclusion

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1	In summary, the fabrication method of Cr-doped gallium oxide thin films with
2	tailored gallium valence state and oxygen deficiencies has been established, based on
3	the reactive cosputtering with Ga_2O_3 and Cr targets. The $Ga(I)$ -Ga(III) mixed valence
4	state homogeneous oxide glass can be formed from a homogeneous mixture of Cr(0)
5	metal atoms and Ga(III) oxides moieties deposited by cosputtering, because Cr(0)
6	scavenges oxygen of Ga(III) oxide due to the relatively large negative energy of Cr-O
7	bonding in comparison to Ga-O bonding. The Ga82Cr18 thin film with optimal Ga(I)
8	contents can reveal remarkable homogeneous resistance switching due to the efficient
9	bulk oxide ion conduction. The on/off ratio of the film is similar to that of the
10	highly-nonstoichiometric $GaO_{1,1}$ thin films prepared by PLD. The film clearly involves
11	multiple resistance state switching where the final device state is determined by bias
12	history, since the oxygen vacancy donor profiles can be incrementally modified by bias
13	duration or number of the applied short pulse biasing. The current results open up a new
14	way to design the bulk mechanism resistive switching metal oxide thin films with
15	tailored oxide ion conductivity.

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- 2 Table 1 List of the sputtering power of Cr for Cr-Ga₂O₃ cosputtering deposition and
- 3 composition and Ga(III) /Ga(I) molar ratios of the corresponding $Cr-GaO_x$ thin films.

4

Rf power / W	Name	Composition #	$Ga(III) / Ga(I)^{\gamma}$	$\operatorname{Ga(III)}/\operatorname{Ga(I)}^{\forall}$
70W	Ga78Cr22	$Ga_{0.78\pm0.06}Cr_{0.22\pm0.03}O_{1.2\pm0.1}$	61/39	58/42
60 W	Ga82Cr18	$Ga_{0.82\pm0.2}Cr_{0.18\pm0.02}O_{1.2\pm0.1}$	69/31	67/33
40 W	Ga91Cr9	$Ga_{0.91\pm0.04}Cr_{0.09\pm0.05}O_{1.3\pm0.1}$	77/23	85/15
30 W	Ga96Cr4	$Ga_{0.96\pm0.02}Cr_{0.04\pm0.02}O_{1.4\pm0.1}$	90/10	93/7

5 # determined by WDX. Υ determined by XPS. \forall calculated by eq (3).

6

 $\mathbf{7}$

1 Figure captions

 $\mathbf{2}$

3 Fig. 1 (a) Cross-section TEM image and (b) ED pattern of Ga82Cr12 thin films.

4 Corresponding elemental EDX mapping of (c) Ga and (d) Cr.

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Fig. 2 Fig. 2 (a) Ga 3d and (b) Cr 2p XPS spectra of 120 nm-thick thin films of (a1&b1)
Ga78Cr22, (a2&b2) Ga82Cr18, (a3&b3) Ga91Cr9 and (a4&b4) Ga96Cr4. (c) Ga 3d
XPS depth profile of 120 nm-thick Ga82Cr18 thin films. The spectra were stored at the
surface (c1) and at the points in 60 nm (c2) and 100 nm depth (c3). Red dots are the
observed and black lines the simulated.

11

12Fig. 3 *I-V* characteristics of $Pt(TE)/Cr-GaO_x$ film/Pt(BE) devices thickness during a repeated voltage sweep cycle of $0 \rightarrow +1 \rightarrow -1 \rightarrow 0$ V at different voltage sweep rates. 13(a1)-(a3) Ga92Cr4, (b1)-(b3) Ga91Cr9, (c1)-(c3) Ga82Cr18 and (d1)-(d3) Ga78Cr22 14thin films with 120 nm thickness. The sweep rate is 100 V s⁻¹ in (a1), (b1), (c1) and (d1), 15 1 V s^{-1} in (a2), (b2), (c2) and (d2) and 0.01 V s $^{-1}$ in (a3), (b3), (c3) and (d3). The cycles 16are repeated 20 time in (c2) and 10 time in others. The numbered arrows indicate the 17direction of the switching cycles (counter-figure-eight loops). 1st, 10th and 20th curves 18are shown by blue, red and green, respectively. The curves of other cycles are shown by 19black. The current levels in (a1)-(a3) are multiplied by 30. 20

2	Fig. 4 (a) On/Off switching ratio vs Cr content at 0.7 V, measured at a voltage sweep
3	rate of 100(+), 1(O) and 0.01(\triangle) V s ⁻¹ . (b) TE area scaling of the current of LRS
4	Ga82Cr18 and TiO _x thin films at -0.5 V.
5	
6	Fig. 5 Current response transients of Ga82Cr18 thin films, measured by applying 500 μs
7	programming bias (+0.95 or -0.95 V) and 100 μ s read bias (+0.1 V). Red is the current
8	measured at 0.1 V read pulse after programmed by a -0.95 V pulse and blue is the one
9	after programmed by a 0.95 V pulse. The insets show pulse patterns used for the
10	measurements.
11	
12	Fig. 6 Schematic representation of bulk mechanism resistive switching in $Cr-GaO_x$ thin
13	film devices. (a) Positive bias and (b) negative bias on TE.
14	
15	
16	



Fig. 1 (a) Cross-section TEM image and (b) ED pattern of Ga82Cr18 thin films. Corresponding elemental EDX mapping of (c) Ga and (d) Cr.





Fig. 2 (a) Ga 3d and (b) Cr 2*p* XPS spectra of 120 nm-thick thin films of (a1&b1) Ga78Cr22, (a2&b2) Ga82Cr18, (a3&b3) Ga91Cr9 and (a4&b4) Ga96Cr4. (c) Ga 3*d* XPS depth profile of 120 nm-thick Ga82Cr18 thin films. The spectra were stored at the surface (c1) and at the points in 60 nm (c2) and 100 nm depth (c3). Red dots are the observed and black lines the simulated.



Fig. 3 *I-V* characteristics of Pt(TE)/Cr-GaO_x film/Pt(BE) devices thickness during a repeated voltage sweep cycle of $0 \rightarrow +1 \rightarrow -1 \rightarrow 0$ V at different voltage sweep rates. (a1)-(a3) Ga92Cr4, (b1)-(b3) Ga91Cr9, (c1)-(c3) Ga82Cr18 and (d1)-(d3) Ga78Cr22 thin films with 120 nm thickness. The sweep rate is 100 V s⁻¹ in (a1), (b1), (c1) and (d1), 1 V s⁻¹ in (a2), (b2), (c2) and (d2) and 0.01 V s⁻¹ in (a3), (b3), (c3) and (d3). The cycles are repeated 20 time in (c2) and 10 time in others. The numbered arrows indicate the direction of the switching cycles (counter-figure-eight loops). 1st, 10th and 20th curves are shown by blue, red and green, respectively. The curves of other cycles are shown by black. The current levels in (a1)-(a3) are multiplied by 30.



Fig. 4 (a) On/Off switching ratio vs Cr content at 0.7 V, measured at a voltage sweep rate of 100(+), 1(\bigcirc) and 0.01(\triangle) V s⁻¹. (b) TE area scaling of the current of LRS Ga82Cr18 and TiO_x thin films at -0.5 V.



Fig. 5 Current response transients of Ga82Cr18 thin films, measured by applying 500 μ s programming bias (+0.95 or -0.95 V) and 100 μ s read bias (+0.1 V). Red is the current measured at 0.1 V read pulse after programmed by a -0.95 V pulse and blue is the one after programmed by a 0.95 V pulse. The insets show pulse patterns used for the measurements.



Fig. 6 Schematic representation of bulk mechanism resistive switching in $Cr-GaO_x$ thin film devices. (a) Positive bias and (b) negative bias on TE.



 $Cr-GaO_x$ film resistance can be incrementally adjusted by tuning the duration and sequence of the applied bias.