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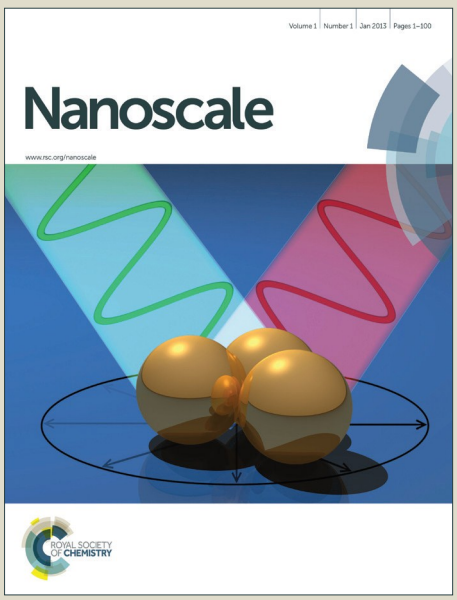

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Electromechanical Coupling in Single-Layer MoS_2 Suspended-Channel Transistors and Resonators

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We report on the analysis of electromechanical coupling effects in suspended doubly-clamped single-layer MoS_2 structures, and the designs of suspended-channel field-effect transistors (FETs) and vibrating-channel nanoelectromechanical resonators. In DC gating scenario, signal transduction processes including electrostatic actuation, deflection, straining on bandgap, mobility, carrier density and their intricate cross-interactions, have been analyzed considering strain-enhanced mobility (by up to 4 times), to determine the transfer characteristics. In AC gating scenario and resonant operations (using 100MHz and 1GHz devices as relevant targets), we demonstrate that the vibrating-channel MoS_2 devices can offer enhanced signals (than the zero-bandgap graphene counterparts), thanks to the resonant straining effects on electron transport of the semiconducting channel. We also show dependence of signal intensity and signal-to-background ratio (SBR) on device geometries and scaling effects, with SBR enhancement by a factor of ~8 for resonance signal, which provide guidelines toward designing future devices with desirable parameters.

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Resonant nanoelectromechanical systems (NEMS) have been demonstrating increasing capabilities and prospects for applications in fundamental physics metrologies, sensing and detection of physical quantities near the ultimate limits, and ultralow-power signal processing at radio and microwave frequencies, thanks to their miniaturized sizes and masses, high speeds, and exceptional responsivities and sensitivities.[1,2,3] Investigations on the approaches of coupling electrical and mechanical properties in movable nanostructures are an important path toward such prospects; and these also benefits from the emerging materials with attractive new properties, and new techniques of making new nanostructures. Lately, atomic layer two-dimensional (2D) crystals have enabled new types of NEMS resonators with interesting attributes; graphene, in particular, has been extensively studied for 2D NEMS, for its ultralow mass, outstanding elastic properties and superior strain limit (strength).[4,5,6,7,8,9,10,11] Only adding to the attractions of graphene as a semimetal, 2D semiconducting crystals, such as atomic layers isolated from transition metal dichalcogenides (TMDCs), also make robust NEMS resonators,[12,13] creating possibilities for directly coupling mechanical motions into the carrier transport in 2D transistors with sizable bandgap, in ways that may be different than in the graphene counterparts with zero bandgap. Among the TMDCs, molybdenum disulfide (MoS_2) is particularly interesting for its thickness-dependent and strain-tunable band structure[14,15] and mobility[16], in addition to its ultrahigh strain limit, high elastic modulus and low weight.[17,18]

MoS_2 field-effect transistors (FETs) have been extensively explored for different MoS_2 thicknesses, contact materials and device structures, with high I_{on}/I_{off} ratio of more than 10^8 and mobility (μ) dependent on thickness.[19,20,21,22,23,24,25,26] Recently, electron mobility of 1020cm^2/(V·s) for monolayer and 34,000cm^2/(V·s) for 6 layer MoS_2 devices at low temperature have been demonstrated by encapsulating MoS_2 in hexagonal boron nitride (h-BN) and using

graphene as electrical contact.[27] Room temperature mobility in this type of devices is 40-120cm^2/(V·s), showing the strong promise of MoS_2 as a material for 2D electronics. MoS_2 nanomechanical resonators have also been demonstrated, showing frequency up to 83MHz[28] and quality (Q) factor up to 710.[12] While optical transduction has been performed for these MoS_2 resonators with pre-patterned cavities,[12,28,29,30] understanding the electromechanical coupling and signal transduction, and their dependence on various device parameters in such structures, are highly desired. Although electromechanical coupling effects via gate voltage have been studied in carbon nanotube (CNT)[31] and graphene[5] resonators, the electromechanical coupling that incorporates both the gating effect and straining effect on mobility remains elusive. Further, as a 2D semiconductor with unique electrical and mechanical properties, electromechanical coupling effects in MoS_2 could be different from that in 1D CNTs and 2D semimetal graphene. While the study of strain effect on mobility has been attempted in MoS_2 transistors,[32] these devices are substrate-supported multilayer MoS_2, and the mobility is nearly constant or slightly decreasing with increasing bending. The effect of straining on device mobility for single-layer suspended MoS_2 FETs, and its effect on nanomechanical resonance has not been studied.

In this work, we demonstrate analysis and modeling of electromechanical coupling effects in suspended single-layer MoS_2, and designs of suspended-channel MoS_2 transistors and resonators. In DC scenario of the suspended MoS_2 FETs, we analyze the multi-physics effects on the channel conductance upon electrostatic gating, especially straining effect on enhancing mobility. We solve the electrostatic force and static deflection self-consistently, by first assuming no deflection, and calculate the electrostatic force, then calculate the amount of deflection that the force induces, which in turn increases the electrostatic force; we keep performing the calculation till the solutions of both the electrostatic force and the deflection converge. Moreover, we

include the mechanical pull-in effect in a modified configuration (with high-κ dielectric) and explore it as a way to improve I_{on}/I_{off} ratio at low operating voltage. From the results obtained in the DC FET modeling, we perform AC and resonance analysis. We demonstrate that by considering the multi-physics coupling in both DC and AC situations, the signal-to-background ratio (SBR) exhibits significant enhancement of up to ~8 times, compared to previous analyses where only gating and capacitance change are considered. We further study the geometric effects on the DC and AC conductance, for varying channel length (L) and initial air gap (g_0), and we observe interesting scaling and dependency between device geometry and the SBR. The analyses provide important guidelines for future experiments toward efficient electrical readout of suspended single-layer MoS_2 vibrating-channel transistors (VCTs). This platform can also be extended to other 2D semiconductors such as WSe_2 and black phosphorus.

When we apply DC or AC gate voltage to the suspended MoS_2 device (Figure 1a), there will be several effects that modulates the channel conductivity (Figure 1b). In DC analysis, first, similar to substrate-supported MoS_2 FETs, the gate voltage (V_G) modulates the carrier density in the channel by changing the Fermi level of MoS_2. Second, V_G induces deflection in MoS_2 (shown by the blue arrow on the top right of Figure 1b), which changes the capacitance between MoS_2 and the back gate, and thus changes the carrier density. Note that there is an intricate problem that the electrostatic force induces displacement, which increases the capacitance, and further changes the electrostatic force. To solve this problem and obtain the displacement at certain V_G, we develop a Matlab program to calculate the solution of both electrostatic force and displacement self-consistently. Third, the displacement induces strain in the device, which reduces the bandgap of MoS_2,[14,15] and thus shifts the threshold voltage (V_T). The shift in bandgap may also influence the carrier density, by altering the Fermi level;[33] but it has been

found theoretically that for monolayer MoS_2, Fermi level does not change much with the application of tensile strain,[34] thus we consider this effect to be small for monolayer devices. Finally, the strain changes the band structure, which changes the effective mass of electrons and reduces phonon scattering, thus enhancing the mobility[35,36] (shown by the arrows on the right in Figure 1b). After the carrier density and mobility are determined, we can obtain the channel conductance and drain current (I_D) at varying gate voltage, and acquire the transfer characteristics (I_D-V_G curve) of the device (Figure 2). For AC and resonant operations with an added small AC gate voltage, we make use of the DC analysis results, and calculate the near-resonance characteristics while also considering multiple parameters such as the strain effect on mobility, which differs from previous analyses on graphene resonators (where several paths illustrated in Figure 1 are not considered).

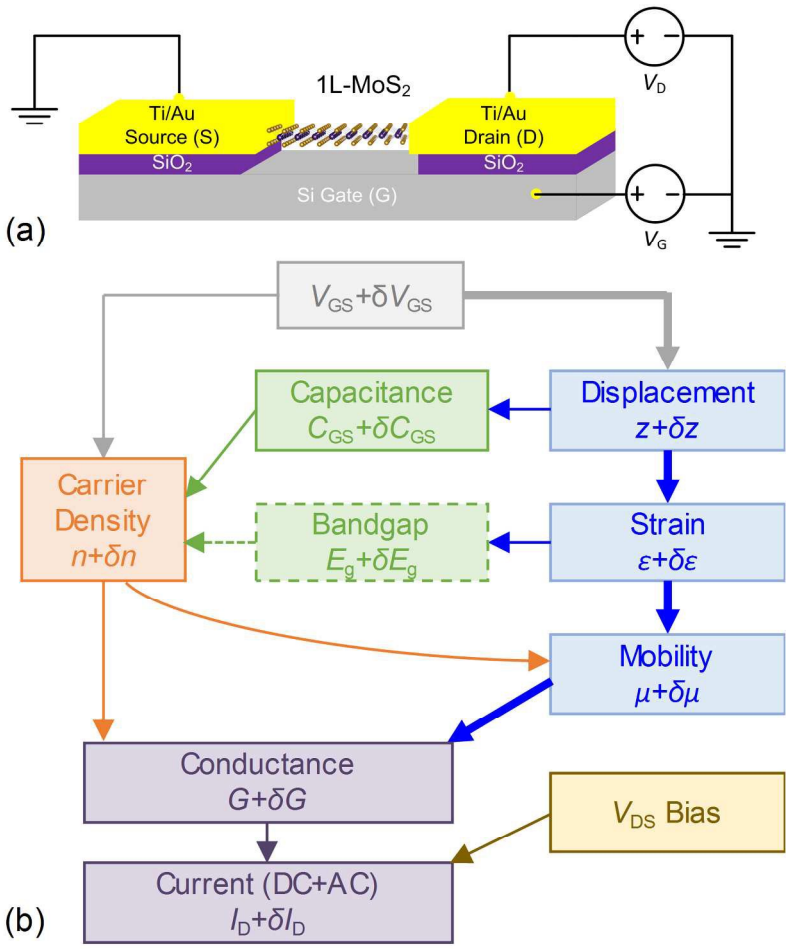

Figure 1. Illustrations of the suspended MoS_2 vibrating-channel transistor and its signal transduction diagram. (a) The schematic of the device with simplified DC electrical connection to the device. (b) Signal transduction diagram for the analysis of the device characteristics under applied DC and AC voltages and the effect on the measured current.

The DC analysis results for a suspended single-layer MoS_2 transistor with MoS_2 length L=2μm, width w=1μm, thickness t=0.65nm, and initial air gap g_0=290nm are shown in Figure 2. The static deflection (z_s) at certain V_G can be obtained by solving the following two equations: [37]

$$-\frac{64}{3L^3}z_s^3-\frac{8\varepsilon_0}{L}z_s+\frac{F_E L}{E_{Y,2D}}=0 \quad (1)$$

$$F_E = \frac{1}{2}\frac{\varepsilon_0^{'}}{\left(g_0 - z_s\right)^2}V_g^2 \quad (2)$$

where ε_0 is the pre-strain (*i.e.*, initial strain), F_E is the electrostatic force on MoS_2 induced by the back gate, $E_{Y,2D}$ is the 2D Young's modulus (180N/m for monolayer MoS_2), $\varepsilon_0^{'}$ is vacuum permittivity. At the initial air gap, we can perform a calculation of F_E using equation 2, then we obtain the z_s due to the F_E by solving equation 1. With the new air gap $g=g_0-z_s$, we calculate F_E again, and get another z_s. We keep doing this iterative calculation till the fractional difference of both z_s and F_E between two calculation steps are smaller than 10^{-4}. With the z_s, we can calculate the total strain in the device using $\varepsilon = \frac{8z_s^2}{3L^2} + \varepsilon_0$,[37] and then obtain the mobility corresponding to the strain level using the relationship in Figure 2c inset.[35]

With the deflection and mobility, we can calculate the transfer characteristics (I_D-V_G) of the vibrating-channel transistor using the 2D materials transistor model.[38] Using the displacement at certain gate voltage V_G, we determine the characteristic length using $\lambda = \sqrt{\frac{\varepsilon_{MoS_2}^{'} t\left(g_0 - z_s\right)}{\varepsilon_0^{'}}}$, where $\varepsilon^{'}_{MoS_2}=4.5\varepsilon^{'}_0$ is the permittivity of MoS_2. We do not explicitly calculate the carrier density, because it later merges with the current calculation. We then calculate the current in two steps. First, with the drain voltage V_D applied, the source and drain electrostatic potentials (φ_S and φ_D) are obtained numerically using equation (6) in Reference [38]. Second, the current is obtained using equation (8) in Reference [38]. In the calculation, we assume MoS_2 is doped n-type with work function φ_{MoS_2}=4.2eV, and the flat-band voltage is V_{FB}=0.8V for back gate being p-type Si; impurity concentration $N_{imp}=10^{13}m^{-2}$; 2D density of states $N_{DOS}=10^{14}eV^{-1}cm^{-2}$; temperature is room temperature (300K). In this study, we focus on the suspended MoS_2 channel, and do not

include the contact resistance, but they can be added into the model easily when considering various specific devices with Ohmic contact, in future experiments.

For lower pre-strain levels, we calculate the transfer characteristics assuming different ε_0 of 0.06%, 0.17%, 0.28% (Figure 2a), corresponding to 0.1N/m, 0.3N/m and 0.5N/m pre-tension, respectively, because they are reported to be in the range of pre-tension in MoS_2 resonators after mechanical exfoliation.[12] We find that the transfer curves with 0% and 0.06% pre-strain are quite similar, and the curve with 0.17% pre-strain is only slightly higher, showing that for very low pre-strain, the gate-voltage-induced strain is quite significant compared to the pre-strain (Figure 2a); specifically, for 0% pre-strain, the total strain at V_G=30V is ε=0.33%, and this value changes to 0.34% and 0.37% for 0.06% and 0.17% pre-strain, respectively (Figure 2b). At 0.28% pre-strain, I_D, strain, and mobility are all evidently higher than other curves (Figure 2a-c). We also calculate the result without considering the strain effect on enhancing mobility, but include the other effects in Figure 1b (blue dash dot line in Figure 2a-c). We find that I_D and μ are lower than when we take into account the strain effect on mobility, especially at large V_G. In Figure 2d-f, we further examine the effect of higher pre-strain levels on the transfer characteristics of the suspended MoS_2 transistor with the same geometry. We observe that I_D is much higher with higher pre-strain, especially when comparing 1% pre-strain with 0.5% pre-strain (Figure 2d). An interesting difference with the transfer characteristics with lower pre-strain is that for ε_0 higher than 1%, the I_D-V_G curve at "On" state is relatively linear (Figure 2d), instead of curving up at high V_G at lower pre-strain (Figure 2a). This is mainly because the gate voltage induced strain is much lower than the pre-strain, and the total strain remains almost constant (Figure 2e), thus mobility and I_D changes very little with V_G, while mostly changes with pre-strain level (Figure 2f). Further, because at higher pre-strain (>2%), the mobility almost

does not change with strain any more (Figure 2c inset), the transfer characteristics for 2%-10% pre-strain are quite similar.

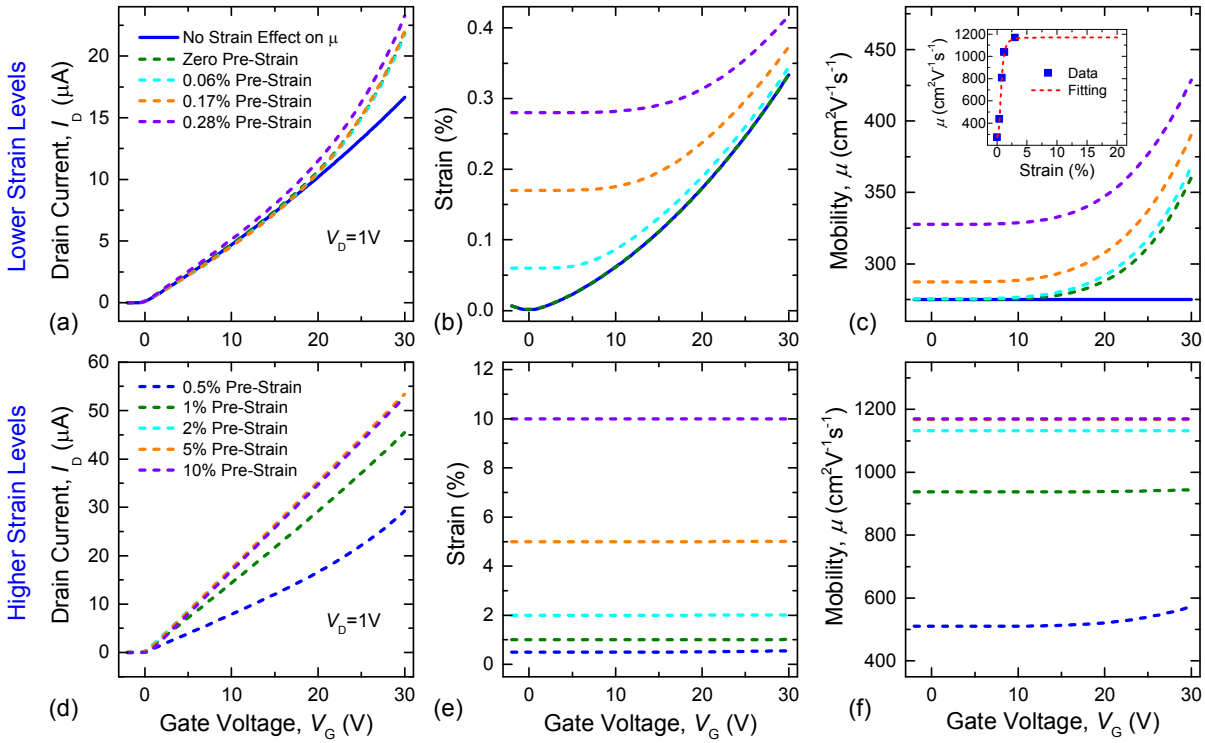

Figure 2. Calculated device performance when DC gate voltage is applied, for (a)-(c) lower, and (d)-(f) higher pre-strain level, for a suspended single-layer MoS_2 transistor with L=2μm, w=1μm, g_0=290nm. (a) Transfer characteristics (I_D-V_G) of the device under different pre-strain levels. (b) The total strain in the device with different V_G. (c) The electron mobility at different V_G. (d)-(f) has the same sequence as (a)-(c). Inset in (c) shows the extracted data points from theoretical calculation of mobility-strain relationship in reference [33], by assuming electron density n of 10^{11}/cm^2.

For a field-effect transistor, I_D increases with decreasing L due to lower resistance. Besides, I_D of an air-gap coupled suspended FET also increases with decreasing g_0 due to higher capacitance. If we consider the strain effect on mobility, the trend will be different. We investigate I_D dependence on L and g_0 under two different conditions: lower pre-strain (0.2%) and higher pre-strain (5%) levels (Figure 3). At lower pre-strain level, although the maximum I_D

is obtained at the shortest L (0.5μm) and smallest g_0 (150nm), another sharp peak is observed at the longest L (2μm) and smallest g_0 (400nm) considered in our simulation (Figure 3a). The reason for this peak is that at longer L, larger deflection of the suspended channel is possible at the same V_G (15V) due to larger electrostatic force and weaker resistance to deflection. Higher deflection of channel results in larger strain, which induces higher mobility and increases I_D. On the contrary, for higher pre-strain level, deflection of the suspended channel at the same V_G is much smaller, and the strain will not increase much even for larger L. As a result, there is no peak at the longest L for higher pre-strain levels (Figure 3b). The observation is more obvious in Figure 3c, which shows the ratio between I_D with and without strain induced mobility enhancement ($I_{D,Strain}/I_D$), and the peak at the longest L and smallest g_0 for lower pre-strain level is observed. This ratio is almost constant for all L and g_0 for higher pre-strain level, achieving enhancement of I_D by a factor of ~4 after considering the strain effect. The subthreshold swing (*SS*) decreases with smaller air gap due to larger capacitance (Figure 3d); and at 30nm air gap, *SS* of 74mV/dec is expected.

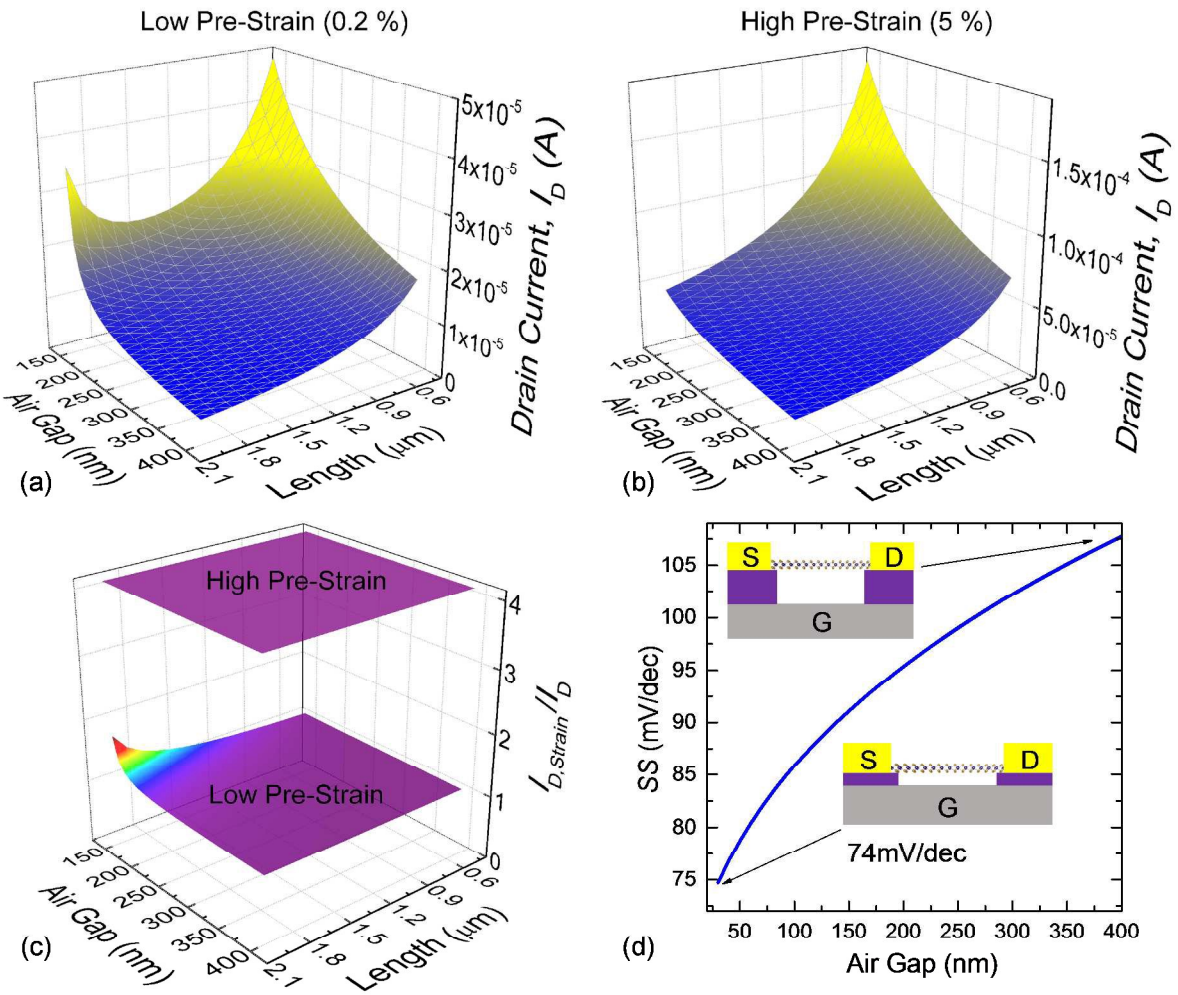

Figure 3. Dependence of drain current (I_D) on channel length (L) and air gap (g_0), computed for (a) low pre-strain and (b) high pre-strain levels, at V_G=15V and V_D=1V. (c) The comparison of the ratio of drain current with and without strain induced mobility enhancement between low pre-strain and high pre-strain is shown. (d) The decrease of *SS* with the decrease of air gap.

We have simulated the effect of mechanical pull-in for this suspended single-layer MoS_2 FET due to the electrostatic force induced by V_G, using a slightly different geometry, by adding the 5nm to 10nm HfO_2 dielectric layer on silicon to prevent the gate from leaking after the mechanical pull-in, as illustrated in Figure 4a insets. For a suspended single-layer MoS_2 device, as we keep increasing V_G, the electrostatic force keeps increasing quadratically while the elastic

restoring force increases linearly with displacement, and beyond certain gate voltage called pull-in voltage (V_{PI}), the electrostatic force is always higher than the elastic force, and the MoS_2 channel is suddenly pulled down. At V_{PI}, there exists no solution to equations 1 and 2; and in the calculation, when the number of iterations reach 10000, we consider the pull-in effect occurs, and the suspended monolayer MoS_2 is suddenly pulled down. Figure 4a shows the mechanical pull-in at V_{PI}=0.8V for a single-layer MoS_2 device with L=5μm, width w=1μm, and g_0=60nm, at 0.15% pre-strain. With the thin dielectric layer, the device is still functional as a normal FET after the pull-in happens. As a result of the abrupt pull-in, there is a sharp increase of drain current (Figure 4b). With shorter MoS_2 channel, V_{PI} increases from 0.65V (L=9μm) to 3.2V (L=2μm) for a device with g_0=70nm, and 0.2% pre-strain (Figure 4b). If we compare a regular, substrate-supported, non-suspended MoS_2 FET with 5nm HfO_2 (purple solid line in Figure 4c) to the suspended devices with pull-in effect, we can observe an improvement of the current on-off ratio from 3800 to 3.6×10^4, if we take V_G=-0.5V as off state and V_G=3V as on state. V_{PI} increases with higher g_0, from 1.2V at g_0=70nm to 2.05V at g_0=100nm (Figure 4c), and I_{on}/I_{off} ratio also increases with g_0 (Figure 4c inset). For logic circuit operation, it is desirable to define off state at V_G=0V, and this can be achieved by properly designing the geometry to maintain relatively high I_{on}/I_{off} ratio at the same time. Figure 4d shows that using L=3μm device with g_0 varying from 60nm to 120nm, we achieve I_{on}/I_{off} ratio of 1.3×10^4 with 120nm air gap, when we take V_G=0V as off state and V_G=5V as on state. The I_{on}/I_{off} ratio also increases with increasing air gap, as shown in Figure 4d inset.

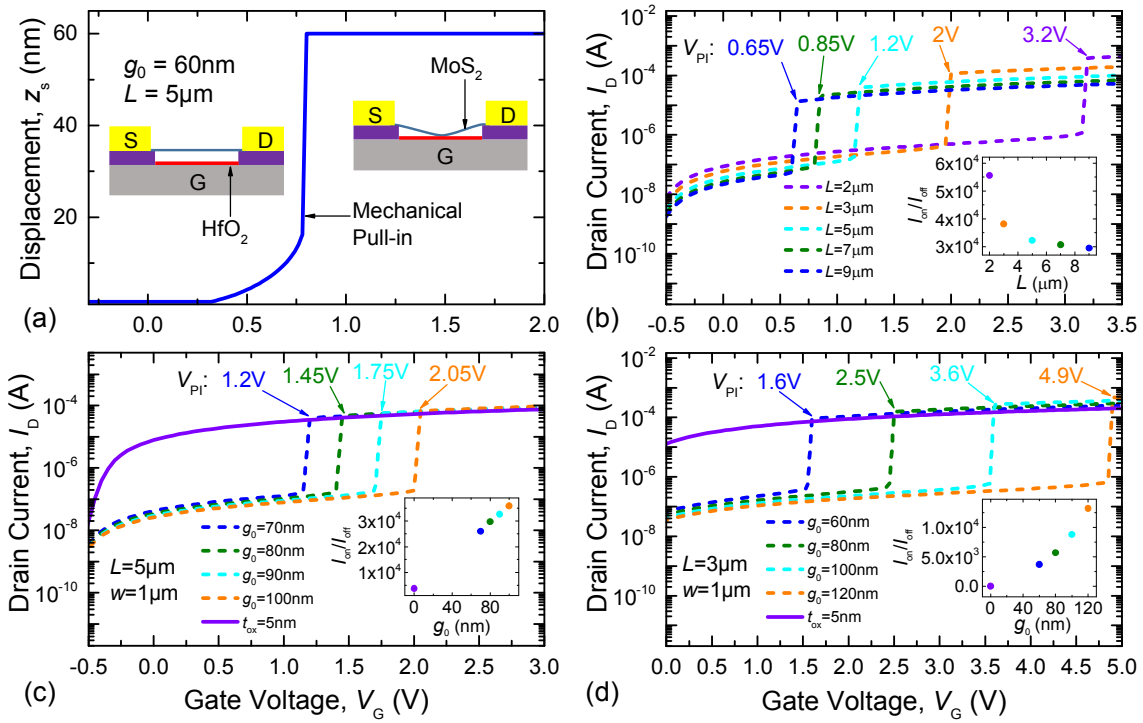

Figure 4. Investigation of mechanical pull-in effect on FET performance, with V_D=0.1V. (a) The displacement of the suspended MoS_2 FET at different V_G, showing the abrupt pull-in effect. Insets: Illustrations of the device structure before (left) and after (right) pull-in. (b) The effect of mechanical pull-in on suspended MoS_2 FETs with different L (g_0=70nm). Inset shows the change of current on-off ratio with L. (c) & (d) The comparison of mechanical pull-in effect on the suspended MoS_2 FET with different g_0, for (c) L=5μm and (d) L=3μm, at 0.2% pre-strain. Insets show the improvement of current on-off ratio with increasing air gap.

In AC operation near resonance, the response also increases after we take the strain effect into consideration (Figure 5a). For the monolayer MoS_2 resonator with L=2μm, w=1μm, and g_0=290nm, we calculate the device resonance at 100MHz assuming the device as a tensioned membrane (Figure 5b). The resonance frequency is expressed as $f_0 = \frac{1}{2L}\sqrt{\frac{\varepsilon E_{Y2D}}{\rho\alpha}}$, where ρ is the 2D mass density and α is adsorbed mass coefficient. If we assume α=5, then 1.4% pre-strain is necessary to achieve 100MHz resonance frequency, and the strain enhances the mobility and

changes the AC signal we can measure. The channel conductance with both DC and AC gate voltage is:

$$G(\mathrm{z}) = G(\mathrm{z_s}) + \frac{dG}{dV_G}\delta V_G + \frac{dG}{dz}\delta z\,, \tag{3}$$

$$\frac{dG}{dz}\delta z = \frac{dG}{dn}\frac{dn}{dz}\delta z + \frac{dG}{d\mu}\frac{d\mu}{dz}\delta z = V_G\frac{dG}{dV_G}\frac{C_G^{'}}{C_G}\delta z + \frac{dG}{d\mu}\frac{d\mu}{d\varepsilon}\frac{d\varepsilon}{dz}\delta z\,, \tag{4}$$

where $G(\mathrm{z_s})$ is the conductance under static deflection (which only exists at DC and does not contribute to the signal at resonance frequency), C_G is the capacitance between the MoS_2 and the back gate, δV_G is the AC gate voltage where $\delta V_G = |\delta V_G|\cos(\omega t)$, δz is the AC deflection where $\delta z = |\delta z|\cos(\omega t + \varphi)$, with its amplitude $|\delta z| = \frac{C_G^{'} V_G |\delta V_G|}{m\sqrt{(\omega_0^2 - \omega^2)^2 + (\omega_0\omega/Q)^2}}$ (m is the mass of the MoS_2 resonator), and φ=0 in ideal case. We can get dG/dV_G from the transfer characteristics under DC static deflection similar to that shown in Figure 2a,d, obtain dμ/dε from the μ-ε relationship in Figure 2c inset, and find dε/dz from the deflection relationship $\varepsilon = \frac{8z^2}{3L^2}$. The second term in equation 4: $\frac{dG}{d\mu}\frac{d\mu}{dz}\delta z$ is a result of the strain effect in enhancing the mobility, which has not been considered before for electromechanically transduced graphene resonator, but its effect is not negligible for suspended monolayer MoS_2 devices. Both the strain effect on mobility and the mutual coupling effect between deflection and electrostatic force contribute to the enhancement of dG/dV_G, which in turn increases the signal strength. As shown in Figure 5b, we find there is a ~4 times enhancement in the peak signal intensity and SBR for the 100MHz resonance. The signal background is mainly determined by the $\frac{dG}{dV_G}\delta V_G$ term in equation (3). Here we assume

V_G=15V, $|\delta V_G|$=1mV and Q=1000. Such Q should be achievable with high quality crystal and minimizing extrinsic damping effects, or measuring in higher vacuum or at low temperature, because Q of >700 has been achieved with fully-covered circular diaphragms measured in moderate vacuum (6mTorr),[12] and Q up to ~10^4 in doubly-clamped graphene resonators has been achieved at 5K.[5] For 1GHz resonance in Figure 5c, we use L=1μm and α=1 to maintain pre-strain level lower than 10% to avoid metal-insulator transition in MoS_2,[39] and obtain that the ε_0 needed is 7.2%. With straining effect on mobility, the signal is much higher than without considering this effect. From Fig. 2c inset, the mobility does not increase much when pre-strain is higher than 5%, which means that $d\mu/d\varepsilon$ term in equation (4) is very small, and thus $\frac{dG}{d\mu}\frac{d\mu}{d\varepsilon}\frac{d\varepsilon}{dz}\delta z$ is very small. Nonetheless, the current will still be enhanced at pre-strain level ε_0=7.2%, because the dG/dV_G term in equation (4) will be enhanced due to the much higher mobility induced by the strain. Note that the background level is also higher for 1GHz resonance, so the SBR is not much enhanced, which is different from the 100MHz device.

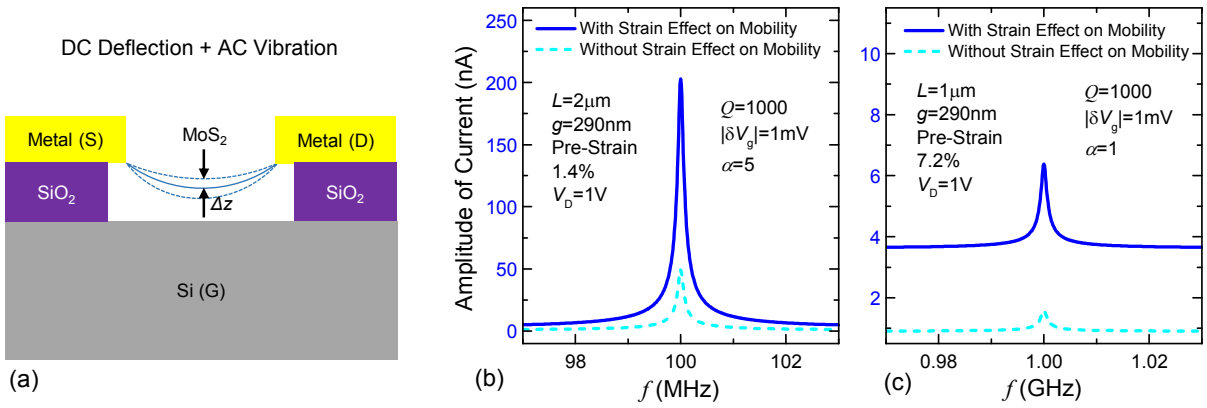

Figure 5. The calculated device performance when both DC and AC gate voltages are applied, showing the device resonance characteristics. The resonance considering the strain effect on mobility is compared with the resonance without considering that, for (a) 100MHz resonance, and (b) 1GHz resonance.

We further investigate the effect of varying the device dimension on the SBR, with comparison against the signal without considering the strain effect (Figure 6). For 100MHz case, we vary L from 0.5μm to 2μm, and g_0 from 200nm to 400nm, and the SBR and SBR enhancement factor (ratio of SBR between considering and not considering the strain effect on mobility) are shown in Figure 6a,b. SBR as high as ~1435 has been projected, at L=1.1μm and g_0=200nm (Figure 6a). Smaller g_0 makes the electrostatic coupling more efficient, resulting in higher measured current. For length variation, we observe the interesting dependence, and at L≈1.1μm we get the best SBR. The effect of L is intricate and it at least includes 4 aspects. Within the length range we consider, we use the effect of larger L as an example. First, with larger L, higher pre-strain is needed to attain the same resonance frequency, which translates into higher mobility and higher DC conductance; while the increasing L also results in higher resistance and lower conductance. The combination of these two effects makes dG/dV_G first decreases, and then increases with L. With larger dG/dV_G, the total $G(z)$ at resonance will be higher, and the background is also higher, which would result in lower SBR. Second, z_s increases with larger L, resulting in smaller g_s, higher capacitance, and especially, higher δz, which then increases both $G(z)$ and SBR. Third, larger L results in smaller $d\varepsilon/dz$, which decreases both $G(z)$ and SBR. Fourth, $d\mu/d\varepsilon$ also depends on the strain level, which again depends on L. With these different factors together influencing the total $G(z)$ and the background, we find that SBR depends on both L and g_0 (Figure 6a), while the SBR enhancement factor (ratio of SBR with and without considering the strain effect on mobility) mostly depends on L, with its highest value of 6.3 at L=1μm (Figure 6b). The SBR clearly increases after we incorporate the strain effect on mobility. We also analyze the SBR and SBR enhancement factor for a 1GHz single-layer MoS_2 nanomechanical resonator (Figure 6c,d). We

consider same range of g_0 from 200nm to 400nm, while different range of L from 0.1μm to 1μm, compared to the 100MHz device, to keep the necessary strain relatively low. The peak SBR is ~23.8 when L=2μm and g_0=200nm, which is smaller than 100MHz resonance, because the displacement is smaller at higher frequency (Figure 6c). We obtain highest SBR enhancement factor of ~8 at L≈200nm and g_0=200nm, which is better than the 100MHz device. We also observe similar dependence on g_0 as the 100MHz device, with higher SBR enhancement factor at smaller g_0 (Figure 6d).

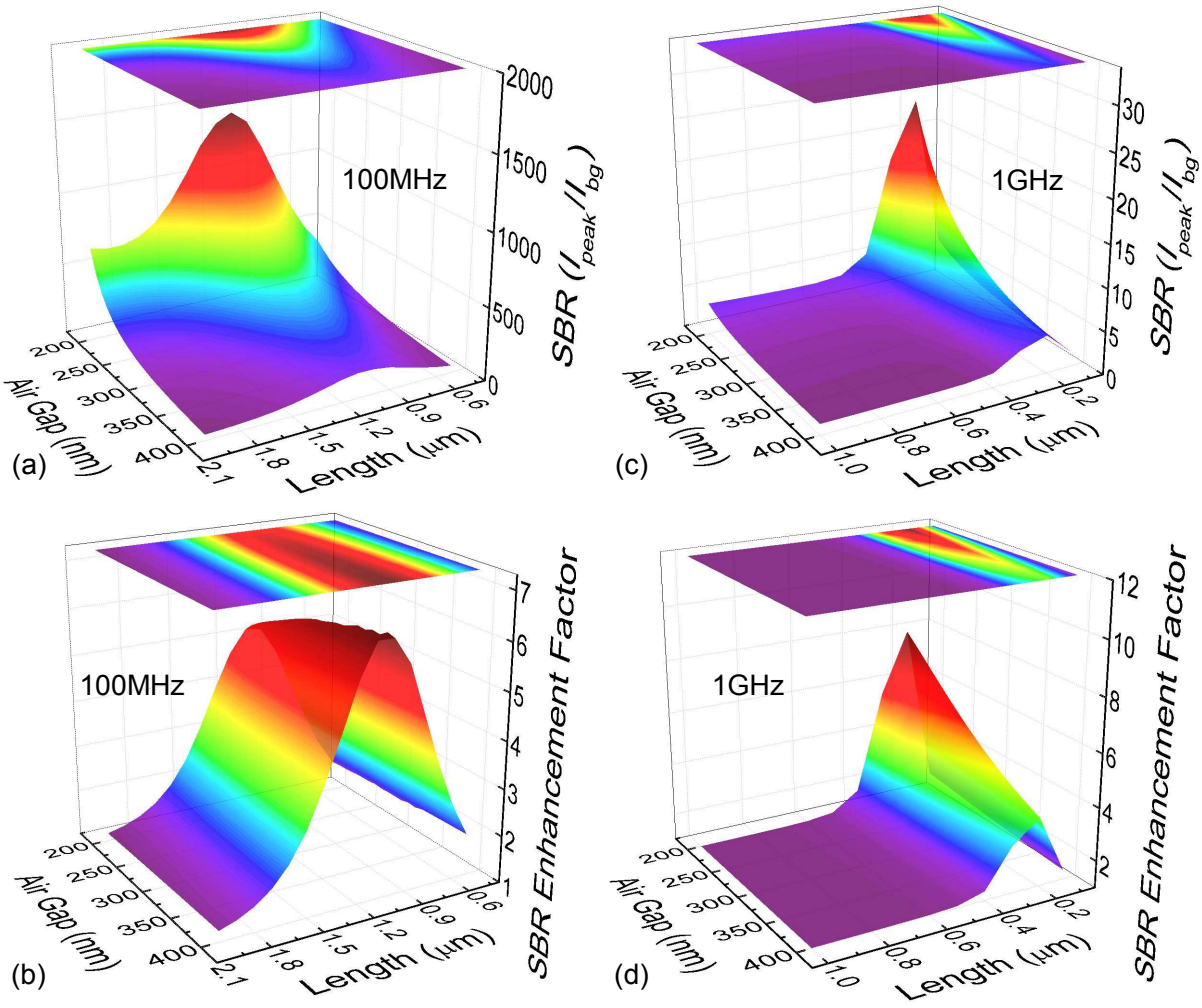

Figure 6. 3D color maps showing the effect of MoS_2 channel length and initial air gap on the resonance responses, with V_G=15V, V_D=1V, $|\delta V_G|$=1mV and Q=1000. (a)-(b) 100MHz resonance for devices with

L=0.5-2μm, and g_0=200-400nm. (c)-(d) 1GHz resonance for device with L=0.1-1μm, and g_0=200-400nm. (a) & (c) The SBR of the resonances at (a) 100MHz, and (c) 1GHz, with the projection of the amplitude shown on the top. (b) & (d) The SBR enhancement factor, at (b) 100MHz, and (d) 1GHz.

Recently, piezoelectricity in single-layer MoS_2 has been experimentally verified,[40,41] however, it should not have effect on the generic devices we consider here. First, the piezoelectric effect is only observed when the electrodes are configured along the zigzag direction, so that the bias electric field is along the armchair direction of the single-layer MoS_2 crystal.[40,41] In this work, however, we consider generic MoS_2 suspended-channel FET_S whose contact electrodes are in arbitrary orientations. Second, even if the drain-source electrodes happen to allow the bias electric field to be along the armchair direction, the occurrence of the piezoelectric effect relies on the Schottky barriers, and the strain-induced charge should asymmetrically modulate the Schottky barriers (the piezotronic effect).[40] In our calculations, however, we focus on the suspended MoS_2 channel rather than the contact electrodes, and we have assumed Ohmic contact without Schottky barriers, and the piezotronic effect will not be evident in this case, because the polarization and charge induced by piezoelectricity will not modulate the Schottky barriers and will be quickly neutralized by free carriers abundant in the electrodes and in MoS_2 channel. Third, for experimental realization of the device, Ohmic contact with small contact resistance has been achieved with graphene electrodes.[27] To rationally design devices to leverage the piezoelectricity effects, crystal orientation and Schottky barriers at the contacts should be carefully engineered, which is beyond the scope of this work.

In summary, we have analyzed the DC static and AC resonance responses of the suspended single layer MoS_2 VCT. Using the self-consistent calculation and taking the strain effect on mobility into consideration, we have first elucidated the effect on DC transfer characteristics, and

then extended the calculation to AC resonance analysis, showing that the signal will be higher after considering this effect. We have also examined the effect of geometry (*e.g.*, L and g_0) on DC and AC signal that can be measured, which provides important guidelines for future designs and experimental demonstrations of this type of devices.

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