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## **Soluble Oxide Gate Dielectrics Prepared Using the Self-Combustion Reaction for High-Performance Thin-Film Transistors**

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†Electronic supplementary information (ESI) available: Transfer and output characteristics with various GPTMS ratios and different composition ratios, dielectric properties, XPS spectra of  $AIO<sub>x</sub>$  thin films, TEM cross sectional images, TG-DTA curves of  $AIO<sub>x</sub>$  precursors with different composition ratios, and XRD patterns are available. Fig. S1-S8.

We report the fabrication of high-performance metal oxide thin-film transistors (TFTs) with  $AIO<sub>x</sub>$  gate dielectrics using combustion chemistry in a solution process to provide energy to convert oxide precursors into oxides at low temperatures. Our self-combustion system utilizing two Al precursors as a fuel and oxidizer is systematically compared with conventional combustive Al precursors with urea in terms of combustion efficiency and dielectric properties. AlO<sub>x</sub> gate dielectric layers are spin-coated from a solution of combustive AlO<sub>x</sub> precursors in 2methoxyethanol and annealed at 250 °C. In this process, organic compounds can be introduced to improve the resulting layer morphology. The thermal behaviors of the self-combustive  $AIO<sub>x</sub>$ precursors are investigated and compared with those of noncombustive  $AIO<sub>x</sub>$  precursors and combustive urea- $AIO_x$  precursors to evaluate the generation of exothermic heat at a relatively low annealing temperature. The  $AIO_x$  dielectrics prepared from self-combustive precursors have uniform and smooth surfaces, low leakage current densities, and high dielectric constants above 8.7. They also exhibit excellent insulating properties and no breakdown at high electric fields. Furthermore, the ZnO TFTs prepared to confirm the operation of the  $AIO<sub>x</sub>$  gate dielectrics show a good mobility of 24.7 cm<sup>2</sup> V<sup>-1</sup> s<sup>-2</sup> and an on/off ratio of 10<sup>5</sup>. It is believed that the AlO<sub>x</sub> dielectrics prepared using the self-combustion reaction at low temperature can form a good interface facilitating the growth of desirable ZnO crystal structures, which leads to a considerable improvement in ZnO TFT performance.

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### **1. Introduction**

The gate dielectric is a critical element affecting the electrical performance of thin-film transistors (TFTs). To obtain high-performance gate dielectrics for TFTs that can provide high capacitance without relying on ultrathin film thicknesses, high-*k* dielectric materials have been actively pursued to replace  $SiO_2$  (dielectric constant  $\sim$ 3.9). The high capacitance and thicker layer also allow for efficient charge injection into the transistor channels and reduce the directtunneling leakage currents. Such advantages have motivated intense research into the development of zirconium oxide  $(ZrO<sub>2</sub>)$ , hafnium oxide (HfO<sub>2</sub>), and aluminum oxide (AlO<sub>3</sub>) as high-*k* dielectric materials.<sup>1-3</sup> However, gate dielectric layers made of these materials must still be fabricated using vacuum processes, which have high costs for processing equipment and facilities.

Recently, numerous research studies into solution-processed TFTs have focused on the development of soluble high-*k* gate dielectric materials for high-performance TFTs.<sup>4-10</sup> Solutionprocessability can lead to several advantages including printability, the possibility of large-area deposition, low-cost device fabrication, and compatibility with mechanically flexible substrates.<sup>11-14</sup> Among many potential materials,  $A_1O_3$  thin films are well known as high-*k* gate dielectrics because of their low interfacial trap density with oxide semiconductors and relative permittivity of  $\sim$ 9.<sup>15</sup> For example, a sodium β-alumina gate dielectric prepared using a sol-gel reaction at 830 °C to produce an oxide TFT showed a mobility of 28 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>.<sup>16</sup> Furthermore, an AlO<sub>x</sub> gate dielectric deposited using a multiple-spin-coating procedure and annealed at 300 °C had an oxide TFT mobility of 33 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1 17</sup> Despite these high reported mobilities for oxide TFTs, however, the high processing temperature and multiple spin-coating processes lead to certain limitations.

As an alternative, the solution combustion reaction is an easy and time-saving production process based on thermochemical concepts used in the propellant and explosive fields. Recently, combustion chemistry has been utilized for the synthesis of nanooxides for electronic applications. The advantages of this process derive from the fact that no special equipment is required to provide additional heat, because the combustion reaction of simple precursor compounds itself generates heat that can be used to form the metal oxide.<sup>18,19</sup>

Research into combustion reactions for TFT fabrication has mostly been conducted using metal oxides such as  $In_2O_3$ , ZTO, IZO, and ITO.<sup>20,21</sup> However, quite recently the addition of urea as a fuel for combustion to form gate dielectric materials has been introduced.<sup>22</sup> Currently, this approach has several limitations that should be solved, such as a low combustion efficiency and the resulting high leakage current of the resulting dielectric. In particular, the low combustion efficiency can result in insufficient heat supply to the internal system, which leads to unsatisfactory dielectric properties. Moreover, it is quite difficult to finely tune the amount of urea needed to realize the optimal combustion reaction.

Here, we suggest a promising and novel method of synthesizing solution-processable gate dielectrics by self-combustion at low temperatures. Our facile, efficient *self-combustion* process overcomes the shortcomings of previous combustive approaches by simply mixing two metal precursors that contain coordinated fuel and oxidizer ligands. We believe that this simple process can provide more stable TFTs and better dielectric performance because of its high combustive efficiency. In addition, hybridization of the dielectric material with organic polymers can be achieved by carrying out a sol-gel reaction with the  $AIO<sub>x</sub>$  precursors to provide more electrically stable and fine-tuned properties in a high- $k$  gate dielectric layer. In the basic synthesis, the  $AIO_x$ gate dielectrics for oxide TFTs were obtained by solution combustion from a mixture of  $AIO_x$ 

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and organic precursors at a low temperature of 250 °C. To confirm the dielectric properties of the  $AIO<sub>x</sub>$  prepared using this combustion system with organic components for use in oxide TFTs, ZnO TFTs were fabricated by spin-coating ZnO precursors on the  $AIO<sub>x</sub>$  films and annealing at 250 °C. Moreover, the crystalline structures of the ZnO on the  $AIO<sub>x</sub>$  films prepared from combustive precursors were compared with those on films prepared from noncombustive precursors and combustive precursors with urea to investigate the influence of the gate dielectric layer on the TFT performance.

### **2. Results and discussion**

The combustion reaction between organic fuels (e.g., acetylacetone, urea, carbohydrazide, and citric acid) and an oxidizer (e.g., nitrate) can lower the effective annealing temperature required for the formation of metal oxide compounds from oxide precursors. When a fuel-oxidizer pair is moderately heated, a highly intense exothermic reaction occurs. The evolved heat can accelerate the formation of metal hydroxides or alkoxides into the corresponding metal oxide frameworks (M-O-M) at sufficiently low external temperatures. In other words, in the self-combustion system, the energy required for complete conversion of the precursors into oxides can be replenished by the exothermic energy from the combustive precursor reaction. Previous studies have demonstrated that metal nitrates (as both a metal source and oxidizer) and acetylacetone (AcAc, as both a chelating agent and fuel) are the most effective among the various oxidizers and fuels for self-combustion.23,24

To investigate the thermal behavior of the  $AIO<sub>x</sub>$  precursor during the self-combustion reaction, thermogravimetric differential thermal analysis (TG-DTA) was conducted. To compare the thermal properties of combustive precursors with those of noncombustive precursors, TG-DTA

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data were plotted for self-combustive AlO<sub>x</sub> precursors, the Al(NO<sub>3</sub>)3</sub>·9H<sub>2</sub>O and Al(C<sub>2</sub>H<sub>3</sub>O<sub>2</sub>)<sub>3</sub> pair, and the noncombustive precursors  $Al(NO_3)_3.9H_2O$  and  $Al(C_2H_5O_2)_3$  (Fig. 1). Furthermore, to analyze and compare the thermal behavior of conventional combustive system, a combustive precursor composed of  $Al(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O$  and urea as an external fuel was investigated. The DTA data for the self-combustive  $AIO<sub>x</sub>$  precursor containing acetylacetonate (fuel) and nitrate (oxidizer) ligands show a single, intense exothermic peak at 170  $\degree$ C in Fig. 1(a) that corresponds exactly to the abrupt mass loss. On the other hand, the combustive precursor with urea shows a negligible exothermic peak and no mass loss at around 250 °C, and the combustion efficiency is remarkably lower and the exothermic temperature is much higher than those of the selfcombustive precursor. This means that quite a small amount of heat can lead to M-O-M formation in the urea-based combustion system. In the case of the noncombustive the Al(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O precursor, a gradual mass loss is completed at around 400 °C without any exothermic peaks, which is possibly due to the decomposition of organic ligands. Even though the TGA data for the  $Al(C_2H_5O_2)$ <sub>3</sub> precursors show abrupt thermal decomposition at a relatively low temperature of 250 °C, Fig. 2(d) shows that they do not undergo an exothermic reaction. This also implies that the  $Al(C_2H_5O_2)$ <sub>3</sub> precursor is a good candidate for producing  $AlO<sub>x</sub>$  gate dielectrics. However, when the  $AIO<sub>x</sub>$  gate dielectric for a ZnO TFT is prepared using Al( $C_2H_5O_2$ )<sub>3</sub> precursors at 250 °C, the electrical performance of the resulting ZnO TFT is particularly poor, as shown in Supplementary Information Fig. S1<sup>†</sup>. This means that noncombustive  $AIO<sub>x</sub>$  precursors require a high temperature of over 400  $^{\circ}C$  for the complete conversion of the precursors into the M-O-M lattice. In addition, this result supports the conclusion that the exothermic heat from a self-combustive  $AIO<sub>x</sub>$  precursor can assist the conversion into the corresponding oxides at low temperatures.

When only the  $AIO<sub>x</sub>$  precursor was deposited without any organic ingredients, the obtained  $AIO<sub>x</sub>$  gate dielectric layer showed a quite poor surface morphology (Fig. 2): the surface of the film was cracked and had numerous pores generated during annealing. The combustive  $AIO_x$ precursor with urea but no other organic components also shows an improper surface morphology for gate dielectric layer. These poor surface properties can render the  $AIO<sub>x</sub>$  gate dielectric layers prepared from only the  $AIO<sub>x</sub>$  precursor difficult to use in actual devices because of their high leakage currents and because they exhibit charge-trapping phenomena. To improve the layer morphology, organic components such as (3-glycidoxypropyl)trimethoxysilane (GPTMS) can be introduced into the oxide network as polymeric binders using the sol-gel method. To fabricate a stable gate dielectric layer with a smooth surface, the combustive  $AIO_x$ precursor can be mixed with GPTMS, the epoxy moiety of which polymerizes during the annealing process (Scheme 1).

The dielectric characteristics of the self-combustive  $AIO<sub>x</sub>$  dielectric films and their dependence on the organic content (GPTMS) were evaluated by measuring the quantitative leakage current and dielectric constant. The supplementary information Fig.  $S2^{\dagger}$  shows typical current density vs. electrical field and capacitance vs. frequency curves for metal-insulator-metal (MIM) structures fabricated from  $AIO_x$  insulators with various amounts of GPTMS. The plot demonstrates that GPTMS-only films exhibit the highest dielectric constant, and higher GPTMS contents result in higher dielectric constants of the  $AIO<sub>x</sub>$  insulators in the range of 8.7–12. Even though the GPTMS film shows a high dielectric constant of 16 at 100 Hz, this film is unsuitable as a gate dielectric because of its unstable nature in all frequency ranges. Furthermore, GPTMS-only films have high leakage current densities because of their nonuniform and rough surface morphologies. This result means that the dense network structure produced by the sol-gel

reaction of GPTMS together with the hybridization of the  $AIO<sub>x</sub>$  precursors is necessary to obtain a satisfactory surface structure for the final gate dielectric film. When the ratio of the  $AIO<sub>x</sub>$ precursor to GPTMS is 1:8, the dielectric constant of the  $AIO<sub>x</sub>$  films is stable in all frequency ranges. When the ratio of the AlO<sub>x</sub> precursor to GPTMS decreases to 1:30 or 1:45, the dielectric properties of the obtained films are unstable, especially in the low and high frequency ranges. Consequently, the operating zone of TFTs fabricated with these GPTMS ratios is limited to specific frequency ranges. The dielectric constants of  $AIO<sub>x</sub>$  insulators prepared without GPTMS could not be measured because of film cracking. Therefore, the ratio between the  $AIO<sub>x</sub>$  precursor and GPTMS was chosen to be 1:8 to obtain the optimal TFT characteristics. The leakage current densities for films with thicknesses of  $d = 200$  nm prepared from  $AIO<sub>x</sub>$  precursors with GPTMS with are around  $10^{-8}-10^{-9}$  A cm<sup>-2</sup> up to E  $\approx$  2 MV cm<sup>-1</sup>. In particular, AlO<sub>x</sub> insulators prepared with a 1:8 ratio of the  $AIO<sub>x</sub>$  precursor to GPTMS show excellent leakage current densities of  $3.92 \times 10^{-9}$  A cm<sup>-2</sup>. All the electrical properties obtained with different AlO<sub>x</sub> precursor:GPTMS ratios are summarized in Table 1. The excellent insulating properties of the films obtained from combustive  $AIO<sub>x</sub>$  precursors hybridized with an organic component are superior to those of the commonly used solution-processable high-*k* gate dielectric materials. For the comparison with the conventional combustive precursor,  $AIO<sub>x</sub>$  insulators were prepared by combustion with urea using a 1:8 ratio of the combustive precursor to GPTMS with annealing at 250 °C. Figure 3 shows typical current density vs. electric field and capacitance vs. frequency curves from the GPTMS-containing  $AIO_x$  films formed by self-combustion and combustion with urea, respectively. The dielectric constant of the  $AIO<sub>x</sub>$  insulators formed by combustion with urea is around 4.4, which is much lower than that of the  $AIO<sub>x</sub>$  formed by self-combustion. In addition, the layer formed by combustion with urea shows a poor leakage current density of  $2.80 \times 10^{-6}$  A

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 $\text{cm}^{-2}$ , which results from the sparse AlO<sub>x</sub> lattice structure. We believe that this structural defect originates from the formation of a flimsy M-O-M structure because not enough exothermic heat was supplied to the system owing to the low combustion efficiency of the combustive precursor with urea (Supplementary Information Fig.  $S3^{\dagger}$ ). To demonstrate the feasibility of the combustive  $AIO<sub>x</sub>$  precursor for forming a gate dielectric

layer, ZnO TFTs with a bottom-gate top-contact structure were fabricated from spin-coated ZnO semiconductors on AlO<sub>x</sub> gate dielectrics that were annealed at 250 °C in air. Al source-drain electrodes of 100 nm thickness were subsequently deposited on the ZnO channel layers. The electrical characteristics of the AlOx-based TFTs with semiconducting ZnO active layers were investigated. The thickness of ZnO channel layer was determined to be approximately 10 nm from cross-sectional transmission electron microscope (TEM) images of the ZnO TFT (Supplementary Information Fig. S4† ). To realize high field effect mobilities and high on/off current ratios from  $ZnO$  TFTs with  $AIO<sub>x</sub>$  gate dielectrics, an equivalent amount of fuel and oxidizer is necessary (Supplementary Information Fig.  $S5^{\dagger}$  and  $S6^{\dagger}$ ). When the content of fuel is higher than that of oxidizer or *vice versa*, the combustion process may not proceed to completion, resulting in a reduced M-O-M and an increased M-OH composition. Therefore, the  $AIO<sub>x</sub>$  thin films' chemical composition between oxidizer and fuel for the proper electrical performance of the corresponding TFTs was determined to be 1:1. Fig. 4 shows the output and transfer curves of ZnO TFTs with  $AIO_x$  gate dielectric layers prepared with a 1:8 ratio of the combustive  $AIO_x$ precursor to GPTMS. The field-effect mobility of  $ZnO$  TFTs with  $AIO<sub>x</sub>$  gate dielectrics prepared from combustive precursors with urea is  $2.48 \times 10^{-3}$  cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>, and their on/off ratio of 10<sup>4</sup>, with a large hysteresis. Such poor electric performance of  $ZnO$  TFTs with  $AIO<sub>x</sub>$  gate dielectrics prepared by combustion with urea can be attributed to insufficient dielectric properties of the

gate dielectric layer due to the low combustion efficiency of the precursors. In contrast, ZnO TFTs with  $AIO<sub>x</sub>$  gate dielectrics prepared from self-combustive precursors exhibit an improved field-effect mobility of 24.7 cm<sup>2</sup>  $\mathrm{V}^{-1}$  s<sup>-1</sup> and an on/off ratio of 10<sup>5</sup>. Furthermore, these ZnO TFTs show good operating stability and excellent mobility with good switching characteristics. The output curve of this ZnO TFT has clear pitch-off characteristics and good current saturation.

In addition, we investigated the device uniformity, which is of substantial importance for practical use of TFTs. To measure the uniformity of ZnO TFTs, 20 TFT units were fabricated with 1:8 ratios of the self-combustive  $AIO<sub>x</sub>$  precursor to GPTMS using an annealing temperature of 250 °C. The performance of the TFT units was measured, and standard deviations were obtained for the mobility ( $\mu = 23.1 \pm 5.22$  cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>) and turn-on voltage ( $V_{on} = 7.87 \pm 3.4$  V) (Fig. 4(e)). It was found that the ZnO TFTs with  $AIO<sub>x</sub>$  gate dielectrics prepared by selfcombustion exhibit good device uniformity and reproducible yields. The mobility, threshold voltage, and on/off ratio are summarized in Table 2. The mobility values for different selfcombustive AIO<sub>x</sub> precursor/GPTMS ratios are in the range of 8.97–24.7 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>, as calculated by extrapolating the capacitance values to a frequency of 1 Hz.<sup>16</sup> Although the ZnO TFTs with other self-combustive  $AIO_x$  precursor/GPTMS ratios operated properly, their output characteristics are unstable, as shown in Supplementary Information Fig.  $S7^{\dagger}$ .

To further investigate the nanocrystalline structures of the  $AIO<sub>x</sub>$  gate dielectrics prepared from combustive precursors, TEM analysis was performed. As shown in Fig. 5, no  $AIO_x$ nanocrystallites were present in films prepared by either combustion with urea or without combustion. However, the  $AIO_x$  nanocrystallites were uniformly embedded in the  $AIO_x$  gate dielectric layer prepared by self-combustion. All of the nanocrystallites possess a lattice structure, as shown in the TEM image in Fig. 5(a) (inset). We believe that the strong combustion reaction of the self-combustive precursor provides sufficient external heat to generate  $AIO<sub>x</sub>$ nanocrystallites, whereas the weak or absent combustion reactions of the other precursors do not generate enough energy to form crystal structures. The existence of the  $AIO<sub>x</sub>$  nanocrystalline lattice further proves that the bottom  $AIO<sub>x</sub>$  structure facilitates the vertical crystal growth of  $ZnO$ crystals when the self-combustive  $AIO<sub>x</sub>$  precursors are used. The crystal lattice mismatch between ZnO and  $AIO_x$  is reduced by the existence of nanocrystalline  $AIO_x$  beneath the ZnO layer, so this environment is more favorable for the vertical ZnO crystal growth. The surface morphology of the ZnO films grown on the  $AIO<sub>x</sub>$  films was analyzed using an atomic force microscope (AFM). Figure 6 shows the surface characteristics of the  $AIO<sub>x</sub>$  films with and without GPTMS prepared by self-combustion. The roughness of the gate dielectric affects the surface morphology of the channel layer in the bottom-gate TFTs. The root mean square (RMS) roughness value of the ZnO film on the  $AIO<sub>x</sub>$  film with GPTMS is 1.19 nm, which means that the surface is much denser and smoother than that of the ZnO film without GPTMS, which has an RMS roughness of 10.4 nm. The improved surface morphology of the ZnO films proves that there is a synergetic function of the  $AIO<sub>x</sub>$  gate dielectric with GPTMS that enhances the carrier mobility and overall performance of ZnO TFTs.

To investigate the possible reason for the significantly enhanced field-effect mobility of ZnO TFTs containing  $AIO<sub>x</sub>$  gate dielectrics produced from self-combustive precursors at low annealing temperatures, the crystallinity of the  $ZnO$  deposited on the obtained  $AIO<sub>x</sub>$  layer was analyzed using the thin-film diffraction method (Fig. 7). For comparison,  $AIO<sub>x</sub>$  gate dielectrics were prepared from combustive and noncombustive  $Al(C_2H_5O_2)$ <sub>3</sub> and  $Al(NO_3)$ <sub>3</sub>·9H<sub>2</sub>O precursors with GPTMS ratios of 1:8 at 250 °C, respectively. The ZnO films with  $AIO<sub>x</sub>$  gate dielectrics prepared using these combustive and noncombustive precursors show amorphous-like diffraction

patterns with no significant diffraction peaks, whereas relatively strong polycrystalline peaks of ZnO are clearly apparent in the ZnO films on  $AIO<sub>x</sub>$  gate dielectrics prepared from selfcombustive precursors. Interestingly, the intensity of the (002) peak is relatively stronger than that of the (100) and (101) peaks in the ZnO film prepared on the  $AIO<sub>x</sub>$  gate dielectric prepared from the self-combustive precursor, indicating that the direction of the ZnO crystals is preferentially with the *c*-axis oriented along the surface of the  $AIO<sub>x</sub>$  gate dielectric. The preferential orientation of the (002) direction indicates vertical growth of the ZnO crystals, which is advantageous for the enhancement the TFT mobility because it provides a favorable charge conduction pathway. It is believed that the more crystalline nature of the  $AIO<sub>x</sub>$  layer due to the additional heat supplied by the self-combustive precursors can affect the microstructure of the adjacent ZnO layer (Supplementary Information Fig. S8† ), as compared to the case for the layers produced from the combustive precursor with urea or the noncombustive precursor. During the annealing of the ZnO layer, the more crystalline structure of the  $AIO<sub>x</sub>$  prepared from selfcombustive precursors beneath the ZnO layer can reduce the amount of interruption of the crystal formation due to lattice mismatching, which ultimately leads to the more crystalline character of the grown  $ZnO$ <sup>25-28</sup> Consequently, the self-combustion reaction of the AlO<sub>x</sub> precursors results in a more crystalline structure of the final  $AIO<sub>x</sub>$  dielectric annealed at a low temperature, which yields a favorable semiconducting ZnO crystal structure for good TFT characteristics.

### **3. Conclusions**

We successfully fabricated an  $AIO<sub>x</sub>$  gate dielectric using a self-combustion reaction in a solution process at a low temperature of 250 °C. The self-combustion of the  $AIO<sub>x</sub>$  precursors was investigated in terms of their thermal behaviors, and the combustive  $AIO<sub>x</sub>$  precursors were

converted into high-*k* AlO<sub>x</sub> gate dielectrics at low temperatures by the self-combustion reaction. In addition, organic components (GPTMS) were incorporated into the soluble  $AIO<sub>x</sub>$  precursors using the sol-gel method to improve the surface properties, which can provide a well-defined interface between the gate dielectric and the active layer. The electrical characteristics of the  $AIO<sub>x</sub>$  gate dielectrics with various amounts of GPTMS were analyzed and compared. The dielectric constants and capacitances of  $AIO<sub>x</sub>$  gate dielectrics with  $AIO<sub>x</sub>$  precursor:GPTMS ratios higher than 1:8 are unstable in the low- and high-frequency ranges. The  $AIO<sub>x</sub>$  gate dielectric layer prepared using a 1:8 ratio of combustive  $AIO<sub>x</sub>$  precursor to GPTMS exhibits quite stable insulating properties with a leakage current density of  $1.80 \times 10^{-8}$  A cm<sup>-2</sup>. Furthermore, the AlO<sub>x</sub> dielectrics prepared from self-combustive precursors exhibit a high dielectric constant of 8.7. In comparison, the layer prepared from the combustive  $AIO<sub>x</sub>$  precursor composed of  $Al(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O$  and urea as an additional fuel, which has been studied previously, shows quite unsatisfactory dielectric properties and a high leakage current due to the low combustion efficiency. The ZnO TFTs with high- $k$  AlO<sub>x</sub> prepared from self-combustive precursors show an excellent field-effect mobility of 24.7 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and an on/off ratio of 10<sup>5</sup>. Such significant enhancement of the mobility of ZnO TFTs due to the vertical growth of the ZnO crystal structure can be attributed to the crystalline structure of the  $AIO<sub>x</sub>$  gate dielectric prepared from the combustive precursors beneath the ZnO layer. This crystallinity was promoted by the heat provided by the combustion reaction. Hence, these electrically stable  $AIO<sub>x</sub>$  gate dielectrics prepared using low-temperature processing demonstrate the potential application of the proposed system in the field of flexible printed electronics.

### **Instrumentation**

A TG-DTA (SDT 2060, TA Instruments, USA) was used to measure the thermal behaviors of the AlO<sub>x</sub> precursors, which were dried at 70 °C for 24 h. Samples of about 10 mg to 15 mg were heated from room temperature to 700 ℃ at a heating rate of 10 ℃/min in ambient air in all cases. An X-ray diffractometer (XRD; RIGAKU Co., Japan) was used to identify the crystal structures of the ZnO thin films coated on the  $AIO<sub>x</sub>$  gate dielectrics and  $AIO<sub>x</sub>$  thin films. XRD measurements were performed using the thin film diffraction technique, in which samples were fixed at low angle of 3° to the X-ray beam during the 2θ scan of the detector. The source power was 60 mA/40 kV, which is stronger than the power used in normal XRD. To investigate the nanocrystalline structure of the AlO<sub>x</sub> film, TEM (JEOL Ltd., Japan) was employed. The TEM samples were prepared by spin-coating the  $AIO<sub>x</sub>$  precursor onto a copper grid. The prebaking and annealing process were conducted in the same manner used for MIM device fabrication described below. The cross sections of the ZnO TFTs were also investigated by TEM. The crosssectional samples were cut by a focused ion beam (JEOL Ltd., Japan). The surface morphology of the  $AIO<sub>x</sub>$  films and ZnO thin films were investigated using a SEM (Philips XL30S FEG, Netherlands) and AFM (Nanoscope System Co., Korea) in an area of 3  $\mu$ m<sup>2</sup> in noncontact mode. Before the analysis of the films, platinum films with a thickness of approximately 100 nm were deposited on the prepared films with a coating machine (E-1030, Hitachi Ltd., Japan).

### **Preparation of self-combustive and noncombustive AlOx precursor solutions**

For the combustive  $AIO_x$  precursor solutions, aluminum nitrate nonahydrate  $(AI(NO_3)_3.9H_2O,$ 99.997%) and aluminum acetylacetonate  $(AIC<sub>2</sub>H<sub>5</sub>O<sub>2</sub>)<sub>3</sub>$ , 99%) were dissolved in anhydrous 2methoxyethanol. All chemicals for this procedure were obtained from Sigma-Aldrich. The

noncombustive  $AIO_x$  precursor solutions were prepared with equivalent concentrations of 0.05 M Al(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O and 0.05 M Al(C<sub>2</sub>H<sub>3</sub>O<sub>2</sub>)<sub>3</sub> in 10 ml of anhydrous 2-methoxyethanol. The solutions were thoroughly stirred for more than 2 h at room temperature to prepare the homogeneous precursor solution. The total concentration of  $AIO<sub>x</sub>$  precursor solution was fixed at 1 M. To improve the surface properties of the  $AIO<sub>x</sub>$  films, GPTMS solutions were added into the  $AIO<sub>x</sub>$  precursor solutions in various molar ratios. The GPTMS solutions were prepared by hydrolyzing GPTMS in 2-propanol by adding a dilute aqueous solution of  $HNO<sub>3</sub>$  and stirring at room temperature for 1 h.<sup>29</sup> For the comparison of self-combustive AlO<sub>x</sub> precursor solutions with noncombustive precursors for  $AIO<sub>x</sub>$  gate dielectrics, two types were prepared by separately dissolving 1 M Al(NO<sub>3</sub>)<sub>3</sub>.9H<sub>2</sub>O and 1 M Al(C<sub>2</sub>H<sub>5</sub>O<sub>2</sub>)<sub>3</sub> in 10 ml of anhydrous 2-methoxyethanol.

### **Preparation of urea-combustive AlOx precursor solutions**

The combustive precursor solutions with urea were prepared as described in the literature.<sup>22</sup>

### **Preparation of ZnO precursor solutions**

For the ZnO precursors, 0.1 M zinc hydroxide  $(Zn(OH)_2, 98\%$ , Junsei Co., Japan) was dissolved in 10 ml of ammonium hydroxide (NH<sub>4</sub>OH, ~20%, OCI Co., Korea) and stirred for less than 24 h in an ice bath.  $30$ 

### **Fabrication of MIM structures**

To measure the electrical characteristics of the  $AIO<sub>x</sub>$  gate dielectric films, MIM structures consisting of Au/AlO<sub>x</sub>/indium-tin-oxide (ITO) layers were fabricated under various conditions. The  $AIO<sub>x</sub>$  precursor solutions were deposited on ITO-coated glass substrates using a spin-coating method. Prior to deposition, the ITO-coated glass substrates were consecutively cleaned by ultrasonication in a detergent solution, acetone, and hot isopropyl alcohol. The ITO-coated glass substrates were then dried and treated with ozone plasma for 20 min. The  $AIO<sub>x</sub>$  precursor solutions were spin-coated at 4000 rpm for 30 s and prebaked on a hot plate at 130  $\degree$ C for 10 min to remove the residual solvent. After prebaking, the  $AIO<sub>x</sub>$  films were annealed at 250 °C for 1 h in a vacuum oven. The circular electrodes (100 nm thickness, Au) were thermally evaporated through a patterned metal shadow mask. The area of the circular electrode was  $25 \text{ mm}^2$ .

### **Fabrication and electrical measurement of ZnO TFTs**

A bottom-gate top-contact design was used as a typical device structure for the ZnO TFTs.  $AIO<sub>x</sub>$ precursor solutions were spin-coated and annealed on ITO in the same manner described above to form a gate dielectric layer. The ZnO precursor solutions were then deposited on the obtained  $AIO<sub>x</sub>$  gate dielectric using a spin-coating method. Prior to deposition, the  $AIO<sub>x</sub>$  gate dielectric was treated with ozone plasma for 20 min. The ZnO precursor solutions were spin-coated at 1000 rpm for 45 s and prebaked on a hot plate at 90 °C for 5 min to remove any residual solvents. After pre-baking, the ZnO thin films were annealed at 250 °C for 1 h on a hot plate in ambient air. The top contact source and drain electrodes (120 nm thickness, Al) were thermally evaporated through a patterned metal shadow mask. The ratio of the channel width to length was 60 (width: 3000 µm; length: 50µm).

The electrical characteristics of the ZnO TFTs were measured in air using an Aglient semiconductor parameter analyzer 4155C. The measurements were typically performed using a continuous method, and the transfer curve was recorded before the output curve. The field-effect

mobility  $(\mu_{lin})$  and the threshold voltage  $(V_{th})$  were calculated for the linear region using Equation 1,

$$
I_{\rm D} = \frac{W}{L} \mu_{\rm lin} C_{\rm i} \quad (V_{\rm G} - V_{\rm th}) V_{\rm ds} \tag{1}
$$

where  $I_D$  is the saturation current,  $C_i$  is the capacitance per unit area of the dielectric,  $V_G$  is the source-gate voltage, and *W* and *L* are the TFT channel width and length, respectively.

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**Scheme 1** Schematic diagram of the  $AIO<sub>x</sub>$  gate dielectric film prepared by self-combustion of metal precursors mixed with organic compounds.



**Fig. 1** TG-DTA curves of combustive and noncombustive precursors: (a) self-combustive AlO<sub>x</sub> precursor, (b) combustive precursor with urea, (c) noncombustive  $Al(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O$ , and (d) noncombustive  $\text{Al}(C_2H_5O_2)_3$ .



**Fig. 2** Atomic force microscope (AFM) step profiler images and surface scanning electron microscope (SEM) images of  $AIO<sub>x</sub>$  thin films prepared by (a, b) self-combustion and (c, d) combustion with urea.



**Fig. 3** (a) Current density vs*.* electrical field and (b) dependence of capacitance on the frequency for  $AIO<sub>x</sub>$  thin films with GPTMS prepared by self-combustion and combustion with urea.





**Fig. 4** (a) Transfer and (b) output characteristics of the ZnO TFTs fabricated from  $AIO<sub>x</sub>$  gate dielectrics with 1:8 ratios of the self-combustive  $AIO<sub>x</sub>$  precursor to GPTMS. (c) Transfer and (d) output characteristics of the ZnO TFTs fabricated from  $AIO<sub>x</sub>$  gate dielectrics with 1:8 ratios of the  $AIO<sub>x</sub>$  precursor with urea to GPTMS. (e) Device uniformity of the ZnO TFTs fabricated from  $AIO<sub>x</sub>$  gate dielectrics prepared with the self-combustive  $AIO<sub>x</sub>$  precursor.



**Fig. 5** TEM images of the  $AIO_x$  gate dielectric films prepared from the (a) self-combustive  $AIO_x$ precursor with GPTMS (with insets showing an  $AIO<sub>x</sub>$  nanocrystallite in the film and diffraction pattern), (b) combustive  $AIO_x$  precursor with urea with GPTMS, (c) noncombustive Al(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O precursor with GPTMS, and (d) noncombustive Al(C<sub>2</sub>H<sub>5</sub>O<sub>2</sub>)<sub>3</sub> AlO<sub>x</sub> precursor with GPTMS.



Fig. 6 AFM images of ZnO films on AlO<sub>x</sub> thin films (a) without GPTMS and (b) with GPTMS.



**Fig. 7** XRD patterns of the ZnO thin films on AlO<sub>x</sub> gate dielectrics prepared from three different precursors after annealing at  $250^{\circ}$ C: (a) self-combustive AlO<sub>x</sub> precursor with GPTMS, (b) combustive  $AIO_x$  precursor with urea with GPTMS, (c) noncombustive  $AI(NO_3)_3.9H_2O$  with GPTMS, and (d) noncombustive  $Al(C_2H_5O_2)$ <sub>3</sub> with GPTMS.





<sup>a</sup> Extrapolated to a frequency of 1 Hz



 $\overline{\phantom{a}}$ 

Table 2 Characteristics of ZnO TFTs fabricated from AlO<sub>x</sub> gate dielectrics with different amounts of GPTMS

combustive precursor	molar ratio (AlO <sub>x</sub> ) precursor: GPTMS)	a mobility $\left(\text{cm}^{2}\text{ V}^{-1}\text{ s}^{-1}\right)$	threshold voltage $(V)$	on/off ratio
self- combustive	1:0		N/A	
	1:8	24.7	6.35	$1.71E + 05$
	1:30	13.9	8.14	$2.35E + 05$
	1:45	8.97	8.32	$4.07E + 05$
combustive with urea	1:8	$2.48 \times 10^{-3}$	7.11	$1.00E + 04$

<sup>a</sup>Calculated from capacitance values extrapolated to a frequency of 1 Hz

The Table of Contents Entry

## **Soluble Oxide Gate Dielectrics Prepared Using the Self-Combustion Reaction**

### **for High-Performance Thin-Film Transistors**

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ToC figure.

