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## Cracked polymer templated metal network as transparent conducting electrode for ITO-free organic solar cells

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**We report a highly transparent, low resistant Ag metal network templated from a cracked polymer thin film and its incorporation in an organic solar cell. The performance of this scalable metallic network is comparable to that of conventional ITO electrode. This is a general approach to replace ITO in diverse thin film devices.**

Organic solar cells (OSCs) are an attractive option for large area and inexpensive production of modules. This is because OSCs are well suited to low cost manufacturing due to simple processing<sup>1</sup> steps that are easily translated to roll-to-roll mass production<sup>2</sup> and leading to near commercialization. In the recent past, extensive research has been carried out on the active semiconductor ingredients<sup>3</sup> and electrode optimization<sup>4</sup> in order to improve the OSC performance,<sup>5</sup> which is typically poorer compared to other types of solar cells such as dye sensitized solar cells<sup>6</sup> and silicon based solar cells.<sup>7, 8</sup> Nonetheless, OSCs occupy unique position in photovoltaic roadmap as solid state, flexible, environmentally benign and ultralight large area devices.<sup>9</sup>

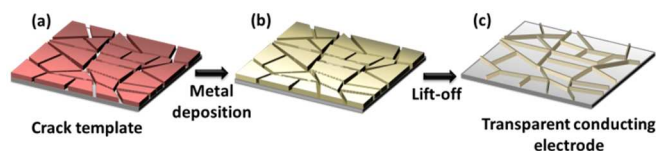
In OSCs, indium tin oxide (ITO) is the most commonly used transparent conducting electrode. Best ITO films exhibit transmittance of 92% in the visible region and a sheet resistance of 10  $\Omega/\square$ .<sup>10</sup> Indium is scarce and expensive;<sup>11</sup> ITO requires high temperature processing and is brittle, develops cracks on flexible substrates.<sup>12</sup> In order to address these issues, there has been much effort in the literature towards alternative electrodes. Graphene,<sup>13</sup> carbon nanotubes (CNT),<sup>14, 15</sup> and Ag nanowires<sup>16, 17</sup> have been proposed as alternatives to ITO. Graphene has outstanding optical properties, but limited sheet resistance of 30  $\Omega/\square$ .<sup>13</sup> The CNT networks also exhibit high sheet resistance and in addition, are less stable in the ambient.<sup>14, 18</sup> Ag nanowire networks show relatively superior performance in terms of transmittance and sheet

resistance.<sup>17</sup> but like any networks produced from pre-synthesized nanowires (tubes), they suffer from contact resistance at innumerable crossbar junctions and high roughness.<sup>19, 20</sup> Further, the redundant wire/tubes in the network can short the OSCs, which restricted its use in roll-to-roll fabrication. Often, they require an extra treatment to improve the performance, such as mechanical pressing,<sup>21</sup> thermal treatment<sup>20</sup> or poly(3,4-ethylenedioxythiophene) poly(styrenesulfonate) (PEDOT:PSS) coating.<sup>19</sup>

Metal grids offer attractive alternatives to the above TCEs.<sup>22-30</sup> Indeed being free of junctions, they exhibit high performance in terms of transmittance and sheet resistance, and have been successfully used in optoelectronic devices including OSCs. Typically, they are produced by patterning using lithographic techniques such as photolithography,<sup>22</sup> soft lithography,<sup>23</sup> phase shift lithography<sup>24</sup> and nanoimprint lithography.<sup>25</sup> A technologically relevant and simple alternative is the direct printing of grids from silver nanoparticle inks either by ink-jet printing<sup>26,27</sup> or laser printing<sup>27,28</sup> or flexographic and thermal imprinting<sup>30</sup>. These recipes for Ag grids result in relatively large feature sizes. Such printed patterns are often used as current collecting grids in diverse applications.

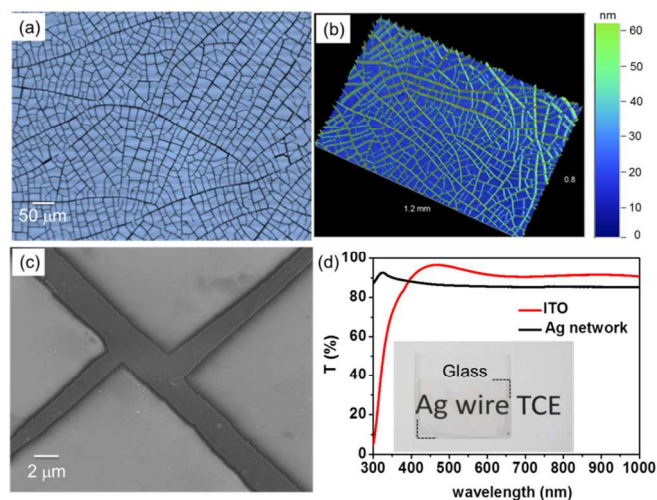
Recently, metal meshes have been fabricated based on cracked TiO<sub>2</sub> templates and their application for such fabricated metal meshes in touchscreens were shown.<sup>31,32</sup> Some of us<sup>33</sup> have shown very recently selective deposition of Cu by electroplating in the cracked regions of a polymer template to produce Cu mesh based TCE. These cracked template methods are highly scalable but need to be tested for their applicability and device integration in thin film organic devices such as OSC, OFET etc. One of the basic challenges is to obtain high light transmission maintaining low sheet resistance for such TCEs. Additionally, in thin film devices, the uniformity and connectivity of such meshes are very critical for the reproducibility of such devices.

In this article, we have explored the feasibility of cracking as a tool for the preparation of such metal meshes from inexpensive polymer templates at room temperature and integrating them as TCEs in bulk heterojunction OSCs. Specifically, using seamless Ag network in the form of a mesh as a replacement for ITO electrodes, several OSCs have been made in inverted geometry, which exhibited performance comparable to those produced with ITO. We like to note that this work emphasizes the suitability and reproducibility of such an ITO-free TCE in OSC and not specifically the optimization of efficiency of any kind of device.



**Fig. 1** Schematic illustration of transparent conducting electrode fabrication (a) Cracked template (b) deposition of metal on cracked template and (c) lift-off of the template giving rise to Ag network based TCE.

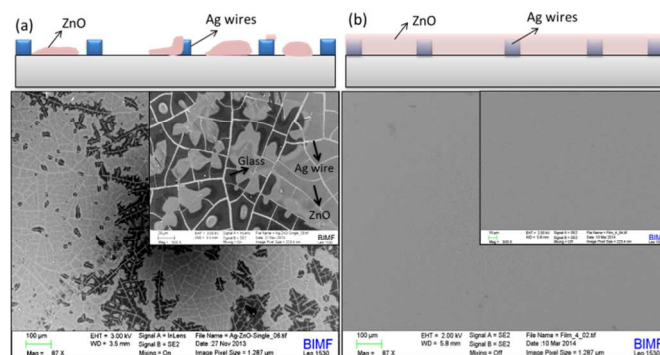
The process flow for templating is shown schematically in Fig. 1. First, a crack layer is produced by drying an acrylic based colloidal dispersion on glass substrate wherein highly interconnected cracks are spontaneously obtained. These are U-shaped grooves are complete cracks down to the substrate to be distinguished from incomplete cracks.<sup>34</sup> Using the cracked template, metal (Ag, 55 nm) is deposited by vacuum evaporation and subsequently, the template is washed away in chloroform.



**Fig. 2** Characterization of Ag network derived TCE: (a) Optical microscope image (transmission mode), (b) optical profiler image in 3D view, (c) SEM image of a network junction and (d) comparison of transmittance spectra of Ag metallic network and ITO. Inset shows the photograph of Ag network TCE on glass substrate.

As shown in Fig. 2a, the Ag mesostructures on glass appear well interconnected throughout the network. The metal fill factor is estimated to be  $\sim 20\%$  with structural width of  $\sim 2 \mu\text{m}$  and average cell size (spacing between the Ag structures) of 20 to 60  $\mu\text{m}$ . The optical profilometric image ( $1.2 \times 0.8 \text{ mm}^2$ ) in Fig. 2b not only reveals the connectivity of the Ag network over large area but also shows its seamless nature. The surface roughness of the network is

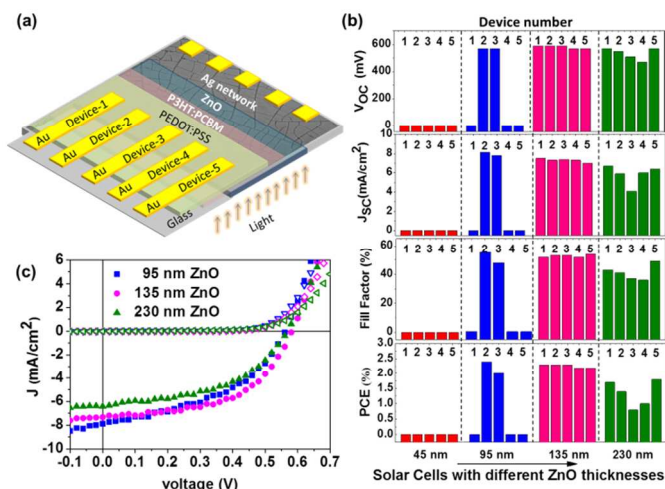
estimated to be  $\sim 5 \text{ nm}$  while the peak to valley roughness, which corresponds to network thickness, is 55 nm. The SEM image in Fig. 2c shows a junction where the network surface continues to be smooth and the junction itself is seamless unlike crossbar junctions commonly seen in network TCEs made from pre-synthesized Ag nanowires.<sup>21</sup> Accordingly, the sheet resistance was found to be  $\sim 10 \text{ ohm}/\square$ , which is rather low considering the thickness of only 55 nm. The transmittance of Ag network was  $\sim 86\%$  at 550 nm (see Fig. 2d) and the overall transmittance is comparable to that exhibited by ITO but importantly, extends down to UV region (see photograph in the inset). ITO, on the other hand, is a good UV absorber.



**Fig. 3:** SEM images and schematic illustrations of Ag network TCE covered with ZnO for layer thickness of (a) 45 nm and (b) 135 nm. For more SEM images, see supporting information, Fig. S1.

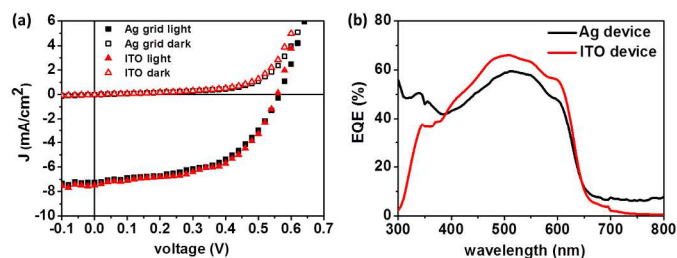
In the next step of solar cell fabrication, ZnO barrier layer was brought in by decomposing zinc acetate film at 150 °C in air (see supporting information for experimental details).<sup>35</sup> For a proposed thickness of 45 nm of ZnO, the obtained layer was non-uniform and discontinuous (Fig. 3a) as evident from the dark and grey regions, the latter corresponding to ZnO covered network. Bailey et al. described previously the fabrication of a Ag nanowire-ZnO nanoparticle composite to obtain semi-transparent top electrode and achieved a good power conversion efficiency of 4.3% in PBDTPD/PC<sub>70</sub>BM devices.<sup>36</sup> Based on this fact, we increased the thickness of the ZnO layer to 95 nm, 135 nm and 230 nm. We observed increasing degree of covering of the metal network with increasing thickness of ZnO. For example, in the SEM image of 135 nm ZnO layer shown in Fig. 3b, the ZnO film is seen to be uniformly submerging the entire Ag network. This is found to be the optimal ZnO thickness required to submerge the 55 nm thick Ag network so as to have reproducible devices without shorting.

Four bottom-illuminated inverted photovoltaic cells (5 devices in each) consisting of poly-3-hexylthiophene (P3HT) and phenyl-C61-butiric acid methyl ester (PCBM) were fabricated on a glass substrate as shown in the schematic in Fig. 4a with Ag network derived TCE. The bar graphs of solar cell parameters ( $V_{oc}$ ,  $J_{sc}$ , FF and  $\eta$ ) for all the four cells with varying thicknesses of ZnO barrier layer are shown in Fig. 4b. In cell 1, all five devices with 45 nm of ZnO layer were defective due to shorting. For cell 2 with 95 nm ZnO layer, only two devices were working with moderate efficiencies. On the other hand, for cells 3 and 4 with 135 and 230 nm ZnO barrier layer thicknesses respectively, all five devices were found to be functioning. Significantly, with 135 nm ZnO (cell 3), the five devices showed uniform performance with average efficiency of  $2.26 \pm 0.05\%$  while those from 230 nm ZnO, exhibited some variation in performance with lower efficiency ( $1.34 \pm 0.43\%$ , see Fig. 4b).



**Fig. 4** (a) Schematic illustration of the inverted P3HT-PCBM solar cell with Ag network TCE, (b) bar graph of cell parameters for solar cells of different ZnO layer thicknesses (45, 95, 135 and 230 nm), (c) examples of J-V characteristics of solar cells with different ZnO layer thicknesses in dark (open symbols) and in light (filled symbols). Note that best performing solar cell is chosen from each cell.

The decrease in the cell efficiency for higher ZnO thicknesses can be attributed to higher series resistance of the cell leading to lower current density and fill factor values (see Fig. 4b).<sup>35</sup> Indeed, the variations in solar cell parameters among the five devices exhibit similar trend. The J-V characteristics of typical solar cells with different ZnO layer thicknesses (95 nm, 135 nm, 230 nm) are shown in Fig. 4b (see supporting information Fig. S2 for J-V characteristics of all working devices).



**Fig. 5** Comparison of (a) J-V characteristics and (b) EQE for optimized Ag network and ITO devices.

In Fig. 5a, we compare the J-V characteristics of a typical device fabricated using Ag network (from cell 3) with that produced using ITO as TCE (ITO:  $T = 93\%$  and  $R_s = 16 \text{ ohm}/\square$ ). The Ag network cell has an optimum thickness of 135 nm of ZnO. For ITO device, the thickness of ZnO blocking layer was kept at 45 nm which is optimum for an inverted solar cell.<sup>35</sup> All other process parameters such as active layer thickness, top electrode, annealing temperature etc. were kept the same for all the devices. It is clear from the plot that the Ag network TCE based solar cell follows a similar trend as the ITO based cell and the derived parameters are quite comparable (see Table 1). Thus, the efficiency of Ag network TCE based solar cell was 2.14%, while that obtained for ITO based cell under similar conditions in ambient was 2.27%.

TCE	Voc (v)	Jsc (mA/cm <sup>2</sup> )	FF (%)	N (%)	Rsh $\Omega/\text{cm}^2$	Rs $\Omega/\text{cm}^2$ @ Voc	Rs $\Omega/\text{cm}^2$ @ Pmax
Ag grid	0.57	7.2	51.8	2.14	421	8.3	49.7
ITO	0.55	7.5	55.2	2.27	413	6.7	48.6

**Table 1** Summary of the solar cell parameters corresponding to J-V characteristics shown in Figure 5a. Note, that Ag grid device has a 135 nm thick ZnO layer, whereas ITO device has a ZnO layer of only 45 nm.

These results presented here clearly demonstrate the potential of Ag network based TCE as an alternative to ITO in OSCs. However, a slightly thicker ZnO layer is required to cover the Ag network thickness. Since the ZnO is filled in the interstitial domains between the metallic network, it improves the charge collection properties, which is similar to coating a PEDOT:PSS layer as reported for conventional metal grids.<sup>19</sup> The external quantum efficiency (EQE) measurements (Fig. 5b) show that the Ag network TCE based cell exhibits slightly less values in the visible region compared to those derived from ITO cell, but in the UV region, the former excels which makes the overall performance similar for both cells. This can be clearly understood from the differences in transmission of the respective electrodes in the UV region (Fig. 2d). The same information can be inferred on comparing the absorption spectra of the two solar cells (supporting information Fig. S3) with their EQE spectra.

The method developed in this study for TCE and associated OSC fabrication has several merits. While the present study has focused on only one type of metallic network, its scope of application can be easily extended to other thin layer devices by varying the template thickness and other parameters in the initial stages of crack template formation. Thus, it is possible to obtain TCEs with different network thicknesses and connectivity and importantly with different metals. Here, Ag was used as a typical example since the work function is favorable for inverted geometry P3HT-PCBM solar cell. However, it can be replaced with other metals such as Cu, Al, Pt depending on the need of application. The metal network based TCE used in the present study works as current collecting grid at tens of micrometer scale with a metal fill factor of 20%.

## Conclusions

In conclusion, we demonstrated the incorporation of a highly transparent and highly conducting Ag metallic network obtained using a cracked polymer template, in a thin film organic solar cell for the first time. The performance of this highly scalable metal network as a TCE is comparable to that of conventional ITO electrode. This is a general approach to replace ITO in diverse thin film devices. The crack template approach is universal for any type of metal or substrate material.

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## Notes and references

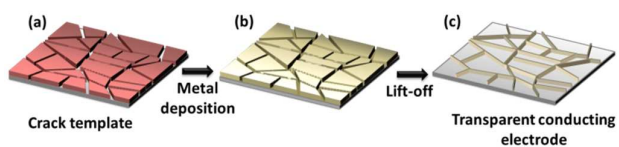
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† Electronic Supplementary Information (ESI) available: Experimental details of device preparation, additional SEM and J/V curves of devices. See DOI: 10.1039/c000000x/

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