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COMMUNICATION

Self-organizing Ge(111)/Al/glass structures through layer exchange in metal-induced crystallization

K. Toko,*^a K. Nakazawa,^a N. Saitoh,^b N. Yoshizawa,^b and T. Suemasu^a

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We achieved the self-organization of Ge/Al/glass structures by using Al-induced layer exchange at 325 °C. Diffusion-limiting layers formed between Ge and Al significantly induced slow growth under thermal equilibrium conditions, and thus allowed for fabricating an energetically-stable (111)-oriented Ge layers with grains as large as 20 µm in size.

Germanium has been used in electronic optical devices such as photodiodes or tandem solar-cells because of its narrow band gap (0.66 eV) and large absorption coefficient (\sim 10⁴ cm⁻¹ at 0.8 eV).^{1,2} Moreover, (111)-oriented Ge acts as epitaxial templates for group III-V compound semiconductors, silicide materials, and aligned nanowires, used for light-emitting devices.³⁻⁵ Thus, Ge-based photonic technology is a powerful candidate for advanced next-generation devices. The expensiveness of bulk-Ge substrates motivates researchers to substitute bulk-Ge substrates with Ge films on inexpensive glass (softening temperature: \sim 550 °C).⁶ Fabricating large-grained polycrystalline Ge (poly-Ge) on glass at low temperatures has been investigated using solid-phase crystallization, sputtering, and chemical vapour deposition; however, the resulting Ge layers consist of small, submicron grains with random orientations. $7-9$

 Al-induced crystallization (AIC), developed for fabricating polycrystalline Si on glass, 10^{-16} is a promising method for preparing large-grained Ge layers on glass at low temperatures.^{17–25} In previous studies, we fabricated a largegrained, (111)-oriented Ge on glass via exchanges between Ge and Al layers, by controlling the annealing temperature and the thickness of Al and Ge layers.^{26,27} Moreover, we promoted the growth in AIC by inserting a thin $(\sim1$ nm) Ge membrane below Al.²⁸ By combining this Ge insertion layer with a GeO_x interlayer, we lowered the crystallization temperature of a-Ge from 325 °C to 180 °C, an applicable temperature for flexible plastic substrates.²⁹

 Fabricating optical devices and characterizing its electrical properties both require a conducting layer under the semiconducting layer. To accomplish this, here we study inverted-AIC, beginning with an Al/amorphous Ge (a-Ge)/substrate structure, contrasting the a-Ge/Al/substrate structure used in normal AIC, as schematically shown in Fig. 1. By using inverted-AIC, we aim at the self-organization of a poly-Ge/Al/glass structure, i.e., the spontaneous formation of an Al lower electrode below the Ge layer, via the layer exchange between Ge and catalytic Al during annealing. Moreover, the inverted structure may allow us to suppress "hillocks" appearing the surface of normal $AIC-Ge$ ²⁷ As revealed by the previous studies on Si, the growth parameters, such as, thickness, annealing temperature, and appropriate interlayer between Al and semiconductor layers, are different between normal and inverted structures.³⁰⁻³³ Thus, this study investigates the inverted-AIC of a-Ge in detail. As a result, we achieve a large-grained $(\sim 20 \mu m)$, (111)-oriented Ge layer on an Al coated glass substrate.

 In the experiments, three kinds of samples were prepared on $SiO₂$ glass substrates as summarized in Table I. The Ge and Al layers were deposited at room temperature by using RF magnetron sputtering with Ar plasma. The deposition rate was 23 nm/min for Ge and 25 nm/min for Al. The degree of purity for the sputtering targets was 99.99% for Ge and 99.9% for Al. Argon pressure during the sputtering was 0.2 Pa. The RF power was set to 100 W. Sample A is a normal AIC sample corresponding to Fig. 1(a), prepared to examine the effect of the inverted structure. This sample has a top a-Ge layer (60 nm) and a bottom Al layer (50 nm). In this study, Ge is slightly thicker than Al to obtain the good surface coverage of Ge on the substrate. The interlayer is a natively oxidized Al (AlO_x) membrane with a few nm thickness, formed by exposing Al to air for 10 min. $26,27$

 Samples B and C are inverted-AIC samples corresponding to Fig. 1(b). These are prepared to obtain Ge/Al/glass structures, and are compared to optimize the interlayer between Ge and Al.

The two samples have a top Al layer (50 nm) and a bottom a-Ge layer (60 nm). Sample B has a natively oxidized Ge (GeO_x) interlayer formed by exposing Ge to air for 24 h. The thickness of the GeO_x is estimated to be about 1 nm according to the previous study.³⁴ Meanwhile, sample C has an AIO_x interlayer formed by exposing an Al layer (2 nm), prepared on Ge, to air for 10 min.

 The samples were then annealed at a temperature of 325 °C in N_2 until layer exchanges were finished: 100 h for sample A, 1 h for sample B, and 15 h for sample C. Because Al reacts readily with O, there is the possibility of reducing GeO_x and forming AIO_x in sample B during annealing. However, a previous study on AIC-Ge using a GeO_x interlayer proved the presence of GeO_x interlayer even after layer exchange.²⁰ This result allows us to examine validly the effect of the interlayer material by comparing samples B and C.

 The surface morphology of each sample was observed by using a scanning electron microscope (SEM) with an energydispersive x-ray spectrometer (EDX), where the electron acceleration voltage was 15 keV. The crystal quality was evaluated by using a micro-probe Raman scattering spectroscopy (spot size: 1 µm, wavelength: 532 nm) and a *θ*-2*θ* X-ray diffraction (XRD) measurement (spot size: 10 mm). The crystal orientations and the grain sizes of the resulting poly-Ge layers were characterized by electron backscatter diffraction (EBSD) analysis. Prior to the EBSD measurements, the surface of poly-Ge was cleaned using a diluted HF solution (1.5% HF) for 1 min. The cross-sectional structure and elemental composition were investigated by the analytical transmission electron microscope (TEM), FEI Tecnai Osiris operated at 200 kV, equipped with EDX and high-angle annular dark-field scanning transmission electron microscopy (HAADF-STEM) system with a probe diameter of \sim 1 nm. Cross-sectional TEM samples were prepared by conventional focused ion beam method.

 Fig. 2 shows the surface morphologies of annealed samples evaluated by using SEM and EDX, indicating significantly different structures among the samples. For sample A, its surface consists of Al and Ge as shown in Fig. $2(a)$ –(c). This is a representative structure obtained by layer exchange in normal AIC: the sample has low-quality Ge hillocks stacked on a highquality bottom-Ge layer.²⁷ For sample B, Fig. 2(d)–(f) show that Ge is present on the surface, demonstrating layer exchange between Al and Ge; however, the Ge layer is separated by Al and results in island shape. In contrast, for sample C, Fig. $2(g)$ – (i) show that Ge covers the entire region of the surface. The significant difference of the surface morphologies between samples B and C is discussed later. Note that strong Ge signals are partially visible in Fig. 2(e) and (h), corresponding to the bright areas in the SEM images shown in Fig. 2(d) and (g), respectively. The bright areas are not attributed to the outermost surface structures but to the inside structures, as determined by the following cross-sectional STEM-EDX analysis.

 Fig. 3(a)–(d) demonstrate that Ge is present on the surface of the annealed sample C, while the bottom layer consists of Al and Ge. The strong Ge signals in Fig. 3(e) and (h) are likely attributed to these Ge/Ge structures corresponding to lowquality hillocks, formed on the sample surface in normal AIC.^{11,13,27} Fig. 3(a)–(d) also demonstrate an AlO_x diffusionlimiting layer remained between the top and bottom layers, suggesting that the formative mechanisms of the top and bottom Ge are completely different. Fig. 3(e) shows the formative mechanism of the hillocks. First, Ge atoms diffuse into Al through the interlayer during annealing. When the Ge

concentration in Al is supersaturated, Ge nucleation occurs.¹² The Ge crystals grow laterally due to the continuous supply of Ge atoms, and then push Al down to the bottom a-Ge layer, 13 making an eutectic system consisting of Al and Ge. In the eutectic system, Ge crystals precipitate with exhaling Al, resulting in the Ge hillocks formed on the bottom layer. Suppressing hillocks in the bottom layer, i.e., fabricating a uniform Ge on the surface is an advantage of inverted-AIC over normal-AIC.

 Now, the difference of the surface morphology between samples B and C is discussed from the perspective of the hillock formation. The cracks in sample B correspond to Al between Ge islands as shown in Fig. $2(d)$ –(f), meaning the insufficient coverage of the top Ge. Previously we found that GeO_x is weaker than AIO_x in terms of a diffusion barrier layer.²⁹ As seen in Fig. 2(e), sample B has relatively the large number of bottom hillocks compared to sample C. Considering the formative mechanism of hillocks presented in Fig. 3(e), the high incidence of hillocks in sample B is likely owing to the fast Al diffusion to Ge. Thus, this reduced the volume of the top Ge layer, resulting in the insufficient coverage of the top Ge layer in sample B.

 The crystal quality of the samples after annealing was evaluated by using Raman spectroscopy and a *θ*-2*θ* XRD measurement. Before measurements, the surface Al layer of sample A was removed by using a diluted HF solution (1.5%) HF). The Raman spectra in Fig. 4(a) show the peaks near 290 $cm⁻¹$ for all samples. These peaks, especially the peak in sample B, shift to lower wavenumbers compared to the actual Ge optical phonon peak (300 cm^{-1}) .⁵ Although these large wavenumber shifts are not completely understood, presence of these peaks warrants the crystallization of Ge.

 The *θ*-2*θ* XRD patterns in Fig. 4(b) show peaks at 27.2° for all samples, corresponding to the (111) plane in relaxed Ge, while other peaks do not appear. Considering the peak intensity ratio in Ge powder diffraction patterns (JCPDS 04-0545), the large XRD peaks in samples A and C indicate the preferential (111) orientation of Ge. In contrast, the weak XRD peak in samples B suggests the non-orientation and/or low crystallinity of Ge, which is consistent with the relatively broad peak in the Raman spectra of sample B shown in Fig. 4(a).

 Using EBSD measurement, we characterized the detailed crystal orientation and the grain size of Ge for samples A–C. Fig. 5 shows the EBSD images in the normal direction (ND) and the transverse direction (TD) to the sample surface. For sample A, Fig. 5(a) and (b) indicate that small-grained, randomly-oriented Ge hillocks are scattered on a large-grained, (111)-oriented bottom-Ge layer. For sample B, Fig. 5(c) and (d) obtained at a Ge island, indicate submicron Ge grains with nearly random orientation. In contrast, for sample C, Fig. 5(e) and (f) indicate the highly (111) orientation and large grains in Ge. By using EBSD analysis, the (111) orientation fraction was calculated to be 96% , and the average grain size nearly 20 μ m for sample C. In the normal AIC structure, the sample with GeO_x interlayer provided faster growth and smaller grain size compared to the sample with AIO_x interlayer.²⁹ The previous result is consistent with the present study on inverted AIC. The significant difference of the crystal orientation and grain size between samples B and C is attributed to the difference of Ge diffusion rates in the interlayers. The diffusion coefficient of Ge in AlO_x is two orders lower than that in GeO_x ²⁹ accounting for the different crystallization times between sample B (1 h) and sample C (15 h). We consider that the slow annealing induced Ge nucleation under thermal equilibrium conditions, resulting

in the energetically stable (111) plane pronounced in sample $C^{14,26,35}$ Employing AlO_x interlayer limits the diffusion of Ge to Al, and thus allows for fabricating the highly (111)-oriented Ge layer.

 Fig. 6(a) shows a representative cross-sectional bright-field TEM image of sample C, indicating the stacking structure of crystalline Al and Ge containing no obvious defects. Such a low-defect Ge is likely owing to the slow growth providing thermal equilibrium conditions. High resolution TEM image in Fig. 6(b) and a fast Fourier transform (FFT) analysis of the image in Fig. $6(c)$ demonstrate amorphous AIO_x remaining on the interface between crystalline Al and Ge layers. This result proves that the AIO_x membrane just works as a diffusion barrier; there is no epitaxial relationship between Ge and AIO_x . Fig. 6(d) shows a grain boundary observed in the upper Ge layer, likely attributed to the collision between lateral growth fronts. Some planar defects in Ge {111} planes such twin boundaries (TBs) and stacking faults (SFs) as shown in Fig. 6(e) and (f) were observed in the upper Ge layer. The lattice structure shown in Fig. 6(f), consisting of the large number of stacking faults, is not observed for poly-Ge formed by the solid-phase crystallization of a-Ge.⁷ According to Huang et al., the lattice structure possibly has a causal relationship with the formation of Al-Ge-O alloy during growth.^{36,37} Note that these planar defects are almost parallel to the substrate owing to the (111) orientation of Ge. Thus, the resulting Ge layer will be useful for an epitaxial template for fabricating low-defect semiconductor layers.

Conclusions

We achieved the self-organization of Ge on conducting layer coated glass substrates utilizing the layer exchange in AIC at a low temperature of 325 °C. Controlling the interlayer between Al and Ge, i.e., limiting the diffusion of Ge to Al, significantly influenced the resulting structures: employing AIO_x interlayer led to the uniform Ge layer with highly (111) orientation (96%) and large grains $({\sim}20 \mu m)$. The resulting structure holds promise for use as buffer layers for the epitaxial growth of Ge, as well as of group III–V compound semiconductors, nanowires, and other advanced materials.

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a Institute of Applied Physics, University of Tsukuba, Tsukuba, Ibaraki 305-8573, Japan.

b Electron Microscope Facility, TIA, AIST, 16-1 Onogawa, Tsukuba 305- 8569, Japan.

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Table I. Sample structures before annealing.

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Fig. 1. Schematics of the sample structures: (a) normal-AIC (sample A) and (b) inverted-AIC (samples B and C) of a-Ge. The Interlayers between Al and Ge are AlO_x or GeO_x, working as diffusion limiting layers.

Fig. 2. Surface images and elemental maps of Ge and Al for annealed samples: (a-c) sample A, (d-f) sample B, and (g-i) sample C. The surface images were obtained by SEM, and elemental maps by EDX.

 (a)

 (c)

Al

 (e)

 AI

Ge

Glass

Fig. 3. Cross-sectional STEM-EDX analysis of sample C after annealing. (a) STEM-HAADF image. (b–d) Elemental maps of (b) Ge, (c) Al, and (d) O corresponding to the region shown in (a). (e) Schematic of layer exchange and Ge hillock formation during inverted AIC.

Fig. 4. (a) Raman scattering spectra obtained at the Ge regions in samples A–C after annealing. (b) *θ*–2*θ* XRD patterns of samples A–C after annealing. The insertion shows magnified patterns around Ge (111) peaks (~27.2°).

Intensity (a.u.)

Intensity (a.u.)

(b)

Intensity (a.u.)

ntensity (a.u.)

(a)

Fig. 5. EBSD patterns in ND and TD of the Ge layers for samples (a,b) A, (c,d) B, and (e,f) C. The ND and TD maps correspond to the same region. Coloration indicates crystal orientation, according to the legend. Black solid lines in the TD maps correspond to random grain boundaries.

Fig. 6. Cross-sectional TEM images of sample C after annealing. (a) Representative bright-field image showing the stacking structure of large-grained Ge and Al. (b) High resolution image of the interlayer between crystalline Al and Ge layers with the magnified lattice image of Ge. (c) FFT pattern of (b). (d) High resolution image of a grain boundary in the Ge layer. (e,f) High resolution image of Ge{111} planar defects such as (e) TBs and (f) SFs. The white and black bars in the images indicate crystal planes.

Table of Contents Use

Self-organizing Ge(111)/Al/glass structures through layer exchange in metal-induced crystallization

Kaoru Toko,* Koki Nakazawa, Noriyuki Saitoh, Noriko Yoshizawa, Takashi Suemasu

Al-induced crystallization enabled the self-organization of high-quality Ge/Al/glass structures through layer exchange at a low temperature of 325 °C.