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ARTICLE

Impact of Area Scaling on the Performance and Stability of Spin-Coated Organic Solar Cells

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Organic solar cells (OSCs) suffer from severe upscaling losses that are predominantly governed by resistive limitations, which hinder efficient lateral charge transport and extraction when reaching the centimeter scale with the current fabrication techniques. In this work, we analyze the impact of the area scaling (from 0.09 up to 4 cm²) on the performance and stability of spin-coated OSCs using PM6:Y7 and PM6:L8-BO in bulk heterojunction architectures. Small-area devices (0.09 cm²) exhibit PCEs of ~17%, fill factors (FF) around 70%, and low series resistances (~1.3 Ω cm²). When scaling up to 4 cm², the open-circuit voltage remains nearly constant, whereas short-circuit current density and fill factor decrease, resulting in PCEs of 10.7% and 11.1% for PM6:Y7 and PM6:L8-BO, respectively. Atomic force microscopy (AFM) and spatially resolved EQE measurements show that efficiency losses arise mainly from increased series resistance (up to ~10 Ω cm²) and minor morphological non-uniformities rather than intrinsic limitations of spin coating process. The stability was also investigated showing PCE losses mainly governed by *J*_{sc} and FF reductions. PM6:Y7-based devices consistently display superior stability compared to PM6:L8-BO, particularly at larger active areas. These results demonstrate that while efficiency losses upon upscaling are primarily dictated by resistive constraints, the stability of spin-coated OSCs is not compromised at the centimeter scale. Moreover, PM6:Y7-based devices exhibit enhanced stability with increasing area.

Introduction

Organic solar cells (OSCs) have attracted increasing interest as a promising thin-film photovoltaic technology due to their lightweight nature, mechanical flexibility, compatibility with solution processing, and potential for low-cost fabrication over large-areas¹⁻⁴. Spin coating remains the most widely used deposition method in research laboratories, offering simplicity, high reproducibility, and suitability for rapid materials screening and device optimization^{5, 6}. Promoted by the development of high-performance donor polymers and non-fullerene acceptors (NFAs), laboratory-scale OSCs have recently achieved power conversion efficiencies (PCEs) over 20%, positioning them among the leading emerging photovoltaic technologies⁷⁻⁹.

Despite these advances, a major barrier to technological deployment persists: the substantial performance loss observed when scaling device areas from typical laboratory dimensions (<0.1 cm²) to larger¹⁰⁻¹². This decrease in efficiency is commonly attributed to non-uniform film formation, increased recombination pathways, and increase in series resistance (*R*_s) associated with the finite sheet resistance of indium tin oxide (ITO). These resistive limitations lead to pronounced reduction in fill factor (FF) and impose strict constraints on the achievable PCE in large-area OSCs^{11, 13}.

Therefore, a detailed understanding of how device area affects charge transport, spatial uniformity, and operational stability is crucial for the transition from small-area prototypes to large-area photovoltaic modules¹³⁻¹⁵.

While scalable deposition techniques such as blade coating, slot-die coating, and printing are widely recognized as essential for the fabrication of large-area organic solar cells and modules, spin coating remains a reliable and effective deposition method for small-to-intermediate active areas. Numerous high-performance OSCs reported in the literature are fabricated by spin coating, owing to its excellent control over film thickness, morphology, and reproducibility, making it a benchmark technique for materials screening and device optimization⁵. Although spin coating is inherently limited in scalability due to material waste and challenges in achieving uniform coatings over very large substrates, several studies have demonstrated that devices with active areas in the range of approximately 1–10 cm² can still exhibit competitive performance and stability when processed by spin coating¹⁶⁻¹⁸. This intermediate area regime is particularly relevant for understanding performance losses associated with area scaling and for establishing reliable structure–property relationships prior to transitioning to scalable deposition methods, which become indispensable in large areas.

In this work, we investigate the impact of active-area scaling-up on the performance and stability of spin-coated OSCs by fabricating devices ranging from 0.09 to 4 cm². Two state-of-the-art donor–acceptor systems were employed as the active

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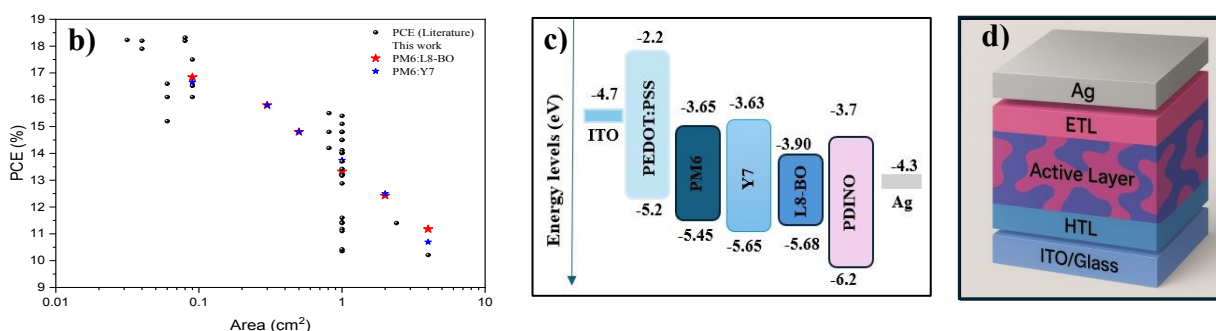
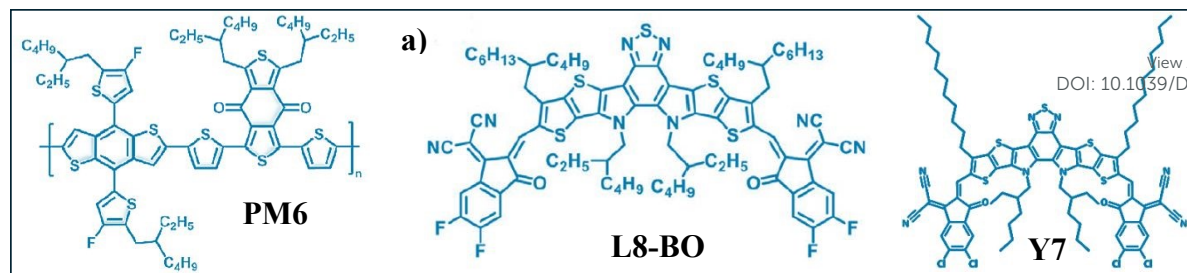


Fig. 1 Molecular structures for the active materials (a) PM6, L8-BO and Y7, (b) PCEs of OSCs with active areas over 4 cm² (Table S1, Supporting Information), (c) Energy level diagram of the OSCs and (d) Schematic illustration of the fabricated device architecture OSCs.

layer: PM6:Y7 and PM6:L8-BO bulk heterojunctions (BHJs)^{19,20}. While Y7 and L8-BO are high-performance NFAs that enable strong near-infrared absorption, efficient exciton dissociation, and balanced charge transport²¹⁻²⁴. By comparing these two BHJ systems, we assess how active layer composition influences device scalability under spin-coating conditions. The results provide valuable insights into efficiency losses induced by device area scaling and emphasize the practical limitations of spin coating when transitioning from small-area prototypes to larger-area OSCs. This understanding is crucial for defining the extent to which spin coating remains a viable deposition method before moving toward scalable coating and printing techniques.

Through a combined analysis of photovoltaic performance parameters, AFM-based morphological mapping, EQE-derived photocurrent uniformity, and stability assessments under ISOS-D-1 and ISOS-L-1 protocols²⁵, we show that spin coating (Fig. 1d) remains a reliable and effective deposition technique for OSCs up to active areas of approximately 4 cm². The observed reduction in device efficiency at larger areas is therefore not inherent to the spin-coating process itself but instead arises from secondary factors—such as edge-related film instabilities, and increased series resistance associated with large-area architectures. These findings clarify how performance losses evolve with increasing active areas in OSCs and show that scalable coating and printing techniques become necessary mainly due to practical, extrinsic limitations, not because spin coating is inherently unsuitable within this area regime.

Results and discussion

Photovoltaic characteristics obtained by current–density (*J*–*V*) measurements

Conventional OSCs with an architecture of ITO/PEDOT:PSS/active layer/PDINO/Ag were fabricated, where

PM6:Y7 and PM6:L8-BO blends were selected as photoactive layers, as shown in Fig. 1d. The chemical structures of PM6, L8-BO, and Y7, are shown in Fig. 1a. The energy level diagrams of the materials are illustrated in Fig. 1c.

The current density–voltage (*J*–*V*) curves of the multiple area OSCs under illumination of AM1.5G (100 mW cm⁻²) are shown in Fig. S1 (Supporting Information), and the photovoltaic performance parameters of the devices based on the PM6:Y7 and PM6:L8-BO blend active layers for the different cell areas are shown in Table 1, which summarizes the average and highest device performance parameters, including the short circuit-current density (*J*_{SC}); open-circuit voltage (*V*_{OC}); fill factor (*FF*); and power conversion efficiency (*PCE*). It also shows the series and shunt resistances (*R*_S, and *R*_{SH}, respectively) extracted from the *J*–*V* characteristics under light. As shown in table 1, the small area devices (0.09 cm²) exhibit comparable *PCE* values of approximately 17 % for both active layer systems (PM6:Y7 and PM6:L8-BO), with average *V*_{OC} values of 841 and 846 mV, *J*_{SC} values of 27.93 and 28.45 mA cm⁻², *FF* values of 71.80 and 70.18 %, *R*_S values of 1.32 and 1.29 Ω cm², and *R*_{SH} values of 563 and 575 Ω cm², respectively.

To place these results in the context of the state of the art, Figure 1b compares the power conversion efficiency (*PCE*) of PM6:Y7 and PM6:L8-BO devices as a function of active area with representative literature reports. Both systems exhibit performance values that are fully competitive with previously reported PM6-based organic solar cells across different device areas. In general, literature reports consistently show a reduction in *PCE* with increasing active area, highlighting the critical role of scaling-related losses in organic photovoltaic devices. Notably, the trends observed in this work follow the same general behavior, further supporting the validity of the scaling effects discussed here. The evolution of the performance parameters of the OSCs fabricated with different active layer materials (PM6:Y7 and PM6:L8-BO) as a function of device area is shown in Fig. S2. For completeness, Table S2



Table 1 Device performance parameters for **PM6:Y7** and **PM6:L8-BO** OSCs with different areas measured under AM1.5G at 100 mW/cm² intensity. All parameters were averaged over twenty devices. The data in parentheses represent the maximum values. The data in square brackets represent integrated J_{SC} calculated from the EQE spectra.

Device area cm ²	J_{SC} [mA cm ⁻²]	V_{OC} [mV]	FF [%]	PCE [%]	PCE_{Loss} [%]	R_s [Ω cm ²]	R_{SH} [Ω cm ²]
PM6:L8-BO							
0.09	27.93 (28.62) [27.2]	841 (850)	71.80 (73.00)	16.84 (17.24)	0	1.32 (1.38)	563 (580)
0.29	26.50 (27.65) [26.3]	843 (850)	70.54 (71.25)	15.80 (16.28)	6.2	2.65 (2.81)	551 (565)
0.5	25.20 (26.43) [25.4]	843 (851)	68.45 (69.34)	14.80 (13.50)	12.1	3.97 (4.27)	513 (531)
1	24.84 (25.36) [24.2]	844 (853)	65.42 (67.10)	13.32 (13.71)	20.1	4.45 (4.63)	496 (512)
2	24.50 (25.41) [24.0]	845 (854)	59.56 (61.37)	12.42 (12.80)	26.2	7.35 (7.65)	479 (495)
4	24.20 (24.74) [23.7]	848 (855)	53.77 (54.56)	11.17 (11.37)	33.6	9.70 (10.3)	466 (482)
PM6:Y7							
0.09	28.45 (29.13) [28.3]	846 (853)	70.18 (71.85)	16.65 (17.10)	0	1.29 (1.34)	575 (597)
0.29	27.56 (28.34) [27.7]	845 (851)	69.54 (70.23)	15.85 (15.30)	4.8	2.65 (2.67)	556(567)
0.5	27.45 (27.68) [27.2]	844 (849)	68.12 (69.03)	14.82 (14.70)	10.9	3.50 (3.70)	538(552)
1	27.31 (27.49) [27.1]	843 (847)	62.70 (63.72)	13.74 (14.22)	17.5	4.80 (4.92)	525(537)
2	26.68 (26.85) [25.8]	842 (846)	57.01 (58.75)	12.51 (13.00)	24.8	7.40 (7.59)	511 (526)
4	25.93 (26.55) [25.1]	842 (845)	50.40 (51.34)	10.69 (11.27)	35.8	9.80 (10.5)	479(497)

summarizes the full photovoltaic parameters for all devices investigated, including different active areas and active layer systems, enabling a direct comparison of device performance across the studied processing and scaling conditions.

As shown in Table 1 and Fig. S2, the active area is increased from 0.09 to 4 cm², distinctive trends are observed in the photovoltaic performance parameters of PM6:Y7 and PM6:L8-BO based OSCs. The V_{OC} remains nearly constant across the entire area range for both active layers, with only minor

fluctuations. In contrast, the J_{SC} shows a clear decrease with increasing area, with a more pronounced reduction for PM6:L8-BO devices. The FF decreases almost linearly from approximately 70–71% for 0.09 cm² cells to 50–53% for 4 cm² devices for both active blends. Consequently, the PCE drops from ~17% to ~11%, with a steeper decrease observed between 0.09 and 1 cm² and a more gradual reduction from 2 to 4 cm². This behavior correlates with a significant increase in series resistance (from ~1.3 to ~10 Ω cm²) and simultaneous

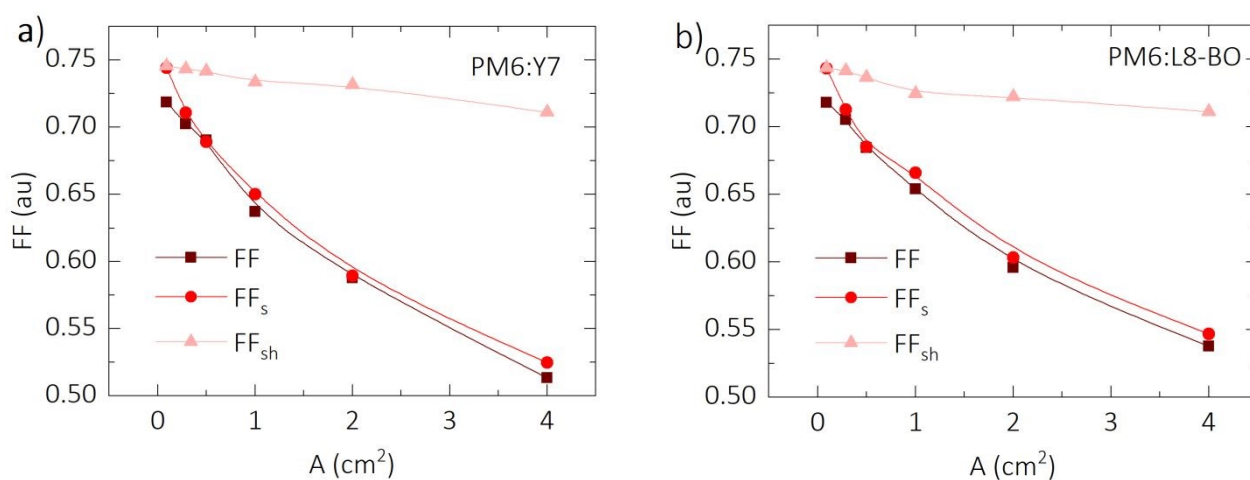


Fig. 2 Evolution of the fill factor as a function of active area for **a)** PM6:Y7 and **b)** PM6:L8-BO (including the experiments (FF, squares), and theoretical predictions for parasitic ohmic losses due to sole series (FF_s, circles) and sole shunt resistances (FF_{sh}, triangles).



decrease in shunt resistance (from ~ 570 to $\sim 470 \Omega \text{ cm}^2$) as the device area increases.

The weak dependence of V_{oc} on device area is consistent with the understanding that V_{oc} in organic solar cells is primarily governed by the energetic offset between the donor HOMO and acceptor LUMO levels, as well as by intrinsic radiative and non-radiative recombination losses, rather than by the lateral dimensions of the device^{26, 27}. In this context, the slightly lower absolute V_{oc} values observed here compared to state-of-the-art reports can be attributed to differences in material batches, commercial suppliers, and processing conditions, all of which are known to influence donor-acceptor energetic alignment, film microstructure, and non-radiative recombination losses. These results are consistent with studies that have reported that when OSCs are scaled from 0.1 cm^2 to a few cm^2 , V_{oc} exhibits only minimal variations, even though other performance parameters decline more significantly^{11, 28-30}.

The reduction in J_{sc} with increasing device area can be associated with geometrical and resistive constraints on charge collection rather than intrinsic limitations of the photoactive materials. This decrease is mainly attributed to ohmic losses within the ITO anode, which impose field-dependent charge

extraction limitations as the device area scales. As lateral transport of the ITO increases, the driving force for charge extraction in regions farther from the collecting electrode is reduced. This, combined with non-uniform current collection

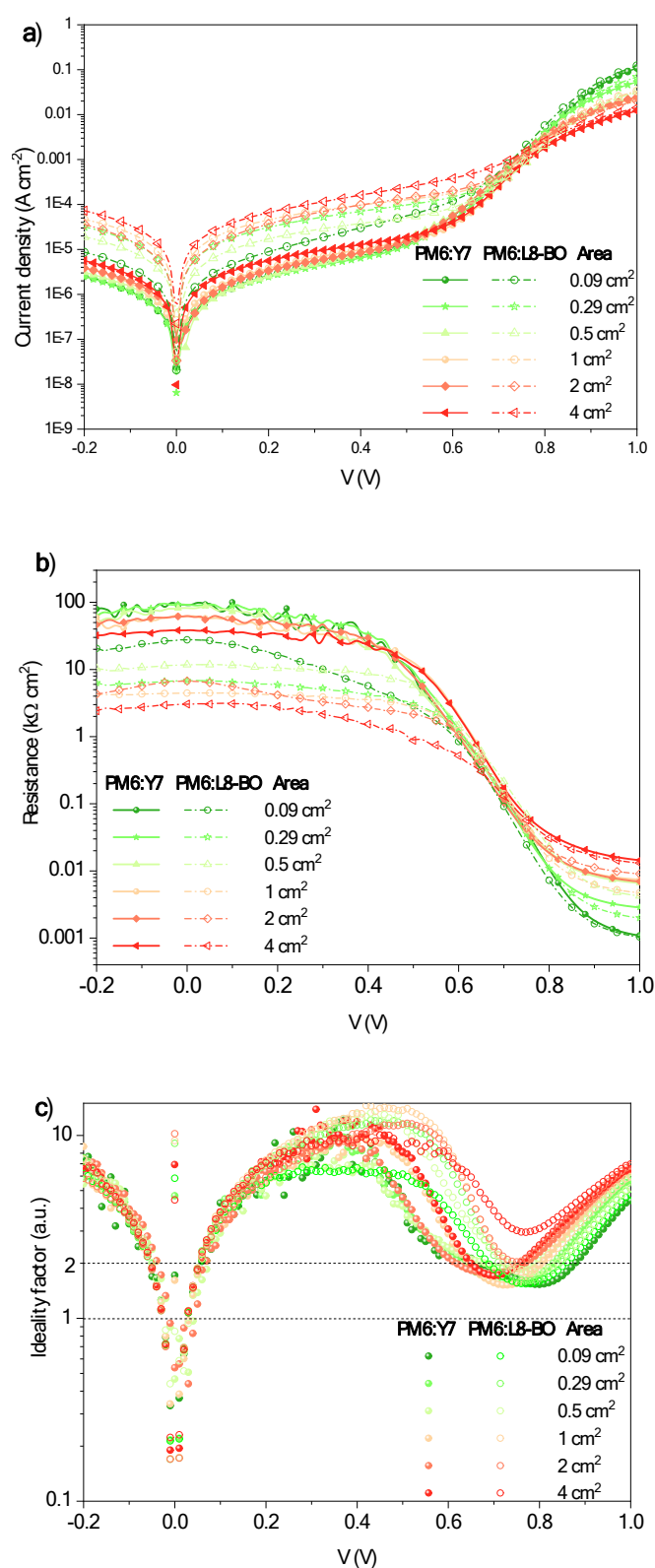


Fig. 3 a) J - V curves measured in the dark b) The dark resistances for different areas of the devices, and c) the ideality factor derived from Dark J - V measurements.



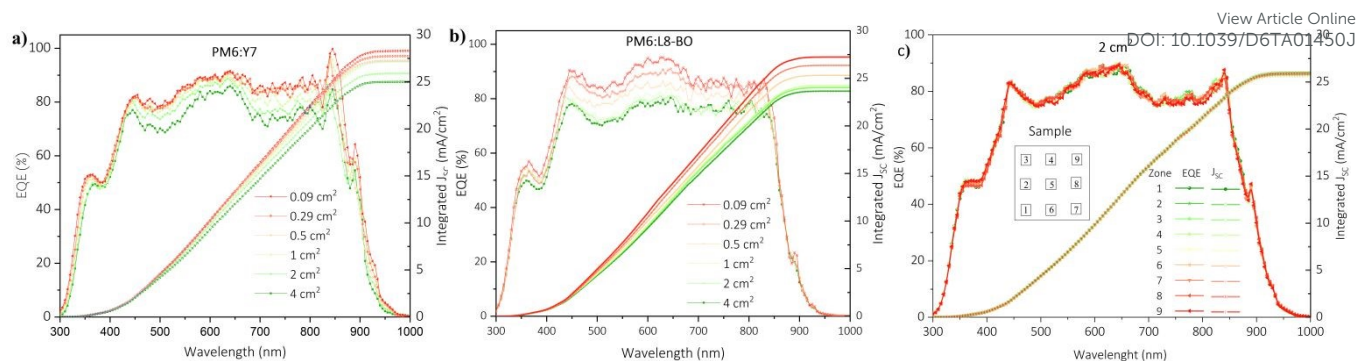


Fig. 4 EQE and calculated J_{sc} for Different area devices **a)** PM6:Y7 OSCs, **b)** PM6:L8-BO OSCs, and **c)** EQE and calculated J_{sc} obtained from nine different locations of a single 2 cm^2 PM6:Y7 OSC device.

and potential edge-related losses, leads to longer carrier residence times. Consequently, bimolecular recombination losses are enhanced before carriers can be successfully collected, even under short-circuit conditions. These effects are consistent with previous studies on large-area OSCs, where non-uniform current distribution and edge-related losses have been identified as key contributors to performance deterioration upon scaling area.^{11, 18, 30-33}

The nearly linear reduction of the FF with increasing active area is primarily associated with the combined effects of increased series resistance and reduced shunt resistance. As the device area increases, longer lateral current collection pathways and the finite sheet resistance of the ITO electrode result in greater resistive losses. In addition, larger device areas increase the probability of localized defects and leakage pathways, which can contribute to a reduction in shunt resistance^{10, 33-35}.

To further elucidate the origin of the fill factor degradation upon device upscaling, a quantitative analysis based on the characteristic photo-resistance ($R_{ph}=V_{OC}/J_{sc}$) was performed, following the framework described in the Supporting Information. In the absence of ohmic losses, the fill factor is expected to follow its ideal dependence on the normalized photovoltage and ideality factor, as described by Eq. (2). However, when the parasitic series or shunt resistances approach the magnitude of R_{ph} , significant deviations from the ideal FF are expected. For the studied devices, R_{ph} was found to be approximately $\sim 33\ \Omega\ \text{cm}^2$, indicating that FF losses should become significant for $R_s > 3\ \Omega\ \text{cm}^2$ and $R_{SH} < 300\ \Omega\ \text{cm}^2$. By comparing the experimentally extracted FF values with theoretical predictions³⁶ considering the isolated effects of R_s and R_{SH} (Eqs. (3) and (4)), it is observed that the FF evolution with increasing active areas is accurately reproduced when only series resistance losses and ideality factors are considered, whereas shunt resistance effects alone fail to capture the

experimental trend (Fig. 2). This further supports the conclusion that series-resistance-related transport and collection losses constitute the dominant mechanism driving FF degradation upon upscaling, while shunt-related leakage pathways play a secondary role in the present devices.



The dark $J-V$ measurements and their derivatives provide additional insight into the voltage-dependent resistive behavior of the devices. From the dark $J-V$ curves (Fig. 3a), the voltage-dependent resistance (Fig. 3b) and the corresponding dark ideality factors (Fig. 3c) were extracted, revealing that both ideality factors and resistance change with applied bias³⁷. The dark series resistance R_S remains of the same order as that extracted under illumination and increases with active area, reaching values close to $10 \Omega\text{cm}^2$ for large area devices. In contrast, the shunt resistance in the dark is orders of magnitude higher (approaching $\sim 100 \text{ k}\Omega\text{cm}^2$) than the photogenerated shunt resistance extracted under illumination. This behavior is consistent with previous observations that dark shunt resistance can scale strongly with device parameters and is indicative of leakage and trapping effects that dominate in the absence of photogenerated carriers, whereas under illumination the effective shunt resistance is limited by additional photocarrier extraction pathways and recombination processes seen in OSCs^{38, 39}.

Because of the simultaneous reductions in J_{sc} and FF , the PCE decreases noticeably with increasing area. The highest drop in efficiency is observed during the initial scale-up (from 0.09 to 1 cm^2) followed by a more gradual decline at larger areas (see Fig. S2d), which is consistent with literature reports^{28, 40-44}. From a mechanistic perspective, this behavior originates from lateral voltage drops within ITO electrode, which become increasingly significant as the device area increases. The finite sheet resistance of the ITO leads to spatial variations in the local potential across the device, resulting in non-uniform current extraction and a reduction in the effective operating voltage.

These lateral resistive effects not only increase ohmic losses, contributing to the observed FF degradation, but also affect charge collection efficiency, thereby impacting J_{sc} .

Overall, the scaling behavior observed in this work is in good agreement with previously reported trends for large-area organic solar cells and highlights that the performance degradation is primarily governed by geometrical-electrical constraints associated with lateral charge transport and electrode conductivity, rather than by intrinsic limitations of the active layer^{42, 44-46}.

The integrated J_{sc} calculated from the external quantum efficiency (EQE) spectra of devices with different areas shows values with a small difference from the results obtained by the $J-V$ measurements (table 1), see Fig. 4. The EQE of 2 cm^2 devices based on PM6:Y7 (Fig. 4c) and PM6:L8-BO (Fig. S3) BHJ OSC was measured by focusing the light spot (21 mm^2) on nine and three different locations of the effective region, respectively. The shape of intensity of the EQE curves obtained from the different zones of the devices show small variation, indicating a good homogeneity in the active layer film. Interestingly, the EQE of the PM6:Y7 device approaches $\sim 100\%$ at $\sim 850 \text{ nm}$, which can be attributed to optical interference effects and reflection from the Ag back electrode, leading to local enhancement of the optical field rather than unity transmittance of the ITO electrode^{47, 48}. The average of the integrated J_{sc} obtained from the different zones of the device for PM6:Y7 OSCs was 25.86 mA/cm^2 , and 24.12 mA/cm^2 for PM6:L8-BO. These small variations are consistent with the results obtained from atomic force microscopy (AFM) images (see Fig. 5).

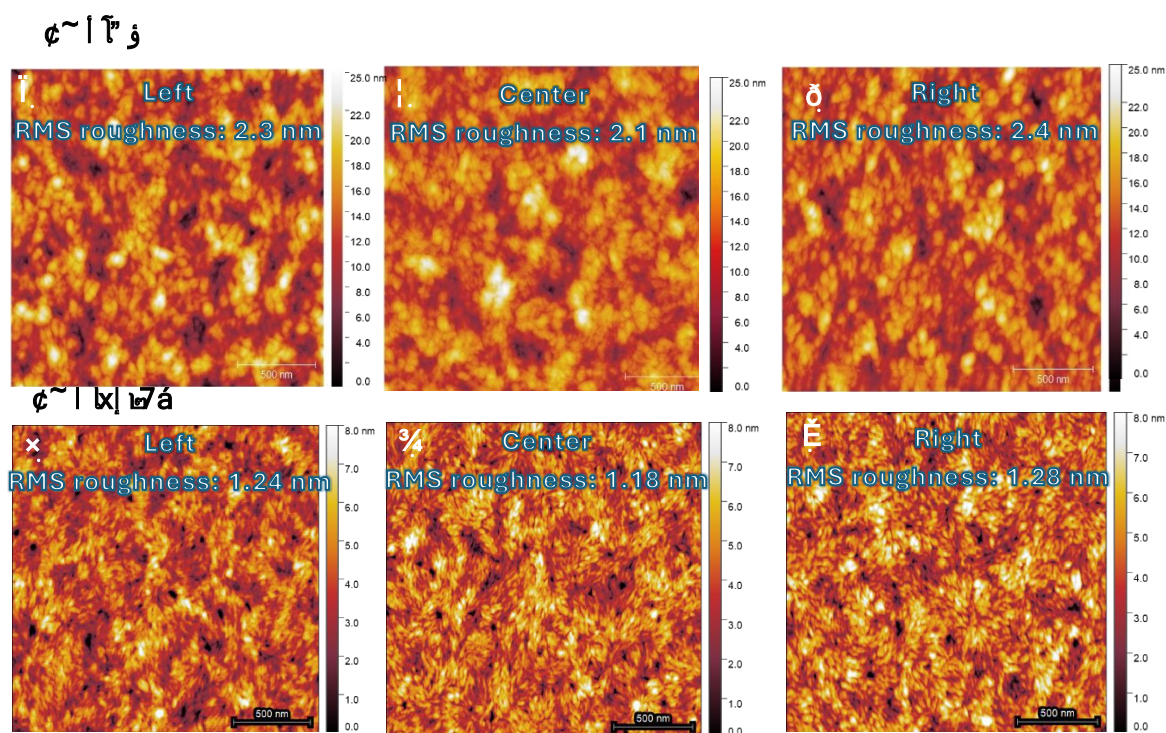


Fig. 5 Atomic force microscope (AFM) Images of PM6:Y7 and PM6:L8-BO blends: a) and d) left, b) and e) center, c) and f) right of each sample, respectively.



The surface topography of the blend films was examined via AFM to assess the influence of the non-fullerene acceptors (L8-BO and Y7), processing solvents (CB and CF), and additives (CN and DIO). Fig. 5 shows AFM topographic images of PM6:L8-BO and PM6:Y7 films deposited on glass/PEDOT:PSS. The PM6:L8-BO films display minimal surface roughness variation, with root mean square (RMS) values of 1.18, 1.24, and 1.28 nm measured at the right, center, and left regions of the substrate, respectively (Fig. 5a-c). In contrast, PM6:Y7 films show higher spatial roughness variations, with RMS values of 2.3, 2.1, and 2.4 nm across the same regions (Fig. 5d-f). These results indicate that both the non-fullerene acceptors (L8-BO and Y7) and the choice of solvent (CB or CF) influence the domain morphology of the blend films, with smoother surfaces observed for CF-based films, which are consistent with reported works^{49, 50}. In the case of CB-based film, larger domains were formed, which can slightly limit the formation of an optimal interpenetrating morphology and lead to a slightly lower FF ⁵¹⁻⁵³.

Therefore, the observed performance reduction in scaled-area devices is not governed by intrinsic limitations in exciton generation or dissociation, but rather by extrinsic, area-dependent losses associated with charge transport and collection. In particular, the increase in series resistance (see Table 1 and Fig. 3a and b) arises predominantly from lateral transport limitations imposed by the finite sheet resistance of the ITO electrode, which lead to spatial variations in the local potential and non-uniform current extraction across the device. These effects reduce the efficiency of charge collection and increase the probability of recombination losses, ultimately contributing to the observed decrease in the EQE-integrated J_{sc} of the upscaled OSCs. Additionally, reduced lateral charge transport efficiency and local inhomogeneities further exacerbate these losses, highlighting the critical role of device geometry and electrode conductivity in limiting performance at larger areas.

To mitigate these scaling-induced losses, both materials optimization and device architecture must be carefully considered. From a materials chemistry perspective, improving charge extraction through interfacial engineering and enhancing electrode conductivity using conductive interlayers or nanostructured additives can help reduce resistive losses and suppress recombination. In parallel, device-level strategies such as incorporating metallic grid structures, integrating highly conductive nanostructures (e.g., Ag nanoparticles or nanowires), or employing narrow stripe geometries with sub-cell widths below ~ 1 cm can effectively shorten lateral charge collection distances and minimize voltage drops across the device. These approaches directly address the geometrical-electrical constraints identified in this work and provide practical design routes for preserving high performance in large-area organic solar cells.

Stability analysis

To evaluate devices lifetime, we performed two tests: shelf life (ISOS-D-1 protocol) and laboratory simulation (ISOS-L-1 protocol)²⁵. The device area-dependent lifetime was analyzed to determine its impact on performance degradation and to

Table 2 The summary of shelf lifetime data for the non-encapsulated devices under different testing conditions, following the ISOS D-1 and L-1 protocols.

Device area (cm ²)	T_{80}	T_{80}	T_{80}	T_{80}
	ISOS-D-1 PM6:Y7 (h)	ISOS-D-1 PM6:L8-BO (h)	ISOS-L-1 PM6:Y7 (min)	ISOS-L-1 PM6:L8-BO (min)
0.09	2174	1220	376	81
0.29	2280	1296	391	108
0.5	2422	1368	400	125
1	2493	1667	994	151
2	2867	1859	1227	227
4	2913	1915	1268	231

assess scalability. The operating shelf lifetime (T_{80}) of each studied cell is summarized in Table 2 and Fig. 6f. Fig. 6a-b shows the evolution over time of the normalized PCE according to ISOS D-1, while Fig. 6c-d show the normalized PCE according to ISOS L-1. Fig. S5 to S8 presented the evolution of the rest of the normalized performance parameters according to ISOS D-1 and ISOS L-1 protocols for OSCs with PM6:Y7 and PM6:L8-BO.

Shelf life (ISOS-D-1 protocol)



The samples stored in dark conditions were systematically measured under standard 1 sun illumination. The resulting V_{oc} trend exhibits the highest stability among all photovoltaic parameters, remaining above 95% of its initial value after 3000 h, independently of the active area. This behavior is consistent with previous reports on PM6:Y7 and related non-fullerene acceptor (NFA) systems^{22-24, 54, 55}. Several studies have shown

that, in the absence of light and oxygen, trap generation and energetic disorder evolve gradually, leading to minimal changes in V_{oc} ^{19, 56}.

The J_{sc} shows only a slight decrease with aging time, typically below 7%, and no strong dependence on device area is observed. Similar weak dark degradation of J_{sc} has been widely reported for high-performance PM6:Y7 devices and has been

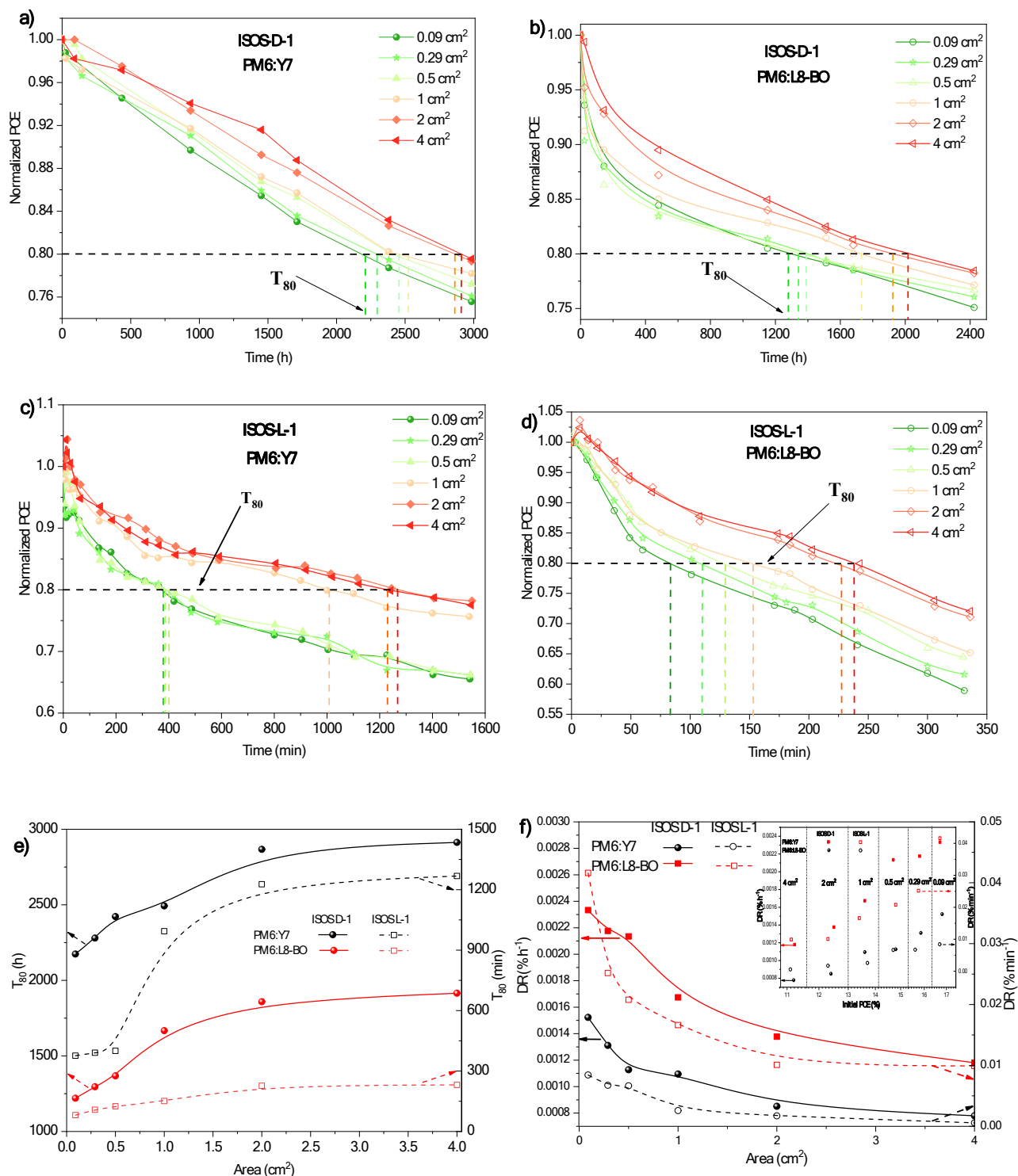


Fig. 6 Normalized PCE over time of each device area with PM6:Y7 and PM6:L8-BO active layer under dark and continuous illumination following the ISOS-D-1 (a-b) and ISOS L-1 protocols (c-d), e) T_{80} vs area, and f) Degradation Rates vs area (inset DR vs PCE_initial of each device area).



attributed to acute changes in charge transport and extraction rather than to bulk photoactive layer degradation^{24, 57-59}.

In contrast, the *FF* exhibits a more pronounced decay over time and represents the main contributor to the *PCE* loss under ISOS-D-1 conditions. While *FF* degradation in OSCs is often associated with an increase in series resistance and a reduction of shunt resistance due to electrical aging of transport layers and interfaces, the extracted R_s and R_{SH} values in our devices show only minor variations with aging time. This suggests that the observed *FF* decay is not primarily driven by bulk resistive losses, but rather by more subtle interface-related degradation effects, such as changes in charge extraction efficiency, contact selectivity or increased interfacial recombination^{60, 61}.

In our work, we find that devices with larger active areas exhibit a smaller increase in series resistance, that leads to a reduced *FF* decay. Consequently, these devices show enhanced stability.

Notably, this stabilization effect is particularly evident for PM6:Y7-based devices, which display the lowest *FF* degradation and the longest T_{80} values. This suggests that the impact of localized weak spots and interfacial defects is statistically averaged over larger device areas.

Laboratory simulation (ISOS-L-1 protocols)

Finally, the long-term stability of the devices was tested in solar simulator under continuous one sun illumination (AM 1.5 G) in open circuit conditions, all devices exhibit an initial transient regime commonly referred to as burn-in^{62, 63}, followed by a slower degradation phase. The V_{OC} shows a small initial decrease during the first minutes of illumination and subsequently remains relatively stable over extended time. This observation matches numerous reports in PM6:Y7 and PM6:L8-BO systems, where photochemical and interfacial degradation processes under controlled conditions induce only minor shifts in V_{OC} , highlighting the relative robustness of the donor-acceptor energetics and interfacial energy alignment. It has been shown that, although light-induced trap states can form during operation, their density is often insufficient to significantly alter the effective bandgap or the non-radiative recombination losses governing V_{OC} ^{23, 24, 64, 65}.

In contrast, the *PCE* degradation under ISOS-L-1 conditions is primarily governed by the evolution of J_{SC} and *FF*. The J_{SC} exhibits the largest relative decrease, particularly in PM6:L8-BO devices, indicating that photo-induced processes affecting charge transport and extraction dominate the performance decay. Since V_{OC} remains comparatively stable while both J_{SC} and *FF* decrease under continuous illumination, the photocurrent loss is more consistently associated with increased recombination and reduced charge collection efficiency than with a dominant loss in built-in potential. In this context, several concurrent mechanisms may contribute to the observed J_{SC} decay, including the formation of photo-induced trap states, increased energetic disorder, gradual disruption of charge percolation pathways, and interfacial degradation affecting carrier extraction. Similar J_{SC} -driven degradation has been reported in Y-series NFA-based OSCs⁶⁶⁻⁶⁸, where light-induced trap formation and subtle chemical modifications of the acceptor phase progressively reduce carrier mobility and

enhance recombination losses. Sangwan *et al.*⁶⁹ reported that UV-induced degradation in Y-series NFAs promotes the formation of shallow trap states associated with reduced carrier mobility and enhanced recombination, while only minor morphological disruption is required to induce substantial photocurrent losses. In addition, broader studies on NFA photodegradation have shown that light-induced chemical modifications and local energetic disorder can progressively impair charge transport and photocurrent generation in Y-series systems. Illumination-induced aggregation of NFAs and the growth of isolated acceptor-rich domains may further disrupt charge percolation pathways and suppress photocurrent generation.^{57, 70}

The *FF* also decreases progressively under illumination and closely follows the reduction of shunt resistance. This behaviour is consistent with literature reports linking *FF* degradation to the accumulation of photo-induced defects and interfacial deterioration, which increase leakage currents and non-radiative recombination losses^{71, 72}. Importantly, the series resistance does not exhibit a dominant increase under illumination. An initial decrease of R_s is observed, followed by a mild and nearly linear increase that remains close to the initial value⁷³. A key finding of this study is that the stability under both dark and illuminated conditions improves with increasing active area. Larger-area devices consistently exhibit longer T_{80} lifetimes and slower degradation rates, despite their lower initial efficiencies, as is shown in Fig. 6e-f.

To verify whether the apparent improvement in stability with increasing device area could arise solely from differences in the initial device performance, the *PCE* evolution was also analyzed in absolute terms (Fig. S4). In both material systems, the degradation curves do not overlap when expressed in absolute values and cannot be superimposed by a horizontal shift along the time axis, indicating that the observed stability trends are not solely determined by differences in the initial performance. Under ISOS-D-1 conditions, all devices exhibit similar degradation profiles, with slightly steeper slopes for smaller-area devices. Under ISOS-L-1 conditions, more pronounced differences are observed, with smaller-area devices displaying a stronger burn-in and faster early-stage decay, whereas larger-area devices show a more gradual evolution. These results confirm that the apparent stability improvement with increasing area is not an artifact of normalization but reflects an area-dependent modulation of the degradation dynamics.

Area-dependent effects on performance and stability have been previously reported in organic solar cells, particularly when transitioning from small-area laboratory devices to larger-area cells or modules. Several studies have shown that degradation in OSCs is often spatially inhomogeneous and dominated by localized failure pathways, such as edge-related defects, interfacial degradation, and non-uniform ingress of oxygen and moisture, which tend to initiate at the device perimeter rather than uniformly across the active layer^{74, 75}. Consequently, small area devices, characterized by a higher perimeter-to-area ratio, are more strongly affected by edge-



driven degradation mechanisms, leading to faster apparent performance losses under similar stress conditions. In contrast, larger-area devices exhibit less relative perimeter exposure, which effectively diminishes the impact of edge-related environmental stress and resulting in a slower degradation rate for comparable aging protocols^{74, 76}. This geometric effect provides a plausible explanation for the improved stability observed for larger-area devices in this work and suggests that the apparent area-dependent stability arises from statistical and geometric average of localized degradation processes, rather than from an intrinsic improvement of the materials or interface stability.

The stability test results can be further evaluated by analyzing degradation rates (DRs) and the behavior of T_{80} as a function of area, which provides a quantitative assessment of the stability for the two active layers (Fig. 6e-f.). The DR is calculated as the difference in performance parameter (*PCE*) between the initial and final states, divided by the test duration (T_{80}), as explained in our previous works^{77, 78}.

Overall, these results indicate that while efficiency losses upon area scaling are primarily governed by resistive and geometric limitations during operation, the intrinsic dark and photo-stability of spin-coated OSCs is not compromised at the centimeter scale. On the contrary, PM6:Y7-based devices exhibit enhanced operational robustness with increasing area, reinforcing the viability of spin coating for intermediate-area OSC fabrication.

Experimental Section

Materials

The indium tin oxide (ITO) patterned glass substrate with a resistivity of $15 \Omega \text{ sq}^{-1}$ purchased from Yanko Shangsheng Business Co., Ltd. PDINO, PM6 (PBDB-T-2F) polymer donor, Y7 and L8-BO NFAs were purchased from One-Material Inc. Chlorobenzene and chloroform solvents, and 1,8-Diiodooctane (DIO) and 1-chloronaphthalene (CN) additives, were obtained from Sigma-Aldrich. A high-purity silver (Ag, 99.99%) wire was obtained from Testbourne Ltd.

Solar cell fabrication

The fabrication of the conventional OSCs was based on the structure of ITO/PEDOT:PSS/PM6:Y7 or PM6:L8-BO/PDINO/Ag. First, the Pre-patterned ITO glass substrates were cleaned sequentially in deionized water with detergent, then ultrasonicated in high-purity solvents (acetone, methanol, and isopropanol) for 10 min, respectively. After, the cleaned ITO glass substrates were dried in an oven at 100 °C for 10 min and treated with ultraviolet light for 15 minutes to remove any organic residue and create a hydrophilic surface. Two spin-coating strategies were employed for material deposition: static and dynamic.

In the static approach, 200 μL of solution were dispensed over 3 s, moving the micropipette tip from the center toward the edges to ensure full coverage before initiating rotation. In the dynamic approach, 100 μL were dispensed in a continuous stream at the substrate's center while already in motion. For the

latter, the micropipette tip was maintained at a height of approximately 1 cm, with a deposition time of 2 s. The PEDOT:PSS solution was deposited onto the cleaned ITO substrates via static-spin-coating (acceleration of 4000 rpm s^{-1}) for 40 s. Following deposition, the films were annealed in air at 150 °C for 15 min. Subsequently, the substrates were transferred to a nitrogen-filled glove box for the active layer deposition.

The active layer solutions were prepared as follows: the PM6:Y7 blend (1:1 wt%, 20 mg mL^{-1}) was dissolved in chlorobenzene (CB) with 0.5% (v/v) 1-chloronaphthalene (CN) and stirred for 3 h at 80 °C. The PM6:L8-BO blend (1:1.2 wt%, 17.6 mg mL^{-1}) was dissolved in chloroform (CF) with 0.25% (v/v) 1,8-diiodooctane (DIO) and stirred for 4 h at 40 °C.

For device fabrication, PM6:Y7 was deposited via static spin-coating at 1500 rpm (1500 rpm s^{-1} acceleration) for 40 s, followed by thermal annealing at 90 °C for 5 min. PM6:L8-BO was deposited via dynamic spin-coating at 2500 rpm (2500 rpm s^{-1} acceleration) for 40 s and annealed at 100 °C for 10 min. Then, the PDINO interlayer (1.5 mg mL^{-1} in methanol) was applied through static spin-coating at 3000 rpm for 30 s.

Finally, the substrates were transferred to an evaporation chamber integrated within the glove box. Where 100 nm silver (Ag) electrode was deposited under high vacuum ($< 1 \times 10^{-6}$ mbar). The active area of the resulting devices ranged from 0.09 to 4 cm^2 .

Solar cell characterization

The current density–voltage (*J–V*) characteristics of the devices under light and dark conditions were obtained at room temperature with a Keithley 2400 source-measure unit using a solar simulator (Abet Technologies model 11 000 class type A, Xenon arc). The solar simulator intensity calibration was performed with a Fraunhofer certified photovoltaic cell to obtain 100- mW cm^{-2} under AM1.5G spectrum. The EQE measurement was performed from 300 nm to 1000 nm using Lasing IPCE-DC model equipment. The devices were analyzed for up to 2500 h under N_2 and under continuous illumination over 1550 min following the ISOS D-1 and L-1 protocols.

Conclusions

In summary, we successfully fabricated OSCs with active areas ranging from 0.09 to 4 cm^2 using a spin-coating process, demonstrating the viability of this technique for intermediate-area OSC fabrication. The spin-coated active layers exhibit highly uniform thickness and low surface roughness across the device area, with comparable morphological characteristics at the center and edges, confirming that film inhomogeneity is not the dominant limitation upon upscaling. An active-area increase of 44 times was achieved while retaining power conversion efficiencies of 10.7% and 11.1% for PM6:L8-BO- and PM6:Y7-based devices, respectively, highlighting the robustness of the fabrication process.

Although this study focuses on PM6:Y7 and PM6:L8-BO as representative high-performance donor–acceptor systems, the dominant scaling losses identified here (due to increased series



resistance and lateral charge transport limitations) originated primarily from architectural and geometric constraints rather than blend-specific effects. As such, these resistive penalties are expected to be broadly relevant across solution-processed organic photovoltaic systems, consistent with previous reports on other donor–acceptor blends. This suggests that the main conclusions derived here are not limited to the specific materials studied but reflect more general scaling constraints in centimeter-scale organic solar cells.

The performance losses observed upon scaling are primarily attributed to resistive and geometric effects rather than intrinsic material or processing limitations. While the open-circuit voltage remains largely unaffected by device area, the reduction in short-circuit current density and fill factor is mainly governed by the increase in series resistance associated with lateral charge transport through ITO electrode. Spatially resolved EQE and AFM analyses confirm that morphological non-uniformities play only a secondary role in efficiency degradation, reinforcing the conclusion that electrode-related resistive losses dominate at the centimeter scale.

A comprehensive stability analysis under ISOS-D-1 and ISOS-L-1 protocols reveals that device stability is not compromised by area upscaling. Under dark storage in an inert N₂ atmosphere (<0.1 ppm O₂ and H₂O), degradation proceeds slowly and is mainly driven by intrinsic electrical aging and interfacial processes involving metallic and organic layers, rather than by chemical decomposition of the photoactive materials. In this regime, the fill factor is the primary contributor to PCE decay, correlating with a gradual increase in series resistance. Under continuous illumination, all devices exhibit an initial burn-in phase followed by a slower degradation regime, with performance losses dominated by the evolution of J_{sc} and FF . Importantly, larger-area devices consistently display reduced degradation rates and longer T_{80} lifetimes under both dark and illuminated conditions.

Between the two systems studied, PM6:Y7-based devices exhibit superior operational stability compared to PM6:L8-BO, specially under continuous illumination. This work demonstrates that while efficiency losses during upscaling are mainly dictated by resistive limitations, the intrinsic photo- and dark stability of spin-coated OSCs is preserved and even enhanced at the centimeter scale. These findings provide important insights into the mechanisms governing large-area OSC performance and stability.

Author contributions

Angel Sacramento: investigation; data processing; formal analysis; methodology; validation; writing. Mohamed Samir: data processing and formal analysis. Osbel Almora: data processing; formal analysis and review. Amina Shehbaz: methodology and data processing, Lluís F. Marsal: review; funding acquisition; project administration.

Conflicts of interest

The authors declare that there are no conflicts of interest.

Data availability

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The data that supports the findings of this study are available on request from the corresponding author. The data is not publicly available due to privacy or ethical restrictions.

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Data Availability Statement

The data supporting the findings of this study are available upon request to the corresponding author. The data is not publicly available due to privacy or ethical restrictions.

