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Extraction of Schottky Diode parameters, Electron Transport and Dielectric Relaxation in PdSe₂ nanoflake

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Abstract

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Palladium Diselenide (PdSe₂) exhibits unique anisotropic electronic behavior, high carrier mobility, and a layer-dependent indirect-to-direct bandgap transition, positioning it as a versatile material for next-generation 2D device architectures. In this work, we investigated the electrical properties; conduction and relaxation mechanisms of mechanically exfoliated PdSe₂ flakes. Raman spectroscopy and AFM were employed to confirm purity and thickness of the sample. Electrical characterizations including current–voltage (IV) measurements and complex impedance spectroscopy (CIS) were performed, revealing crucial information about charge carrier transport mechanisms, contact behavior, and resistive properties. For contact-limited conduction mechanisms, Schottky emission was investigated, and the thermionic emission model was employed to determine Schottky diode parameters, along with a re-evaluation of Richardson’s constant. In bulk-limited conduction mechanisms, Poole–Frenkel (PF) emission was ascertained with a determination of the dielectric constant. To gain insight into relaxation mechanisms, complex impedance spectroscopy, complex dielectric permittivity, and complex modulus spectroscopy were conducted. A switching ratio of $\sim 10^2$ was achieved, indicating the suitability of PdSe₂ for applications in memory devices, neuromorphic computing, and sensing technologies.

Keywords: PdSe₂, IV characteristics, Schottky diode, Dielectric permittivity, Diode parameters



Highlights

- Raman spectroscopy and AFM confirmed the few-layered structure of PdSe₂ with a thickness of 4 nm leading to its semiconducting behavior.
- Temperature dependent I-V characteristics showed a switching ratio of $\sim 10^2$.
- The fitting of thermionic emission model led to the extraction of important Schottky diode parameters including Richardson coefficient, ideality factor and saturation current.
- Impedance spectroscopy was carried out to study the dielectric behavior and relaxation mechanisms.



Introduction

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Recent advances in materials science have placed nanomaterials at the center stage of scientific and technological innovation with atomically thin 2D transition metal dichalcogenides (TMDCs) emerging as a major focus of current research. This attention has roots in the vast array of applications ranging from electronics and optoelectronics to sensing, catalysis and flexible devices. Unlike their bulk counterparts, TMDCs give access to highly tunable optical, electronic, chemical and mechanical properties (1, 2, 3). Among TMDCs, noble metal dichalcogenides (NMDCs), exhibit distinctive properties including thickness dependent electronic structure, remarkable air stability, unusual electronic phase transitions and in some cases even superconductivity (4). These materials comprise of chemical formula MX_2 where M is a Noble metal from group-10 (Pd, Pt) and X is a chalcogen (S, Se) (4).

Within the family of NMDCs, $PdSe_2$ draws attention as it is characterized by an orthorhombic phase resulting in an in-plane anisotropy and a layered, puckered pentagonal structure (5). The characteristics of these features are what make it exhibit a striking electronic band structure, such as a transition between semiconducting and metallic behavior with changing number of layers. Also, $PdSe_2$ has high air stability(5), tunable bandgap (6) and robust photo response properties(7), and thus is an appealing material to be incorporated in the infrared photodetectors, field-effect transistors, and gas sensors (8). Notably, $PdSe_2$ holds advantages over other group-10 dichalcogenides such as combining the durability of $PdTe_2$ under ambient exposure with the band tunability of $PtSe_2$, while maintaining thermodynamic stability that limits PdS_2 . This unique combination places $PdSe_2$ as a more versatile and durable candidate for probing polarization phenomena and its integration into next-gen optoelectronic and sensing devices(9). However, the performance and reliability of these devices is strongly influenced by the underlying charge transport processes making it necessary to understand the conduction of charges across $PdSe_2$ layers for efficient and stable devices. This understanding can be achieved through detailed AC and DC analyses of conduction mechanisms. Despite the advances in $PdSe_2$ applications, simultaneous systematic studies of its AC and DC transport mechanisms remain relatively limited, highlighting the need for deeper investigation.

In this work, we address this gap by systematically investigating the conduction mechanisms in mechanically exfoliated nanoflakes of $PdSe_2$ with silver contacts by performing temperature dependent IV characteristics, complex impedance spectroscopy and complex dielectric permittivity analysis along with a brief complex modulus spectroscopy analysis. The observation



of non-linear behavior in the I-V plot of PdSe₂ was consistent with Schottky type contacts. The Schottky diode parameters were evaluated using thermionic emission model and a modified value of Richardson's coefficient for Ag/PdSe₂/Ag was re-evaluated. Analysis using Poole-Frenkel emission model enabled the extraction of dielectric permittivity constant of PdSe₂. The temperature and frequency dependence of the impedance in PdSe₂ was examined using Complex Impedance Spectroscopy (CIS) to elucidate the underlying charge transport and relaxation processes. Complex dielectric spectroscopy showed strong frequency and temperature dependence in permittivity (ϵ'), dielectric loss (ϵ''), and the complex modulus (M^*), all of which provided evidence for localized carrier relaxation. This work not only expands the fundamental understanding of PdSe₂ but also highlights its potential as a robust candidate for future nano-electronic and optoelectronic applications.

Materials and methods

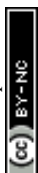
Bulk PdSe₂ crystals synthesized via chemical vapor transport (CVT) technique were acquired from a 6Carbon Technology, China. Interdigitated electrode arrays (IDAs) used for device fabrication were obtained from MicruX Technologies. Each IDA consisted of silver electrodes patterned on a glass substrate, arranged in an interdigitated comb-like configuration with individual finger dimensions of 10 $\mu\text{m} \times 10 \mu\text{m}$.

PdSe₂ flakes with controlled thickness and lateral dimensions were mechanically exfoliated from the bulk crystals using thermal release tape (TRT). The exfoliation tape was gently pressed onto the target silicon substrate to ensure strong adhesion, followed by controlled thermal delamination, resulting in reliable transfer and adhesion of the exfoliated PdSe₂ flake onto the substrate surface.

The PdSe₂ flake with a thickness of approximately 4 nm was transferred onto a silver-contacted IDA using TRT and subjected to temperature-dependent I-V measurements in a cryogenic probe station of an Agilent 4156C semiconductor analyzer for a temperature range of 293 K – 393 K kept under control by liquid nitrogen. Moreover, AC transport measurements were made using an LCR meter 4980A in a frequency range of 200Hz to 2MHz for temperatures 293 K – 343 K with the temperature being controlled by liquid Nitrogen.

Results And Discussions

Mechanically exfoliated PdSe₂ flakes of desired thickness were then subjected to structural characterizations (figure 1). Raman spectroscopy was employed to investigate the structural and vibrational characteristics of the PdSe₂. Figure 1 (a) shows the Raman spectrum of bulk PdSe₂,

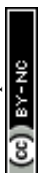


whereas Figure 1 (b) illustrates a distinct blueshift in the characteristic peaks, reflecting reduced interlayer coupling and stronger confinement effects showing a decrease in the number of layers. The few layered sample exhibited all 3 A (A_g^1 , A_g^2 , A_g^3) modes but only two B (B_{1g}^1 , B_{1g}^2) modes. This is because B_{1g}^3 mode is very close to A_g^3 mode. Raman analysis validated the high phase purity of the synthesized PdSe₂, evidenced by the absence of any peaks attributed to impurity. The observed vibrational modes correspond closely to those reported for crystalline PdSe₂, confirming the material's structure and integrity (8). The sharpness and intensity of these peaks confirmed the crystalline nature of the sample and validated the successful transfer of a high-quality flake onto the substrate. The peaks observed in the lower frequency region at 75 cm⁻¹, 100 cm⁻¹ and 125 cm⁻¹ are due to symmetry variations of PdSe₂. Absence of any background signals shows that the material is structurally intact and set for subsequent electrical characterizations.

For PdSe₂, the flake was exfoliated using TRT and placed onto Silicon wafers to perform AFM. Figure 1 (c) shows the visuals of the sample and figure 1 (d) shows the recorded deflection of the cantilever. A monolayer of PdSe₂ exhibits an average thickness of approximately 0.71 nm (10). The recorded value of the height profile obtained from atomic force microscopy (AFM) measurements for PdSe₂ shows that the thickness of the flake is ~4nm which is an equivalent of nearly 5 layers.

The schematic in figure 2 (a) depicts the device prepared as a flake is transferred onto a comb-like, 10 μm spaced, interdigitated silver electrodes of an IDA. Fig 2 (b) exhibits the I-V characteristic plots for the PdSe₂ flake with silver contacts in a temperature range of 293 K–393 K. The non-linear behavior of the curves becomes more pronounced upon an increase in temperature which is consistent with the semiconducting behavior. The switching ratio being ~10² makes it a reasonable candidate for analog synaptic elements in Neuromorphic devices, high-voltage sensors and high-voltage resistive switching devices (11, 12, 13).

In figure 3 (a), Schottky emission characteristics were analyzed by plotting $\ln(I)$ and \sqrt{V} over the temperature range of 343 K–398 K. In Schottky emission, the current is governed by the thermionic emission of charge carriers across the metal-semiconductor junction, where the electrons are excited to energy levels high enough to surmount the potential barrier. The thermionic emission of electrons is expressed as (14):



$$I = AA^*T^2 \exp \left\{ \frac{e \left(\varphi_b - \sqrt{\frac{eV}{4\pi\epsilon_0\epsilon_r Ad}} \right)}{k_\beta T} \right\} \quad (2)$$

Where A is the effective area, A^* is the Richardson's constant, V is the applied voltage, φ_b is the barrier height and d is the distance between the contacts, ϵ_0 is the free space permittivity constant and ϵ_r is the dielectric permittivity. Applied voltages below 1.1 V result in nearly straight-line behavior in the plot which is a characteristic indication of conduction in this regime being governed primarily by Schottky emission.

The thermionic emission model fits the forward biased current in Schottky diode is illustrated in Fig 3 (b) and it expresses the forward biased current as (15):

$$I = I_s \left[\exp \left(\frac{qV}{nk_\beta T} \right) - 1 \right] \quad (3)$$

Where V denotes the applied voltage, n is the ideality factor, q is the electronic charge, k_β is the Boltzmann's constant, T is the temperature and I_s is the saturation current. The saturation current is the reverse bias leakage current due to the minority charge carriers crossing the junction. It is expressed as (15):

$$I_s = AA^*T^2 \exp \left(-\frac{q\varphi_b}{k_\beta T} \right) \quad (4)$$

The Schottky barrier height can be expressed by molding the above equation as follows:

$$\varphi_b = \frac{k_\beta T}{e} \ln \left(\frac{AA^*T^2}{I_s} \right) \quad (5)$$

The fitted curves helped to evaluate the values of saturation current and ideality factors for different temperatures using equation (3). These values are plotted against temperature in figure 3 (c). This plot helps gather Schottky diode parameters that are necessary to conclude possible device applications along with their performance. The ideality factor provides insight into the deviation of the device's behavior from that of an ideal diode. For an almost ideal case with low energy loss in semiconductors an ideality factor close to 1 is required. A value greater than 1 can be attributed to high field influencing the band. The ideality factor can be improved by taking measures towards improved charge carrier transport. The deduced values may allow for applications in heterojunction solar cells and photovoltaics.



Using these retrieved values, barrier height was calculated using equation (5) for different temperatures to be in the range of 0.128 eV – 0.150 eV. Height of the potential barrier governs the degree of charge carrier separation and associated energy losses. The nonlinear trend of barrier height ϕ_b is attributed to barrier inhomogeneities and is characteristic of multiple conduction mechanisms taking place at different points along the curve, which can be improved by performing strain engineering, doping etc. The temperature dependence of the barrier potential enables the device to serve not only as a sensor but also as a diagnostic platform for material characterization (16).

Equation 4 can also be modified to gain the value of Richardson's coefficient:

$$\ln\left(\frac{I_s}{T^2}\right) = \ln(AA^*) - \frac{q\phi_b}{k_\beta T} \quad (6)$$

According to 6, upon linear fitting of $\ln(I_s/T^2)$ vs $1/T$, the intercept of the plot should give Richardson's coefficient. The obtained value of the coefficient was significantly smaller than the theoretical expectation. The considerable discrepancy between the calculated and expected values can be attributed to barrier inhomogeneities(16). However, a corrected coefficient was determined by assuming a Gaussian distribution of the potential barrier heights, which introduces two new parameters: the mean barrier height (ϕ_{b0}) and the standard deviation (σ). These parameters are interrelated through the following expression:

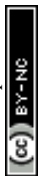
$$\phi_b = \phi_{b0} - \frac{q\sigma^2}{2k_\beta T} \quad (7)$$

Here, ϕ_b is the experimentally determined value of potential barrier height whereas, ϕ_{b0} is to be measured using the expression above. When ϕ_b is plotted against $1/T$ the slope, it renders the value of σ , which came out to be 104.3 mV.

For corrected value of Richardson's constant, we used the Gaussian corrected equation (17):

$$\ln\left(\frac{I_s}{T^2}\right) - \left(\frac{q^2\sigma^2}{2(k_\beta T)^2}\right) = \ln(AA^*) - \frac{q\phi_b}{k_\beta T} \quad (8)$$

Figure 3 (d) shows the plot between $(\ln I_s/T^2) - (q^2\sigma^2/2(k_\beta T)^2)$ vs $1/T$ is a straight line whose intercept was used to calculate the value of modified Richardson's coefficient. The intercept returned $421 \text{ Acm}^{-2}\text{K}^{-2}$ as modified Richardson's coefficient and 0.322 eV as the mean barrier height. Since these values are well within expected range, the Gaussian distribution being assumed was a success.



In figure 4, the plot between $\ln(G)$ vs \sqrt{V} is illustrated for temperature range of 348 K–398 K for a voltage varying from 0 V–2 V. It depicts the Poole-Frenkel emission where electric field helps thermally weak, trapped charge carriers escape traps inside the potential valleys. PF emission obeys the following equation (18):

$$I \sim V \exp \left(\frac{\beta_{PF} \sqrt{V} - \varphi_{PF}}{k_{\beta} T} \right) \quad (9)$$

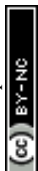
Where the β_{PF} is the lowering of potential barrier height, φ_{PF} denotes the potential barrier. Linear fit of the plots provides strong evidence of PF emission leading the conduction of charge carriers from 0 V–2 V. The slope of the plot provides the dielectric permittivity constant for the given temperature range according to expression (14):

$$S = \frac{\sqrt{\frac{q^3}{d \varepsilon_0 \varepsilon_r \pi}}}{k_{\beta} T} \quad (10)$$

Where k_{β} is the Boltzmann constant, d is the electrode spacing, ε_0 is the permittivity of free space, ε_r is the dielectric permittivity and q is the electronic charge. The inset of figure 4 depicts the change in dielectric constant ε_r with increase in temperature from 348K–398K. The average value of ε_r came to be around 8, which is well within the expected range. Dielectric constant of PdSe₂ is moderate in terms of polarizability. Therefore, moderate screening of the external field by the charge carriers is expected. The variation in dielectric constant is owed to thermally activated release of trapped charges, enhancing the polarization and screening ability of the material. The moderate yet tunable dielectric response highlights the potential of PdSe₂ for temperature sensitive applications like memristors and neuromorphic devices. With enhanced channeling abilities evident, PdSe₂ may also find applications in solar energy devices and memristors.

To delve deeper into the electrode and grain properties of PdSe₂ device, complex impedance spectroscopy was performed for a temperature range of 293 K–343 K at different frequencies. In figure 5 (a), Nyquist plots between the values of Z'' and Z' measured for the selected frequency domain are plotted. An equivalent circuit was designed using Z-view software and has been displayed in the inset of figure 5 (a). It consists of an R-CPE circuit showing crystalline nature of PdSe₂. The complex impedance of a system is given by(19):

$$Z^* = Z' - j Z'' \quad (11)$$



where Z' and Z'' are the real and imaginary parts of the complex impedance which are then further individually expressed as (20):

$$Z' = \left[\frac{R}{1 + (\omega RC)^2} \right] \quad (12)$$

$$Z'' = \left[\frac{\omega CR^2}{1 + (\omega RC)^2} \right] \quad (13)$$

Where ω = angular frequency, R is the resistance and C is the capacitance of the device. The Nyquist plots exhibit a decrease in resistance as the temperature increases which is a characteristic of semiconductors with a negative temperature coefficient of resistance. Considering that PdSe₂ acts as a semiconductor when the number of layers is ~ 5 layers or less, it is consistent with the AFM values determined for this PdSe₂ device. The Nyquist plots were fitted using an equivalent circuit in Z-View, and the simulated curves showed excellent agreement with the experimental data (Table 1). As the temperature increases, the reduction in resistance is attributed to enhanced probability of thermally activated detrapping of charge carriers from defect states, increasing the free carrier concentration and effective mobility. The subsequent decline in total resistance with further temperature elevation clearly indicates a negative temperature coefficient, thereby reaffirming the semiconducting behaviour of the material.

Figure 5 (b) illustrates the decreasing trend of the real part of impedance (Z') with increasing frequency over the temperature range of 293 K–343 K. At lower frequencies, Z' exhibits negligible frequency dependence; however, as the frequency increases, a more pronounced decline is observed, and the Z' values corresponding to different temperatures converge at higher frequencies. This behavior is attributed to the enhanced mobility of charge carriers and the resulting decrease in space-charge resistance, denoting improved electrical conduction in upper frequency domain. The inset of figure 5 (b) shows the varying trend of Z'' with the increase in frequency. The plot displays a rise in magnitude with increasing frequency, reaching a distinct peak, followed by a sharp decline that gradually converges as the frequency continues to increase. The decrease of Z'' is attributed mainly to the increased thermal activity. The peaks correspond to a certain value of frequency known as the relaxation frequency f_o . The values of f_o increase with the increase in temperature indicating an enhanced conduction of the localized charge carriers through hopping with the widening of peaks being a contribution from the temperature dependence of the relaxation mechanism.

Understanding the dielectric properties of a material is essential as it reveals the conduction mechanisms, polarization behavior, and dielectric relaxation processes that determine the device



performance (21). The polarization response of a material is inherently determined by its electronic, ionic, and interfacial characteristics, each of which shifts significantly with changes in frequency and temperature. The dielectric permittivity ϵ^* has two parts, real and imaginary with the former usually referred to as the dielectric constant. It is expressed as (19):

$$\epsilon^* = \epsilon' - j \epsilon'' \quad (14)$$

The real part ϵ' and the imaginary part ϵ'' are then further represented as:

$$\epsilon' = \frac{t}{\omega A \epsilon_0} \left(\frac{Z''}{Z'^2 + Z''^2} \right) \quad (15)$$

$$\epsilon'' = \frac{t}{\omega A \epsilon_0} \left(\frac{Z'}{Z'^2 + Z''^2} \right) \quad (16)$$

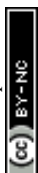
Where A is the cross-sectional area of the flake, t is its thickness, ϵ_0 is the free space permittivity, Z' and Z'' are the real and imaginary parts of impedance, respectively. In our case, the area is approximately $A = 1 \times 10^{-10}$ m and the thickness of the flake ~ 4 nm. Using these values, the t/A ratio was calculated to be 40 m^{-1} .

In figure 5 (c), the plot between dielectric permittivity ϵ' vs T for a temperature range of 293K-343K at specific frequencies shows a decrease in dielectric permittivity with the rise in frequency at all temperatures. This is because the interfacial polarizations are screened out at higher frequencies leaving behind only the ionic and electronic polarizations. The interfacial polarizations, key contributors, see an overall rise at lower frequencies due to better adaptation to field reversal but drop at higher values as per their inability to cope with the rapidly changing field. The contribution from other polarizations, however, becomes evident at higher frequencies but still the net polarization declines.

Figure 5 (d) depicts the change in dielectric loss $\tan \delta$ with temperature at specified frequencies. The dielectric loss is dependent on both parts of the dielectric permittivity and is represented as (20):

$$\tan \delta = \frac{\epsilon''}{\epsilon'} \quad (17)$$

Since, ϵ' and ϵ'' are both parts of the same entity, they increase and decrease under the same conditions, the dielectric loss shows a similar trend. Therefore, as the temperature increases, the loss also increases due to enhanced thermal activity. This loss decreases with increase in frequency given the number of defects carrying charges also decreases (20).



In figure 6 (a), the change in calculated values of M' with changing M'' is demonstrated for a temperature range of 293 K-343 K. the two parameters are related by the following relation (19):

$$M^* = M' + j M'' \quad (18)$$

Where M^* is the dielectric complex modulus, M' is the real part of the modulus related to energy storage and M'' is the imaginary part that reflects loss. These two are then further expressed as:

$$M' = \left(\frac{\epsilon'}{\epsilon'^2 + \epsilon''^2} \right) \quad (19)$$

$$M'' = \left(\frac{\epsilon''}{\epsilon'^2 + \epsilon''^2} \right) \quad (20)$$

Where ϵ' and ϵ'' are the real and imaginary parts of the dielectric constant. Complex modulus is often employed as an analytical tool to understand the relaxation mechanisms in a system. It helps understand the suppression of electrode polarization and enhances bulk relaxation. It gives a clearer insight into charge carrier dynamics and relaxation mechanisms.

Complex electrical modulus provides valuable information about the material's electrical response, revealing how varying polarization mechanisms behave under changing electric fields and temperatures. In figure 6 (a), a single semicircular arc is visible at all measured temperatures, exhibiting a slight movement toward higher M'' values as the temperature rises. This shift suggests that the modulus resistance decreases with rising temperature, which is in good agreement with previously reported findings (22). Figure 6 (b) illustrates the trend of M' with varying frequency for a temperature range of 293 K-343 K. At low frequencies, minimal electrode polarization occurs due to decreased effective restoring force to control charge carrier motion under the applied electric field, resulting in very small M' values (23). As frequency increases, M' gradually rises and peaks at higher frequencies, signifying the presence of relaxation dynamics in the PdSe₂ flake throughout the studied range. The maximum M' value, however, diminishes with rising temperature. Since M' reflects the capacity of the material to store electrical energy, its reduction at elevated temperatures indicates improved charge carrier mobility within the system.

In figure 6 (b) inset, M'' is plotted against frequency for a temperature range of 293 K-343 K. A broad relaxation peak appears in M'' moving toward higher frequencies as temperature rises. This shift signifies a thermally activated relaxation process, where the frequency span under the peak represents the range of long-distance charge carrier mobility (24). The frequency at which a relation peak is observed is the characteristic frequency f_{\max} . Below f_{\max} the charge carriers can



easily cover the polarization length but greater than f_{\max} the charge carriers cannot travel the polarization length, and the charge carriers are localized. As the temperature increases, the charge carriers gain energy and, thereby, they relax relatively slower while the peak shifts towards increasing frequency. This shift from long range mobility to short range mobility is evident in M'' plot (25).

Figure 6 (c) represents the response of complex modulus following scaling behavior by a plot of M''/M''_{\max} vs $\ln(f/f_{\max})$. As the plotline climbs towards the peak, the charge carriers get restricted to their potential wells. The peak itself is evidence of transition of long-range movement to short-range. Moreover, the plot for different temperatures overlapping shows that the relaxation mechanisms are receiving equal thermal support (20).

In Figure 6 (d), the variation of Z'' and M'' with frequency provides valuable insight into the underlying relaxation mechanisms of the material. The two trendlines approach one another closely but do not overlap, signifying the coexistence of both localized relaxation processes and long-range conduction elements in PdSe_2 (26).

Conclusion

In this work, PdSe_2 flakes, exfoliated from bulk crystal using TRT and transferred onto the silver IDA contacts, were analyzed through temperature-dependent AC and DC transport mechanism between 293K and 398K. Both bulk-limited and contact-limited conduction mechanisms were observed. These include Schottky emission and PF emission. Schottky emission was investigated, and thermionic emission model was used to find the diode parameters and their trends with varying temperature. This device has shown a decrease in ideality factor, increase in saturation current and an inhomogeneous barrier height with an increase in temperature. The analysis of PF emission revealed the dielectric constant in the range of 7–10. Complex The investigation of complex impedance and modulus helped in determining the behavior of relaxation mechanisms and trend of dielectric permittivity in response to the changing polarity of the field. This study reinforces the suitability of PdSe_2 for high-performance nanoelectronics and optoelectronic applications and provides insights for optimizing device architectures. This work therefore contributes significantly to the broader understanding of Pd-based transition metal dichalcogenides and their role in next-gen 2D material technologies.

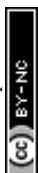


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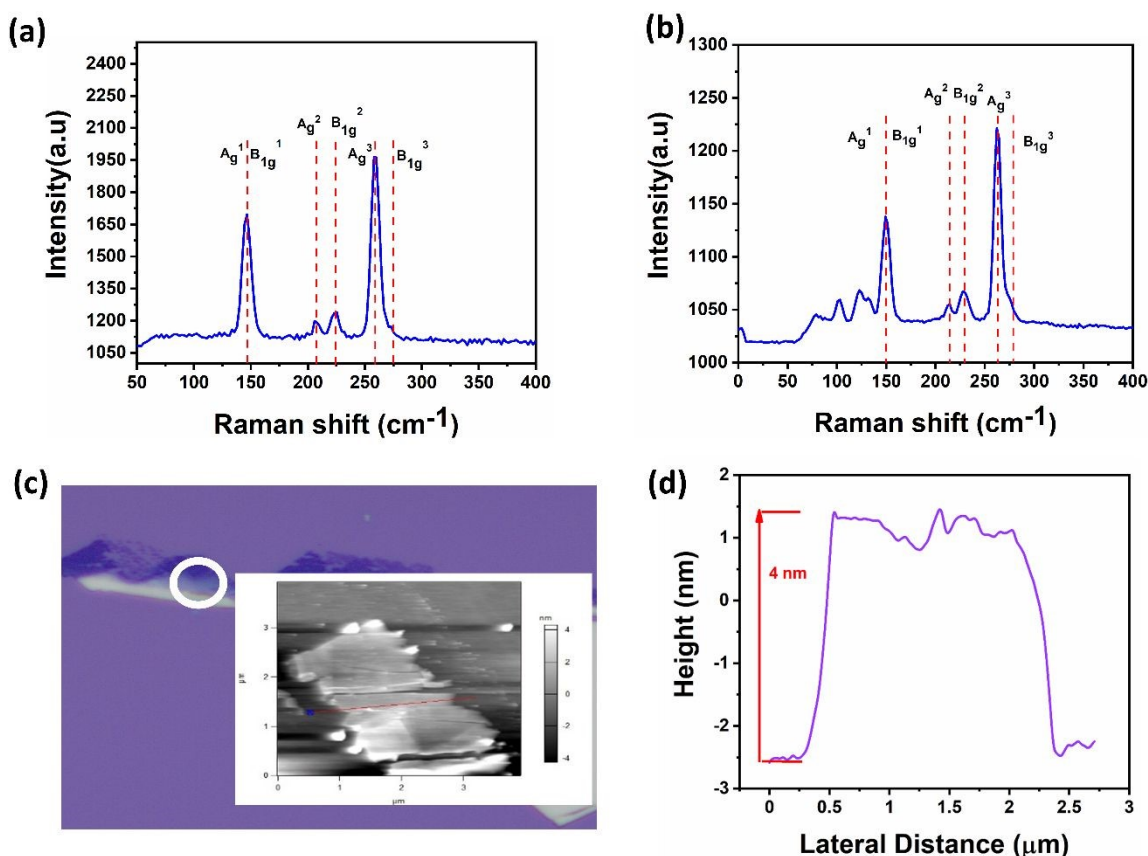
Figure 1View Article Online
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Figure 1: (a) Raman spectrum of the bulk PdSe₂ showing the characteristic vibrational modes corresponding to bulk phase (b) RAMAN spectrum of the mechanically exfoliated few-layered PdSe₂ flake confirming layer reduction by via blue shifts. (c) AFM image showing the magnified region of the exfoliated PdSe₂ flake used for thickness measurement. The selected area highlights the region analyzed to determine the layer thickness of the flake. (d) AFM height profile of the exfoliated few-layered PdSe₂ flake on a Si/SiO₂ substrate.

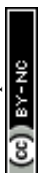


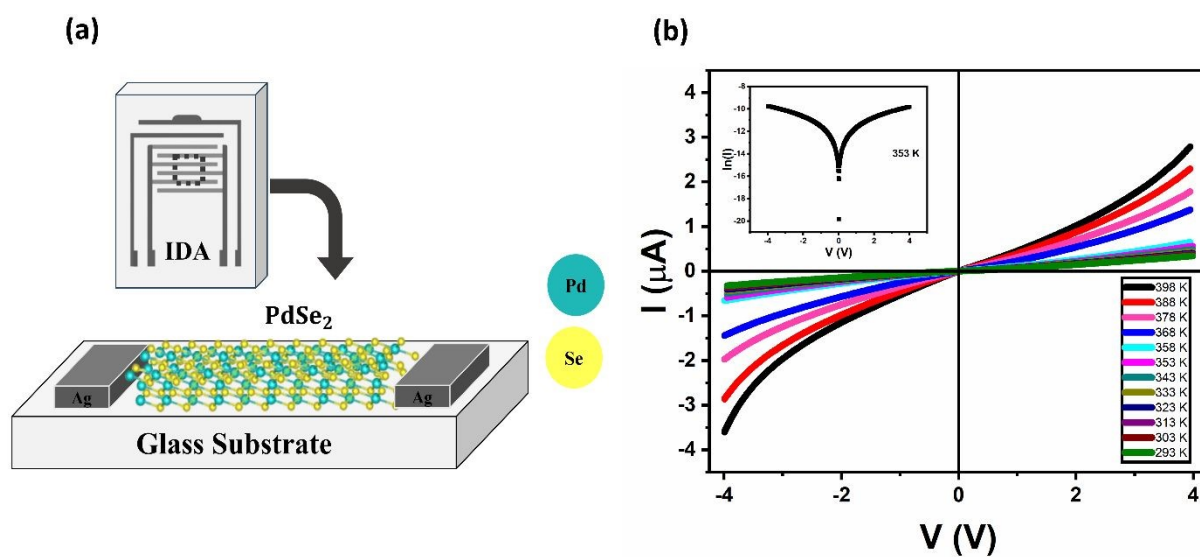
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Figure 2: (a) Schematic of PdSe₂ flakes on IDA with silver contacts 10 μm apart on a glass substrate. (b) Temperature-dependent IV characteristics of PdSe₂ flakes in the temperature range of 293 K–398 K show negative temperature coefficient.

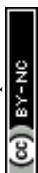


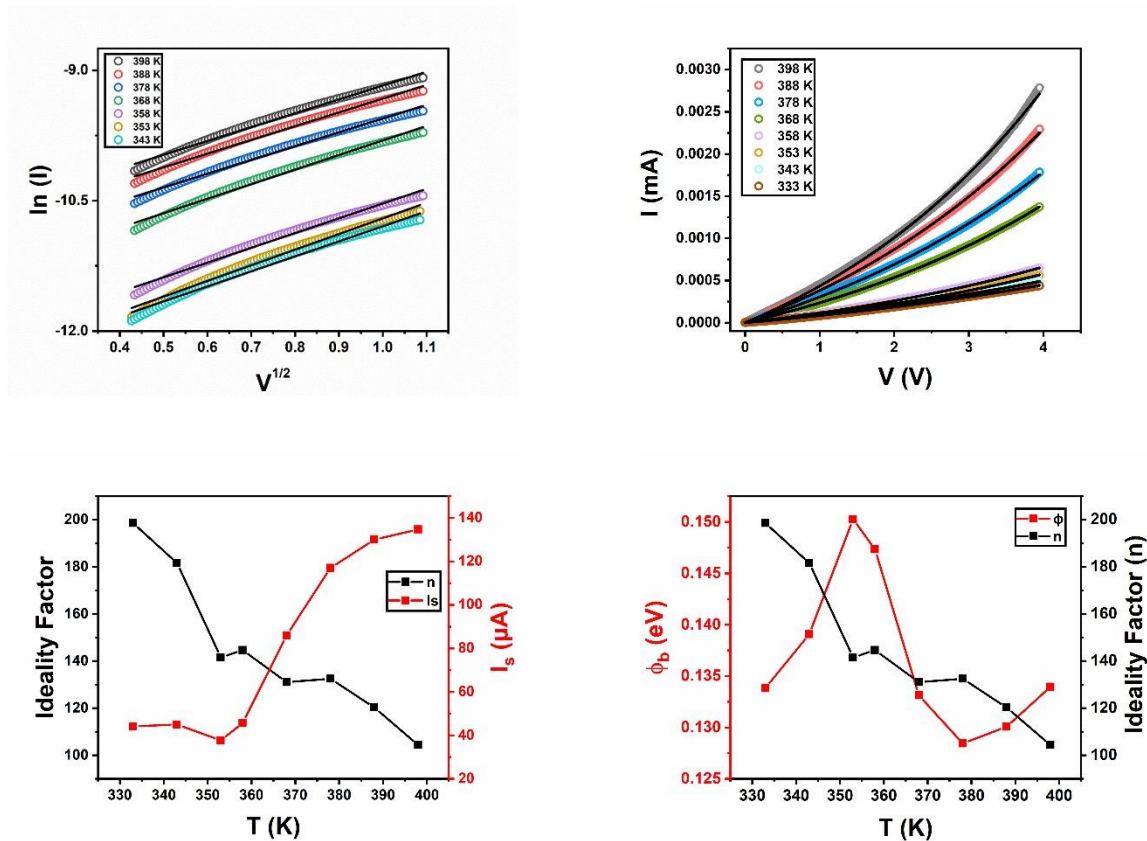
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Figure 3: (a) Plot between $\ln(I)$ vs $V^{1/2}$ for Schottky emission fitting (b) Equation fitting of thermionic emission model (c) Variation of ideality factor and saturation current with temperature (d) Modified Richardson plot according to Gaussian distribution of the barrier heights for PdSe₂.



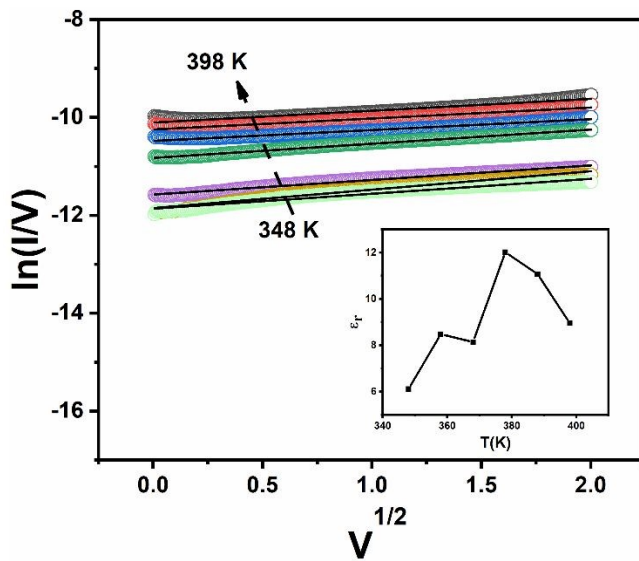
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Figure 4: $\ln(I/V)$ vs. $V^{1/2}$ for the Poole–Frenkel emission at temperatures 348 K–398 K; the inset shows the variation of dielectric constant.

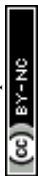


Figure 5

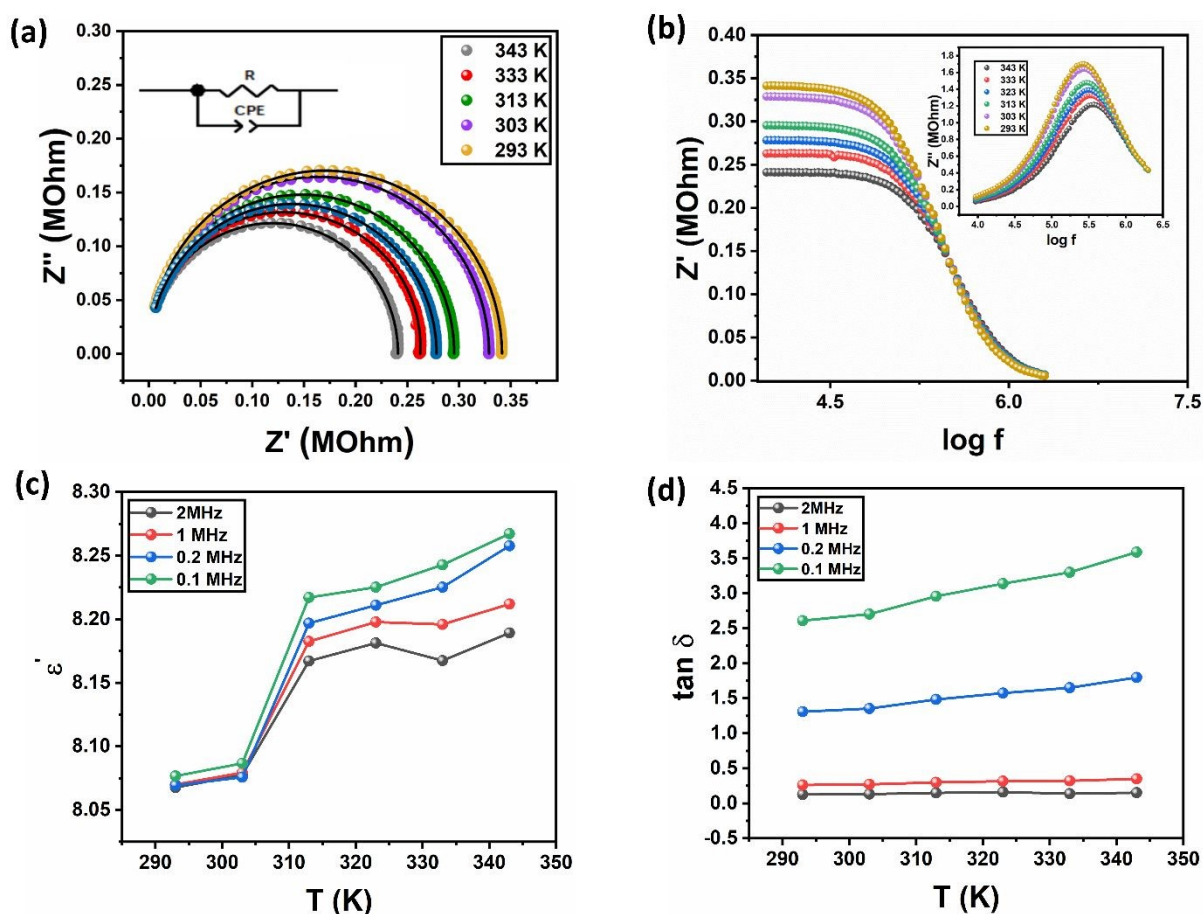
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Figure 5: (a) The Nyquist plots of PdSe₂ flake in the temperature range of 293 K–343 K along with the circuit fittings and the inset shows the equivalent circuit containing a resistance (R) and a constant phase element (CPE) connected in parallel. (b) The variation of Z' with frequency in the temperature range of 293 K–343 K; the inset exhibits the behavior of Z'' with frequency in the temperature range of 293 K–343 K. (c) The trend of ϵ' with temperature at specific values of frequency. (d) The trend of $\tan \delta$ with temperature at specific values of frequency.

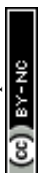


Figure 6

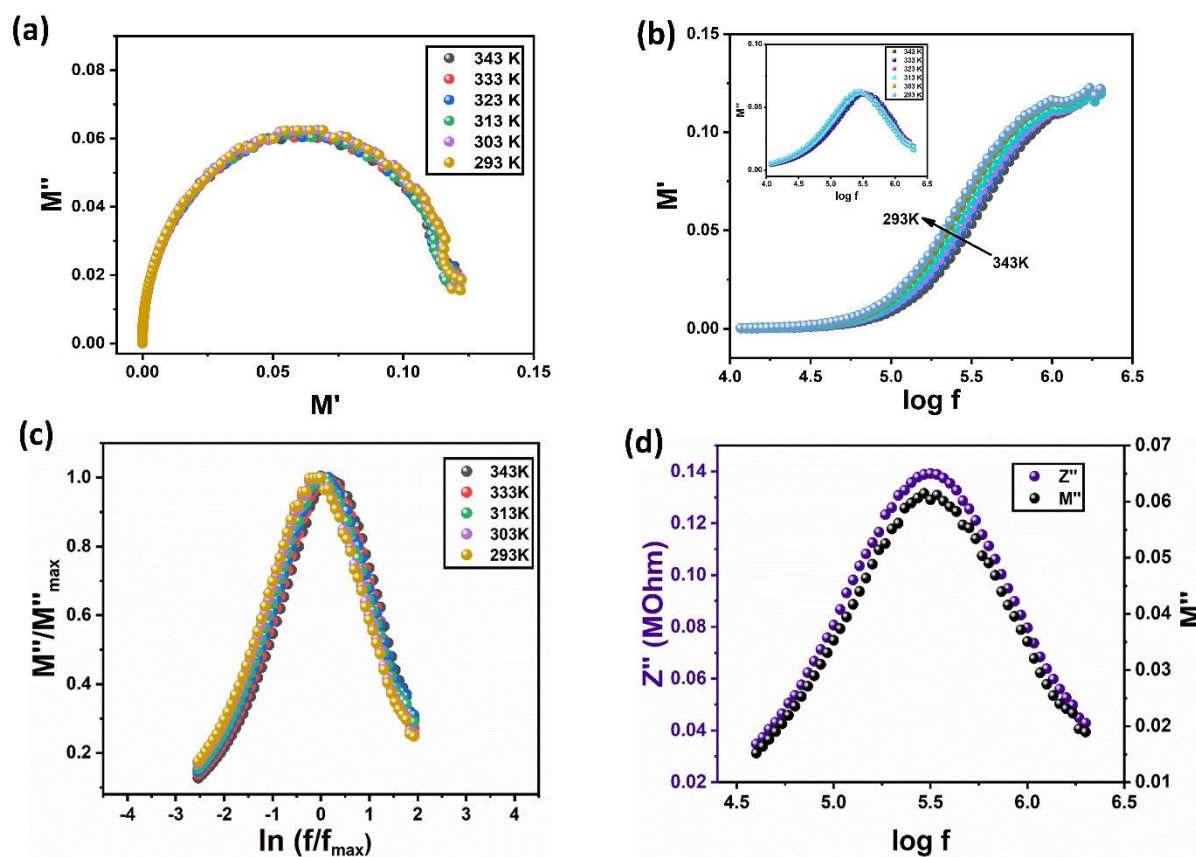
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Figure 6: (a) The M' vs M'' plots of mechanically exfoliated PdSe₂ flake in the studied temperature range. (b) The behavior of M' with frequency at different temperatures; the inset shows the behavior of M'' with frequency at different temperatures. (c) M''/M''_{\max} vs $\ln(f/f_{\max})$ plot at specific values of temperature. (d) The comparison between Z'' and M'' with frequency at a specific temperature of 323 K.



Table 1View Article Online
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Table 1: The Z-view fitted parameters of the model (R-CPE) at specific temperature values.

Temperature (K)	Resistance R (Ohms)	Capacitance C (pF)	n
343	2.418×10^5	1.847	1.0
333	2.634×10^5	1.833	0.9
323	2.783×10^5	1.825	0.87
313	2.955×10^5	1.821	0.92
303	3.291×10^5	1.795	0.88
293	3.413×10^5	1.779	0.79



Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

