



Cite this: DOI: 10.1039/d5tc01664a

Received 24th April 2025,
Accepted 9th July 2025

DOI: 10.1039/d5tc01664a

rsc.li/materials-c

Vertical p–i–p perovskite photoconductors
combining intrinsic and doped organic transport
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Photoconductors are a class of photodetectors that can exhibit photoconductive gain, a key advantage for achieving high sensitivity. While most reported devices rely on lateral architectures, here we present fully vacuum-deposited vertical p–i–p perovskite photoconductors based on methylammonium lead iodide (MAPI). The influence of hole transport layers (HTLs), both intrinsic and doped, was systematically investigated. Devices incorporating intrinsic HTLs show significantly reduced dark currents, particularly when the intrinsic layer is sufficiently thick. When intrinsic and doped HTLs are combined, the devices benefit from both suppressed dark current and enhanced charge extraction, leading to superior performance. Optimized structures achieve quantum efficiency >3000% and high on/off ratio, demonstrating the potential of this vertical architecture for highly sensitive optoelectronic applications.

Introduction

Semiconducting metal halide perovskites (herein simply perovskites) have attracted significant attention in optoelectronics due to their suitable properties: high optical absorption coefficient,¹ tuneable and direct bandgap,^{2–5} high defect tolerance,^{6,7} low dark current^{8–10} and high charge mobility.^{11–13} For this reason, perovskites have been applied to a variety of devices such as solar cells,^{14–18} photodetectors,^{19–22} lasers,^{23,24} and light-emitting diodes.^{25–27} Photodetectors are optoelectronic devices that transduce light into electrical signals, in the form of either current or voltage. They can be broadly classified as photodiodes, photoconductors, and photomultiplication detectors. Photodiodes are widely used vertical devices, as they are fast and reliable, with external quantum efficiency (EQE) maximum at unity, as each absorbed photon can only trigger one charge carrier.^{19,28–31} On

the other hand, photoconductors (PCs) are typically lateral devices with symmetrical electrodes, mostly reported using perovskite single crystals.^{32–34} PCs can achieve EQE values above unity, as one incident photon can lead to a charge carrier circulating several times within the device, a phenomena referred to as photoconductive gain.^{35–37} This gain comes from defects/trap states that capture the minority charge carriers (either holes or electrons), allowing the majority charge carriers to flow multiple times through the external circuit.^{38,39} As mentioned, most of the reported PCs are lateral devices, with only very few works describing vertical structures. For instance, Zhang *et al.* designed a photoconductive device with a photodiode structure comprising an electron transport layer of C₆₀ (fullerene) doped with 2,3,5,6-tetrafluoro-7,7,8,8-tetracyanoquinodimethane (F₄-TCNQ), in which the dopant provided deep trap states to capture the photogenerated electrons from the perovskite (electron trapping). The hole injection barrier was thin enough to allow holes to tunnel through, resulting in hole injection and gain.⁴⁰ While representing a neat example, the device is a photomultiplication-type detector rather than a photoconductor, as it rectifies as a diode in the dark and shows gain under illumination. Zhao *et al.* demonstrated vertical perovskite PCs with the structure ITO/C₆₀/MAPI/C₆₀/Ag, where the incorporation of the charge transport layer, C₆₀, improved the crystal quality of the solution-processed (spin-coated) perovskite, and enhanced the EQE and the photocurrent of the methylammonium lead iodide (MAPI) photodetector.⁴¹ The characterization of the device was however limited to the EQE, with no description of the photo- and dark currents of the device. Hence, to our knowledge, there has been no thorough study on the effect of the transport layers on the performance of vertical perovskite PCs, and we haven't found reports on vapour-deposited PCs employing multilayer transport layers. Vapour or vacuum deposition of perovskite devices offers several advantages. Importantly, physical vapour deposition techniques are already well established in industry for large-area processing. Being solvent-free, these methods eliminate the need for thermal post-processing, enabling integration with sensitive substrates such as

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† Electronic supplementary information (ESI) available. See <https://doi.org/10.1039/d5tc01664a>



plastic foils. Moreover, vapour deposition allows the fabrication of multilayer devices with arbitrary thicknesses—capabilities that are difficult to achieve with solution-based processing.^{42–44}

In this work, we fabricate fully vacuum-deposited perovskite PCs, where the perovskite layer is sandwiched between two organic hole transport layers (HTLs) in a vertical p–i–p configuration. We study the influence of the incorporation of HTLs (both intrinsic and doped) on the performance of the devices, as well as the effect of the thickness of the transport layers and the dopant concentration. We show that intrinsic HTLs are essential to reduce dark current in vertical PCs. Moreover, when combining intrinsic and doped HTLs, the devices benefit from both layers, with a reduction of the dark current (intrinsic HTL) and an improved charge extraction/transport (doped HTL). EQE > 3000% and high on/off ratio were obtained for devices with high dopant concentrations and thick intrinsic hole transport layers.

Results and discussion

Vertical perovskite PCs in a p–i–p configuration (p-type organic/intrinsic perovskite/p-type organic) were fabricated with the following stack: glass/ITO/HTL/MAPI/HTL/Ag, where ITO is indium tin oxide. The HTLs were systematically varied on either side of the active layer and on both sides. The organic semiconductor used as the HTL is *N*4,*N*4',*N*4'',*N*4'''-tetra([1,1'-biphenyl]-4-yl)-[1,1':4',1''-terphenyl]-4,4'-diamine (TaTm). When indicated, TaTm was doped with 2,2'-(perfluoronaphthalene-2,6-diylidene) dimalononitrile (F₆-TCNNQ). We also explore multilayers of TaTm and TaTm:F₆-TCNNQ. All layers were deposited by thermal vacuum deposition. Details on the processing can be found in the methods section.

We initially fabricated simple PCs by depositing 500 nm thick MAPI films directly on an ITO-coated glass, and coating

the perovskite with a 100 nm thick silver electrode (Fig. 1a). Dark and photocurrent density *versus* voltage (*J*–*V*) characteristics of the PCs were measured only in the reverse bias quadrant, from –2 to 0 V, which is relevant for applications. In this way we can neglect current hysteresis between the forward and reverse voltage scans, resulting from the coexistence of electronic and ionic transport within the perovskite itself,⁴⁵ phenomena that are not within the motivation of this work. The reference ITO/MAPI/Ag device shows an ohmic behaviour both in the dark and under illumination (see linear *J*–*V* scan in Fig. S1a, ESI†). The dark current density (*J*_{dark}) is, however, very high (nearly 800 mA cm^{–2} at –2 V), and the current density under illumination (*J*_{ph}) is only slightly higher, with a rather voltage-independent on/off ratio *J*_{ph}/*J*_{dark} between 1.3 and 1.4. This device configuration might suffer from two different effects: (i) the MAPI film is evaporated directly onto a metal oxide (ITO), which is known to retard and alter the perovskite formation from the vapour phase,^{46,47} and (ii) the Ag electrode is directly evaporated onto the perovskite, inducing damage and/or chemical reactions at the MAPI/Ag interface.⁴⁸

Inspired by our previous work on vacuum-deposited perovskite solar cells and photodiodes employing organic transport layers, we studied the influence of using intrinsic and doped organic semiconductors, as well as their combinations, at the interface between the perovskite and the electrodes.^{49,50} Intrinsic TaTm films are typically thin (10 nm) in view of their moderate mobility, while doped layers of the type TaTm:F₆-TCNNQ (10 wt% doping) can be used thicker (40 nm) as they are more conductive.

When an intrinsic, 10 nm thick, TaTm layer is introduced between the electrodes and the perovskite, the dark current (Fig. 1b and Fig. S1b, ESI†) is slightly reduced, while the photocurrent increases, leading to a larger *J*_{ph}/*J*_d ratio > 3. This might result from reduced shunts, alleviating detrimental

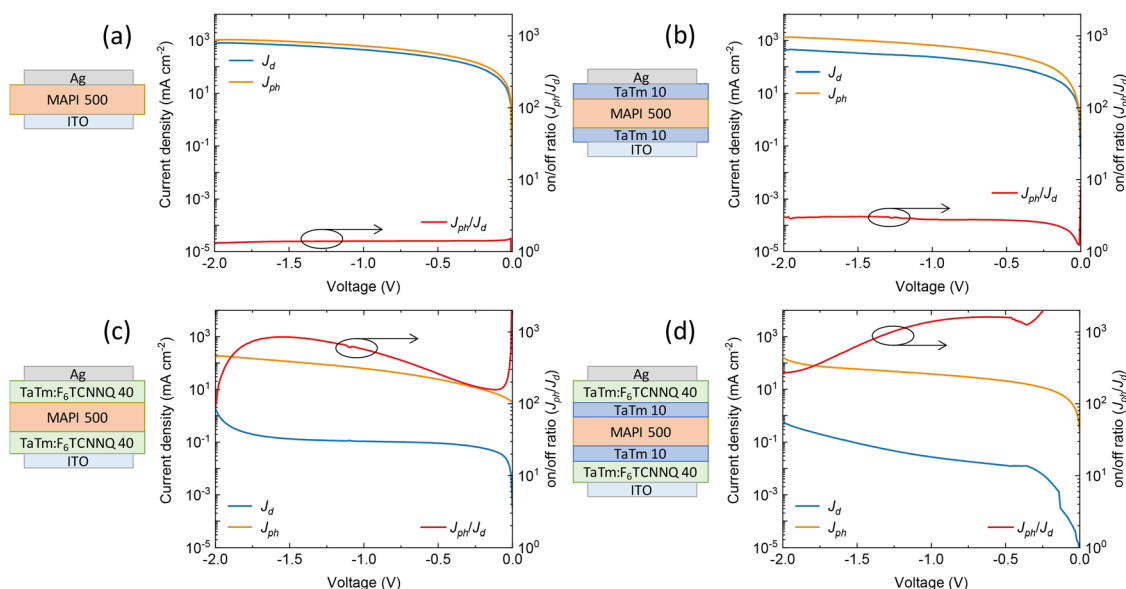


Fig. 1 Electrical characterization of vertical photoconductors with (a) electrodes only, (b) intrinsic HTLs, (c) doped HTLs and (d) a combination of intrinsic and doped HTLs. On the left axes, *J*–*V* characteristics in the dark (blue line) and under 100 mW cm^{–2} illumination (orange line). On the right, the corresponding calculated on/off ratio between *J*_{ph} and *J*_d (red line).



effects at the interfaces as discussed above. When thicker, doped TaTm:F₆-TCNNQ layers (Fig. 1c) are used, we observed a noticeable reduction of the dark current (more than 3 orders of magnitude), approximately 0.1 mA cm⁻² at -1 V, and increasing to approximately 2 mA cm⁻² at -2 V. Compared to the device with TaTm only, the photocurrent is also slightly reduced, most likely due to parasitic absorption. However, the net result is a maximum J_{ph}/J_d ratio of 850 at -1.5 V. At higher bias, the on/off ratio diminishes due to the increasing J_d , while at lower bias it is limited by the J_{ph} . Finally, we evaluated the incorporation of both intrinsic and doped charge transport layers, as commonly used on efficient perovskite solar cells.^{49,51} When the combination of intrinsic and doped layers is symmetrically placed at the electrode interfaces (Fig. 1d), the photocurrent is essentially unaltered (compared to the PCs with doped HTLs), but a further reduction of the dark current is observed, in the range from 0 to -1.5 V. As a consequence, a maximum J_{ph}/J_d ratio exceeding 1600 is obtained between -0.5 and -0.75 V, monotonically reducing (due to increasing J_d) to >250 at 2 V. In this case, the doped layer is used at the interface with the electrodes, to match the work function ensuring ohmic charge injection/extraction, while the intrinsic HTLs are used at the interface with the perovskite, to reduce charge recombination and induce carrier-type selectivity. Both concepts, arising from photovoltaics and light-emitting diodes, seem to be also beneficial in these vertical PCs.

Subsequently, we investigated the influence of the intrinsic HTL thickness and dopant concentration on both the dark current and photocurrent in the vertical PCs. The J - V curves for devices with the structure ITO/TaTm/MAPI/TaTm/Ag and with two thicknesses of TaTm (10 nm and 20 nm), are shown in Fig. 2a. We observed a striking reduction of J_d when increasing

the thickness of TaTm to 20 nm, to values below 10⁻⁴ mA cm⁻², which fall within the noise region of our measurement setup. To our knowledge, such low dark current density has not yet been reported for vertical perovskite photoconductors.^{41,52} This is likely a consequence of the increased injection/extraction barrier at the electrode interface, and partially of the thicker HTLs, with a lower mobility compared to the perovskite. J_{ph} also diminishes with thicker TaTm, but in general the on/off ratio is much higher compared to the same device with 10 nm thick TaTm (albeit not quantified in this case, in view of the noise in J_d). We then introduced the doped HTLs at the electrode interfaces, and systematically varied the doping concentration during co-deposition of TaTm and F₆-TCNNQ (from 3.5 to 20 wt%). For devices with 10 nm thick TaTm, increasing the dopant concentration (Fig. 2b) leads to an increased J_d in the high bias region (below -1 V), approximately varying from 0.2 to 10 mA cm⁻² at -2 V. The photocurrent is found to scale with the doping concentration, reaching J_{ph} > 350 mA cm⁻² (between -1 and -2 V) for the samples with doping concentration of 11 and 20 wt%. More interesting is the profile of the on/off ratio, whose maximum shifts towards lower applied bias with increasing doping. At low voltage, the on/off ratio is higher for the PCs including doped layers at high F₆-TCNNQ concentration, with maxima between approximately 5 × 10³ and 10⁴ for the 20 and 11 wt% dopant, respectively. However, due to the rapid increase in J_d with applied bias in these devices, the on/off ratio monotonically decreases as the voltage becomes more negative, reaching values between 40 and 60 at -2 V. At this same bias, devices with lower dopant concentrations exhibit a slightly higher on/off ratio, around 200. For these samples, the on/off ratio also increases as the voltage becomes more positive, peaking at approximately

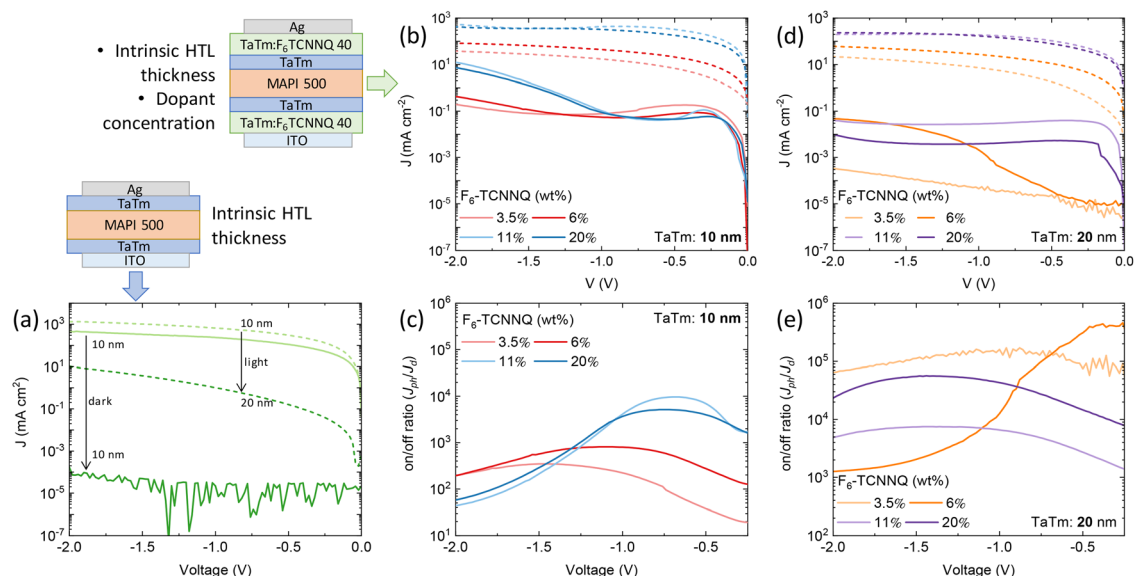


Fig. 2 (a) J - V curves under illumination (dashed) and in the dark (solid) of vertical PCs employing solely intrinsic HTLs (schematic of the device structure above the graph). J - V curves under illumination (dash) and in the dark (line) of vertical PCs using a combination of doped transport layers with increasing doping levels and either a (b) 10 nm or (d) 20 nm thick intrinsic HTL (schematic of the device structure on the top left). On the bottom, the profile of the on/off ratio (c) and (e) calculated from the J - V curves in (b) and (d), respectively (schematic of the device structure to the left of graph (b)).



800 and 350 for dopant concentrations of 6 wt% and 3.5 wt%, respectively.

When the thickness of the intrinsic TaTm is increased to 20 nm, J_d is generally reduced (Fig. 2d), as observed for the simpler devices in Fig. 2a. J_d remains below 10^{-3} mA cm $^{-2}$ at reverse bias up to -2 V for the 3.5 and 6 wt% samples. As the dopant concentration increases, J_d also increases substantially, particularly evident at 11 and 20 wt%, where it reaches a maximum of up to 10^{-1} mA cm $^{-2}$, indicating enhanced conductivity with doping.

Under illumination, all devices show a significant increase in current density across the entire bias range. The current density enhancement is particularly pronounced for the 11 and 20 wt% samples, with J_{ph} exceeding 10^2 mA cm $^{-2}$ near -2 V. Lower dopant concentrations (3.5 and 6 wt%) still show clear photoresponse, although with more modest photocurrent values. As compared to the devices with thinner intrinsic TaTm, J_d is lower while J_{ph} varies only modestly. As a consequence, the on/off ratios (Fig. 2e) are also enhanced. The device with 6 wt% doping concentration shows a very high but not representative on/off ratio at low bias, resulting in the combination of very low J_d and high J_{ph} . In a more realistic working regime (< -0.5 V), however, the on/off ratio diminishes rapidly, reaching a minimum of $>10^3$ at -2 V. For the PCs with a lower F $_6$ -TCNNQ concentration, the on/off ratio shows less bias-dependency and is very high, up to 10^5 , a consequence of the very low J_d . For higher doping concentrations, the maxima of the on/off ratio are located at approximately -1.5 V, and increase from 7.5×10^3 to 5.5×10^4 for 11 and 20 wt% dopants, respectively. Overall, these PCs, which incorporate an intrinsic 20 nm TaTm layer combined with highly doped TaTm, exhibit the best performance among all devices presented thus far. They deliver a simultaneously high photocurrent and a very large on/off ratio, highlighting their potential for efficient light detection.

We also quantified the photoresponse of selected vertical PCs to different light intensities. Here the photocurrent was recorded from 100 mW cm $^{-2}$ down to 10^{-5} mW cm $^{-2}$, by using a solar simulator and neutral density filters. The linear dynamic range (LDR), defined as the range over which J_{ph} maintains a linear relationship with incident light intensity, was estimated using the expression $LDR = 20 \times \log(P_{max}/P_{min})$, where P_{max} and P_{min} represent the maximum and minimum light intensities

within the linear response regime of the detector.^{20,53,54} The linear dynamic range was calculated for the photoconductors with the stack ITO/doped-TaTm/TaTm/MAPI/TaTm/doped-TaTm/Ag, for the two thicknesses of TaTm (10 and 20 nm) and for the two extreme dopant concentrations (3.5 and 20 wt%, respectively). These devices show similar LDR values (Fig. S2, ESI †), approximately 90 dB and 80 dB for photoconductors with 3.5 wt% and 22 wt%, respectively.

Fig. 3a summarizes J_d values measured at -2 V for all devices reported so far. Devices incorporating a sufficiently thick HTL, as indicated by the red dashed line, exhibit strongly suppressed J_d on the order of 10^{-4} mA cm $^{-2}$. In contrast, devices combining a 20 nm intrinsic TaTm layer and additional 40 nm thick doped TaTm:F $_6$ -TCNNQ film, show a non-linear increase in J_d with increasing dopant concentration (red dots), reaching 2.8×10^{-2} mA cm $^{-2}$ for a 20 wt% dopant concentration. On the other hand, when the intrinsic TaTm thickness is reduced to just 10 nm (blue dashed line), the dark current increases dramatically to approximately 10^{-3} mA cm $^{-2}$, highlighting the importance of sufficient (intrinsic) layer thickness for suppressing leakage currents. Devices incorporating both intrinsic and doped HTLs within this thinner configuration (blue dots) show comparatively lower J_d , indicating that even a modest intrinsic layer can provide a beneficial barrier when paired with a doped transport layer.

Fig. 3b illustrates the corresponding J_{ph} for all PCs. When the intrinsic TaTm layer is 20 nm thick, the combination of intrinsic and doped HTLs results in improved photocurrent, due to the increased electrical conductivity of the doped layer that results in a more efficient charge injection/transport, similar to what was previously reported for perovskite solar cells.⁵⁵ However, this trend does not persist, and at the highest dopant concentration (20 wt%), the photocurrent begins to decline slightly compared to the 10 wt% case, likely due to the high absorbance of the doped-HTL, which introduces parasitic optical absorption. Therefore, there is a trade-off between improved charge extraction and parasitic absorption.

In summary, devices that combine intrinsic and doped HTLs benefit from both a low dark current thanks to the intrinsic layer, and a higher photocurrent due to the improved conductivity of the doped layer. Hence, we selected these PCs to estimate their spectral response. Fig. 3c shows the external

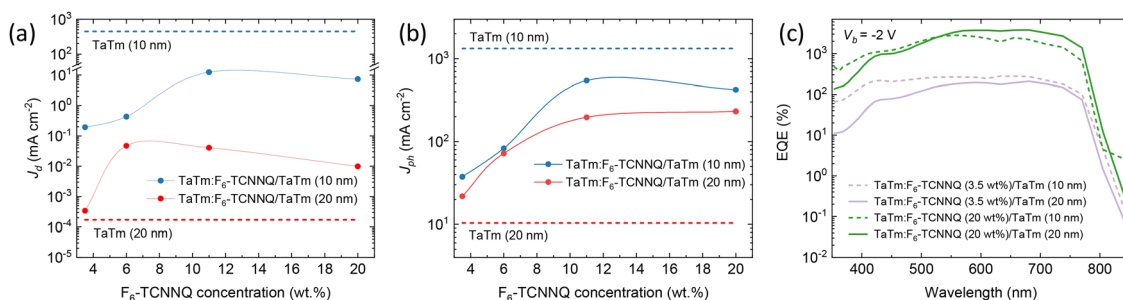


Fig. 3 Summary of the detector signals at a -2 V applied bias for the series of devices discussed in this work, with only an intrinsic HTL (dash lines) and a combination of intrinsic and doped-HTL (dots, the lines are a guide to the eye) at different dopant concentrations: (a) dark current and (b) photocurrent. (c) EQE spectra of selected vertical photoconductors, also measured at an applied bias of -2 V.



quantum efficiency (EQE) of the photoconductors with ITO/doped-TaTm/TaTm/MAPI/TaTm/doped-TaTm/Ag, for the two thicknesses of TaTm (10 and 20 nm) and for the two extreme dopant concentrations (3.5 and 20 wt%, respectively). All devices exhibit EQEs exceeding unity, indicative of photoconductive gain. As schematically shown in Fig. S3 (ESI[†]), the gain in our devices originates from the unipolar nature of their structure. In the dark, only holes can be injected at moderate bias, as the energy barrier for electron injection from Ag into the LUMO of TaTm is too high. Under illumination, the perovskite becomes more conductive due to the generation of free carriers. However, the large energy offset between the perovskite conduction band and the LUMO of TaTm confines electrons within the perovskite. Holes, on the other hand, remain mobile and can circulate multiple times through the external circuit before recombining with the trapped electrons, resulting in EQE values exceeding unity.³⁹ Devices with a thinner intrinsic TaTm layer (10 nm, dashed line) show higher EQE values in the UV-blue region of the spectrum (350–500 nm) compared to those with 20 nm of TaTm. This enhancement is attributed to reduced optical absorption in the thinner hole transport layer, allowing more light to reach the active perovskite layer. Furthermore, increasing the dopant concentration from 3.5 wt% to 20 wt% leads to a nearly two orders of magnitude increase in EQE, reaching values as high as 3800% at 680 nm. This trend is consistent with the previously observed photocurrent enhancement and reflects the improved charge transport and collection enabled by higher conductivity in the doped HTL. Finally, the specific detectivity (D^*) was estimated assuming that shot-noise is the predominant source of the noise in the device (Fig. S4, ESI[†]).^{56–61} Devices with higher dark current (those with 10 nm of the intrinsic TaTm layer, dashed line), show lower D^* , as expected. The maximum specific detectivity is 4×10^{13} Jones at -2 V for the devices with the combination of intrinsic (20 nm) and doped HTL (40 nm, 20% wt), which is in line with similar vertical perovskite photoconductors reported so far.⁴¹ However, this D^* estimate is likely a rough overestimation as it does not account for other noise sources.⁶²

Conclusions

We demonstrated fully vacuum-deposited p–i–p vertical photoconductors incorporating a methylammonium lead iodide (MAPI) perovskite active layer sandwiched between organic hole transport layers (HTLs). The role of the HTLs in determining device performance was systematically investigated by employing intrinsic TaTm, F₆-TCNNQ-doped TaTm, and combinations of both. We further evaluated the influence of intrinsic HTL thickness and dopant concentration. The inclusion of intrinsic HTLs was found to be critical for suppressing the dark current, particularly when the TaTm thickness reached 20 nm, yielding values below 10^{-4} mA cm⁻². Devices combining intrinsic and doped HTLs benefitted from both low dark currents, enabled by the intrinsic layer, and relatively high photocurrents due to the

enhanced conductivity of the doped HTL. Notably, photoconductors featuring thick intrinsic layers and high dopant concentrations achieved high performance, with external quantum efficiency (EQE) up to 3800% and on/off ratio $> 50\,000$ at -2 V, highlighting the potential of this architecture for sensitive light detection applications.

Materials and methods

Materials

N4,N4',N4'',N4'''-Tetra([1,1'-biphenyl]-4-yl)-[1,1':4',1''-terphenyl]-4,4'-diamine (TaTm) and 2,2'-(perfluoronaphthalene-2,6-diylidene)dimalononitrile (F₆-TCNNQ) were provided by TCI Chemical. CH₃NH₃I (MAI) and PbI₂ were purchased from Luminescence Technology Corp.

Device fabrication

Pre-patterned ITO-coated glass substrates were subsequently cleaned with soap, water and isopropanol in an ultrasonic bath, followed by 20 min UV-ozone treatment. Substrates were transferred to a vacuum chamber integrated in a nitrogen-filled glovebox and evacuated to a pressure of 10^{-6} mbar. In general, the deposition rate for TaTm was kept constant at $0.8 \text{ \AA s}^{-1}$ while varying the deposition rate of the dopant (F₆-TCNNQ) during co-deposition. Pure TaTm was deposited at a rate of $0.5 \text{ \AA s}^{-1}$. Ag was deposited in a second vacuum chamber using an aluminium boat as the source by applying currents ranging from 2.5 to 4.5 A. MAPI was deposited following a previously published protocol.⁶³ For encapsulation, Al₂O₃ (30 nm) was deposited by atomic layer deposition in an Arradiance reactor at 40°C , using a previously developed process.⁶⁴

PD characterization

The J - V curves of the PDs were recorded with a Keithley 2612A SourceMeter with 0.01 V steps and were illuminated under a Wavelabs Sinus 70 LED solar simulator using a custom LabVIEW program. The light intensity was calibrated before every measurement using a calibrated Si reference diode. Intensity-dependent data were carried out by measuring J - V curves in the same system using neutral density filters of decreasing optical density. In the EQE measurements, the devices were illuminated with a Quartz-Tungsten-Halogen lamp (Newport Apex 2-QTH) using a monochromator (Newport CS130-USB-3-MC), a chopper at 77 Hz and a focusing lens. The device current was measured as a function of energy from 3.5 eV to 1.5 eV in 0.05 eV steps using a lock-in amplifier (Stanford Research Systems SR830). The system was calibrated and the solar spectrum mismatch was corrected using a calibrated Si reference cell.

Conflicts of interest

There are no conflicts to declare.



Data availability

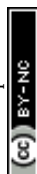
Data from the manuscript will be made available at the University of Valencia's repository Roderic (<https://roderic.uv.es/>) and European repository Zenodo (<https://zenodo.org/>).

Acknowledgements

The authors acknowledge financial support from the Comunitat Valenciana (CISEJI/2022/43), the Ministry of Science and Innovation (MCIN) and the Spanish State Research Agency (AEI): project PID2022-139910NB-I00 funded by MCIN/AEI/10.13039/501100011033 and by FEDER, UE; grant PREP2022-000246 funded by MCIN/AEI/10.13039/501100011033 and by the FSE+.

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