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1 Introduction

Barium strontium titanate $(Ba_{1-x}Sr_xTiO_3)$ is a perovskitestructured ferroelectric system that is part of the widely studied A-site substituted BaTiO₃ family of materials.¹⁻³ The substitution of Ba²⁺ for Sr²⁺ efficiently shifts the phase transition of the BaTiO₃ toward room temperature while the cationic disorder broadens the phase transition, thereby reducing the temperature dependence of the resulting functional properties.4,5 Consequently, Ba_{1-x}Sr_xTiO₃ (BST) has been taken under consideration mainly as dielectric bolometers for infrared detection, tunable capacitors for microwave devices and pyroelectric imaging detectors.⁶⁻¹⁰ Recently, however, BST has gained great attention due to its potential applications in energy storage because it possesses relatively high energy storage density and efficiency, remarkable breakdown strength, and good reliability.¹¹

Theoretical and experimental studies have evidenced that the Ba/Sr ratio can greatly influence the energy storage properties of BST ceramics depending on the operating electric

High performance BaTiO₃-SrTiO₃ ceramics with refined microstructure obtained by current-controlled reactive flash sintering of mixed powders†

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Barium strontium titanate is a well-known perovskite-structured ferroelectric system that has recently gained attention for its energy storage capabilities. Here, fine-grained BaTiO₃-SrTiO₃ ceramics are obtained by reactive flash sintering of mixed BaTiO₃ and SrTiO₃ powders, a one-step sintering process that significantly reduces the energy consumption involved. The current control mode is employed here to manage the reactive flash sintering event for a striking refinement of the microstructure. The effect of microstructure on the dielectric, ferroelectric, and energy storage properties of flash-sintered ceramics is discussed through the analysis of impedance spectroscopy data. The quality and electrical homogeneity of the obtained materials are shown to be key factors in optimizing the energy storage properties of dielectric materials. This work highlights the potential of current-controlled flash sintering as a powerful tool for microstructural engineering in dielectric materials, offering new pathways for the development of energy-efficient energy storage systems.

> field.^{12,13} Thus, compositions with high Sr content (*i.e.*, linear dielectrics) are desirable for high working electric fields $(>300 \text{ kV cm}^{-1})$, but these materials show low dielectric permittivity and are limited by the breakdown strength they can reach (typically lower than 300 kV cm⁻¹).¹⁴ Therefore, BST compositions commonly explored for energy storage are those with x = 0.5-0.7, though the published results are diverse.¹⁵⁻²⁰ Hence, not only are different results reported for the same composition, but similar energy storage properties are also achieved from compositions with different Ba/Sr ratios, which can be explained in terms of the grain size effect.

> The effect of grain size on the energy storage properties of electroceramics is widely discussed in the literature.²¹ Grain size engineering has been proven to be an efficient strategy for simultaneously improving energy storage performance and mechanical properties in lead-free perovskite ferroelectrics.14,22 In BST, particularly, although some functional properties certainly worsen as grain size decreases,^{23,24} the energy storage performance has been demonstrated to enhance.^{19,20} It has been shown that compositions with different Sr content experience an enhancement in their energy storage properties through proper microstructure control, primarily related to an improvement in electrical breakdown strength. In general terms, smaller grains down to the submicron scale yield higher energy storage performance.

> Dense, fine-grained BST ceramics has been generally obtained by spark plasma sintering,^{19,20,23,25} a well-known technique that

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require a rather complex and expensive equipment. Recently, however, Ba_{0.6}Sr_{0.4}TiO₃ was successfully flash-sintered from powders prepared via the conventional solid-state reaction method using BaCO₃, SrCO₃, and TiO₂, resulting in a finegrained microstructure that enhances its properties as a tunable dielectric ceramic.²⁶ In this work, we use current-controlled reactive flash sintering of mixed BaTiO₃ and SrTiO₃ powders to obtain highly dense BST ceramics with a refined microstructure, aimed at improving energy storage performance. Flash sintering is a novel, low-energy consumption, and significantly cheap fieldassisted sintering route that is attracting growing interest because dense ceramics can be obtained in few minutes and at relatively low temperatures.^{27,28} A considered suboptimal composition, namely Ba_{0.6}Sr_{0.4}TiO₃, is taken here as a reference to demonstrate that microstructure control is the definitive key factor for improving the energy storage capabilities of ferroelectric ceramic systems.

2 Experimental

2.1 Sintering experiments

Reactive flash sintered samples are obtained by performing flash sintering experiments using commercially available BaTiO₃ and SrTiO₃ powders as precursors. Barium titanate (Sigma Aldrich, \geq 99.5%, Ref. 338842-100G) and strontium titanate (Sigma Aldrich, \geq 99%, Ref. 396141-100G), both with particle size of $<2 \mu m$, were used in a molar ratio of 60:40 to produce the Ba_{0.6}Sr_{0.4}TiO₃ composition. Green samples (GS) are formed by uniaxial pressing the powder into a dog bone shape using a load of 270 MPa (Fig. S1a, ESI[†]). To perform the sintering experiments, GS is hanged in the centre of the furnace using two platinum wires attached to its handles following a typical flash sintering setup (Fig. S1b, ESI⁺). A programable DC power supply (Ametek XG 600-2.6) is controlled employing a homemade software, changing its operation mode automatically upon reaching the previously set current limit. In order to ensure electrical contact between the sample and the platinum wire electrodes, platinum paint is used (SPI supplies, 04990-AB) mixed with platinum paint thinner (SPI supplies, 04989-AB). The use of an adequate paint geometry allows to obtain a uniform electric current flow across the sample by mimicking a plane parallel capacitor (Fig. S1a, ESI⁺). Luminescence and shrinkage of the samples are recorded with a CCD camera.

Conventional reactive flash sintering (RFS) and currentcontrolled reactive flash sintering (CRFS) experiments (Fig. S2, ESI†) are carried out to study the influence of the electric parameters over the resulting samples' density and microstructure. For the case of RFS experiments, the furnace is heated from room temperature at a rate of 10 °C min⁻¹ while the sample helds under a constant electric field. When the flash event takes place (*i.e.*, the current runaway) the power source changes to constant current mode and the sample is left to dwell for a set time. Finally, the furnace is shut down and the sample cools down naturally. For the case of CRFS experiments, the furnace is heated once again using a rate of 10 °C min⁻¹ from room temperature up until a preset temperature, which depends on the specific experiment, but no electric field is preset. The samples are then held exclusively in current control mode until reaching the maximum preset current, when they are then left to dwell for a set time. After finishing the experiment, the furnace is shut down and the samples are left to cool down naturally.

2.2 Microstructure and functional characterization

In order to ensure reproducibility and minimize inhomogeneities, the sintered specimens are cut far from the vicinity of the electrical contacts in rectangular cuboid shape (10 mm \times 3 mm \times 1 mm). Density is measured *via* Archimedes method. Fieldemission scanning electron microscope measurements (FESEM JEOL-7100) are performed in polished and thermally etched samples. X-ray diffraction patterns of dense samples were obtained from a PANalytical X'Pert PRO MPD diffractometer with CuKa radiation. The electrical characterization of the specimens is done after the application of electrical contacts via gold sputtering. Permittivity values of unpoled samples are measured using an LCR meter (Agilent E4980A) from approximately 400 K to 20 K using frequencies that range from 100 Hz up to 1 MHz. Electric field-induced polarization (P-E) hysteresis loops are obtained using a typical Sawyer-Tower setup by applying a triangular electric field of up to 5 kV mm⁻¹ at a frequency of 1 Hz at room temperature. Impedance spectroscopy experiments are conducted with a frequency response analyser (AMETEK solartron analytical). Impedance measurements are taken across a frequency range from 100 mHz to 1 MHz at various temperatures. The samples are heated in a furnace from room temperature with a ramp rate of 5 $^{\circ}$ C min⁻¹ until the target temperature is reached. At this point, the sample is allowed to stabilize thermally for 10 min.

3 Results and discussion

3.1 Reactive flash sintering

A comprehensive study about the influence of the flash sintering parameters over the densification and microstructure of BaTiO₃ (BTO) has been recently published,²⁹ which has served as the basis for the RFS experiments of BaTiO₃ and SrTiO₃ mixed powders. A set of electrical conditions were employed in order to attempt high densification of the resulting Ba_{0.6}Sr_{0.4}-TiO₃ composition. Results are recorded in Fig. S3 (ESI⁺), showing that low electric fields and moderate electric current densities (15-30 mA mm⁻²) yield better results. Although RFS experiments were not successful in achieving highly dense specimens, best results have been considered as starting point for the CRFS experiments. The current control is an emerging parameter in flash sintering that has recently gained attention.²⁹⁻³³ It is known to both enhance densification and be closely related to defect formation and microstructure tailoring during flash sintering. In CRFS experiments, the furnace temperature is set constant, and therefore, the sample accommodates an appropriate electric field to maintain the desired

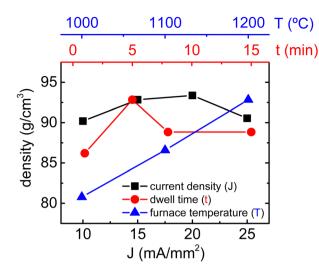


Fig. 1 Density of CRF-sintered Ba_{0.6}Sr_{0.4}TiO₃ samples upon different electrical condition. Different furnace temperatures were tested while keeping the current density and dwell time constant at 15 mA mm⁻² and 5 min, respectively. Various dwell times were then used maintaining the furnace temperature and the current density at 1200 °C and 15 mA mm⁻², respectively. Finally, several current densities were explored while keeping the furnace temperature and the dwell time at 1200 °C and 5 min, respectively, which correspond to the optimal conditions from the previous experiments. Only current densities yielding a density higher than 90% were considered.

electric current, which slowly rises. This approach avoids the sudden power spike of the flash event and minimizes temperature gradients, thereby ensuring a homogeneous sintering. Undoubtedly, these experiments require controlling a higher number of parameters such as the electric profile and current slope, which may initially seem like a hindrance. However, the CRFS technique provides a powerful set of tools that allow fine-tuning the microstructure. In fact, previous studies suggest that CRFS experiments not only finely tune functional properties and enhance densification, but also provide a new path to precisely achieve defect-engineered microstructures.^{29,34}

Fig. 1 shows the resulting density of flash-sintered Ba_{0.6}Sr_{0.4}-TiO₃ specimens for several CRFS configurations. First, the effect of the furnace temperature was evaluated by keeping a maximum current density of 15 mA mm⁻² and a dwell time of 5 min. Results show that a furnace temperature of 1200 $^\circ C$ yields good densification, and therefore, no higher temperatures were assessed. Next, various dwell times were explored showing that 5 min seems to be an optimal time to ensure high densification. In this case, a maximum current density of 15 mA mm⁻² and a furnace temperature of 1200 °C were used. Finally, a set of maximum current densities was tested since it is well-known that current density is a key parameter for finetuning the sample's microstructure in flash sintering.^{29,35,36} High densification (i.e., higher than 90%) is achieved for a current density range of 10-25 mA mm⁻², with a furnace temperature of 1200 °C and a dwell time of 5 min. Poor densification is expected for lower furnace temperature and/ or a different dwell time.

3.2 Microstructure and electrical properties

Samples exhibiting poor density (*i.e.*, lower than 90%) are discarded for further characterizations. Therefore, Fig. 2 and 3 show dielectric and ferroelectric properties only of the highdensity samples, which are labelled as CRFS/x, with x being the maximum current density at which each of them was sintered by CRFS. Real permittivity and dielectric losses versus temperature curves (Fig. 2) show slight differences between the samples. The real permittivity peak associated with the ferroelectric-paraelectric transition of Ba_{0.6}Sr_{0.4}TiO₃ is observed at 279 K, except for the CRFS/10 sample, where it is 10 degrees lower. Other two anomalies are also observed for CRFS/15, CRFS/20, and CRFS/25 samples, which correspond to the expected tetragonal-orthorhombic and orthorhombic-rhombohedral phase transitions.⁴ However, these anomalies are not as evident in the CRFS/10 sample, where an apparent single peak is revealed, although dielectric losses indicate the presence of more than one phase transition. Both the broadening of the real permittivity curve and the shift of the phase transition temperature are common features of grain size effect. The noticeable differences in maximum real permittivity could be related to the samples' density (Table 1), although in the case of the CRFS/10 sample, grain size effects make this correlation more complex. No frequency dispersion in the dielectric response is observed in any of samples (Fig. S4, ESI⁺). Typical ferroelectric hysteresis loops are displayed in Fig. 3 for all selected CRF sintered samples, showing notable difference depending on the sintering condition. Particularly, it can be

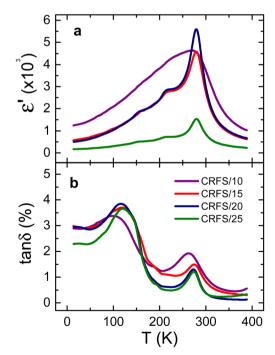


Fig. 2 Dielectric properties of the representative CRF-sintered $Ba_{0.6}Sr_{0.4}$ -TiO₃ samples. Temperature dependence of (a) real permittivity and (b) dielectric losses for all selected samples, at 10 kHz. Noticeable differences depending on the maximum current density employed during CRFS is shown.

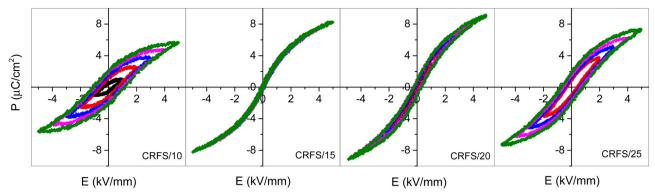


Fig. 3 Ferroelectric properties of the representative CRF-sintered $Ba_{0.6}Sr_{0.4}TiO_3$ samples. P-E hysteresis loops show appreciable differences depending on the current density, highlighting that a fully slim P-E response is evidenced when $Ba_{0.6}Sr_{0.4}TiO_3$ is obtained with a particular combination of furnace temperature and electrical conditions during CFRS.

Table 1 Room temperature ferroelectric (coercive field, E_c , remnant polarization, P_r , and saturation polarization, P_S) and dielectric (dielectric constant, ε' , and dielectric losses, tan δ) properties of dense flash-sintered samples. Sintering conditions (current density, J, furnace temperature, T, and dwell time, t), density, medium grain size (GS), and the temperature of the real permittivity peak (T_m) are also reported for all samples

	CRFS/10	CRFS/15	CRFS/20	CRFS/25
$J (\text{mA mm}^{-2})$	10	15	20	25
T (°C)	1200	1200	1200	1200
$t (\min)$	5	5	5	5
Density (%)	90.2	92.8	93.3	90.5
GS (µm)	3.64	4.27	5.84	5.77
$T_{\rm m}$ (K)	269	279	279	279
ε΄	3850	2870	3100	870
Tan δ (%)	1.04	1.00	4.57	5.64
$E_{\rm C}$ (kV mm ⁻¹)	1.08	0.10	0.50	0.77
$P_{\rm r}$ ($\mu \rm C \ cm^{-2}$)	1.94	0.30	1.71	1.94
$P_{\rm s}$ (µC cm ⁻²)	5.67	8.23	8.21	7.31

seen that the sample CRFS/15 stands out from the rest owing to its extremely slim ferroelectric response, making it ideal for energy storage purposes.^{11,21,37}

Table 1 records sintering conditions and some properties of the selected samples. It is worth noting that room temperature dielectric properties are quite different, with samples sintered at lower current densities exhibiting better properties (*i.e.*, high dielectric constant and low losses). The highest dielectric constant value of the CRFS/10 sample seems to be related to the permittivity peak broadening. However, this sample achieves the worst maximum polarization. On the contrary, the CRFS/15 sample exhibits the highest value of maximum polarization along with an extremely low value of remnant polarization. In order to analyse the origin of this feat, a comparison of the samples' microstructure is first conducted. Fig. 4(a-d) shows a clear dependence between the resulting microstructure and the maximum current density employed. Finer grains and lower grain size dispersion seem to be obtained with lower current densities, as shown in Fig. 4e-h. However, the apparent refined microstructure of the CRFS/10 sample does not correlate with its poor functional response. Given the high density of the samples (Table 1), the porosity observed in Fig. 4(a-d) can be

attributed to the polishing process. Home-made polishing tools, consisting of high-speed rotating disks coated with diamond paste, were used for sample preparation. These harsh conditions may have dislodged small fragments from the samples prior to SEM imaging.

Image analysis software allows the calculation of the individual area of all grains visualized in an SEM image. The grain diameters are determined by approximating each measured area to a circle. Once the diameters are calculated, individual grain volumes can be estimated by approximating each grain to a sphere. Then, the volume contribution can be obtained as the volume occupied by grains of a given size relative to the total volume of all grains. By carefully inspecting the grain size by volume contribution (Fig. 4i-l), important microstructural features emerge. It becomes evident that apparently refined microstructures are actually quite heterogeneous. For instance, the grain size by volume contribution in CRFS/10 reveals that this sample has a trimodal microstructure with similar volumetric contributions from each mode. The sample CRFS/15, however, has the most homogeneous microstructure; i.e., it appears to be the only monomodal sample. This specimen has previously shown an excellent ferroelectric response for energy storage capabilities. Therefore, it is possible to hypothesize that the extremely slim ferroelectric response of the CRFS/15 sample may be due to microstructure refinement.

Elucidating the mechanisms involved in the evolution of the microstructure requires a detailed analysis of phase formation and densification for each sample. Although this analysis is beyond the scope of this work due to its scientific and technical complexity, EDX results (Fig. S5, ESI[†]) allow us to hypothesize that the abnormal grain size distribution observed when the sample is flash-sintered at 10 mA mm⁻² is due to the localization of flash sintering in SrTiO₃-rich regions. At 15 mA mm⁻², the current is sufficiently high to enable homogeneous sintering throughout the sample (but not so high as to induce abnormal grain growth), resulting in a uniform microstructure. At even higher currents, the microstructure becomes bimodal again due to the well-known abnormal grain growth that occurs during flash sintering at high currents, resulting from the

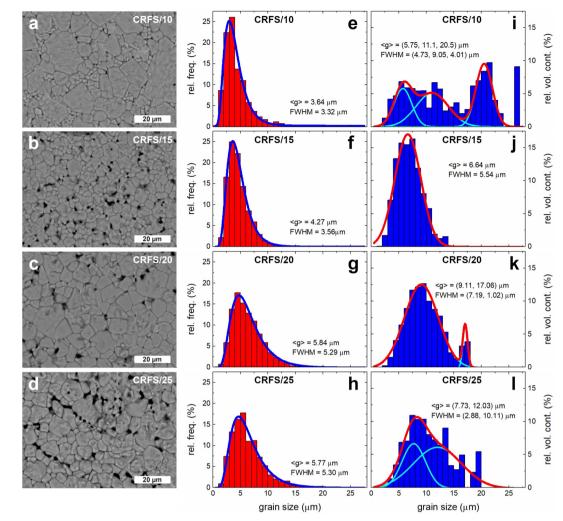


Fig. 4 Microstructure of CRF-sintered $Ba_{0.6}Sr_{0.4}TiO_3$ samples. (a–d) Micrographs of samples obtained by CRFS using different current density. (e–h) Grain size distribution for each sample. The relative frequency of occurrence of each grain size is shown. The obtained average grain size ($\langle g \rangle$) and the size dispersion (FWHM) are reported in each panel, showing that both tend to increase with the current density. (i–1) Grain size volume contribution of each grain is plotted, evidencing that the microstructure homogeneity heavily depends on the current density during CRFS. The obtained average grain sizes ($\langle g \rangle$) and size dispersions (FWHM) are reported in each panel, showing that only the CRFS/15 sample exhibits a monomodal microstructure.

localized melting of grains. XRD patterns of the representative samples (Fig. S6, ESI[†]) show no evidence of undesired phases, with all patterns indicating a cubic perovskite structure, as expected for a $Ba_{0.6}Sr_{0.4}TiO_3$ composition at room temperature.^{4,26}

Energy storage capabilities of the CRFS/15 sample are shown in Fig. 5. The energy storage density and efficiency were computed from the unipolar P-E loops (Fig. S7, ESI[†]) as is usually reported in the literature (some details about the calculation procedure are given in Fig. S8, ESI[†]).^{11,14,37} As shown, the energy that can be stored increases with the applied electric field, while at the same time, the energy storage efficiency decreases, as commonly reported for this and other systems.^{17,18,22} The values of the recovered energy density were computed until the energy storage efficiency dropped below 85%. The obtained values are in accordance with previous results in Ba_{1-x}Sr_xTiO₃ system (Fig. S9, ESI[†]),^{15–20} but with one significant difference: an optimal composition of Ba_{1-x}Sr_xTiO₃ for energy storage is expected for x = 0.5-0.7. Therefore, a composition *a priori* considered as suboptimal (x = 0.4) achieves similar energy storage properties to those expected for more optimal compositions. This fact must be related to the microstructure refinement attained by CRFS. At this juncture, an important question emerges: why does a refined, more homogeneous microstructure show better electrical properties?

The concept of electrical microstructure, understood as a measure of the quality and electrical homogeneity of materials, has been proposed as one of the key factors for optimizing energy storage properties of dielectric ceramics.²¹ A homogeneous distribution of local conductivity has been proved to be an important feature to enhance energy storage.³⁸ Impedance spectroscopy is recognized as a powerful tool for evaluating the electrical microstructure, as there is a link between microstructure and the AC electrical response of electroceramics.³⁹

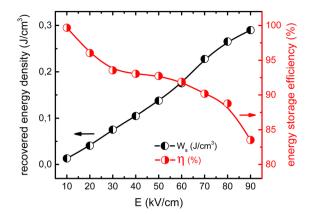


Fig. 5 Energy storage properties of CRF-sintered $Ba_{0.6}Sr_{0.4}TiO_3$ sample at a current density of 15 mA mm⁻² (CRFS/15). Energy storage density and energy storage efficiency of CRFS/15 are plotted as a function of the applied electric field. While the energy storage increases, the energy efficiency decreases.

In the analysis of impedance data of electroceramics, it is standard practice to present the data using impedance complex plane plots, Z''/Z' (Z^*). A representative complex plane plot recorded at 400 °C for the CRFS/20 sample is shown in Fig. 6c. The Z^* plot shows two components: one semicircle at high frequencies and another at lower frequencies. The high frequency and lower frequency semicircles could be modelled, to a first approximation, on two parallel resistor–capacitor (RC) elements in series. The associated capacitances, ~20 pF cm⁻¹ at high frequencies and ~2 nF cm⁻¹ at lower frequencies are typical of bulk and grain boundary responses, respectively.³⁸ All samples showed similar behaviour at different temperatures (Fig. S10, ESI†).

Arrhenius plots of bulk and grain boundary conductivity values, $\sigma_{\text{bulk}} = 1/R_{\text{bulk}}$ and $\sigma_{\text{GB}} = 1/R_{\text{GB}}$, extracted from the impedance complex plane plots over a range of temperatures for all samples are shown in Fig. 6a and b. Analysing the data obtained from the Arrhenius plots for the CRF-sintered samples, it is possible to notice a linear behaviour in all cases with similar activation energies, which suggests that the conduction mechanism is the same in all cases. Moreover, the effect of the CRFS conditions is evident; the Arrhenius plots of the sintered

samples show significant differences in bulk and grain boundary resistivities depending on the sintering conditions. Specifically, the CRFS/15 sample shows higher bulk and grain boundary resistivities than the rest of the samples, which can be related with its particularly slim ferroelectric response.

Impedance spectroscopy is a powerful technique for distinguishing between the electrical responses of bulk and grain boundaries in ceramics. In this method, the electrical behaviour is often modelled using an equivalent circuit consisting of two parallel resistor-capacitor (RC) elements connected in series, representing bulk and grain boundary contributions. While impedance (Z^*) plots in the complex plane (e.g., Z'' vs. Z') are commonly used, they can overemphasize the most resistive components, potentially obscuring regions with lower resistance. This limitation is particularly relevant for inhomogeneous ceramics. To gain a more comprehensive analysis and mitigate this issue, it is useful to present impedance data using at least two of the following four formalisms: impedance (Z^*) , admittance (Y^*), permittivity (ε^*), and electric modulus (M^*). The interconversions between these formalisms can provide a more detailed understanding of the impedance characteristics.³⁹ Each of these formalisms has real and imaginary components, such as $Z^* = Z' - jZ''$.

Following these interconversions, data may be plotted as either complex plane plots, e.g. Z'' vs. Z' or as spectroscopic plots, e.g. Z'' and M'' vs. log f. When employing impedance spectroscopy to investigate electrical heterogeneity in ceramics, it is often more informative to use combined spectroscopic plots of Z'' and M'' rather than Z^* plots. The M'' spectra predominantly reflect electroactive regions with low capacitances, typically associated with grain-type responses. In contrast, Z'' spectra (and Z^* plots) are primarily influenced by the most resistive electroactive regions in the ceramic, such as grain boundary-type responses. Typically, each Debye peak observed in Z" and M" spectroscopic plots is initially interpreted as a parallel RC element. Thus, when multiple Debye peaks are present in the Z'' and M'' spectra, the data can be modelled using an equivalent circuit comprising two parallel RC elements connected in series.⁴⁰

Spectroscopic plots of Z'' and M'' spectra at 450 °C for CRFS/ 15 is shown in Fig. 7a. M'' plots identify those elements with the

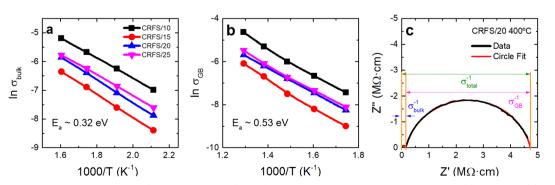


Fig. 6 Impedance spectroscopy analysis of CRF-sintered $Ba_{0.6}Sr_{0.4}TiO_3$ samples. Temperature dependence of (a) bulk and (b) grain boundary conductivities. (c) Z^* plot for CRFS/20 sample at 400 °C.

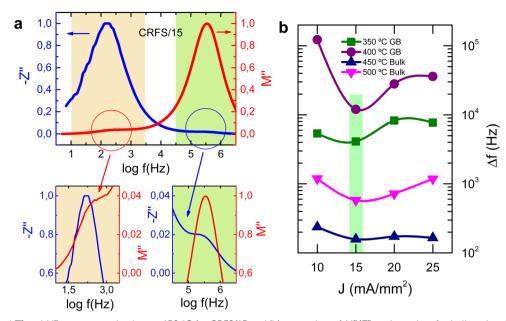


Fig. 7 (a) Combined Z'' and M'' spectroscopic plots at 450 °C for CRFS/15 and (b) separation of M''/Z'' peak maxima for bulk and grain boundary peaks at different temperatures for all selected samples. The Z'' and M'' values are normalized for better visualization of the peak maxima in the combined plot.

smallest capacitance since the M'' peak maximum is equal to $C_0/2C$, where C_0 is the vacuum capacitance of the conductivity cell, without a sample in place. However, two peaks are observed in the M" spectrum, one not-well resolved at 300 Hz and a second, well resolved, at 355 kHz. The associated capacitance values of these peaks are ~ 170 and ~ 6 pFcm⁻¹, respectively, and can be associated with the grain-boundary and bulk components. Impedance plots highlight the most resistive elements in the sample, since the impedance peak high, Z'', is equal to R/2. Resistivities of ~3.6 M Ω cm and ~84 k Ω cm are obtained from the Z'' spectroscopy plot for the low and high frequency components, respectively, and can be associated to the grain-boundary and bulk resistivities. It is important to mention that the M'' spectrum does not show any further peaks or tails at high frequencies which could indicate bulk heterogeneity. Therefore, data can be interpreted on a series of two RC parallel elements attributed to the bulk and grain boundary. Only one peak is observed at lower and higher temperatures, corresponding to the bulk and grain boundary response, respectively. No other components are observed in the CRFS/15 sample.

It is commonly observed a separation of M''/Z'' peak maxima which data may arise from non-ideal behaviour, as described by the constant phase element (CPE). However, it is critical to differentiate this effect from potential peak separation caused by sample heterogeneity.⁴¹ In order to determine whether the separation between M''/Z'' peak maxima, in both low and high frequency peaks, is a consequence of sample heterogeneity or a non-ideality, the peak difference was determined for all samples at different temperatures. As shown in Fig. 7b, the difference between the M'' and Z'' peak maxima are highly dependent on the sintering conditions. The CRFS/15 sample exhibits the smallest M''/Z'' peak difference across both low- and high-frequency regions and at various temperatures. In contrast, the other samples show larger peak differences, with the CRFS/10 sample displaying the largest deviation. To further highlight deviations from ideal behaviour and to identify overlapping peaks or weak signals that may be obscured on a linear scale, log–logplots of the M''/Z'' spectra for CRF-sintered samples at 300 °C, where only the bulk region is observed, are presented in Fig. 8. The CRFS/10, CRFS/20 and CRFS/25 samples (Fig. 8a, c and d) shows a broadening of the response at high frequencies in the M'' spectroscopic plot. This behaviour strongly suggests some electrical heterogeneity within the grains of the CRFS/10, CRFS/20 and CRFS/25 samples. In contrast, the high-frequency region of the M'' spectroscopic

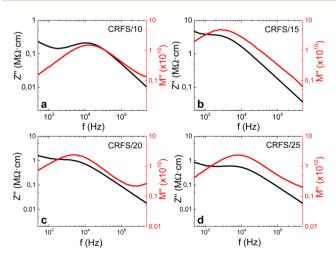


Fig. 8 Spectroscopic plots of Z" and M" versus frequency on logarithmic scales for (a) CRFS/10, (b) CRFS/15, (c) CRFS/20, and (d) CRFS/25 samples at 300 $^{\circ}$ C.

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plot of the CRFS/15 sample (Fig. 8b) exhibits a behaviour that closely approximates Debye-like relaxation, indicating an electrically homogeneous grain response. The M''/Z'' spectra clearly indicate a substantial improvement in electrical homogeneity of the CRFS/15 sample compared to CRFS/10, CRFS/20 and CRFS/25 samples. This enhanced electrical homogeneity, coupled with higher resistivity, is more likely to foster enhanced energy storage properties.

4 Conclusions

Highly-dense Ba_{0.6}Sr_{0.4}TiO₃ ceramics are obtained by currentcontrolled reactive flash sintering of mixed BaTiO₃–SrTiO₃ powders. While current control may seem to increase the complexity of sintering by introducing additional parameters to manage, it proves to be an effective strategy for fine-tuning the microstructure. Moreover, current control in flash sintering is evidenced to provide a new pathway to precisely achieve a defect-engineered microstructure. High densification is achieved at a furnace temperature of 1200 °C with a dwell time of 5 min, showing notable differences in dielectric and ferroelectric properties depending on the current density, which ranges between 10–25 mA mm⁻². These differences are revealed to be related to the microstructure, particularly the grain size distribution by volume.

The sample with the most refined microstructure was characterized for energy storage applications. Despite the Ba_{0.6}Sr_{0.4}-TiO₃ composition being non-optimal for energy storage, the results demonstrated energy storage capabilities comparable to those reported for more tailored compositions in the literature. It is important to point out that the pursuit of exceptional properties for the Ba_{1-x}Sr_xTiO₃ system was not the goal of this work, as it is well-known that there are compositions that outperform Ba_{1-x}Sr_xTiO₃ for this application. The objective of this study is to demonstrate that microstructural refinement is a key tool for optimizing the performance of ferroelectric materials for energy storage. The studied composition was deliberately chosen outside the optimal range to make our demonstration even more evident.

The quality and electrical homogeneity of the flash-sintered samples were evaluated using impedance spectroscopy. The CRFS/15 sample exhibited higher bulk and grain boundary resistivities, as well as greater electrical homogeneity. These unique electrical properties are undoubtedly associated with its slim ferroelectric response and, consequently, its improved energy storage capability. This work highlights that microstructural refinement and, consequently, electrical homogeneity, are key factors in enhancing the energy storage capacity of ferroelectric materials.

Author contributions

Samuel Lopez-Blanco: conceptualization, methodology, formal analysis, investigation, writing – original draft; Xavier Vendrell:

methodology, formal analysis, visualization, writing – review & editing; Nerea Morante: methodology, investigation; Jose E. Garcia: conceptualization, methodology, visualization, writing – review & editing, supervision, project administration.

Data availability

All data included in this study will be made available upon request from the corresponding author.

Conflicts of interest

There are no conflicts to declare.

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