

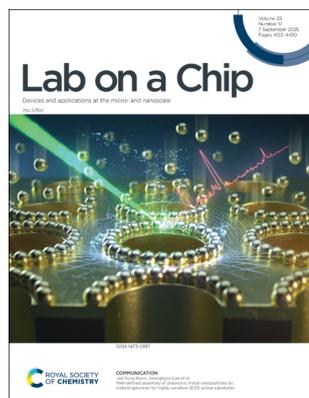
Lab on a Chip

Devices and applications at the micro- and nanoscale
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See Seunghyun Lee, Sung Kwon *et al.*, pp. 4309–4316. Image reproduced by permission of Seunghyun Lee from *Lab Chip*, 2025, 25, 4309.



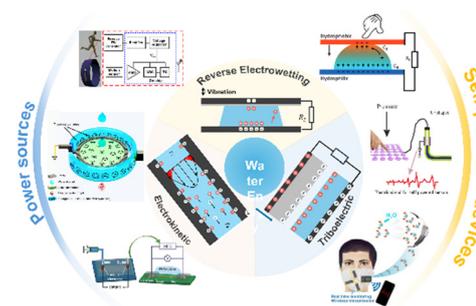
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Lingxuan Hao, Mohammed Imran Khan, Yilin Lei, Shuneng Zhou and Bei Fan*

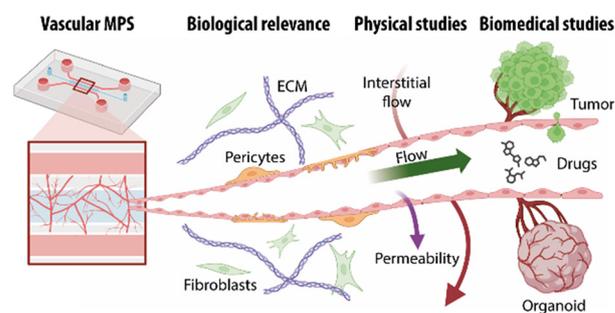


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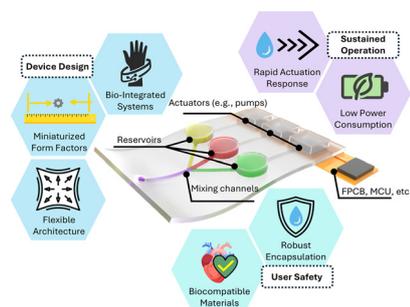


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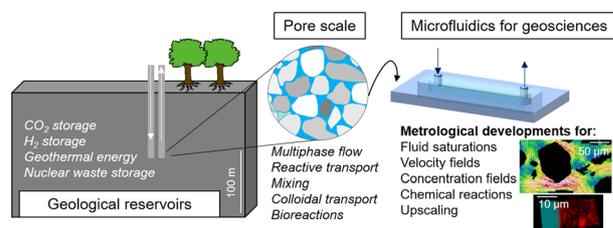
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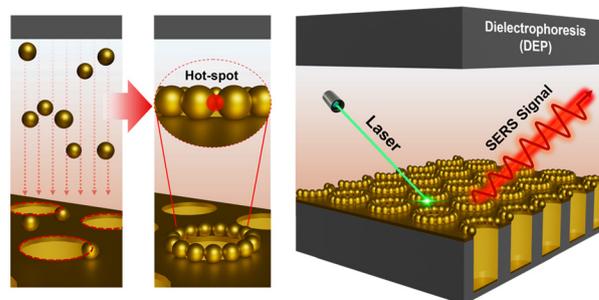


COMMUNICATION

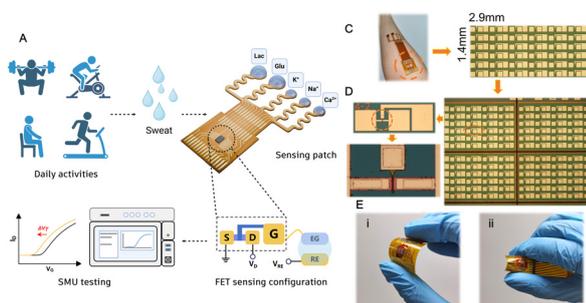
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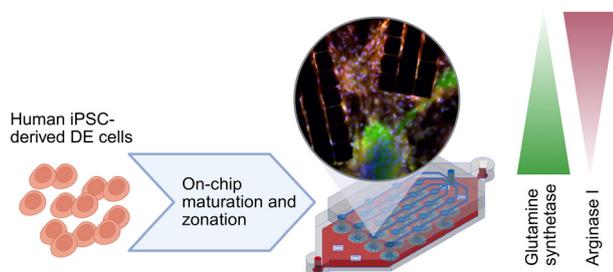
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A multi-channel wearable sensing patch based on gate-all-around field-effect transistors

Zhe Xing, Qiang Liu, Bo Lin, Shuang Li, Yuxin Liu, Guanyang Zhang, Wenjie Yu, Zhenhua Wu* and Hongju Mao*

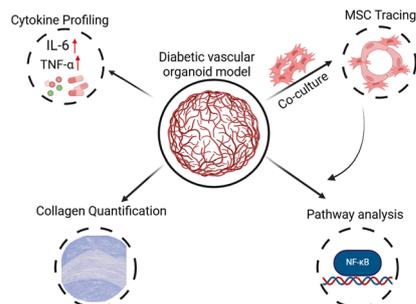
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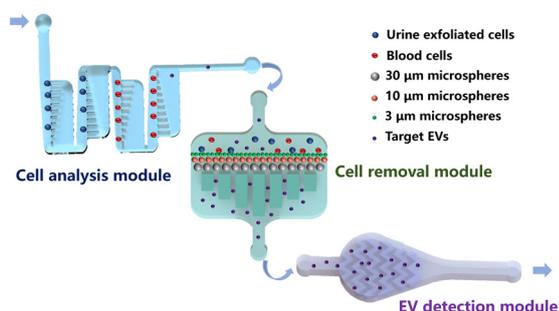
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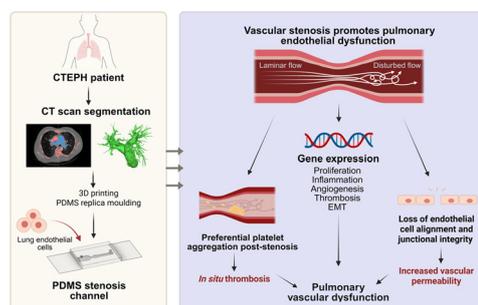
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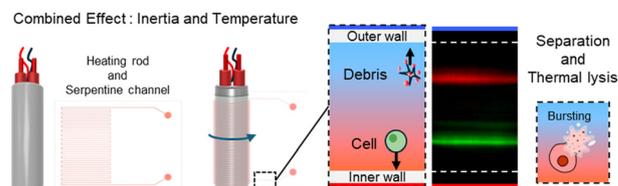
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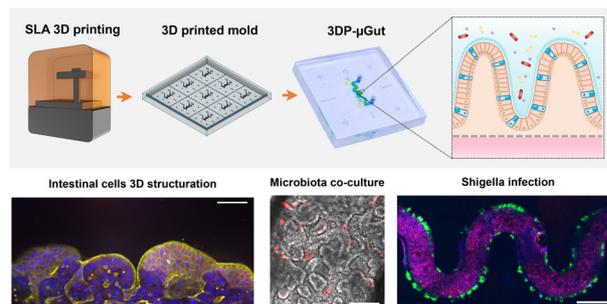
Junho Kim, Hwisu Jeon, Kyunghun Lee and Taesung Kim*



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Gut-on-chip methodology based on 3D-printed molds: a cost-effective and accessible approach

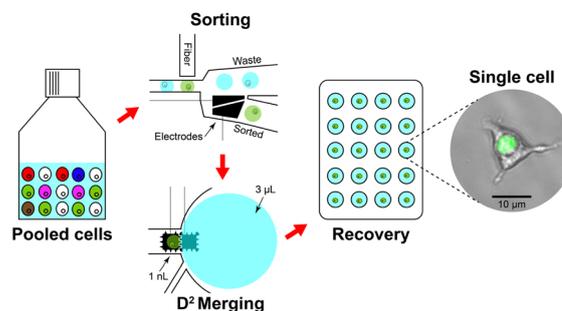
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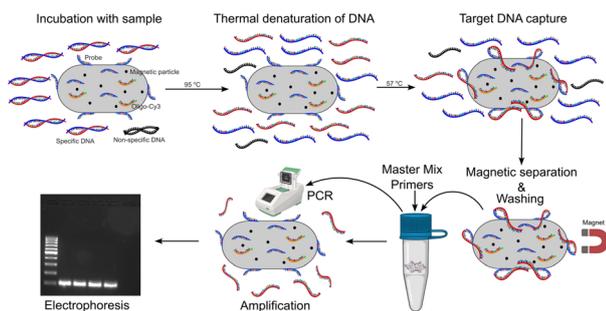
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Zhiyang Deng, James M. Perry, Marian Weiss, Robert Genth, Alexis Autour, Christoph A. Merten* and Steve C. C. Shih*



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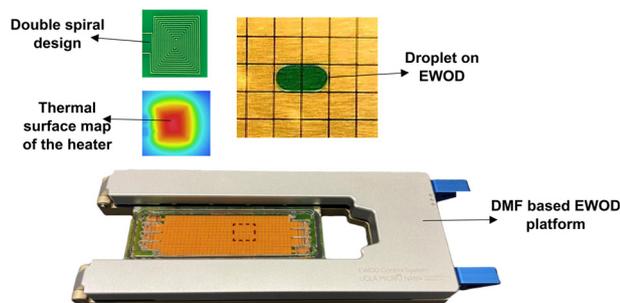
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Lokman Alpsoy, Stephan Tieu, Sina Dehestanizad, Thomas Brandstetter and Jürgen Rühle*

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Hyeri Choi, June Ho Shin, Hyeonsu Jo, John B. Sunwoo* and Noo Li Jeon*

