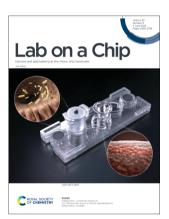
Lab on a Chip

Devices and applications at the micro- and nanoscale rsc.li/loc

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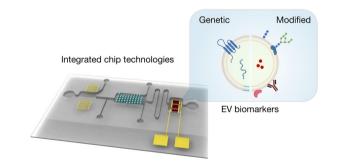
Inside cover See Tanmay Mathur, Abhishek Jain et al., pp. 2620-2631. Image reproduced by permission of Abhishek Jain from Lab Chip, 2025, **25**, 2620.

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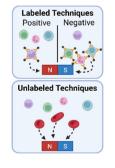
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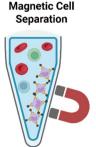
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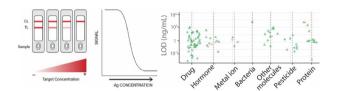
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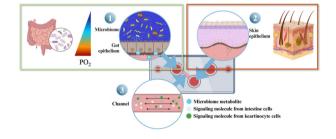


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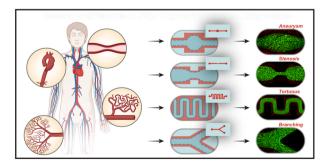
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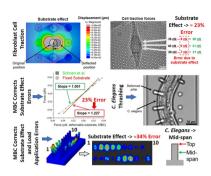


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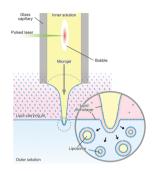
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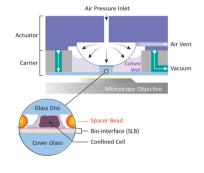
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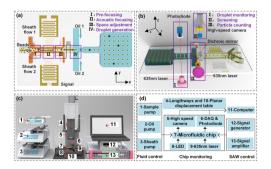
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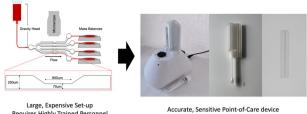
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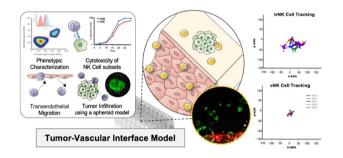


Requires Highly Trained Personnel

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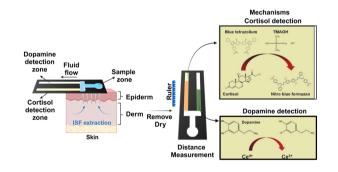
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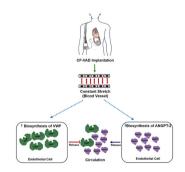
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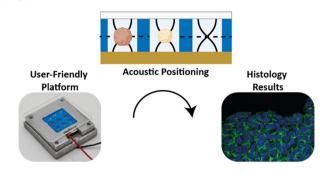


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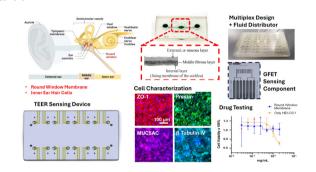


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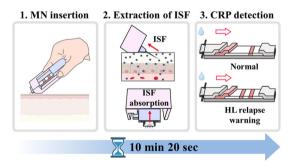
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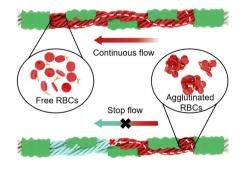
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