

Lab on a Chip

Devices and applications at the micro- and nanoscale
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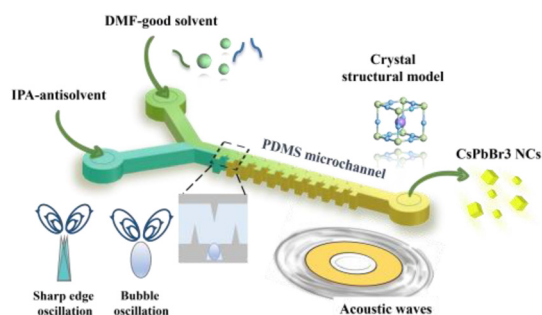
Cover
See Guorui Zhu *et al.*,
pp. 7–15.
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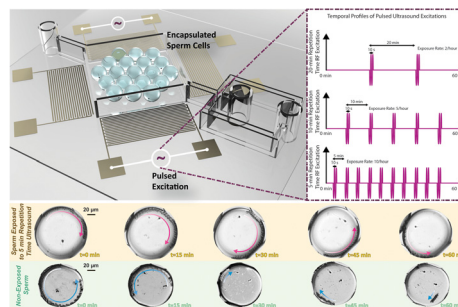
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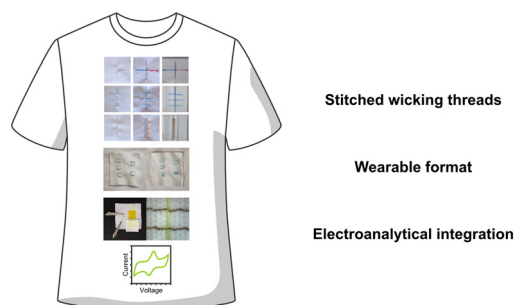
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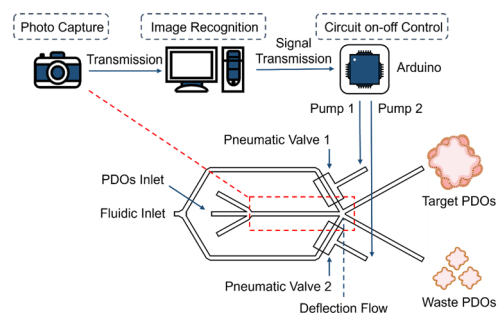
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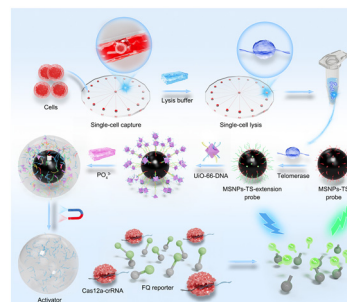
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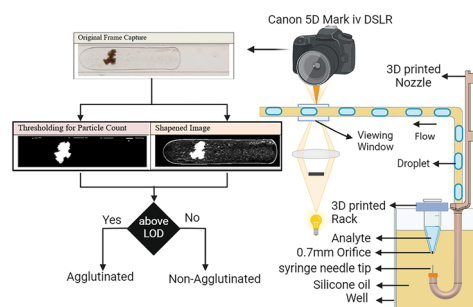
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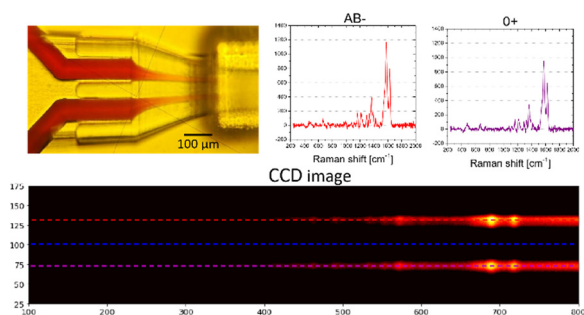
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Abdul Basit Zia* and Ian G. Foulds



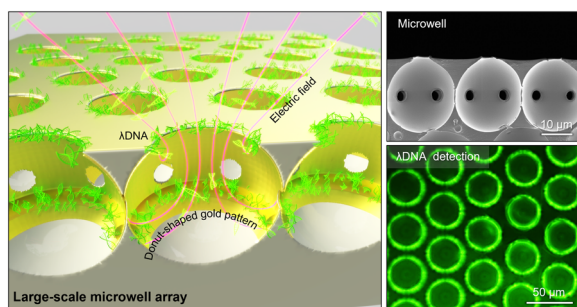
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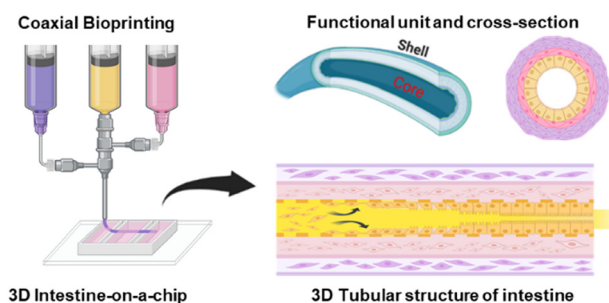
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