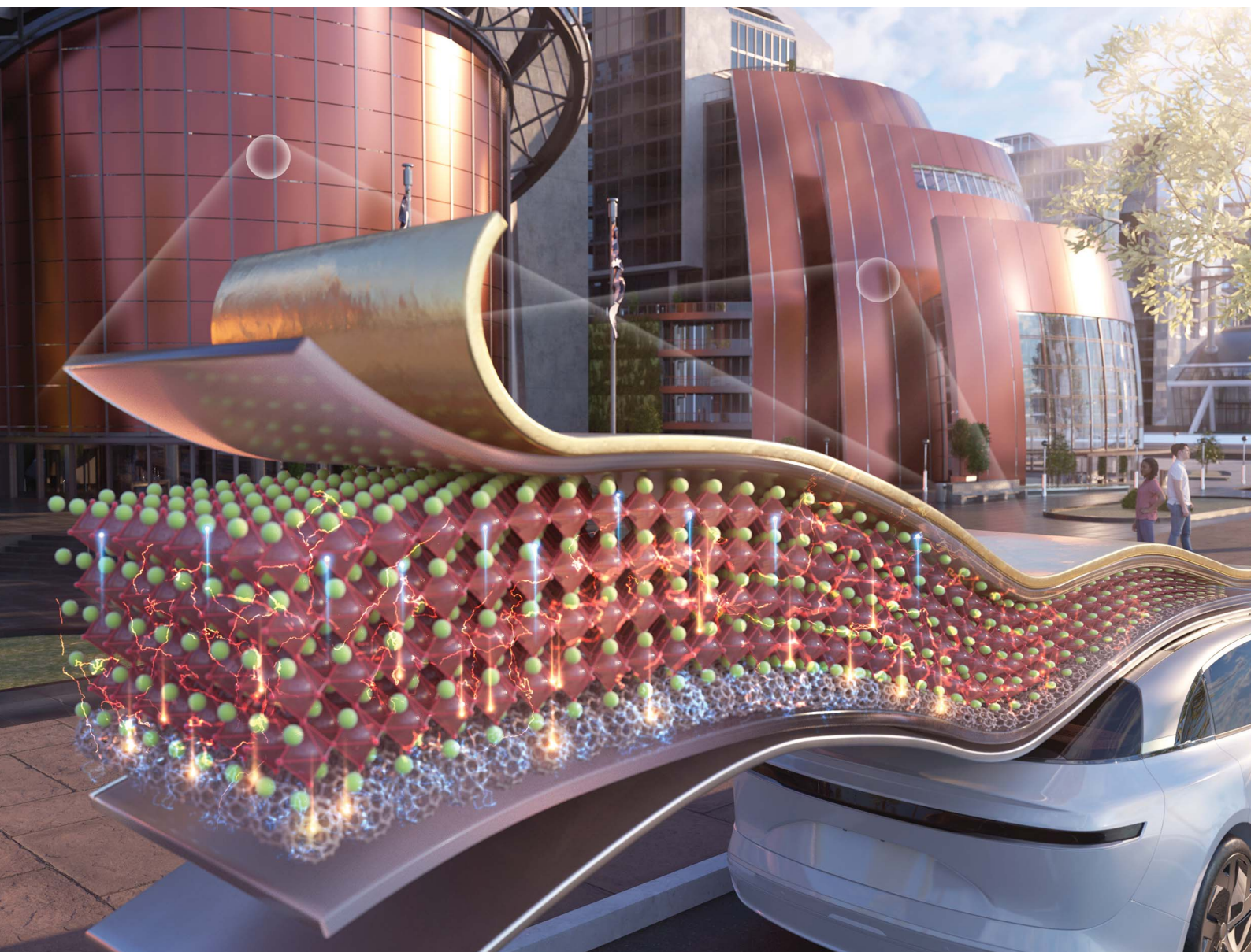


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on stainless steel substrates *via* C₆₀ buffer layer integration