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Mixed valence that induces superior dielectric properties of $\text{CdCu}_3(\text{Tb}_{1/2}\text{Ta}_{1/2})_x\text{Ti}_{4-x}\text{O}_{12}$ ceramics[†]

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The considerable demand for energy and the accelerated growth of the microelectronics sector has prompted extensive research into dielectric materials with high dielectric constants for use in electronic storage devices. In this study, a conventional solid-phase method was used to enhance the dielectric properties of $\text{CdCu}_3\text{Ti}_4\text{O}_{12}$ by co-doping Ti sites with Tb and Ta ions. Impressively, the $\text{CdCu}_3(\text{Tb}_{1/2}\text{Ta}_{1/2})_x\text{Ti}_{4-x}\text{O}_{12}$ ceramics exhibit superior dielectric properties with a dielectric constant ($\epsilon \sim 3.21 \times 10^4$) and a low loss tangent ($\tan \delta \sim 0.020$) at 1 kHz. In addition, the temperature dependent-coefficient of variation of its dielectric constant ($\Delta\epsilon/\epsilon_{25^\circ\text{C}}$) is less than $\pm 15\%$ over the temperature range of -13 to 174°C . The dielectric response is mainly due to intrinsic and extrinsic effects, and the inherent effects may be due to the formation of defect clusters (i.e., $2\text{Tb}'_{\text{Ti}}-\text{V}_\text{O}^{\bullet\bullet}$, $\text{Tb}'_{\text{Ti}}-\text{Ta}'_{\text{Ti}}$ and $3\text{Ti}'_{\text{Ti}}-\text{V}_\text{O}^{\bullet\bullet}-\text{Ta}'_{\text{Ti}}$). While the non-intrinsic effects are caused by the microstructure of the internal barrier layer capacitor, the impedance spectra show the presence of semiconductor grains as well as the huge resistance at the grain boundaries, where the heterogeneous structure causes an increase in grain boundary resistance. This study explores the dielectric response of a new type of giant dielectric ceramics and provides a new candidate for the fabrication of ceramic capacitors.

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1. Introduction

Electric capacitors are prized for their high power density, elevated working voltage, and extended operational lifespan, making them highly effective in meeting the substantial demands of energy storage systems. They also offer promising applications in electronic systems that leverage pulse power technology. Giant dielectric materials exhibit an ultra-high dielectric constant ($\epsilon > 10^4$), indicating a superior capacitance storage capacity compared to other dielectric materials of equivalent size and thickness. This remarkable characteristic aligns with the evolving trends in electronic component development, thus drawing considerable attention to research on giant dielectric materials.^{1–3} However, the critical parameters of

giant dielectric materials hinder their wider application and development.^{4,5} For example, $\text{CaCu}_3\text{Ti}_4\text{O}_{12}$ -based (CCTO) materials suffer from high loss tangent ($\tan \delta$) and exhibit frequency/temperature instabilities of ϵ .^{6,7} To meet the changing demands of modern electronics development, it is crucial to optimize the performance and reliability of the materials.

Previous studies have reported that various elemental doping strategies have been employed to modify structure and tuned dielectric behavior of CCTO ceramics. For instance, doping with Zn,⁸ Zr/Nd⁹ and In/Ta¹⁰ has been reported to enhance dielectric properties of CCTO. Rare earth (RE) elements are known as “industrial vitamins” in the post-industrial era. The rare earth elements La,¹¹ Eu,¹² Dy,¹³ Yb,¹⁴ Tb,¹⁵ Y/Mg,¹⁶ Pr/Co¹⁷ and Gm/Sm¹⁸ are widely used in CCTO ceramics for dielectric modification. In the above compounds, The RE ions predominantly replace Ca site in the CCTO lattice, leading to the formation of $\text{Ca}_{1-3x/2}\text{RE}_x\text{Cu}_3\text{Ti}_4\text{O}_{12}$ or $\text{Ca}_{1-x}\text{RE}_x\text{Cu}_{3-y}\text{A}_y\text{Ti}_4\text{O}_{12}$ compounds. When RE ions are doped at the Ca site, ϵ tends to decrease while $\tan \delta$ tends to increase. However, the doping of RE ions at the Ti site in CCTO has rarely been documented. Besides the improved dielectric characteristics, the dielectric mechanisms exhibited by CCTO ceramics have become a subject of significant interest. Typically, the dielectric behavior in giant dielectric materials is a result of

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both intrinsic and extrinsic contributions. Thomas *et al.*¹⁹ summarized ten mechanisms, while studies on grain boundary and first-principles calculations confirmed the external effects of CCTO.^{20,21} The internal barrier layer capacitance (IBLC) model connects inhomogeneous structure consisting of ceramics conductive grains and insulating grain boundaries to electrical performance.²² With its emphasis on microstructural inhomogeneities, this model is now broadly recognized as the leading plausible explanation for the giant dielectric response.

Research has revealed that introducing donor ions, such as Nd³⁺, into CCTO ceramics alongside valent dopants like Zr⁴⁺ effectively enhances grain boundary resistance and significantly improves dielectric properties.⁹ Furthermore, co-doping Ti⁴⁺ with covalent elements proves to be an efficient method for reducing the tan δ of CCTO ceramics while preserving a high ϵ . For example, co-doping CCTO ceramics with Al³⁺ and Nb⁵⁺ enhances the relative dielectric constant ($\epsilon \sim 4.0 \times 10^4$), while also leading to a reduction in dielectric loss (tan $\delta \sim 0.050$).²³ In this study, the selected donor ion was the pentavalent dopant Ta⁵⁺ ($r_6 = 0.64$ Å), which closely matches the ionic radius of the host Ti⁴⁺.²⁴ Ta⁵⁺ has an extra layer of outer electrons, has a strong electron binding ability to make the electrons more localized, and is expected to completely displace the Ti⁴⁺ position. Meanwhile, Tb ions with distinct ionic radii (Tb³⁺: $r_6 = 0.92$ Å and Tb⁴⁺: $r_6 = 0.76$ Å) were chosen as covalent/equivalent dopants.²⁵ The varying valence states and ionic radii of Tb ions were explored to assess their effect on the dielectric properties. Doping with the larger Tb³⁺ ions can induce the formation of defect clusters or oxygen vacancy-related defects (V_O[•]). The doping of CCTO-based materials with equivalent dopants has been observed to result in a lower concentration of V_O[•], as evidenced by Sn⁴⁺ doped CCTO ceramics.²⁶ Moreover, the equivalent doping of Tb⁴⁺ increases the grain-boundary resistance, and excessive doping may hinder the formation of V_O[•]. The expected result of embedding these dopants into the CCTO lattice is a systematic augmentation of the material's dielectric features.

The CdCu₃Ti₄O₁₂ (CdCTO) ceramic structure is classified as a chalcogenide-like structure. Cd²⁺ (1.31 Å) and Ca²⁺ (1.34 Å) exhibit similar ionic radii when in 12-fold coordination. However, their initial dielectric properties of CdCTO and CCTO ceramics differ significantly. Subramanian *et al.*²⁷ reported that the ϵ (~409) of pristine CdCTO is relatively low, while the tan δ (~0.093) is considerably high. Recently, through the enhancement of the preparation methods and the alteration of doping, we have successfully prepared dielectric ceramics with exceptional dielectric properties in our research team ($\epsilon > 2.0 \times 10^4$, tan $\delta < 0.1$).²⁸⁻³⁰ Based on the IBLC effect, grain boundary and grain resistance can be tuned by hetero/covalent element doping to enhance overall dielectric properties. Unfortunately, the co-doping of CdCTO with Tb^{3+/4+} and Ta⁵⁺ at Ti sites to improve dielectric properties has never been reported.

To improve the performance of CdCTO-based ceramics, we applied a conventional solid-phase method in this study. Tb^{3+/4+} ions were introduced as acceptor/equivalent dopants, while Ta⁵⁺ acted as a donor dopant to optimize dielectric properties through inter-ion self-charge compensation and

the improvement of grain boundary resistance. The results show that ϵ and low tan δ values remain stable over a specific range of frequency and temperature. Notably, the TT10 ceramics exhibited the best performance ($\epsilon \sim 3.21 \times 10^4$, tan $\delta \sim 0.020$ at 1 kHz). The microstructure and possible defect clusters within the ceramics have been comprehensively analyzed, providing a detailed discussion of the mechanisms underpinning the giant dielectric response.

2. Experimental

The synthesis of CdCu₃(Tb_{1/2}Ta_{1/2})_xTi_{4-x}O₁₂ ceramics was carried out *via* the traditional solid-state reaction pathway, with x values specified as 0.01, 0.025, 0.05, 0.10, and 0.20. The samples can be abbreviated as TT01, TT025, TT05, TT10, and TT20, respectively. The raw materials utilized in the process included CdO (99%), CuO (99%), TiO₂ (98%), Tb₂O₃ (99.99%), and Ta₂O₅ (99.99%). The materials underwent wet ball milling with agate balls in anhydrous ethanol, followed by drying at 80 °C in a thermostatic oven, subsequent milling, and pre-firing. Polyvinyl alcohol (PVA) at 5% by mass was added to the pre-fired powder for granulation and sieving. With a pressure of 6 MPa, the screened powder was compacted into pellets with a diameter of 11.5 mm and a thickness of approximately 1.0 mm. Subsequently, the sintering of the pellets was carried out at 1020 °C for 15 hours. After sintering, the samples were ground with emery and polished with a polishing compound, then ultrasonically cleaned. Subsequently, silver electrodes were applied using screen printing technique with silver paste. Finally, the ceramic samples were sintered in a chamber furnace at 840 °C for 30 minutes to ensure proper contact between the samples and electrodes for subsequent testing.

To analyze the phase structure and microstructure of the CdCTO-based ceramics, an X-ray diffractometer (XRD, Mini-Flex600, Rigaku) and a scanning electron microscope (SEM, SU3500, Hitachi) were employed. Raman spectral shifts were analyzed *via* laser Raman spectrometry (inVia Reflex, Renishaw, England). The field emission scanning electric microscopy (FE-SEM, SU8020, Hitachi) and energy dispersive spectrometer (EDS, X-MAX80, Horiba) systems were used in tandem to further assess the elemental distribution and chemical composition. The precision LCR meter (4294A, Agilent) was used to assess impedance spectra and the dielectric performance at room temperature. The temperature dependent of dielectric properties and impedance spectra were obtained using dielectric temperature spectroscopy measuring system (DMS2000, Balab). The analysis of the chemical valence states of elements in CdCTO-based ceramics was conducted using an X-ray photoelectron spectrometer (XPS, Kratos Analytical Ltd, AXIS ULTRA) and experimental data fitting was performed with Casa XPS software.

3. Results and discussion

The XRD patterns of CdCu₃(Tb_{1/2}Ta_{1/2})_xTi_{4-x}O₁₂ ceramics are illustrated in Fig. 1(a). The presence of characteristic peaks in

the XRD patterns of all samples matching those of CdCTO (PDF# 48-0208), which indicates the successful substitution of Tb and Ta into the lattice. Notably, only the primary CdCTO crystalline phase was observed, without the production of secondary phases usually found in similar compounds, such as CuO,^{31,32} Tb₄O₇ (PDF # 13-387), or Tb₂Ti₂O₇ (PDF # 41-363).³³ The solubility limit of Tb ions in CdCu₃(Tb_{1/2}Ta_{1/2})_xTi_{4-x}O₁₂, as determined by experiments, exceeds $x = 0.20$. The chalcogenide-like material CCTO crystallizes in a body-centered cubic matrix containing 20 atoms, falling under the centrosymmetric Th ($Im\bar{3}$) space group. The $Im\bar{3}$ crystallographic structure generates 24 Γ -point phonons, with eight identified as Raman-active modes.^{34,35} CdCTO exhibits weak Raman scattering, with only five of the eight modes being detectable, specifically at $F_{1g} \sim 263 \text{ cm}^{-1}$, $E_{1g} \sim 322 \text{ cm}^{-1}$, $A_{1g} \sim 444 \text{ cm}^{-1}$, $A_{2g} \sim 510 \text{ cm}^{-1}$ and $F_{3g} \sim 576 \text{ cm}^{-1}$. Among these five modes, the modes 444, 510 and 576 cm^{-1} are relatively intense, whereas 263 and 322 cm^{-1} are weaker. The F_{1g} ($\sim 263 \text{ cm}^{-1}$), E_{1g} ($\sim 322 \text{ cm}^{-1}$), A_{2g} ($\sim 510 \text{ cm}^{-1}$), and A_{1g} ($\sim 444 \text{ cm}^{-1}$) modes correspond to symmetric rotational vibrations of the [TiO₆] octahedral structure. Meanwhile, F_{3g} ($\sim 576 \text{ cm}^{-1}$) represents the asymmetric telescopic vibrational mode of Ti-O-Ti.^{34,36-38} The successful substitution of Ti lattice sites with Tb and Ta ions in CdCTO ceramics was confirmed by XRD and Raman spectroscopy analyses. Furthermore, neither a second phase was formed nor the solid solution limit was exceeded in the compounds, which further confirms the successful synthesis of a homogeneous solid solution of CdCu₃(Tb_{1/2}Ta_{1/2})_xTi_{4-x}O₁₂.

Fig. 2(a)–(d) illustrates the hot corrosion microstructures of CdCu₃(Tb_{0.5}Ta_{0.5})_xTi_{4-x}O₁₂ ceramics. These images reveal pronounced grain boundaries and excellent crystallinity in all the prepared ceramics, with the insets presenting the grain size distribution. As the Tb/Ta doping level increases to $x = 0.10$, the decrease in average grain size (ADS) shifting to $27.9 \pm 0.5 \mu\text{m}$, accompanied by a more homogeneous grain size distribution. The observed decrease in ADS may result from the solute drag phenomenon related to Ta ion and the inhibition of grain growth by Tb ion.^{35,39} In conclusion, increasing doping levels

lead to a diminished grain size, and this microstructural evolution may influence their dielectric properties. The observed trend indicates a critical balance between doping levels and the resultant microstructure and electrical properties.

Additionally, the electron dispersive spectroscopy displays the elemental distribution of thermally etched TT10 ceramic as illustrated in Fig. S1 (ESI†). The elements Tb and Ta exhibit nearly uniform diffusion into the CdCTO lattice. At the grain boundaries, Cu segregates to form a Cu-rich phase. The IBLC model identifies these Cu-rich phases as insulating grain boundaries and Cu-deficient phases as semiconductor grains.⁴⁰⁻⁴³ Dopant ions with larger ionic radii introduce lattice distortion, which promotes the development of a Cu-rich layer along the grain boundaries. This layer boosts grain boundary resistance, effectively reducing the low-frequency dielectric loss in the ceramics.^{41,44}

To assess the influence of co-doped Tb/Ta ions on the dielectric properties of CdCTO-based ceramics, their frequency-dependent behavior was analyzed. The dielectric properties of TT01, TT025, TT05 and TT10 ceramics at room temperature and across a frequency range of 40–10⁶ Hz are illustrated in Fig. 3(a). The tested samples all exhibit a broad dielectric plateau and demonstrate superior frequency stability. Fig. 3(c) compares the ϵ and $\tan \delta$ of the ceramics, revealing that all samples display colossal $\epsilon > 2.10 \times 10^4$ and low $\tan \delta < 0.04$ at 1 kHz. These findings confirm that the dielectric properties are largely independent of frequency. However, as shown in Fig. 3(b), when x exceeds 0.10, both the ϵ and $\tan \delta$ deteriorate significantly. This degradation is attributed to the excessive doping, which increases interfacial layers within the ceramics. Consequently, the interfacial effects are notably influenced, leading to a corresponding deterioration in the dielectric properties.⁴⁵

Fig. 3(d) illustrates the link between AC conductivity (σ') and frequency (f) of co-doped CdCTO-based at room temperature. Below $f = 10^5$ Hz, σ' remains nearly constant, while a sharp increase is observed beyond $f = 10^6$ Hz. Notably, the conductivity of the ceramics increases gradually with doping in the high-frequency region, a trend closely related to the resistance of

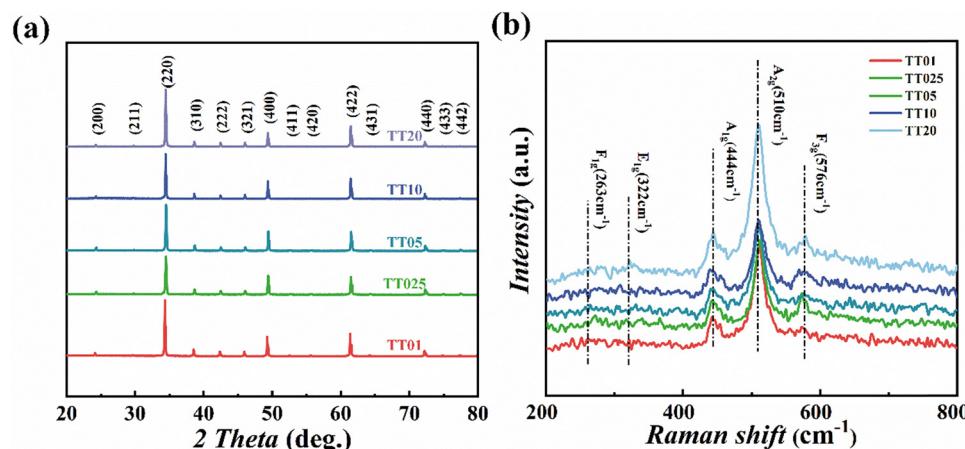


Fig. 1 (a) XRD patterns of CdCTO-based ceramics. (b) Raman patterns of CdCTO-based ceramics.

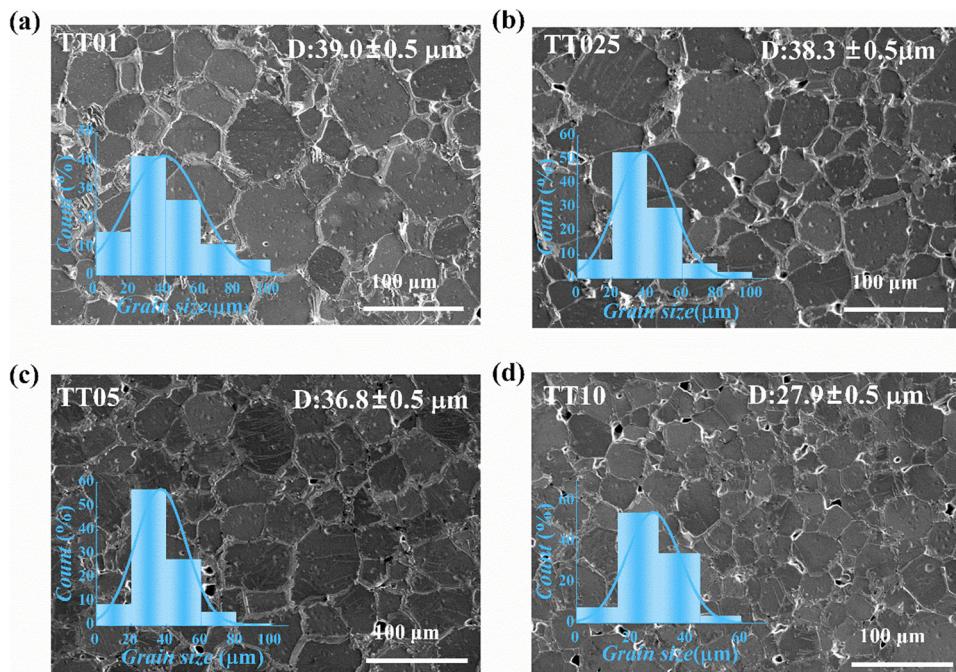


Fig. 2 SEM images of samples, and the inserts show the rain size distribution. (a) TT01, (b) TT025, (c) TT05, (d) TT10.

the semiconductor grains.³⁵ Conversely, at low frequencies, all ceramic samples exhibit conductivity approaches zero, which is

primarily attributable to the response of the insulating grain boundary resistance.

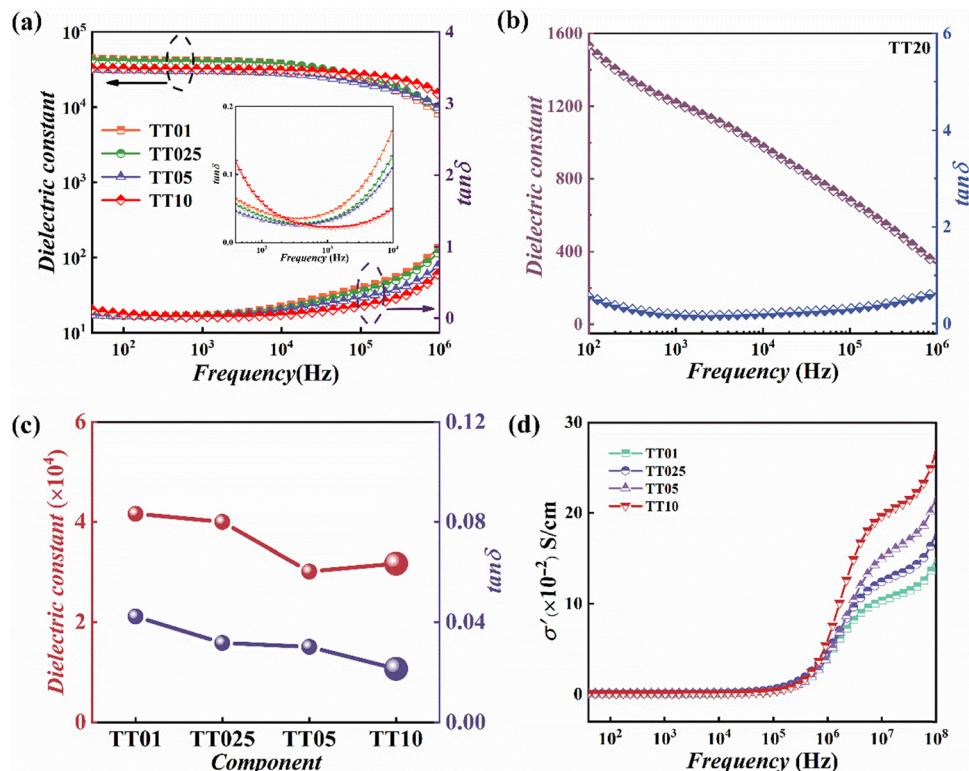


Fig. 3 (a) Frequency dependence of the dielectric performance for CdCTO-based ceramics, the inset shows the dielectric loss diagram at low frequencies. (b) Frequency dependence of the dielectric performance for TT20. (c) The dielectric constant and dielectric loss at 1 kHz. (d) Room temperature conductivity of CdCTO-based ceramics.

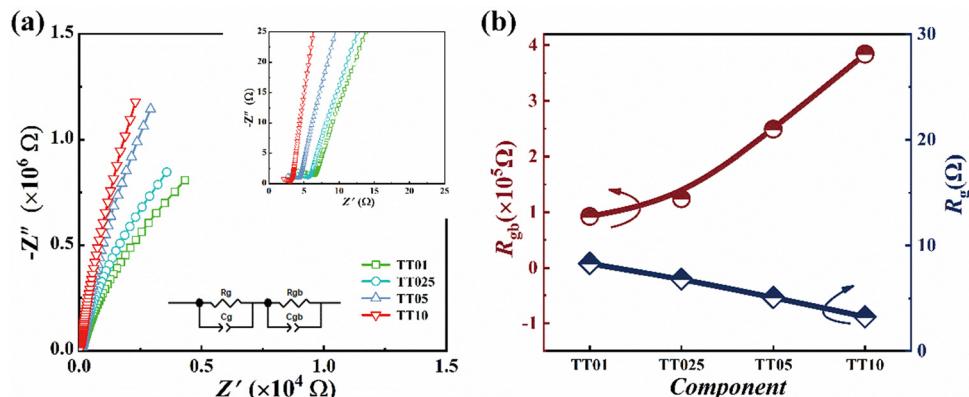


Fig. 4 (a) The complex impedance at room temperature. (b) R_{gb} and R_g values of CdCTO-based ceramics.

In order to reveal the inhomogeneity and relaxation mechanism of $\text{CdCu}_3(\text{Tb}_{1/2}\text{Ta}_{1/2})_x\text{Ti}_{4-x}\text{O}_{12}$ ceramics, impedance spectroscopy was utilized for a comprehensive analysis of their electrical response. Following the IBLC model, the impedance spectrum shows the inhomogeneity of grain boundary resistance (R_{gb}) and grain resistance (R_g).⁴⁶ The values of R_{gb} and R_g were calculated using the ZSimp Win software, which utilized an equivalent circuit comprising two series-connected RC elements. The relationship between the impedance real (Z') and impedance imaginary (Z'') components of impedance is depicted in Fig. 4, along with the corresponding R_g and R_{gb} values for the ceramic sample at room temperature.⁴⁷ In Fig. 4(b), large semicircular curve is visualized with respect to the insulating grain boundaries, while the non-zero intercepts at high frequencies indicate that semiconducting grains are present. Notably, the R_{gb} values ranging from approximately 9.21×10^5 to $3.84 \times 10^6 \Omega$, are considerably higher than the R_g values, which span from 3.23 to 8.31 Ω . The disparity in impedance values suggests that grain boundary relaxation exerts a key factor influence in the dielectric behavior of ceramics.

The temperature stability is a parameter that measures the ability of a dielectric material to be used in practical applications. Fig. 5 illustrates the temperature-dependent variation in the dielectric properties of TT01 and TT10, which was used to

evaluate the thermal stability of the ceramic. It can be observed that the dielectric temperature stability of TT10 is optimal, with the dielectric constant remaining nearly constant from RT to 200 °C. As illustrated in Fig. 6(a) and (b), the dielectric temperature profile of the ceramics reveals two distinct relaxation peaks, denoted as A1 ($-150 \sim 0$ °C) and A2 ($0 \sim 150$ °C). These peaks are attributed to thermally activated Debye-like relaxation behavior.^{48,49} Generally, A1 relaxation close to room temperature is associated with inter-electron hopping ($\text{Ti}^{4+}-\text{Ti}^{3+}$).⁵⁰ Conversely, the A2 relaxation observed at higher temperatures is linked to interfacial polarization.⁵¹ Notably, the amplitude of the A2 dielectric relaxation peak decreases significantly in the high-temperature region, especially in the case of optimal double doping. This reduction likely contributes to the enhanced temperature stability of the doped ceramics.

Moreover, the temperature coefficient of dielectric constant (TCK), which is crucial for the practical implementation of ceramics and is calculated as:

$$\text{TCK} = \frac{(C_T - C_{25^\circ\text{C}})}{C_{25^\circ\text{C}}} \times 100\% \quad (1)$$

Here, C_T represents the capacitance of ceramics at the tested temperature and $C_{25^\circ\text{C}}$ is the capacitance at 25 °C.⁵² The TT10 ceramics exhibit a narrow temperature range in which the relative change in dielectric constant ($\Delta\epsilon/\epsilon_{25^\circ\text{C}}$) does not exceed $\pm 15\%$.

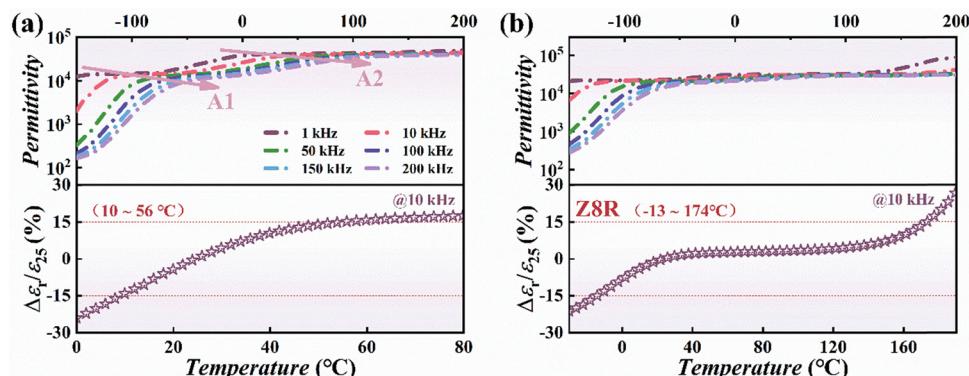


Fig. 5 Temperature dependent dielectric properties and temperature coefficients of dielectric constant for CdCTO-based ceramics. (a) TT01, (b) TT10.

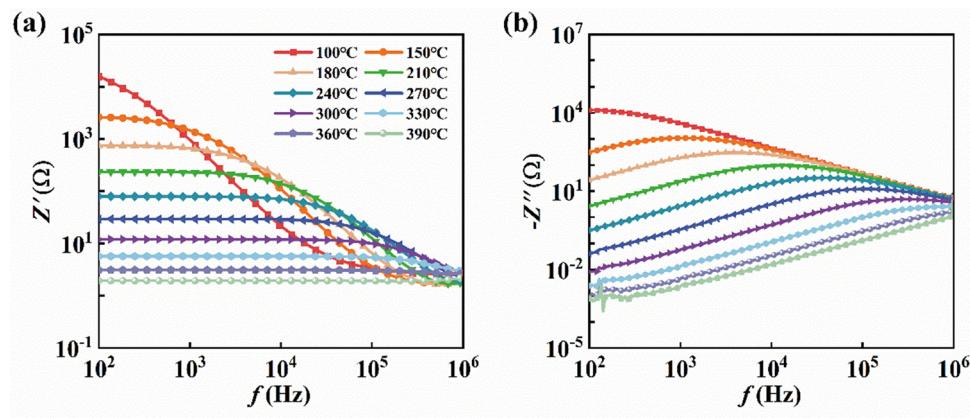


Fig. 6 TT10 ceramics at different temperatures from 100 to 390 °C. (a) Plot of real part (Z') versus frequency. (b) Plot of imaginary part ($-Z''$) versus frequency.

It is noteworthy that the most thermally stable TT10 ceramics demonstrate a TCK variation of under $\pm 15\%$ throughout the temperature range from -13 to 174 °C. This performance aligns with the stringent requirements for Z8R capacitor applications ($\Delta\epsilon/\epsilon_{25\text{ }^\circ\text{C}} \leq \pm 15\%$ from 10 to 150 °C). This characteristic forms the basis of their application in cutting-edge electronic components where consistent performance is critical.

The analysis of Z' and Z'' impedance components as a function of frequency offers valuable understanding of the sample's

relaxation behavior. Fig. 6 presents the plot of impedance against frequency for TT10 ceramics within a temperature range of 100 to 390 °C. Fig. 6(a) illustrates the impedance characteristics, where Z' decreases as both frequency and temperature increase, suggesting enhanced alternating current conductivity. With increasing temperature, the impedance plateau observed at low frequencies extends into the high-frequency range and converges to a single curve. This behavior is attributed to diminished potential barrier and the subsequent release of space charge.^{53–55} Generally, the

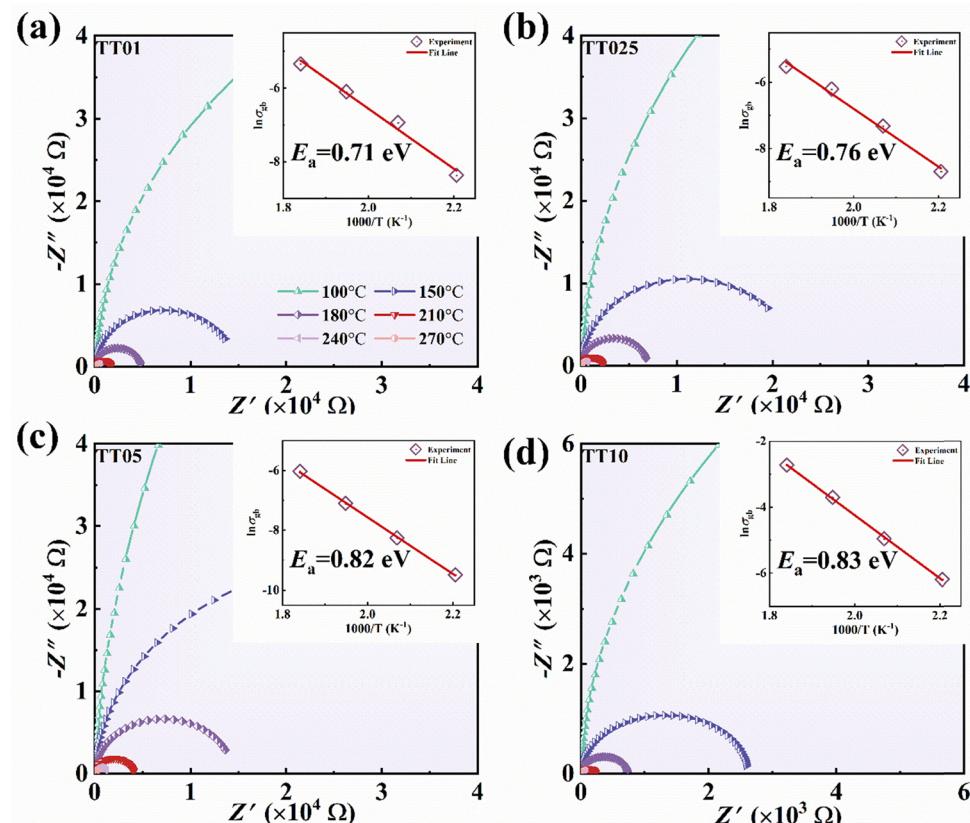


Fig. 7 Complex impedance of CdCTO-based ceramics at different temperature and the insert shows the relationship about $\ln\sigma_{gb}$ and $1000/T$ for CdCTO-based ceramics. (a) TT01, (b) TT025, (c) TT05, (d) TT10.

high-temperature, low-frequency impedance plateau in Z' coexists with a Debye-like relaxation peak in Z'' .⁵⁶ The corresponding Debye-like relaxation peak is visible in Fig. 7(b). The shift of the relaxation peak shifts toward higher frequencies with rising temperature indicates the occurrence of thermally activated relaxation behavior. In contrast, the low-frequency relaxation peak is mainly caused by the contribution of grain boundaries. Consequently, the impedance plot allows for the observation of both the grain boundary effects and grain contributions.

Fig. 7 illustrates the complex impedance of ceramics is presented for temperature ranging from 100 to 270 °C. At higher temperatures, a complete Nyquist semicircular plot is evident. Charge carriers in giant dielectric materials are readily excited at high temperatures, which leads to their long-range migration.⁵⁷ The equation provided can be used to calculate the value in question:

$$Z^* = Z' - iZ'' = \frac{1}{i\omega C_0 \epsilon^*} \quad (2)$$

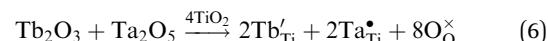
where Z' corresponds to the real part of Z^* , and Z'' corresponds to the imaginary part of Z^* . Additionally, $\epsilon^* = \epsilon' - i\epsilon''$, where ϵ' denotes the complex permittivity and i is the imaginary unit. Furthermore, $C_0 = \epsilon_0 S/d$, where S represents the sample area, and d is the thickness of the sample. The R_{gb} exhibits an inverse relationship with temperature, characteristic of semiconductors. The connection between conductivity and temperature is expressed by the following equation:

$$\sigma_{gb} = \sigma_0 \exp\left(-\frac{E_{gb}}{KT}\right) \quad (3)$$

where σ_0 is the pre-exponential factor, E_{gb} denotes the activation energy for grain boundary conduction, K is the Boltzmann constant, and T represents the test temperature.⁵⁸ To further

examine the grain boundary relaxation, a plot of grain boundary resistivity ($\ln \sigma_{gb}$) versus temperature ($1000/T$) is shown in the inset of Fig. 7 in conjunction with eqn (3). The sample exhibits grain boundary relaxation behavior, which can overcome a potential barrier energy of 0.71–0.83 eV. Generally, electron transfer is hindered in ceramics with high activation energy, which results in low losses.

As depicted in Fig. 8, the formation of defect clusters plays a critical role in the dielectric response of ceramics. In order to further investigate the origin of the giant dielectric constant mechanism, we conducted XPS analysis on the TT10 samples. Fig. 8(a) illustrates the distinct 2d peaks of Cd at 402.3 and 409.1 eV within the ceramic samples, with no additional valence transitions observed. In Fig. 8(b), the O 1s spectrum reveals a binding energy of 526.7 eV, typically associated with the Ti–O bond. The peak at 528.5 eV corresponds to V_O^{2+} in the sample or to the hydroxyl group (OH) present on the sample's surface.^{59,60} Generally, Ta^{5+} functions as a donor by providing electrons to Ti^{4+} , reducing it to Ti^{3+} . Simultaneously, Tb^{3+} serves as an acceptor by substituting for Ti^{4+} , resulting in the formation of charge-neutral V_O^{2+} to compensate for the introduction of trivalent ions. The formation process is described by in eqn (5) and (6):



The Cu in the ceramic samples exists in a mixed Cu^+/Cu^{2+} valence state, as illustrated in Fig. 8(c). A prominent peak at approximately 931.0 eV corresponds to Cu^{2+} , while a secondary set of peaks at a lower binding energy (~928.2 eV) indicates the suggests of Cu^+ .⁶¹ During the high-temperature sintering

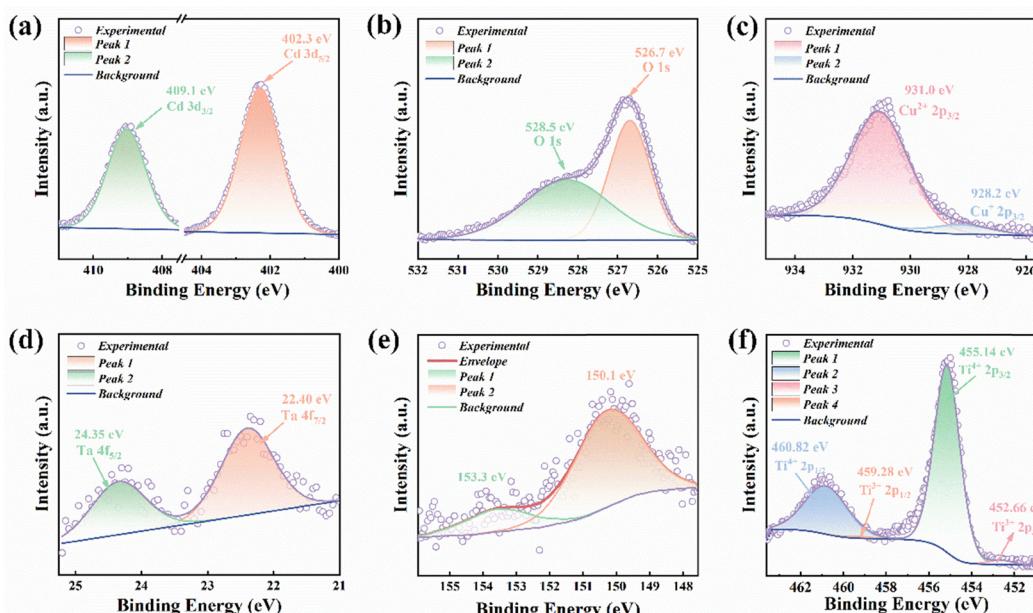


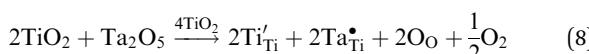
Fig. 8 The XPS spectra of TT10 ceramics, (a) Cd, (b) O, (c) Cu, (d) Ta, (e) Tb, (f) Ti.

process, electrical compensation for Cu^+ occurs *via* the formation of oxygen vacancies.⁶² The two XPS peaks observed at 24.35 and 22.40 eV in Fig. 8(d) correspond to $\text{Ta} 4f_{5/2}$ and $4f_{7/2}$, respectively. This suggests that the oxidation valence state of Ta is +5.⁵⁸

Fig. 8(e) displays the XPS spectrum of Tb 4d, indicating the existence of $\text{Tb}^{3+}/\text{Tb}^{4+}$ mixed valence states. The observed binding energies of about 153.3 eV and 150.1 eV provide evidence for the presence of mixed-valence $\text{Tb}^{3+}/\text{Tb}^{4+}$ states. As Tb^{4+} is an equivalent dopant, it is expected that Tb^{4+} substitutes Ti^{4+} in TiO_2 without creating V_O^{2+} .⁶³ As assumed in the reaction according to eqn (7):



The XPS spectra of the Ti element are presented in Fig. 8(f), where the binding energies of 460.90 and 455.05 eV correspond to the two characteristic orbital peaks of $\text{Ti} 2p_{1/2}$ and $2p_{3/2}$, respectively, confirming the presence of Ti^{4+} . Additionally, two smaller peaks at 459.25 and 455.75 eV are observed, further indicating the presence of Ti^{3+} , with the introduction of Ta^{5+} providing free electrons to the system.⁶⁴ As shown in eqn (8)–(9), Ti^{4+} is reduced to Ti^{3+} after gaining electrons:



In summary, Ta^{5+} and $\text{Tb}^{3+/4+}$ ions occupy the Ti^{4+} sites, and the semiconducting grains of the ceramics arise from electron hopping between Ti^{4+} and Ti^{3+} . The coupling effect between the defects restricts carrier transport and achieves lower losses, which is essential for understanding the mechanism of the dielectric properties.

4. Conclusion

In summary, the preparation of a novel low-loss $\text{CdCu}_3(\text{Ta}_{1/2}\text{Ta}_{1/2})_x\text{Ti}_{4-x}\text{O}_{12}$ ceramic is described, achieved through a conventional solid-phase method, and investigates the source of the giant dielectric mechanism. The TT10 ceramic exhibits remarkable dielectric properties, with $\epsilon \sim 3.21 \times 10^4$ and $\tan \delta \sim 0.020$ at 1 kHz. Furthermore, across the temperature range of -13 to 174 °C, the variation of ϵ relative to its 25 °C value ($\Delta\epsilon/\epsilon_{25\text{ }^\circ\text{C}}$) remains within $\pm 15\%$. The dielectric response mechanism of $\text{CdCu}_3(\text{Ta}_{1/2}\text{Ta}_{1/2})_x\text{Ti}_{4-x}\text{O}_{12}$ ceramics has also been investigated. Analyses of XPS and relaxation behavior reveal that the grain boundary resistance is enhanced by equipotent doping, while the optimization of dielectric properties attributed to the IBLC effect. Additionally, the defect structure may influence performance. It is evident that further investigations are needed to enhance the dielectric properties of CdCTO ceramics and to gain insight into the response mechanisms. This will facilitate the expansion of their applications in modern electronics, sensors and other fields.

Data availability

The data that support the findings of this study are available from the corresponding author and first author upon reasonable request.

Conflicts of interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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