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Interfacial charge doping effect in C8-DNTT/PDIF-CN₂ heterojunction field-effect transistors†

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In the last 15 years, DNTT-based compounds have emerged as a new generation of hole-transporting (p-type) organic semiconductors with superior charge transport properties. Even today, indeed, this class of derivatives is under intense scrutiny for the achievement of high-performance field-effect transistors to be applied in the development of advanced organic circuitry. Here, we analyze the growth of evaporated C8-DNTT films on HMDS-treated SiO₂ surfaces, highlighting the dependence of the related morphological and electrical properties on the substrate temperature (T_{sub}) held during film condensation. In this way, we identified a T_{sub} range able to guarantee high mobility values (larger than $2.5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$) and morphological features being more compatible for the growth of additional layers on their top surfaces. This finding was the basic point to investigate the deposition of n-type PDIF-CN₂ films on bottom C8-DNTT layers for the fabrication of heterojunction field-effect transistors. The electrical characterization of these devices, providing a shift of the threshold voltages and a minor sensitivity to the bias stress effect in comparison with the single-layer C8-DNTT counterparts, suggests the formation of a charge accumulation region at the organic/organic interface. This scenario was confirmed by additional electrical analyses performed on complementary double-layer structures.

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Introduction

Initially introduced in the 90s, thienoacenes represent a wide class of π -extended hetero-aromatics characterized by a conjugated core where thiophene rings are combined with hydrocarbon structures (acenes) based, for their part, on linearly fused benzene rings.^{1,2} When compared with polyacenes, being among the most studied compounds (*e.g.* pentacene) within the first generation of organic semiconductors, thienoacenes exhibit generally low-lying HOMO levels and larger values of optical band gaps.

Despite the interest in their potential application for innovative electronics, until the mid-2000s, devices based on thienoacenes exhibited rather limited performances. The situation rapidly changed when a specific family of thienoacenes, featuring a conjugated molecular core with two fused thiophene rings in the central part and external acenes, was demonstrated to exhibit superior charge transport properties and, consequently, a strong appeal for the fabrication of organic field-effect

transistors (OFETs).³ In particular, the [1]benzothieno[3,2-*b*]-[1]benzothiophene (BTBT) molecule, when functionalized with additional terminal groups such as alkyl chains or phenyl rings, was found to provide charge carrier mobility values larger than $1 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, overcoming the performances of prototypical pentacene.⁴ In this context, it was argued that, in comparison with carbon, the larger electron densities on the sulfur atoms in the HOMO configuration could favor the effective overlap between the frontier molecular orbitals of neighboring molecules, thus enhancing the related charge transfer rate.¹ BTBT-based materials are now widely investigated for the deposition of thin films,⁵ in particular, through solution-based techniques, achieving mobility values which, in some cases, were reported to exceed $10 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$.⁶

In 2007, a π -extended BTBT analogue, called dinaphtho[2,3-*b*:2',3'-*f*]thieno[3,2-*b*]thiophene (DNTT), was synthesized, revealing excellent self-assembly properties and high mobility values when evaporated in the form of thin films.⁷ Nowadays, DNTT is one of the most investigated hole-transporting organic compounds⁸⁻¹⁰ and is widely considered for the development of innovative devices such as environmental sensors¹¹ and radiation detectors.¹² Several studies have outlined that, owing to its large ionization potential (*i.e.* the HOMO level is about -5.4 eV),¹ DNTT exhibits much more stable electrical performances under ambient conditions if compared with pentacene, which can be more easily oxidized (the HOMO level is about -5.0 eV).¹³ It was also demonstrated that even better charge

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transport properties can be obtained when the DNTT basic molecular core is functionalized with side alkyl chains. A detailed analysis by Kang *et al.*¹⁴ pointed out that the length of the alkyl-chains impacts the final electrical performances with the best results achievable when a decyl chain (C10-DNTT) was considered.

In evaporated films of alkyl-DNTT derivatives, molecules stand approximately upright with their longer axis being almost orthogonal to the growth surface (*i.e.* the conjugated plane is parallel to the substrate). Molecular packing follows a herringbone motif providing an almost isotropic charge transport within any monolayer. This feature is related to the specific molecular arrangement which gives rise to similarly large intermolecular overlaps of the π -orbitals in all the different planar directions.⁹ The peculiar 2D character of the charge transport in these compounds is also beneficially combined with the reduction of the dynamic disorder, associated with intermolecular thermal vibrations having lower amplitudes in comparison with alternative conjugated molecules.¹⁵

Even today, C10-DNTT is the compound of this family with the reported best charge transport performances and charge carrier mobility approaching $10 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$.¹⁶ These outstanding properties motivated several studies about the growth mode of this compound when evaporated from effusive Knudsen cells on various types of substrates. Hence, it was shown that, along with their superior electrical features, C10-DNTT films display specific morphological properties and the characteristic presence of very tall (up to hundreds of nanometers) lamellae protruding from the underneath surface.¹⁶ It has also been clarified that these features are strongly correlated with the temperature of the substrate (T_{sub}) held during film deposition. When T_{sub} is increased and overcomes a certain range (well above 100°C), the lamellae density is strongly reduced and the basic 2D-like film structure with flat surfaces tends to emerge with much more clarity. For C10-DNTT, however, this effect is also accompanied by a worsened connectivity of the crystalline grains and the related mobility values get considerably lowered.¹⁶

In comparison with C10-DNTT and other DNTT-based molecules bearing biphenyl side chains,¹⁷ the growth by evaporation of the dioctyl-DNTT (C8-DNTT) derivative (see Fig. S1 in the ESI† for the molecular structure and the HOMO–LUMO levels) has been much less explored and very few studies are presently available.¹⁸ Although this compound was reported to exhibit lower mobility values in comparison with C10-DNTT, it was recently investigated in terms of its thermal transport characteristics, demonstrating suppressed thermal conductivity in comparison with the parent DNTT molecule. This behavior was explained by invoking the strong localization of the intermolecular vibrational modes induced by the presence of the alkyl side chains.¹⁹ Among others, this observation aroused new interest for this compound in view of its possible application in thermoelectric devices.

In this work, we carefully analyzed the growth of C8-DNTT films on SiO_2 substrates functionalized with hexamethyldisilazane (HMDS). Similar to that reported for C10-DNTT,¹⁶ we identified the temperature range between 60 and 90°C for T_{sub}

as the most promising to obtain large charge carrier mobility values. When T_{sub} was fixed at 85°C , moreover, the film surface appears flatter, and the occurrence of the protruding 3D lamellae is noticeably attenuated. Starting from these findings, we investigated the response of double-layer active channels consisting of *N,N'*-1*H*,1*H*-perfluorobutyl-1,6-dicyanoperylene-3,4:9,10-bis(dicarboximide) (PDIF-CN₂) (Fig. S1, ESI†) layers grown with different thicknesses on pre-deposited 15 nm thick C8-DNTT films. PDIF-CN₂ is still today one of the most widely investigated compounds because of its remarkable air-stable electron-transporting features.²⁰

As a whole, the electrical analysis of the final heterojunction transistors suggests that, although the combination of PDIF-CN₂ and C8-DNTT does not provide field-effect devices with a balanced ambipolar response, it allows the achievement of an interfacial charge accumulation region producing a clear shift of the threshold voltages for the hole-transporting channel and a significantly reduced sensitivity to the bias stress phenomenon in comparison with the single-layer devices. More evidence about the formation of a conducting interface between C8-DNTT and PDIF-CN₂ was obtained by analyzing the response of double-layer channels deposited on glass substrates and field-effect transistors relying on the inverted PDIF-CN₂/C8-DNTT configuration.

Experimental

C8-DNTT and PDIF-CN₂ (ActivInk N1200) were purchased from LUMTEC and FLEXTERRA, respectively, and used without any further purification. Film evaporation was carried out in a high vacuum ($P \sim 10^{-7} \text{ mbar}$) by Knudsen cells with evaporation rates ranging between 0.4 and 0.5 nm min^{-1} for both compounds. The substrate temperature (T_{sub}) was set in the range between 25 and 100°C according to the specific experiment. For the fabrication of bottom-contact transistors, the organic films were evaporated on substrates consisting of a 500- μm -thick highly doped silicon (Si^{++}) layer, working also as the gate electrode, a 200-nm-thick SiO_2 dielectric barrier and pre-patterned gold electrodes with an interdigitated layout.²¹ For top-contact field-effect devices, similar $\text{Si}^{++}/\text{SiO}_2$ substrates, without pre-patterned contacts, were utilized. In this case, after the organic film deposition, the devices were completed through the evaporation using a shadow mask of gold rectangular pads acting as source/drain electrodes ($P \sim 10^{-5} \text{ mbar}$, a deposition rate of about 2 nm s^{-1} , and a final thickness of 40 nm). The same top-contact configuration was employed to perform current–voltage (IV) measurements with the organic layers being evaporated on glass substrates. For all the tested samples, before the organics' deposition, the growth surface was cleaned and made more hydrophobic by applying HMDS self-assembling monolayers following a previously optimized protocol.²² The morphological features of the film surface were analyzed by atomic force microscopy (AFM) carried out using a XE-100 Park atomic force microscope, equipped with silicon-doped cantilevers (resonance frequency around 300 KHz)



provided by Nanosensor™. AFM images were acquired at a resolution of 512×512 pixels. Film thickness values were monitored *in situ* using a quartz microbalance and checked *ex situ* by AFM. The electrical tests were performed in a vacuum ($P \sim 10^{-5}$ mbar) by means of a cryogenic probe station connected to a Keithley 4200 semiconductor parameter analyzer.

The field-effect mobility (μ) and threshold voltage (V_{th}) values were estimated by appropriately fitting the transfer-curves using the standard MOSFET equations in the linear and saturation regimes, being, respectively:

$$I_{DS} = \frac{WC_{ins}}{L} \cdot \mu \cdot (V_{GS} - V_{th}) \cdot V_{DS} \quad (1)$$

$$I_{DS} = \frac{WC_{ins}}{2L} \cdot \mu \cdot (V_{GS} - V_{th})^2 \quad (2)$$

Here, C_{ins} is the dielectric capacitance per unit area, being 17.3 nF cm^{-2} for all the considered samples, while V_{DS} and V_{GS} are the voltages applied, respectively, between the drain and source and the gate and source. For bottom-contact devices based on photo-lithographically patterned gold electrodes, the channel width (W) and channel length (L) were 22 nm and $40 \mu\text{m}$ ($W/L \sim 500$), respectively. For the top-contact samples bearing gold electrodes achieved by shadow masks, active channels with $L = 200, 150, 100$ and $50 \mu\text{m}$ were fabricated, while W was invariably fixed at $500 \mu\text{m}$. To avoid current overestimations related to the fringe effects, the active area of all the devices was carefully isolated from the remaining organic film by scratching with metallic tips operated under an optical microscope.

Results

C8-DNTT film deposition and characterization of the related field-effect devices

The initial part of this study was devoted to analyze the morphological properties of C8-DNTT films, deposited on the HMDS-treated SiO_2 surface, by carefully tuning the substrate temperature (T_{sub}) held during the evaporation process. AFM images in Fig. 1(a-e) dealing with 50 nm thick films, indeed, show very clearly the T_{sub} relevant impact on the overall morphological organization. When the substrate is kept at room temperature, the C8-DNTT film surface is almost completely covered by tall lamellar structures with heights overcoming 100 nm . This specific type of nanoscale features, which make quite challenging the acquisition of clear AFM images, was also observed for C10-DNTT and diphenyl-DNTT (DPH-DNTT) layers deposited by similar evaporation processes.^{16,17} On the other hand, when T_{sub} starts to increase, the overall density of lamellae tends to be reduced even if their size apparently increases. The AFM image in Fig. 1c, taken for a film grown with $T_{sub} = 70 \text{ }^\circ\text{C}$, reveals that the main axis of the lamellar structures can approach $1 \mu\text{m}$. The further increase of T_{sub} towards values of $100 \text{ }^\circ\text{C}$ or even larger temperatures provides a still more significant lowering of the lamellae density, with the film surfaces getting flatter and flatter and the presence of rounded-shaped molecularly terraced C8-DNTT islands emerging in the AFM images (Fig. 1d). In parallel, however, it is also possible to observe the formation of large cracks corrupting the film structural integrity (Fig. 1e). Consequently, the temperature range between 80 and $90 \text{ }^\circ\text{C}$ was

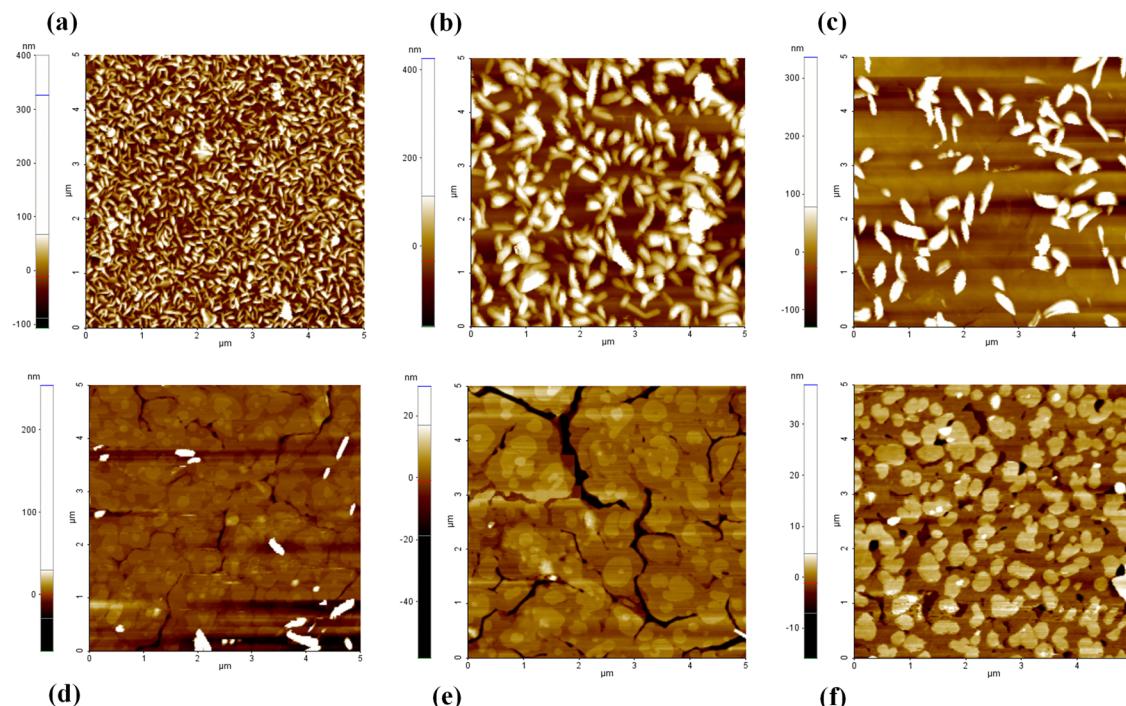


Fig. 1 AFM images ($5 \times 5 \mu\text{m}^2$) of the surface of C8-DNTT films (thickness = 50 nm) deposited at different substrate temperatures (T_{sub}): (a) $T_{sub} = 25 \text{ }^\circ\text{C}$, (b) $T_{sub} = 55 \text{ }^\circ\text{C}$, (c) $T_{sub} = 70 \text{ }^\circ\text{C}$, (d) $T_{sub} = 85 \text{ }^\circ\text{C}$, and (e) $T_{sub} = 100 \text{ }^\circ\text{C}$. (f) Surface of a 15 nm thick C8-DNTT film deposited at $T_{sub} = 85 \text{ }^\circ\text{C}$.



identified as the best option to obtain a trade-off between these two morphological regimes reducing the lamellae density and avoiding the formation of the fracture lines. This favorable scenario is even clearer in Fig. 1f where a 15 nm thick film grown at $T_{\text{sub}} = 85$ °C is reported, confirming the presence of circular crystalline domains with an average diameter up to several hundreds of nanometers. From these films, we could estimate the height of the molecular terraces being close to 3.4 nm (see Fig. S2, in the ESI†), in very good agreement with the values reported in previous studies.¹⁴

To check the correlation between the above discussed morphological features and the corresponding charge transport performances, bottom-contact C8-DNTT transistors, featuring active channels deposited at different T_{sub} , were fabricated and electrically characterized. As expected, all the analyzed devices correctly display a p-type response with the I_{DS} current enhancement occurring at applied negative V_{GS} (Fig. S3, in the ESI†). Significant hysteretic effects are also visible in the transfer curves and (see below for a comparison with the top-contact devices) are likely related to charge trapping phenomena mainly active in the source/drain contact regions, where the film morphology quality usually degrades for organic materials evaporated on pre-patterned metal electrodes.^{23,24} The maximum values of the hole mobility (μ_h) extracted for these devices in the saturation regime were slightly larger than $0.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, with Fig. S3c (ESI†) demonstrating that the T_{sub} range between 60 and 90 °C provides the best electrical performances. Conversely,

mobility values turn out to be remarkably lowered when T_{sub} approaches 100 °C and this last behavior can be obviously ascribed to the increased presence of macroscopic fractures affecting the connectivity level between the different crystalline domains (Fig. 1e).

Based on the previous analyses, our attention was then focused on the fabrication of C8-DNTT single-layer devices bearing active layers deposited at $T_{\text{sub}} = 85$ °C and completed with 40 nm thick gold electrodes evaporated on their surfaces (*i.e.* top-contact configuration). Typical output and transfer-curves of this class of transistors are reported in Fig. 2 and Fig. S4 (ESI†). Output curves (Fig. 2a and b and Fig. S4a and b, ESI†) show the presence of very well defined saturation regions and an almost ideal linear response (see the following sections for a more detailed discussion on the contact resistance effect) under the application of, respectively, large and low V_{DS} . The hysteresis effects are strongly attenuated in the transfer-curves (Fig. 2c) with the final μ_h values ranging between 2.5 and approximately $5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. Hence, these top-contact devices display mobility values which are about one order of magnitude larger than their bottom-contact counterparts and result among the largest ever reported for the C8-DNTT compound.^{18,25} Interestingly, when reducing the channel length (down to 50 µm), we observed a progressive increase of the mobility, with the values extracted in the saturation regime being larger and larger than those evaluated in the linear region. These features seem to suggest a favorable effect of the lateral (*i.e.* between the drain

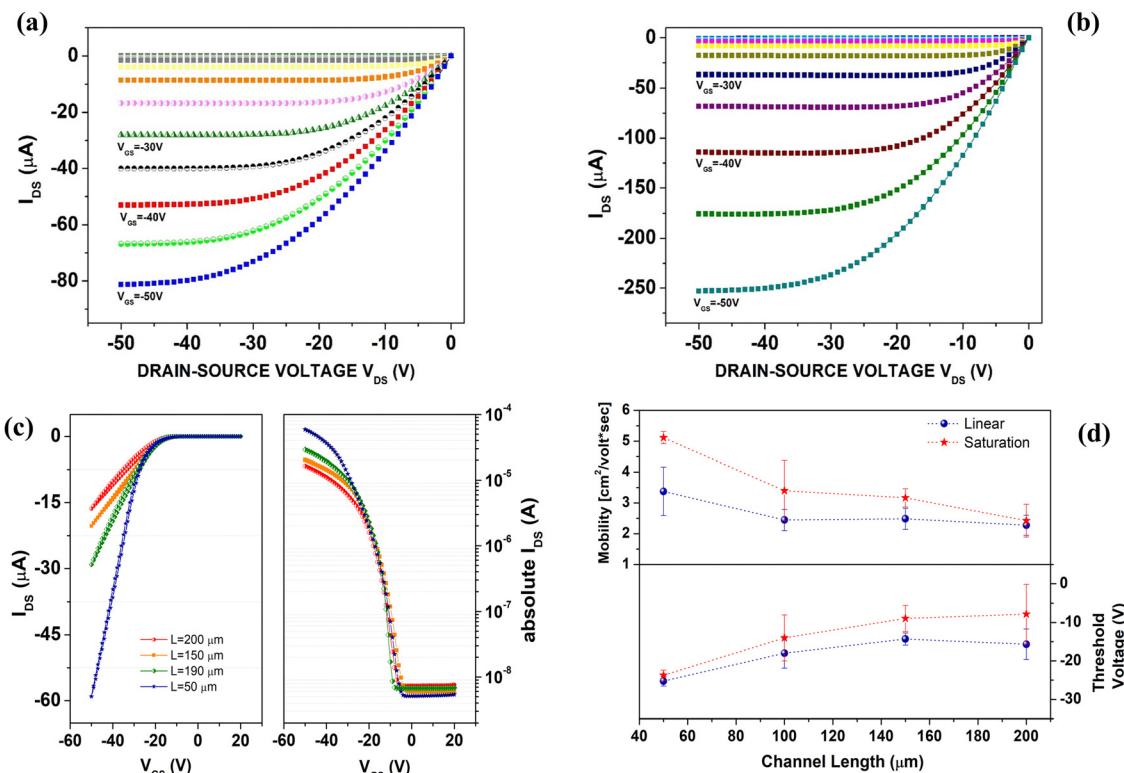


Fig. 2 Electrical response of top-contact C8-DNTT single-layer transistors with 15 nm thick active channels deposited at $T_{\text{sub}} = 85$ °C: output curves of devices with channel lengths (L) of (a) 200 and (b) 50 µm. (c) Transfer curves measured in the linear regime ($V_{\text{DS}} = -5$ V) for devices with different L . (d) Mobility and threshold voltage values extracted in the linear and saturation regimes as a function of the channel length.



and source) electric field which tends to enhance the charge carrier mobility. It is also worth to outline that, as shown in Fig. 2c, the threshold voltages (V_{th}) are largely negative being comprised between -10 and -25 V. This occurrence is commonly explained by considering the presence of trapping states which require to be compensated before charge carriers can be accumulated in the channel.²⁶

C8-DNTT/PDIF-CN₂ heterojunction devices

The possibility to combine C8-DNTT and PDIF-CN₂ in the development of p-n heterojunction-based devices posed some interesting challenges since both molecules, when evaporated in the form of thin films, are characterized by significant dependence on the deposition conditions and, more specifically, on the substrate temperature (T_{sub}). While the range between 80 and 90 °C was previously identified for C8-DNTT as the best option to limit the presence of the surface lamellar structures and preserve the macroscopic film integrity, several previous studies demonstrated that PDIF-CN₂ films exhibit the best charge-transport performances when T_{sub} is set between 100 and 110 °C.^{27,28}

Hence, we tried initially to satisfy these diverse requirements by depositing sequentially the two molecules at the corresponding optimized T_{sub} (*i.e.* 85 °C for C8-DNTT and 100 °C for PDIF-CN₂). As shown in Fig. S5 in the ESI,† however, this procedure affects noticeably the morphology of the C8-DNTT films which are likely subjected to a reorganization when T_{sub} is increased at 100 °C in view of the PDIF-CN₂ deposition. These changes are directly reflected in the electrical response of the related field-effect devices showing a considerable degradation of the hole mobility (μ_h) which was reduced to about

$0.5\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$, while the electron mobility (μ_e) values in the PDIF-CN₂ channel were lower than $10^{-4}\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$ in any case. Given these findings, a different strategy was considered, and both films were sequentially deposited while keeping the substrate temperature at 85 °C, thus reducing also the time required for the overall evaporation process. Fig. 3 shows the morphology of a few films achieved in this way, where the C8-DNTT thickness was fixed at 15 nm and that of PDIF-CN₂ was varied from 10 (b–c) to 20 nm (e–f). By exploiting the shadow effect related to the use of a metallic mask to confine the organic layer mainly in the central part of the substrates, for the various samples, we were able to observe the morphology of C8-DNTT films on HMDS-SiO₂ and that of the PDIF-CN₂ layers grown on both HMDS-SiO₂ and C8-DNTT surfaces. From these images, we can still recognize the above discussed morphology of the C8-DNTT layers, featuring well-defined circular-shaped islands and average roughness (σ_r) slightly higher than 3 nm, while PDIF-CN₂ is characterized by the typical compact organization given by the coalescence of smaller rounded crystallites.²⁷

Significantly, the PDIF-CN₂ morphology on C8-DNTT is maintained even with an enhanced σ_r which becomes larger at increasing thickness (δ_F). While on HMDS/SiO₂, the PDIF-CN₂ film roughness is about 2.5 nm for all the investigated thicknesses, the perylene layers on C8-DNTT result much rougher with σ_r exceeding 5 nm when $\delta_F = 30$ nm. This phenomenon resembles that recently observed for PDIF-CN₂ films grown on P3HT bottom layers.²⁹

C8-DNTT/PDIF-CN₂ heterojunction transistors, fabricated according to the aforementioned protocol ($T_{sub} = 85$ °C) and with different thicknesses (δ_F) of the PDIF-CN₂ top

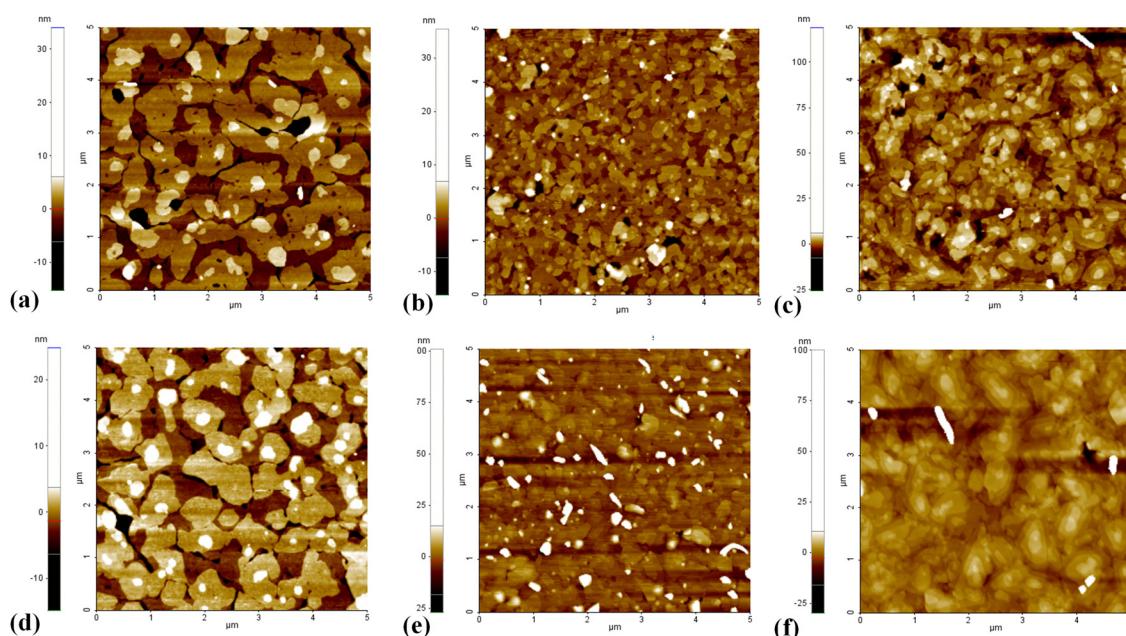


Fig. 3 AFM images ($5 \times 5 \mu\text{m}^2$): (a) and (d) surfaces of 15 nm thick C8-DNTT layers grown on HMDS-treated SiO₂, (b) and (e) surfaces of PDIF-CN₂ films with thicknesses of 10 nm and 20 nm, respectively, grown on HMDS-treated SiO₂, and (c) and (f) surfaces of PDIF-CN₂ films, with thicknesses of 10 nm and 20 nm, respectively, grown on C8-DNTT bottom layers. All layers were deposited with a T_{sub} of 85 °C.



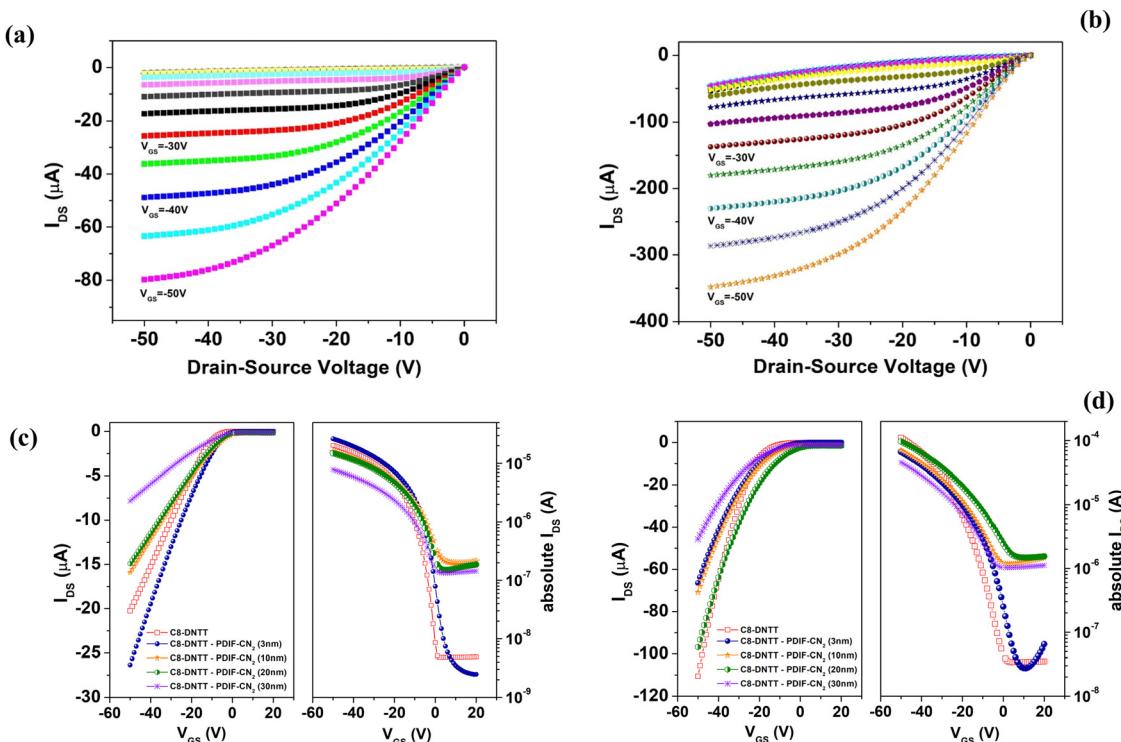


Fig. 4 Electrical response of top-contact C8-DNTT/PDIF-CN₂ heterojunction transistors in the hole accumulation region: output curves of C8-DNTT (15 nm)/PDIF-CN₂ (20 nm) heterojunction transistors with active channels deposited at $T_{\text{sub}} = 85$ °C and channel lengths of (a) $L = 200$ and (b) 50 μm . Transfer-curves in linear and semi-log plots of C8-DNTT/PDIF-CN₂ heterojunction transistors ($L = 200$ μm), with different thicknesses (δ_F) of the PDIF-CN₂ top layer, measured in the (c) linear ($V_{\text{DS}} = -5$ V) and (d) saturation regions ($V_{\text{DS}} = -50$ V).

layer deposited on the 15 nm thick C8-DNTT bottom film, were electrically tested to assess the possibility to accumulate both holes and electrons. Typical responses of these devices in the hole accumulation regions (*i.e.* under the application of negative V_{GS} and V_{DS}) are reported in Fig. 4 and Fig. S6 (ESI[†]).

Output curves (Fig. 4a and b and S6a (ESI[†]), measured with V_{GS} ranging between +20 and -50 V) resemble those achieved with C8-DNTT single-layer devices unless the presence of non-negligible I_{DS} currents recorded for positive V_{GS} . This feature is more clearly shown in Fig. S6b (ESI[†]), where a set of output curves, measured for V_{GS} going from +20 V and -10 V, is reported. For large negative V_{DS} , we can notice here that the I_{DS} current decreases (in absolute value) when V_{GS} goes from +20 V to 0 V, while it raises again for $V_{\text{GS}} = -10$ V. For positive V_{GS} , indeed, the current flowing across the channel is dominated by electrons (negative charges) injected from the drain electrode, where the voltage difference with the gate (V_{GD}) becomes largely positive. When V_{GS} assumes more negative values, conversely, the hole accumulation channel is restored, and the I_{DS} current is given only by the flow of holes injected from the source electrode. In a complementary way, output curves in Fig. S7a and b (ESI[†]) are recorded for positive V_{DS} and V_{GS} ranging from 0 to +70 V. In this case, the response (Fig. S7a, ESI[†]) appears ruled by the injection of holes from the drain electrode providing the typical power dependence of I_{DS} on V_{DS} . The electron accumulation effect is obtained only when

V_{GS} overcomes +50 V with the progressive increase of I_{DS} up to $V_{\text{GS}} = +70$ V (Fig. S7b, ESI[†]). The above discussed output curves clarify that the C8-DNTT/PDIF-CN₂ heterojunction transistors are characterized by a strongly unbalanced ambipolar response with the hole-transporting channel being much more effective than the electron-transporting counterpart. This scenario is still more evident when the transfer-curves are considered. Fig. 4c and d report a set of transfer-curves measured, respectively, in the linear ($V_{\text{DS}} = -5$ V) and saturation ($V_{\text{DS}} = -50$ V) regimes of the hole accumulation region. More specifically, these data compare the behavior of a C8-DNTT single layer device with that of C8-DNTT/PDIF-CN₂ heterojunction transistors having different thicknesses (δ_F) of the top layer (from 3 to 30 nm). For all these curves, the p-type response (*i.e.* hole accumulation effect) is visible well and the hole mobility values retain large values (see the discussion below). Moreover, we observe an evident enhancement (by about two orders of magnitude) of the I_{DS} current for positive V_{GS} when δ_F is larger than 10 nm.

For the sake of completeness, Fig. S7c (ESI[†]) reports two transfer-curves measured in the electron accumulation region under an application of $V_{\text{DS}} = +50$ V. As discussed for the output curves in Fig. S7b (ESI[†]), the electron accumulation and the associated increase of the I_{DS} current take place only when V_{GS} exceeds +50 V. In this case, the V_{GS} -driven overall percentage variation of I_{DS} is only 10–20% in comparison with the minimum value. For the C8-DNTT/PDIF-CN₂ transistors investigated

in this study, no electron accumulation effect was observed for the devices with the thinnest ($\delta_F = 3$ nm) PDIF-CN₂ films. Conversely (see Fig. S7d, ESI†), the electron mobility (μ_e) values were found to be comparable for the devices with δ_F equal to 10 and 20 nm, remaining, in any case, mainly comprised between 10^{-3} and 10^{-2} cm² V⁻¹ s⁻¹. A further μ_e reduction was finally observed for the C8-DNTT/PDIF-CN₂ devices having $\delta_F = 30$ nm. This finding should be basically associated with the enhanced 3D morphological character of the thicker PDIF-CN₂ films evaporated on C8-DNTT. This occurrence is translated in a worse connection degree between the crystalline domains and a larger access resistance effect toward the organic/organic interfacial regions where electrons are accumulated.

Ascertained the prevailing p-type response of the C8-DNTT/PDIF-CN₂ devices, most efforts were carried out to extract the μ_h and V_{th} values associated with the hole transport region with the specific goal (see also the Discussion and conclusions section) to analyze the related dependence on the PDIF-CN₂ thickness (δ_F). The overall analysis is summarized in Fig. 5 where the electrical parameters, estimated in both the linear and saturation regimes, are separately plotted as a function of the device channel length (L). In general, μ_h values for the

heterojunction devices tend to be reduced in comparison with the C8-DNTT single-layer transistors. The largest μ_h values are 1.5 cm² V⁻¹ s⁻¹ being further lowered down to about 1 cm² V⁻¹ s⁻¹ when the PDIF-CN₂ thickness is increased at 30 nm. On the other hand, threshold voltages (V_{th}) for the devices based on C8-DNTT/PDIF-CN₂ active channels are considerably less negative with average values approaching 0 and -5 V in the saturation and linear regions, respectively. In comparison with the C8-DNTT single-layer devices, hence, we can assume an average ΔV_{th} shift of being close to 10 V which, using the equation $(n = \frac{C_{ins} \cdot \Delta V_{th}}{e})$, provides an increase of the charge carrier density by about 1.1×10^{12} cm⁻².

A further comparison between C8-DNTT single-layer and C8-DNTT/PDIF-CN₂ heterojunction devices was considered in terms of the bias stress (BS) effect. This is a quite general phenomenon for OFETs, consisting of a progressive and reversible reduction of the I_{DS} current under static voltage polarizations which drive the devices in the accumulation region. Given its importance to assess the device reliability in practical applications, this effect has been widely investigated for several organic compounds with both p- and n-type characters.^{30–34}

Fig. 6 summarizes the results of a few experiments assessing the BS impact on the single-layer and heterojunction devices.

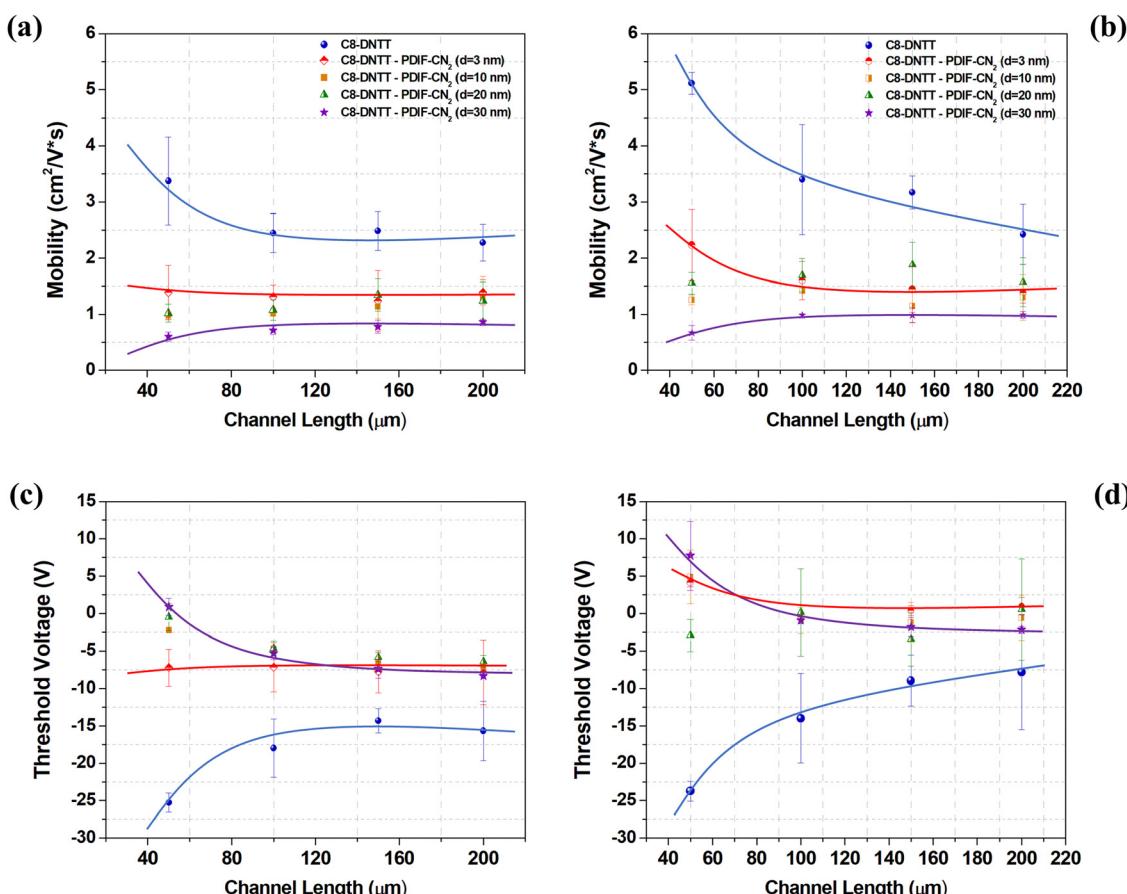


Fig. 5 Electrical response of top-contact C8-DNTT/PDIF-CN₂ heterojunction transistors with different thicknesses of the PDIF-CN₂ top layer: comparison between the mobility (a) and (b) and the threshold voltage (c) and (d) values for the hole accumulation region, extracted in the linear (a) and (c) and saturation (b) and (d) regimes, as a function of the channel length. The color curves are just a guide for eyes.

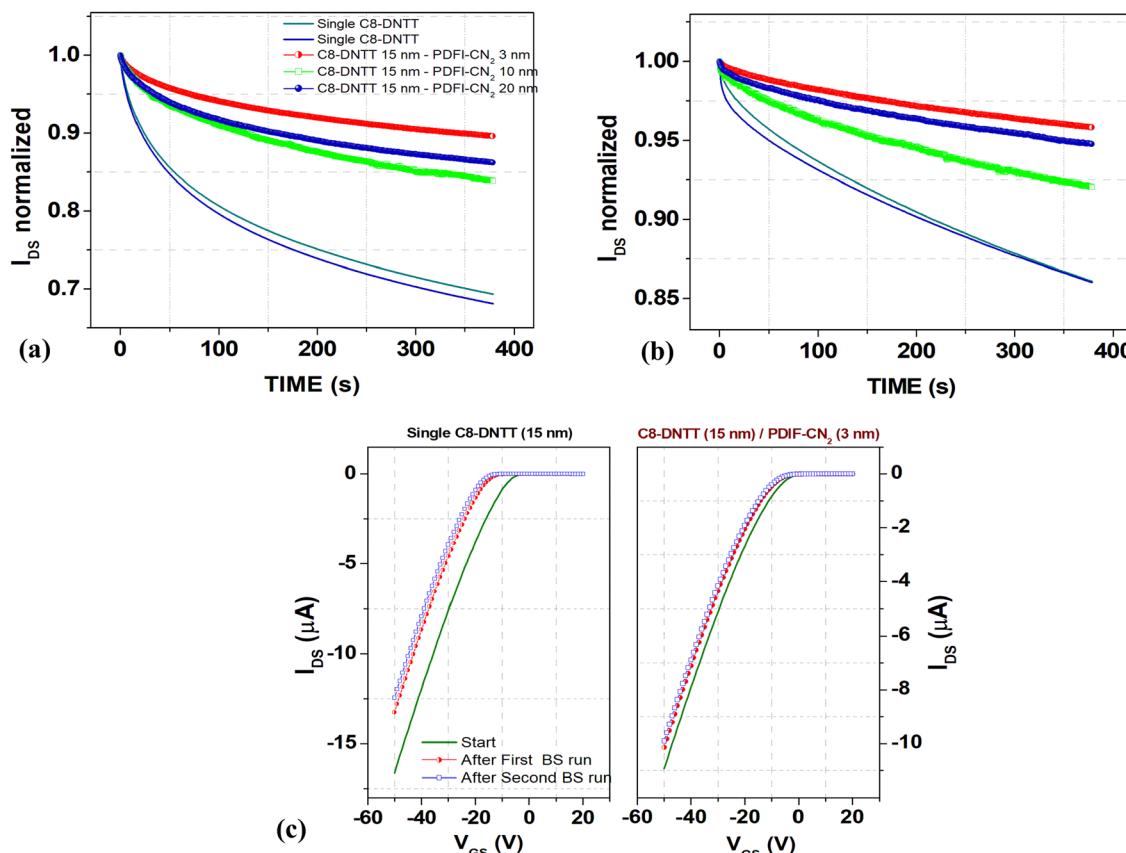


Fig. 6 Bias stress (BS) experiments performed with C8-DNTT single-layer and C8-DNTT/PDIF-CN₂ heterojunction transistors in the hole accumulation regions ($V_{DS} = -10$ V and $V_{GS} = -40$ V): normalized I_{DS} current curves, as a function of the stressing time, recorded during (a) test I and (b) test II. Here, $t = 0$ s represents the beginning of the experiments for each run. (c) Comparison among transfer-curves measured at $t = 0$ s, $t = 10$ min (after test I), and $t = 20$ min (after test II) of the BS experiment.

For any analyzed transistor, we performed two consecutive BS experiments under the application of fixed polarizations ($V_{GS} = -40$ V and $V_{DS} = -10$ V) for 6 minutes. During the stressing periods, the I_{DS} current was monitored continuously over the time. Transfer curves in the linear region were also recorded at the beginning of the experiment and after the two consecutive BS tests. All the data in Fig. 6 indicate that the heterojunctions are characterized by a minor sensitivity to the BS. This behavior can be observed very clearly in the $I_{DS}(t)$ curves (Fig. 6a and b), where the overall I_{DS} decay factors are considerably reduced for the heterojunction transistors. As far as the first stressing period is concerned, for example, Fig. 6a shows a 30% decrease of the initial current for two different single-layer devices, while this attenuation factor is more than halved for the heterojunction ones. Coherently, in Fig. 6c, we can notice that, for a C8-DNTT/PDIF-CN₂ transistor, the transfer-curves recorded after the BS periods exhibit a much minor V_{th} shift (about 2.5 V) if compared with the C8-DNTT single-layer counterpart where V_{th} becomes more negative by about 7 V. In very good agreement with the literature, the BS effect for the investigated devices impacts mainly on V_{th} values while producing negligible modifications of the charge carrier mobility (*i.e.* the I_{DS} slope in the transfer curves).

Discussion and conclusions

In summary, while analyzing the evaporation of C8-DNTT films on hydrophobic HMDS-treated SiO₂ surfaces, we found that the related growth mode shows a distinctive behavior as a function of the substrate temperature (T_{sub}) held during deposition. We observed the presence of characteristic morphological features (*i.e.* protruding lamellae with a height larger than 100 nm) on the C8-DNTT film surfaces, in which the density can be significantly reduced at increasing T_{sub} but at the expense of the major occurrence of macroscopic structural fractures. A value of $T_{sub} = 85$ °C was found to offer a good trade-off between these two morphological regimes and related top-contact C8-DNTT single-layer devices show a p-type response with hole mobility values larger than $2.5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. However, charge trapping mechanisms seem to have a non-negligible impact on the electrical behavior of these devices providing residual hysteresis effects, mobility dependence on the lateral electric field between the drain and the source, largely negative V_{th} threshold voltages and a certain sensitivity on the bias stress phenomenon.

By exploiting the T_{sub} range between 80 and 90 °C to improve the flatness of C8-DNTT film surfaces, p/n heterojunction field-effect devices were fabricated by sequentially depositing PDIF-CN₂



layers on C8-DNTT bottom films. These double-layer transistors display a strongly unbalanced ambipolar behavior, with the hole-transporting channel exhibiting much better performances than the electron-transporting counterpart (*i.e.* μ_h is larger than μ_e by about three orders of magnitude).

In comparison with the C8-DNTT single-layer devices, the hole accumulation response of the C8-DNTT/PDIF-CN₂ heterojunction transistors was characterized by slightly reduced mobility values but, at the same time, exhibits a beneficial shift of the threshold voltages toward less negative values and a significantly attenuated dependence on the bias stress effect. As reported in the literature for other organic systems, in general, these findings can be ascribed to the formation of an interfacial charge accumulation layer between the p and n regions, which provides an enhancement of the density of charge carriers.^{35–38}

However, in order to gain more insights into the diverse features here described, we analyzed more carefully the evolution of the main electrical parameters evaluated for C8-DNTT/PDIF-CN₂ devices with different PDIF-CN₂ overlayer thicknesses (δ_F).

For this purpose, in Fig. 7a (top panel), we reported the contact resistance (R_C) values estimated by the transmission line method (see Fig. S8, ESI†) for a set of C8-DNTT/PDIF-CN₂ heterojunction transistors. The experimental data achieved for $\delta_F = 0$ nm refer to C8-DNTT single-layer devices for which R_C values were found to be typically ranging between 1 and 1.5 k Ω cm. When δ_F was increased until 10 nm, the R_C values were not significantly enhanced, while, conversely, a more pronounced increase was observed for $\delta_F = 20$ nm and, still with more evidence, $\delta_F = 30$ nm. In the other two panels of Fig. 7a, the hole mobility (μ_h) and threshold voltage (V_{th}) values, extracted in the

linear regime for C8-DNTT/PDIF-CN₂ transistors with a channel length L of 200 μ m, were also plotted *versus* δ_F . Our attention was focused on the electrical parameters extracted in the linear region since they could be potentially more affected by the R_C effect. From Fig. 7a, we can clearly infer that, while μ_h and V_{th} values in the C8-DNTT/PDIF-CN₂ devices remain almost constant when δ_F ranges between 3 and 20 nm, more significant variations can be observed only for $\delta_F = 30$ nm. Given these findings, we conclude that the contact resistance (R_C) effect impacts on the μ_h reduction only for the largest PDIF-CN₂ thickness while it cannot be considered as the ruling factor when δ_F is lower.

In the small δ_F range, the μ_h decrease should rather be associated with the complex interplay between the charge accumulation regions at SiO₂/C8-DNTT and C8-DNTT/PDIF-CN₂ interfaces. Similar to what discussed in ref. 36, the interaction between the gate electric field and the charge-transfer organic–organic interface, with this latter acting basically as the dopant element, makes the conduction channel more spatially distributed in the bulk of the C8-DNTT film. Hence, this phenomenon should explain the μ_h decrease since, in comparison with single-layer field-effect devices, the overall charge transport in the investigated heterojunctions tends to involve also the topmost molecular layers of the C8-DNTT films being characterized by more structural defects because of the characteristic 2D + 3D growth mode (Fig. 7b) and, consequently, by an increased occurrence of charge scattering phenomena.

The energy level landscape at both C8-DNTT/SiO₂ and C8-DNTT/PDIF-CN₂ interfaces is summarized in Fig. 7c showing how the related frontier molecular levels are bent upwards and

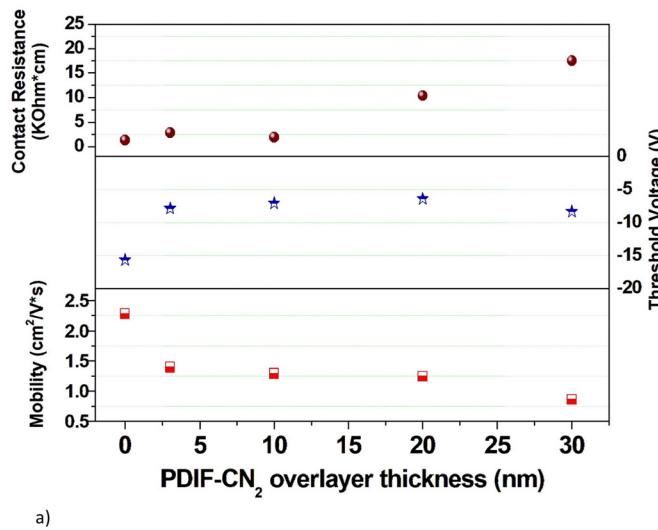
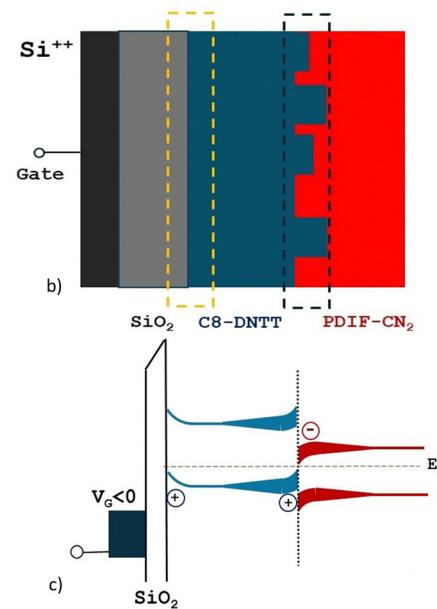


Fig. 7 (a) Contact resistance values, estimated for C8-DNTT/PDIF-CN₂ heterojunctions with different thicknesses of the PDIF-CN₂ overlayer, are compared with the hole mobility and threshold voltage values evaluated in the linear regime for corresponding heterojunction devices with a channel length L of 200 μ m. Here, the devices with $\delta_F = 0$ nm represent C8-DNTT single-layer transistors. (b) A sketch of the two main charge-transporting interfaces achieved in the investigated C8-DNTT/PDIF-CN₂ structures. (c) Related energy level scenarios.



downwards, respectively, in correspondence with the enhanced density of holes and electrons. It is also significant to outline that the charge-transfer phenomenon taking place in the C8-DNTT/PDIF-CN₂ region is favoured by the band broadening of the energy levels induced by the enhanced morphological disorder. In this way, the tail states of the HOMO (C8-DNTT) and LUMO (PDIF-CN₂) levels experience a major degree of overlapping, supporting the ion-pair formation between the molecular compounds.³⁹ Overall, the increased morphological disorder at the C8-DNTT/PDIF-CN₂ interface seems to favour the generation of charge carriers but, contemporarily, reduces the mobility performances referred to both holes and electrons.

In phenomenological terms, the μ_h reduction here observed for small δ_F resembles that reported in the literature for organic transistors where active channels of evaporated small molecules are covered by insulating polymer layers (*i.e.* top-gate configuration). In several of these devices, indeed, the field-effect mobility was found to be depressed when the accumulated charges are confined in the proximity of the more disordered semiconductor/dielectric interfacial region.⁴⁰ This scenario does not contradict even the different sensitivities here observed on the bias stress (BS) which, in the analyzed devices, can be more associated with the detrimental action of residual water-related traps present at the SiO₂ interface. In this way, BS impacts more consistently on the operation for the C8-DNTT single-layer transistors than that for the C8-DNTT/PDIF-CN₂ ones. The same behaviour was actually encountered in some of the aforementioned top-gate OFETs which, despite the effective mobility reduction induced by the confinement of the charge carriers in the more disordered interfacial region, display a significant resilience to the BS effects owing to the use of highly hydrophobic insulating fluorinated polymers (*i.e.* CYTOPTM).⁴⁰

To obtain additional evidence about the formation of the charge-transfer interface between C8-DNTT and PDIF-CN₂, we considered alternative and simpler structures featuring C8-DNTT single layers and C8-DNTT/PDIF-CN₂ heterojunctions deposited at $T_{\text{sub}} = 85$ °C on glass substrates and completed with top-contact gold electrodes. As shown in Fig. S9 (ESI[†]), while the current flowing across the C8-DNTT channel ($L = 200$ μm) is extremely low and does not exceed 1 nA even under the application of 30 V, the presence of a 10 nm thick PDIF-CN₂ top layer enhances the overall current by about three orders of magnitude and reduces considerably the hysteresis effects. By linearly fitting the IV curves of the C8-DNTT/PDIF-CN₂ channels in the low range (up to 10 V) of the applied voltage and assuming the current distributed in the 10 nm thick interfacial region of the double-layered channel, we estimated values of the effective conductivity (σ_{eff}) slightly larger than 2×10^{-2} S cm⁻¹.

Finally, we explored the response of heterojunction transistors achieved by inverting the deposition sequence of PDIF-CN₂ and C8-DNTT films. In this case, 15 nm thick layers of PDIF-CN₂ were deposited on HMDSSiO₂ with a T_{sub} of 100 °C, followed by the growth (at $T_{\text{sub}} = 85$ °C) of 10 nm thick upper layers of C8-DNTT. This evaporation sequence is fully compatible with the optimized deposition conditions of both

molecular compounds. However, even in this case, the devices display an unbalanced ambipolar response with a hole mobility (μ_h) smaller than 10^{-3} cm² V⁻¹ s⁻¹ while, for electrons, the mobility (μ_e) is about 0.2 cm² V⁻¹ s⁻¹. In this regard, Fig. S10 (ESI[†]) reports a comparison between the transfer-curves in the electron-accumulation region and related μ_e and V_{th} values achieved for PDIF-CN₂ single-layer and PDIF-CN₂/C8-DNTT heterojunction transistors. With reference to single-layer devices, the heterojunction ones exhibit again the presence of a large off current (for very negative V_{GS} values), while the threshold voltages are here shifted towards more negative values as a further confirmation of the presence of a charge-transfer region at the organic/organic interface.

In conclusion, the weak ambipolar behavior of the C8-DNTT/PDIF-CN₂ system suggests that the presence of the long side chains in both compounds, with a dominant Stranski-Krastanov (2D + 3D) growth mode, hampers the possibility of preserving the expected quality of the charge transport response in the upper layer. In this sense, the combination of organic systems featuring more diverse growth modes and, in particular, a prevalent 3D growth mode for the upper layer as we recently observed for picene films grown on PDIF-CN₂,⁴¹ appears as a more favorable strategy to obtain balanced ambipolar field-effect devices by decoupling the structural interactions of the two organic channels.^{42,43} On the other hand, strong mutual dependence of the molecular growth modes, as observed here for C8-DNTT and PDIF-CN₂, can favor the occurrence of interfacial electronic interactions and the possibility to obtain charge accumulation effects which increase the overall carrier density of the system.

Conflicts of interest

The authors declare no conflicts of interest.

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