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Cobalt-doped zinc oxide based memristors with nociceptor characteristics for bio-inspired technology†

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Neuromorphic computing is a new field of information technology, which is inspired by the biomimetic properties of the memristor as an electronic synapse and neuron. If there are electronic receptors that can transmit exterior impulses to the internal nervous system, then the use of memristors can be expanded to artificial nerves. In this study, a layer type memristor is used to build an artificial nociceptor in a very feasible and straightforward manner. An artificial nociceptor is demonstrated here through the fabrication and characterization of a cobalt-doped zinc oxide (CZO)/Au based memristor. In order to increase threshold switching performance, the surface effects of the CZO layer are eliminated by adding cobalt cobalt-doped zinc oxide (CZO) layer between the P⁺⁺-Si and Au electrodes. Allodynia, hyperalgesia, threshold, and relaxation are the four distinct nociceptive behaviours that the device displays based on the strength, rate of relapse, and duration of the external stimuli. The electrons that are trapped in or released from the CZO layer's traps are responsible for these nociceptive behaviours. A multipurpose nociceptor performance is produced by this type of CZO-based device, which is crucial for artificial intelligence system applications such as neural integrated devices with nanometer-sized characteristics.

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1 Introduction

The discovery and creation of new types of memory devices have encouraged numerous research activities in recent years since traditional memory cannot keep up with the demands of rising data capacity and processing speed.^{1,2} To the best of our knowledge, resistive random-access memory (RRAM) is a promising memory device that has enjoyed attention as a potential contender for next-generation devices,³ This is because of its many good qualities, such as its small switching voltage,⁴ low power consumption, quick switching speed,⁵ basic

design structure, substantial potential for use in synthetic synapses, analog circuits, and high storage density,⁶ *etc.* Recently, the memristor devices also referred to as the fourth essential circuit element, was created by utilizing the resistive switching (RS) effect.² Future memory devices can use the RS property of memristors,⁷ for logic,⁸ brain-inspired computing systems,⁹ and sensor applications.¹⁰ Numerous materials, including complex oxides,^{11,12} organics,¹³ and binary metal oxides¹⁴ *etc.* have shown the RS characteristics. Among various material systems, RRAM devices having insulating layers of metal oxides such as HfO₂, AlO₃,¹⁵ ZnO,¹⁶ TiO₂,¹⁷ and Fe₂O₃¹⁸ have been widely investigated because of their greater RS performances and compatibility with complementary metal-oxide semiconductor (CMOS) processing. The capacity of memristors to store and recall information through resistance modulation allows them to replicate the adaptive nature of synapses. This feature is pivotal for creating artificial neural networks that can exhibit behavior similar to the brain's synaptic connections.⁹ Memory, nonlinearity, passivity, a two-terminal construction, and scalability are just a few of the distinctive qualities that the memristor or memristive device possesses.¹⁹ The goal of neuromorphic computing and their details are mention in ESI.†

Due to the existence of oxygen vacancies, Zinc Oxide (ZnO) exhibits n-type semiconducting activity and is a versatile

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semiconducting material.²⁰ Excellent RS capabilities have been demonstrated with ZnO in recent years²¹ Furthermore, ZnO-based devices are used to exhibit flexible,²² transparent,²³ and soft²⁴ features. Additionally, ZnO-based devices are frequently utilized to replicate the functions of actual synapses and neurons. ZnO is a type of metal oxide material, which demonstrates exceptional qualities among other metal oxide materials, including a broad direct band gap, low synthetic temperature, low cost, and customizable doping. Its physical properties may be changed by doping various elements into it. Defects' impact on the RS effect has been well studied, and it is widely acknowledged that they play a substantial role. A substantial body of research suggests that metal oxide thin films generally exhibit the defects-induced RS effect.²⁵ Therefore, the impact of flaws must be taken into account when producing RS memory devices. One frequently used approach for the deposition of oxide films is sputtering. Additionally, it was possible to modify the temperature and reactive gas flow ratio during the film deposition stage in order to adjust the properties of ZnO thin films.^{26,27} To adjust the defect concentration in ZnO films, however, the elements doping approach is more successful than varying the sputtering parameters and deposition temperature.²⁸ According to Xu *et al.*²⁹ more V_O in Co-doped ZnO increases strength and decreases set voltage, suggesting that co-doping can improve RS behavior. Similar findings by Younis *et al.*³⁰ showed that Ti doping can improve RS performance, showing that the HRS/LRS ratio of Ti-doped ZnO was superior to that of undoped ZnO. With the addition of a Cr dopant, Xu *et al.*³¹ similarly showed an upgrade in the on/off ratio and steady RS behavior in ZnO. Transition metals have been added to ZnO, in particular, as a technique for creating multifunctional semiconductor materials suited for next-generation memory systems.³² Based on electrochemical properties, the electrode categories of inert, oxidizable, and active metals are frequently grouped with a switching material. An inert electrode (IE) like Pt, Au, or Ru may function as a cathode and produce a high interface barrier, producing RS characteristics.³³ The increased ON/OFF ratio was attributable to the inert electrodes' high work function.³⁴ Even though the electrode has a large work function, its strong electrochemical behaviour and inert behaviour result in an efficient redox process. Regrettably, there are limits to how much oxygen can be maintained in an inert electrode.³⁵

In the current study, we developed a P^{++} -Si/CZO/Au (CZO-Co-doped ZnO) memristive device to demonstrate the nociceptive feature, which is basically distinct from threshold switching, relaxation time, allodynia, and hyperalgesia. Here, asymmetrical capacitive coupled threshold switching behavior is achieved *via* I - V sweeping at a steady sweep rate on P^{++} -Si & Au electrodes. The main reason of Au as a top electrode, Au has excellent electrical conductivity, which ensures that electrical impulses are transmitted efficiently within the memristor-based artificial nociceptor. This conductivity is critical for the device's correct operation, letting it to reliably respond to external stimuli and display the required nociceptive behaviors. In addition, Au is well-known for its chemical stability and biocompatibility, both of which are important factors in neural-

inspired computing applications. These qualities reduce the danger of deterioration or unfavorable responses, which might jeopardize the artificial nociceptor's function or safety when interacting with biological systems. Furthermore, Au compatibility with common fabrication techniques improves device manufacture and integration into neuromorphic computing systems, adding to its viability and ease of deployment. A switch from low-bias Schottky emission to high-bias trap-aided quantum tunnelling (TAT) governs the switching behavior. The identical experimental procedures utilized to create the P^{++} -Si/CZO/Au memristor device were also employed to create a P^{++} -Si/ZnO/Au device for comparison. In contrast to ZnO-based memristors, the CZO-based memristor displays threshold switching and resistance switching features in response to electrical stimulation. The experimental results show that the dynamic charge trapping and detrapping of charge carriers, as well as the low activation energies are related to the imperfections and oxygen vacancies on the CZO layer, which results in volatile threshold switching behavior. Additionally, P^{++} -Si/CZO/Au threshold switching promotes nociceptor behavior such as relaxation, allodynia, hyperalgesia, and threshold dynamics. Finally, a thorough investigation of this CZO memristor's charge transport mechanism was conducted at various temperatures. This offers an alluring opportunity for CZO memristors to serve as trustworthy basic elements for the creation of a synthetic sensory alarm system, a crucial step toward the development of novel artificial nociceptor devices for artificial intelligence systems like humanoid robots.

Fig. 1a shows a schematic diagram of the typical biological nociceptor neuron. The influence of thin film structure on resistive switching properties is a topic of significant interest in the field of memristive devices. As highlighted in ref. 36 and other works, variations in thin film morphology, grain size, and crystallinity can profoundly impact the electrical characteristics and performance of ZnO-based memristors. For instance, grain boundaries and defects within the thin film structure can serve as preferential sites for filament formation and dissolution, thus affecting switching kinetics and stability. Additionally, the film thickness can influence the diffusion kinetics of mobile ions and the formation of conductive paths, ultimately influencing the device's switching behavior. By investigating the structural properties of thin films and their correlation with resistive switching behavior, researchers can gain insights into optimizing device performance and reliability for various applications. Neurons and synapses are essential for the human brain's ability to transmit signals. The greatest sense organ in humans, the skin covers the whole body. Somato sensors, which are abundant in every area of the skin, are sensors that actively quantify the amount of external stimuli that are detected.³⁷ The input from the skin senses is a sign of health. For example, pinpricks are used to examine how the neurological system reacts and determine the extent of paralysis resulting from nerve injury. Intelligent robotics and healthcare are two fields that stand to benefit greatly from artificial skin receptors with this kind of feedback capability.³⁸ These receptors can function as the machine-machine or human-machine interface's feedback mechanism, enhance the perception of particular stimuli, or



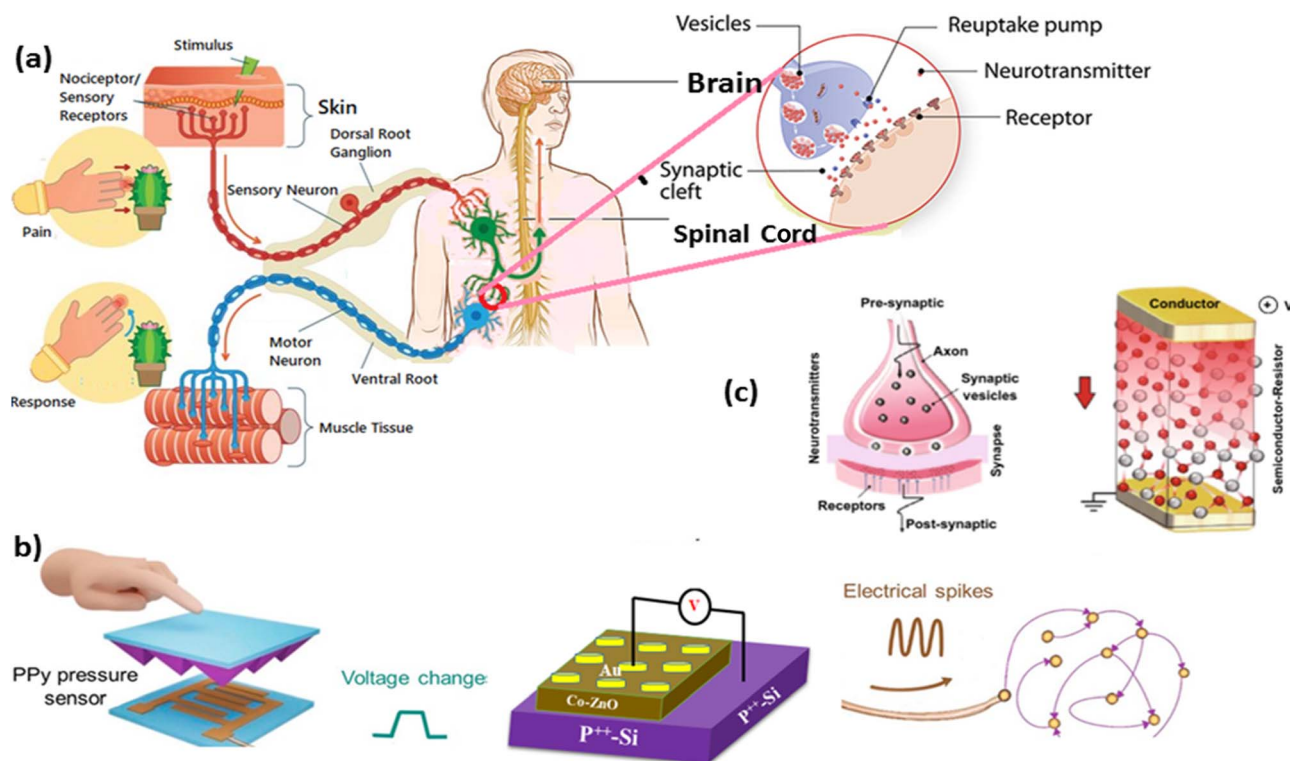


Fig. 1 (a) A distinct nerve system and a biological tactile receptor are shown schematically. Transmission of mechanical signal to the brain via synapses and neurons as a result of external applied stress. The bio-synapse is sketched in the middle (enlarged). (b) Diagrammatic representation of the suggested artificial touch perception neuron. (c) Typical schematic illustration of the biological synapse (natural) and the self-doping memristors (artificial counterpart).

replace damaged receptors. The most common and important skin receptors are the Pacinian corpuscle,³⁹ thermoreceptor,⁴⁰ and nociceptor,^{41,42} which are related to pressure, temperature, and pain, respectively. Each of these receptors has the ability to recognize stimuli, gauge their intensity, and send indications to the human brain that cause responses. The principle of operation is similar to other common senses, including taste, olfaction (smell), hearing, vision, and somatic sensation.⁴³ The dorsal root ganglion connects human somato sensors to the spinal cord, which transmits data to the brain Fig. 1a. The layers of membranes filled with fluid are called Pacinian corpuscles.⁴⁰ One obvious example of a Pacinian corpuscle is a fingerprint. A portion of the corpuscle deforms in response to local pressure applied to the body, causing a shift in chemical ions (such as potassium or sodium) and a subsequent potential of the skin-ending nerve receptor (nociceptors/sensory receptors).⁴⁰ When the receptor potential reaches the threshold, it produces an electrical impulse inside the corpuscle. The motor reaction is then triggered by this electrical impulse that passes *via* the central nervous system, through the nerve fibers depicted in Fig. 1a. The gold (Au)-coated micropyramid structure serves as the fabrication site for the piezoresistive pressure sensor, which has the ability to transform pressure sensations into long-lasting, very sensitive electrical impulses. Memristors mimic the basic synapse function, which includes spiking time dependent plasticity (STDP)-like behaviours and paired pulse facilitation/depression (PPF/PPD). Pressure sensors and memristor

synapses combined (Fig. 1b) allow external touch to activate the memristor-based synapse, which in turn allows this artificial sensory system to process and learn the history of pressure stimuli with amplitude, duration, frequency, and speed. When considering the nervous system, the biological synapse serves as the essential building block for the sensorimotor system, enabling several functions, including the neural system's ability to transmit pain signals. Similar synaptic functionalities are found in the analogous artificial nociceptive device, which is made up of an ultra-thin semiconductor film between two conductive layers Fig. 1c. When the intensity of the post-synaptic potential reaches a particular level, the post-neuron produces an action potential, concluding the process of signal transmission between two neurons.⁴⁴ It is important to note that the brain, which has between ~100 billion neurons and ~100 trillion synapses, is capable of solving a wide range of unstructured problems extremely quickly and with extremely little power consumption. This has led to the development and demonstration of artificial synapses in a variety of physical devices.

Our CZO memristor-based nociceptors' threshold voltage keeps an eye on the electric field that traps injected electrons. Assume that there is not enough electrical input bias. Since the memristor will maintain its initial HRS and no output current will be observed, the external stimulation is therefore safe. Furthermore, the threshold value of nociceptor neuron is achieved if the stimulus intensity rises. Next, the memristor turns



ON (LRS) when an output terminal current pulse is detected, suggesting that the external stimulation may be hazardous.

2 Experimental section

2.1 Film deposition

By depositing a 100 nm CZO thin film on a P^{++} -Si substrate (resistivity 0.002–0.006 cm) using ZnO and Co targets on position A and B in a magnetron RF sputtering chamber (AC source for the ZnO target and DC source for the Co target), a device with the structural formula P^{++} -Si/CZO/Au was created. At 70 W, 5 mTorr, and 50 °C, respectively, the RF power, working pressure, and substrate temperature were maintained. The CZO was made more crystalline by performing an open environment annealing process at 500 °C for 30 min. The ratio of the flow rate of Ar to O gas during the magnetron RF sputtering was 40/5. First, the Au (50 nm) electrode layer was deposited by a thermal evaporator using Au wire, then the switching layer that is CZO, was deposited on the top of P^{++} -Si but before that, the P^{++} -Si/CZO sample was covered by a metal mask with a pattern of a circular array. In order to finish off the device structure, the 50 nm Au layer was placed on top of the CZO layer. The same manufacturing procedure and environmental conditions were employed to create the P^{++} -Si/ZnO/Au device.

2.2 Device characterization

An X-ray diffractometer was used to take X-ray diffraction data. Using a scanning electron microscope (SEM, SM-7001F, JEOL), images of the P^{++} -Si/CZO/Au (bottom) memristors in cross-

section and from above were captured. The oxygen chemical states of CZO were assessed by X-ray photoelectron spectroscopy (XPS) (Thermo-VG Scientific ESCA Lab) 250. A source meter system (Keithley 2612B) was used to obtain all of the room-temperature and high-temperature I - V characteristics, reading voltages of 3, 8, 10, and 15 V under dark and light circumstances, respectively. An aluminum hot plate with changeable temperature was employed for temperature-dependent I - V characterization. The ambient conditions were used for all of the measurements.

3 Results and discussion

3.1 Structure of the diffusive memristor

A diagrammatic representation of the proposed memristor is shown in Fig. 2a. It has a CZO layer packed in between a top electrode of Au and a bottom electrode of P^{++} -Si. The experimental part contains more detailed information on the design and characterization of devices. The crystallinity of the CZO film was examined using X-ray diffraction (XRD), and the spectrum is displayed in Fig. 2b. According to data card JCPDS card no. 89–0510 and the literature, the diffracted peaks of CZO at 31.95°, 34.64°, 36.50°, and 56.72° reflect the (100), (002), (101), and (110) planes of the wurtzite-structure.^{45,46}

To confirm the device assembly and the constituent's makeup of the deposited films, we used SEM and TEM mapping to investigate the dimensional analysis of the P^{++} -Si/CZO/Au device. The thickness of the Au TE is roughly 50 nm, and a 100 nm CZO film is well formed on a substrate, according to

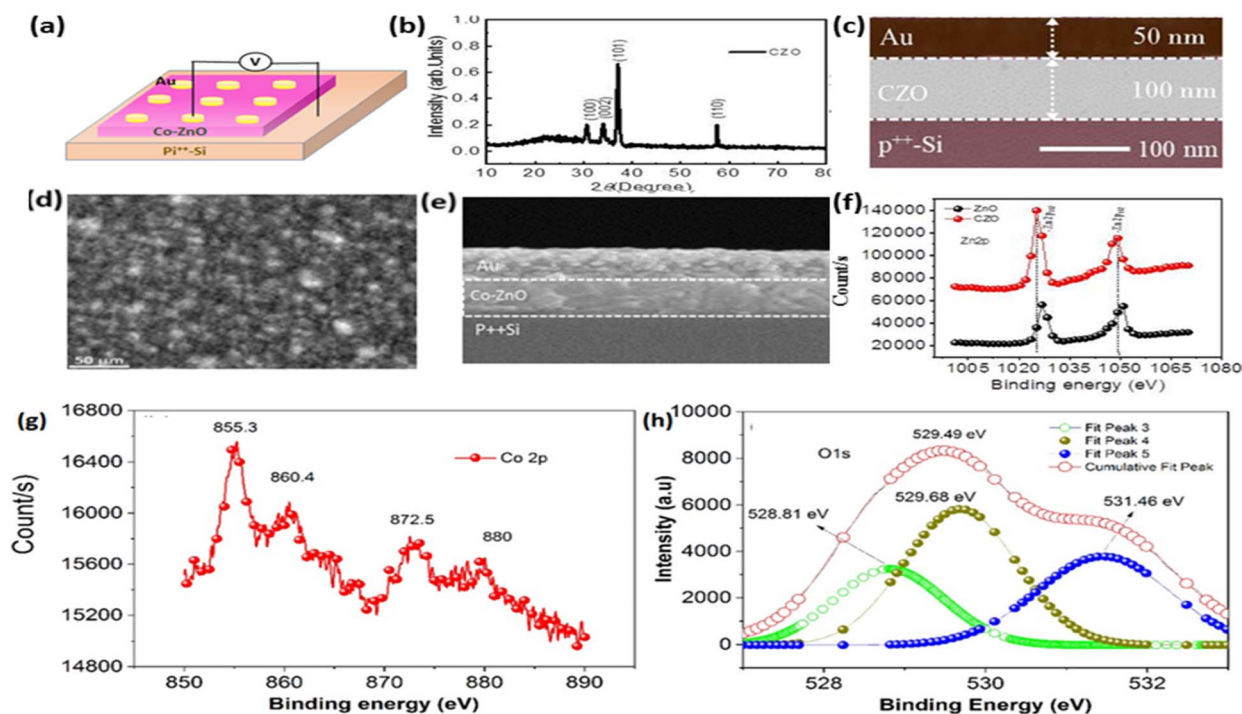
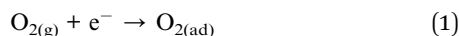


Fig. 2 Schematic diagram and characterization of the structural response: (a) schematic of the three-dimensional structure, (b) X-ray diffraction pattern, (c) TEM cross-section picture, (d) surface electron microscopy picture, (e) cross-sectional SEM view, (f) 2p XPS spectrum of CZO and un-doped ZnO devices. (g) XPS spectrum of Co 2p, (h) XPS spectrum of O 1s.



the 100 nm scale bar in the SEM and TEM images in Fig. 2c–e. From XPS survey spectra the only identified constituents are, Zn, O, and Co, indicating the purity of the CZO layer deposited by RF sputtering. Fig. 2f shows Zn 2p_{3/2} and Zn 2p_{1/2} of the ZnO thin film located at 1026.76 and 1049.58 eV, which indicates that Zn is in the divalent oxidation state.⁴⁷ Furthermore, take note that the Zn 2p_{3/2} and Zn 2p_{1/2} peak positions were found to differ by 23.2 eV, which closely matches the ZnO characteristic value.⁴⁸ As seen in Fig. 2f, the inclusion of Co²⁺ ions in the CZO film causes the Zn 2p_{3/2} and Zn 2p_{1/2} peaks of CZO to shift slightly toward lower binding energy when compared to pure ZnO. This result indicates that Co ions have replaced Zn sites in these thin films, as the majority of the Co ions are in the Co²⁺ state.⁴⁹

The O 1s spectrum is shown in Fig. 2h, with peaks at 528.81, 529.68, and 531.46 eV. The O_L ions bonding in the ZnO wurtzite structure and the O_V ions in the oxygen-deficient areas (oxygen vacancy defects), are responsible for the peaks at 528.81 eV and 529.68 eV, respectively. The procedures of ZnO growth and annealing introduce the oxygen vacancy flaws. The peak at 531.46 eV can be attributed to chemisorbed oxygen,^{50,51} i.e., oxygen molecules in the air chemisorb on the surfaces of the ZnO thin layer and grab any free electrons in the n-type ZnO conduction band before converting them to oxygen ions,⁵² as presented in the reaction (1):



Four peaks can be identified in the Co 2p spectra of CZO given in Fig. 2g at the respective energies of 855.3, 860.4, 872.5,

and 880.0 eV. The 20.4 eV energy difference between the peaks of Co 2p_{3/2} (860.4 eV) and Co 2p_{1/2} (800.0 eV) is what causes Co to be in the 2⁺ valence state.⁴² The presence of the satellite peak also shows that high-spin divalent Co²⁺ is present in the film but that no impurities were produced because the Co ion was substituted at the Zn location. The CZO sample contains more O₂ species than pure ZnO because it has a higher donor fraction. Consequently, the presence of several oxygen vacancies changed the surface of the CZO sample.

3.2 I–V characteristics and conduction mechanism study

The CZO device's current–voltage (*I–V*) curves for a deposition area of 100 nm with continuous positive and negative voltage bias are shown in Fig. 3a and b in normal and semi-logarithmic scales. To protect the devices against long-term disruption, a compliance current of 13 mA was utilized together with the bias voltage sequence of 0 V → 15 V → –15 V → 0 V (dual sweep). The absence of any forming processes in the CZO devices may have been caused by the pre-existing V_O in the thin films that had just been deposited. Fig. 3a shows a symmetrical hysteresis curve, which suggests resistive switching behavior. The device is initially in a highly resistive state with currents in the nA range when the voltage is less than 5 V. An insulating condition (HRS) transforms into a voltage-dependent low resistive state (LRS) at higher voltages, where the current reaction increases quickly as the voltage rises. The current spontaneously relax to its baseline value when the voltage is swept back from 15 V. As a result, unlike memristive devices, which frequently exhibit zero-crossing behaviour, the *I–V* sweeping of the CZO device does not reach zero. This feature sets this

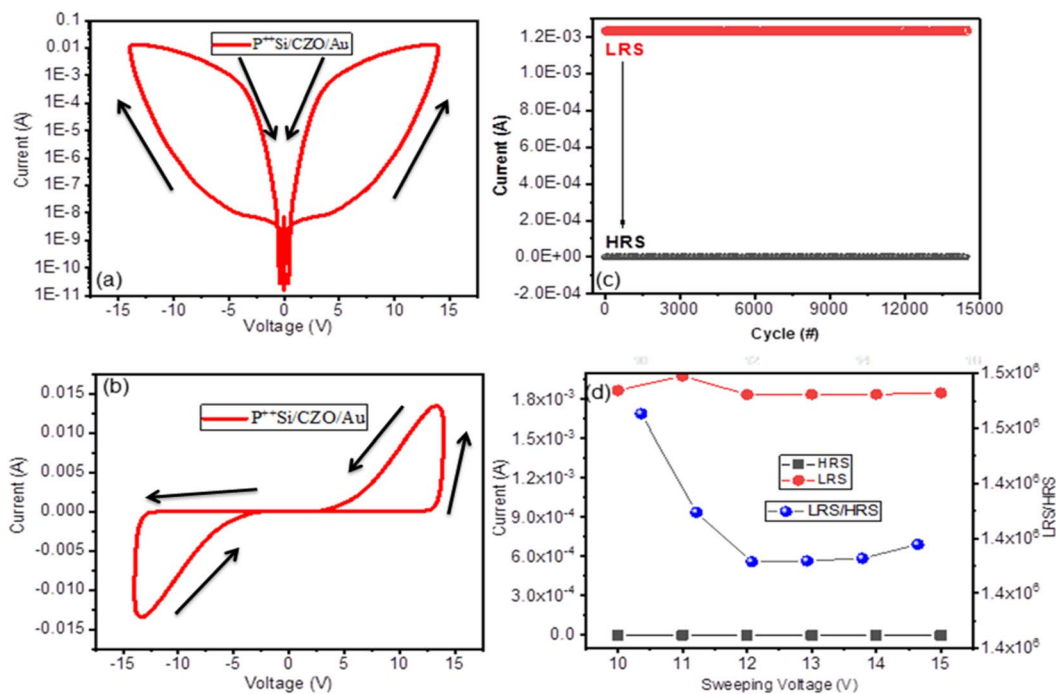


Fig. 3 The electrical characterization of fabricated memristor device (a) plotting of the sweeping performance at 15 V at a steady 0.1 V s⁻¹ sweeping rate on a linear scale (b) plotting *I–V* curves on a semi-logarithmic scale (c) performance endurance (LHS/RHS) for device at reading voltage 3 V over 15 000 sweeping cycles (d) LHS/RHS at different sweeping voltages.



threshold switch device apart from others completely. Other threshold switch devices experience a sudden transition between resistance states due to conductive path formation and rupture, which results in a memory behaviour with zero-crossing characteristics.

Additionally, for the range of reverse sweeping voltage, the devices exhibit symmetrical activity, suggesting threshold-switching action in both directions. The V_0 helped the CFs form quickly, which allowed the BRS characteristic to be seen in the CZO device even in the absence of a forming process. The CZO device has set and reset voltages of 1.22 and -1.01 V were seen in Fig. 3b, which showed our device's resistive, respectively. Hysteresis loops in the I - V curves switching characteristic. This trait differs noticeably from the non-volatile trait shown in the well-studied CMOS film sandwich between Pt-electrode devices, which exhibits threshold-switching behavior at lower sweeping bias and maintains its resistance states.^{2,53-55} The ZnO layer sandwiched between the Pt electrodes, where a bipolar nonvolatile resistive switching performance is commonly seen, is likewise different from this.⁵⁶

The results in Fig. 3a demonstrated a rectifier's normal symmetrical property, which demonstrated that the positive current branch and the negative current branch are identical. The CZO device's capacitive characteristic transforms into a capacitive-coupled memristor characteristic at higher voltages. This phenomenon has already been documented in metal-oxide-based nonvolatile memory (NVM) devices.⁵⁷ With different annealing temperatures but the same sputtering deposition parameters, an undoped ZnO-based P^{++} -Si/ZnO/Au (100 nm) device was also synthesized. The memristive behaviour of P^{++} -Si/ZnO/Au devices on a semilogarithmic scale reveals minimal variation in resistance states under voltage sweeping, with high conductivity being the main feature, as illustrated in Fig. S-2 (a-f).[†] The results of the endurance measurement are shown in Fig. 3c, At a read voltage of 3 V, the resistance in the HRS and LRS was measured. For over 15 000 repetitive cycles, the HRS and LRS can be sustained, and the RS ratio can be maintained up to 10^3 , indicating good stability and repeatability of the CZO device. As indicated in Fig. 3d, the LRS/HRS ratio depends on the sweeping voltage, this opens the door to employing the applied electric field to accurately control the resistance state. It was observed the I - V curve shows non-zero crossing. The observed non-zero crossing in the I - V curve of the Au/Co-ZnO/ P^{++} Si memristor, as highlighted in Fig. 3a and elaborated upon in the enlarged part of Fig. S3,[†] suggests a complex interplay between resistive switching mechanisms and capacitive effects.⁵⁸ This behavior indicates dynamic charge redistribution within the device under different voltage biases. The capacitive coupling between the Co-ZnO layer and the electrodes (Au bottom electrode and P^{++} Si substrate) likely contributes to the observed phenomenon, influencing charge dynamics and resistance switching characteristics. Understanding this interplay is crucial for elucidating the device's behavior and exploring its potential applications in emerging technologies.

Since conduction processes are often temperature dependent in different ways, plotting the conduction current against

temperature delivers highly important information on the structure of the conduction currents. With a 10 mA compliance current, the temperature dependency of the I - V curves in insulating and conducting states at temperatures between 298 and 368 K is shown in Fig. 4a. To study the charge transport mechanism of the CZO device, As illustrated in Fig. 4a-f, we assessed the temperature-dependent I - V sweeping performance and separated the current response into four phases in the forward sweeping range. According to Fig. 4a, the current reaction considerably upsurges as temperature rises, pointing to a temperature-dependent charge transfer process in CZO device. The essential equations defining the charge transport process at different phases divided by the threshold voltage are fitted to the current under forward bias as a result of the symmetrical response. The behavior of the current in phase-I below the threshold voltage is shown in Fig. 4b; the linear slope of $\ln(J/T^2)$ with $1/T$ denotes the emergence of a Schottky barrier. This is consistent with the theory that the Schottky interaction barrier is formed by the electrodes and CZO layer. The standardized Schottky emission model as mentioned in eqn (2) is depicted below:

$$J = \frac{4\pi qm^*(KT)^2}{h^3} \exp\left(\frac{-q\left(\phi_B - \sqrt{\frac{qE}{4\pi\epsilon}}\right)}{KT}\right) \quad (2)$$

where ' J ' is the surface current density, ' q ' the charge of electron, ' m^* ' the electron's effective mass, ' h ' the Planck constant, ' B ' the height of the potential barrier, ' T ' the absolute temperature, ' k ' the Boltzmann constant, ' ϵ_0 ' the permittivity of the dielectric material, and ' E ' the electric field. According to above equation, for different voltages and temperatures, the plot of $\ln(J/T^2)$ vs. $1/T$ will be a straight line as shown in Fig. 4b and f. This shows that during phase I, at the junction of the CZO layer and the Au electrode, an effective Schottky barrier is formed. Due to huge difference between of electron affinity of Au (5.1 eV)⁵⁹ and ZnO (4.2 eV)⁶⁰ in the CZO/Au system, a Schottky barrier exists at junction. Nonetheless, it is well known that, the formation of both ohmic and Schottky junctions of Au with n-type ZnO are dependent on ZnO crystal flaws. Because of the imperfections of the non-crystalline CZO layer, the oxygen vacancies on the surface also serve as local charge carrier centers, providing carriers with a mechanism to move from one local place to another with an external field. Applications based on 1-D semiconductor materials are where this phenomenon is frequently studied.⁶¹ The curve of $\ln(R)$ vs. $1/T$ at various voltages of the CZO device can be clarified using a Mott-variable range hopping conduction (VRH) mechanism, as given in the eqn (3) below, because phase I has a low electric field:⁵⁷

$$R = R_0 \exp\left(\frac{T_0}{T}\right)^{1/4} \quad (3)$$

where the material's constants R_0 and T_0 are the parameters. The CZO device resistance ($\ln R$), is proportional to $1/T^{1/4}$, as illustrated in Fig. 4(c). In substantially degenerate semiconductor layers with widely dispersed and random imperfections, as those found in amorphous materials, VRH conduction



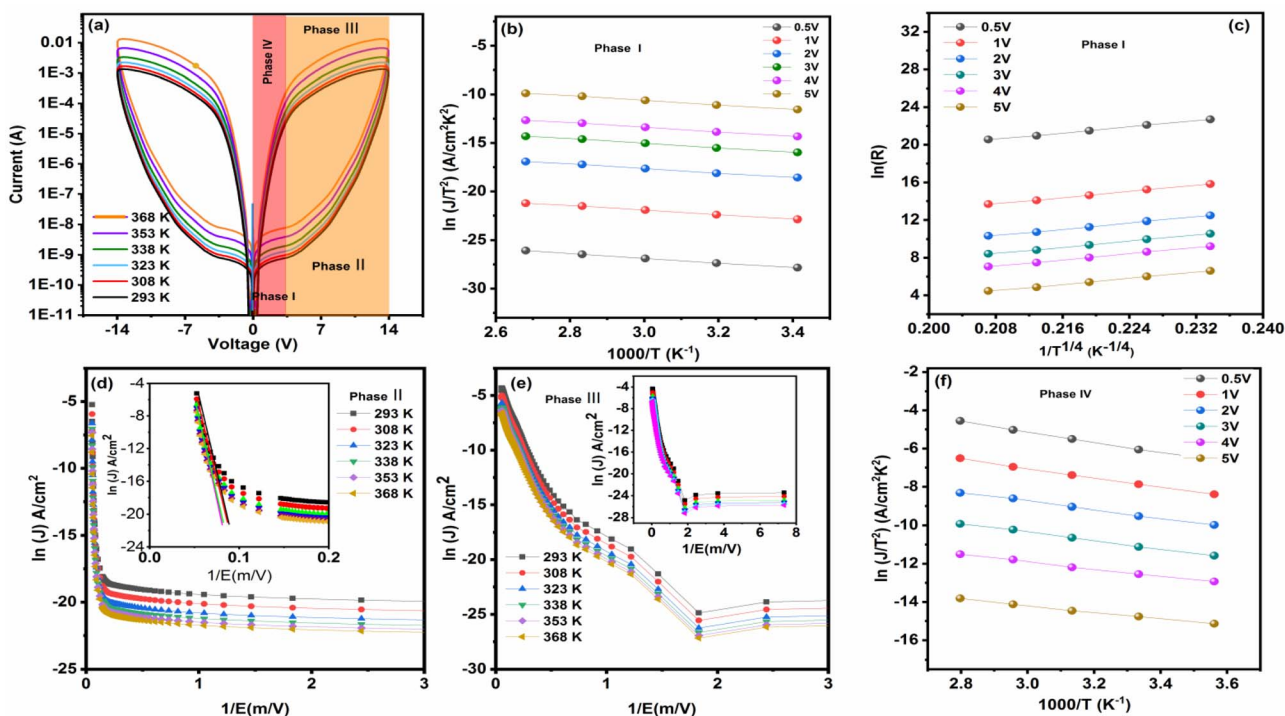


Fig. 4 A CZO device's charge transport mechanism: (a) performance of sweeping depending on temperature between 293 and 368 K. For every measurement, a consistent sweeping rate of 0.1 V s^{-1} is employed. For fitting with various equations, four steps related to applying a bias are highlighted. (b) The relationship between inverse temperature ($1/T$) and $\ln(J/T^2)$ for voltages in phase I. (c) The relationship between temperature ($T^{-1/4}$) and $\ln(R)$ at various phase I low voltages. (d) The relationship between $\ln(J)$ and inverse electric field ($1/E$) at various temperatures for voltages in phase II. (e) The relationship between $\ln(J)$ and inverse electric field ($1/E$) at various temperatures for voltages in phase III. (f) The relationship between inverse temperature ($1/T$) and $\ln(J/T^2)$ for voltages in phase IV. At each temperature, five sweeping cycles were recorded, and the corresponding average values and standard deviations were computed.

typically occurs. At the Au/CZO interface, where there is a large number of defects, the electrons injected from the electrode can jump to a specific range of distinct local states under these circumstances. Fig. 5 band diagram systematically illustrates this phenomenon. To investigate the current response further in phase-II, a curve is attained by using the trap-assisted tunneling (TAT) eqn (4)^{62,63}

$$J = A \exp\left(\frac{-8\pi\sqrt{2qm^*}\Phi_T^{3/2}}{3hE}\right) \quad (4)$$

Here 'A' is a constant, ' Φ_B ' the trapped energy level, ' m^* ' the effective mass of an electron in the dielectric material, and ' h ' is the constant. Fig. 4d illustrates the linear relationship between $\ln(J)$ and $1/E$ at various temperatures, which is dependent on the effective trap level Φ_B . This relationship is derived using the TAT equation. Usually, TAT behaviour happens when electrons tunnel through a tiny, severely faulty dielectric layer. In this device, the surface flaws at the Au-CZO interface might serve as trap cores, enabling the tunneling of injected electrons from Au-electrode to the conduction band of CZO. The graph of $\ln(J/T^2)$ against $(E)^{1/2}$ deviates from a linear fit, as shown Fig. 4d-inset, therefore the Schottky emission effect is not included in this recession. Additionally, Fowler-Nordheim tunneling may potentially contribute (see inset of Fig. 4e). Nevertheless, the TAT mechanism should be the crucial driving mechanism given that there is a higher

likelihood of electron trapping than of electron tunneling across the Au/ZnO contact. The TAT equation is also valid to the high voltage range of phase-III for the low resistance state, and its slope is substantially less, as illustrated in Fig. 4e. This suggests that phase-II and phase-III share very similar transport processes but with somewhat different levels of effective trap energy. The availability of trap sites with the electric field causes the motion of oxygen vacancies introduced by the electric field, it is evident that there are fewer traps available in phase-II than in phase-III. Furthermore, thermal activation releases these electrons into the conduction band, where they are swept to a different electrode (and easily trapped by shallow oxygen vacancies in phase-II). The negative slope observed in Fig. 4e depicting $\ln(J)$ against inverse electric field ($1/E$) at different temperatures for the Au/Co-ZnO/ P^{++} Si configuration suggests the presence of a capacitive effect resulting from the accumulation of interface charges. This phenomenon arises due to the interaction between the memristors layers and the P^{++} Si substrate. The accumulation of charges at the interfaces between the Co-ZnO layer and both the Au bottom electrode and the P^{++} Si substrate creates a capacitive behaviour, influencing the electrical characteristics of the device. The slope of the curve indicates the rate of change in current density concerning the inverse electric field, implying a dynamic response influenced by the capacitive effect. To provide a comprehensive understanding, the author should discuss the



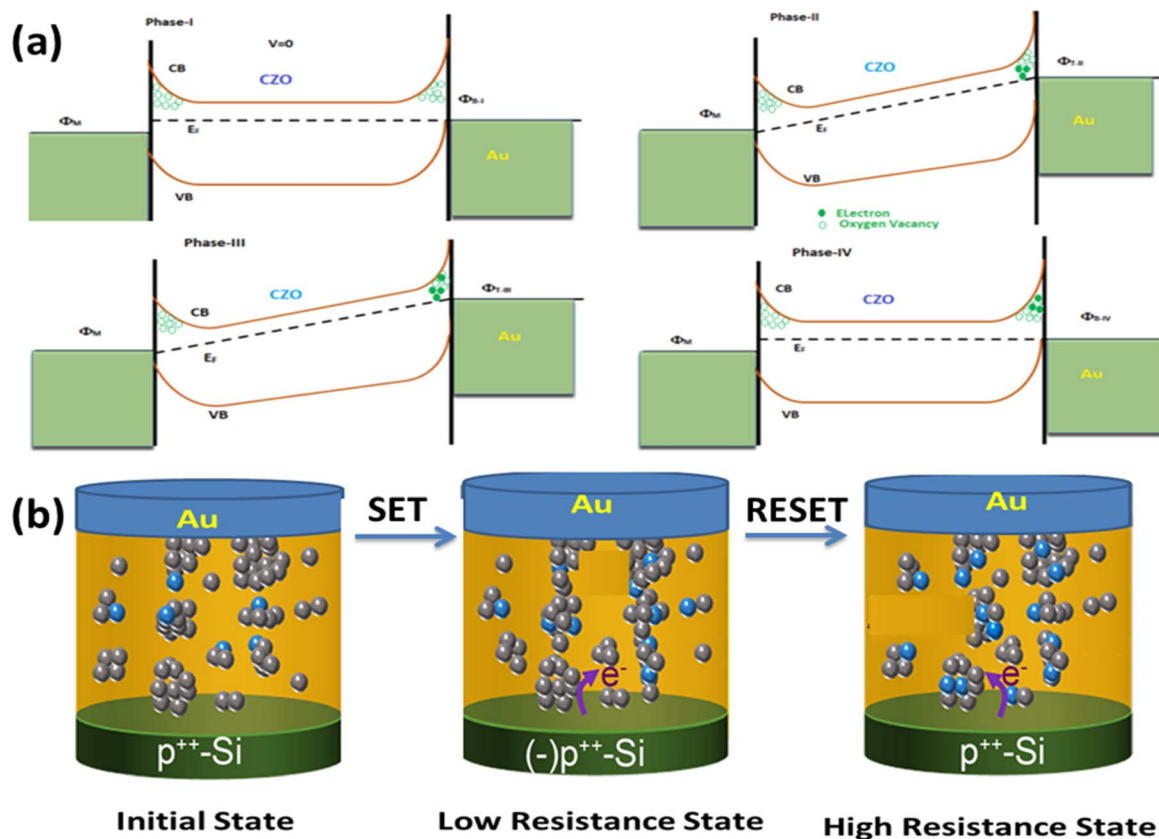


Fig. 5 (a) Representation of mechanism I in the form of band diagram and (b) the resistive switching mechanism of CZO/Au in various phases.

implications of this observed capacitive behavior in the context of memristors operation and its significance for device performance, potentially exploring its impact on switching dynamics, stability, and overall functionality. Additionally, delving into the mechanisms underlying interface charge accumulation and its relation to the memristor's operation would enhance the discussion, offering insights into the underlying physics governing the observed electrical behavior. Conversely, the accessible shallow-depth traps in phase-III are either being occupied or moving to the other end of the system due to the cumulative impact of the electrical field, as schematically seen in Fig. 5a. This may contribute to the transition from a highly resistant to conductive state, which results in the memristive behavior, or hysteresis, that is observed. This non-equilibrium dynamic trapping behavior is further supported by the device's sweeping rate I - V performance, as shown in Fig. 5a, where the short period of charge trapping results in a reduced hysteresis effect at greater sweeping rates. At different temperatures in phase IV, the linear relationship between $\ln(I/T^2)$ and $(1/T)$ may be fit using the Schottky emission equation, as shown in Fig. 4f. This shows that the Schottky barrier in phase IV is lower than it is in phase I, as depicted in phase IV of Fig. 5a, thus, it should be possible for the charges to cross the barrier and enter the ZnO conduction band. Based on the aforementioned temperature-dependent study, the charge transport mechanism for the CZO device may be characterized as TAT at a greater electric field and Schottky emission at a lower electric field, with differing effective trap energy levels

(Fig. 4). As a result, there is a threshold switching voltage whereby VRH (phase I) and TAT (phase II) phase out. Furthermore, the apparatus displays hysteresis in the change from resistive to conductive states, *i.e.* memristive behaviour, because of variations in trap energy levels. Since the zero voltage of the Schottky barrier may possibly nullify all of these potential trap sites at the Au/CZO interface, as seen in Fig. 5a phase 1, so the pristine device initially stays in the HRS.

In phase I, Schottky emission dominates charge transport due to the realization of the electrons expected at many nearby trap locations in the small forward biasing (schematic Fig. 5). When the electrode is forward biased with high external voltage, the trap locations starts to be occupied by injected electrons from the Au electrode, to lower the Fermi level of the trap level. The defect-facilitated TAT behavior in Fig. 5a phase II is influenced by the trap states that are present at the Au/CZO interface. Dynamic electron trapping causes the trap-assisted tunneling phase to modulate, which causes hysteresis in phase III (phase II of Fig. 5a). When the effective Schottky barrier in phase IV decreases as a result of trapped electron accumulation under the applied electric field, spontaneous charge detrapping takes place, restoring the device to its starting condition (Fig. 5a phase IV). Shallow flaws on the CZO layer serve as a conductive transport channel throughout the procedure. An analogous method of charge transfer is obviously anticipated on both electrodes given the device's symmetrical energy band structure, which results in the experimental symmetrical I - V sweeping behavior.^{62,64}



A similar RS mechanism is also shown in Fig. 5b to help understand the switching process in freestanding ZnO-based memristor devices. The behavior of Au in ZnO-based cells may be characterized using electrochemical processes (migration and buildup of Au ions and Au atoms) across bipolar electrodes, as has already described.⁶⁵ In contrast, CZO-based memristor devices have a smaller bandgap (~ 3.45 eV) and a greater dielectric constant (~ 8.17), resulting in a stronger electric field within the CZO layer along with more Au atoms dissolving in the switching layer.⁶⁶ ZnO limited ion mobility and redox reaction rate limit the movement and aggregation of Au atoms and Au ions across the interfacial layer.⁶⁷ These two facts may result in the creation of a nano-cone-shaped filament from TE to BE. The concentrated metallic area in the shape of efficient containment of filament development in the shape of a nano-cone from TE to BE can provide control over resistance states during cyclic operation. When the top Au electrode is sufficiently positive biased across the double layers, the electric field across the dielectric layers is enough to move the Au ions from the Au TE to p⁺⁺-Si BE through the interfacial layer, resulting in a reduction in the actual gap between electrodes (Fig. 5b). The Au-filaments do not disintegrate unless a negative voltage is provided, and they retain their initial state even when the bias voltage is withdrawn. When a negative voltage is provided, a typical RESET occurs, and Au CFs are partially desolated (generally at the thinnest point) as a result of the thermal-assisted electrochemical process.⁶⁷ The memristor device turns off and returns to HRS (final panel of Fig. 5b), before cycling reversibly between two states (Fig. 5b) as a memristor device. The left panel of Fig. 5b illustrates that the filaments formed in this study are expected to possess greater thickness compared to those depicted in the center panel of the same figure. The latter filaments exhibit resistance to dissolution and rupture unless subjected to a negative voltage. Notably, the filaments within the CZO layer maintain a notably thinner profile, resembling a nano-cone structure. Consequently, the application of a negative bias leads to rapid filament rupture, as depicted in Fig. 5b. Moreover, with further voltage increment and subsequent current rise, there is a discernible risk of negative-SET occurrence within a high bias range, attributed to the presence of residual Au atoms near the surface of the BE.

3.3 The memristive nociceptor's pulse response

By measuring the diffusive memristor's pulses, the experimental demonstration of the three key characteristics of a normal nociceptor (undamaged case), namely "threshold," "no adaptation," and "relaxation," was carried out. A natural nociceptor's triggering mechanism is strongly influenced by the strength, period, and magnitude of the motivations.^{68,69} To simulate external stimuli for our P⁺⁺-Si/CZO/Au device, we used electrical surges of varied intensities, pulse widths, and pulse counts, as illustrated in Fig. 6. A train with fixed pulse amplitudes (15 V) and various pulse widths with time intervals of 10 ms was used to research the impact of pulse width on the device's response. We found that a 20 ms pulse width

was sufficient to activate the nociceptor, and increasing the width to 100 ms resulted in a higher output current, as shown in Fig. 6a. It was discovered that the initial current jump took place after some time. This is consistent with a biological nociceptor's rise in response intensity rising with noxious stimulus intensity. Only voltage pulses of 2 V (50 ms width) for a train of electrical pulses with different amplitudes could activate the memristive nociceptor. However, as seen in Fig. 6b, the threshold value was not achieved until the pulse reached 5 V per 50 ms at a time interval of 10 ms. A memristive nociceptor's threshold for a noxious stimulus was determined by increasing the amplitude to 15 V (50 ms breadth, 10 ms time interval), which produced a bigger output current in line with the intensifying reaction. A higher voltage of 11 V is required for the device to initiate the current and the output current for device steadily increases, indicating a selective response to their triggered amplitude range Fig. 6b. This again displays how the voltage amplitude affects the device's threshold property. The fact that a 100 ms long pulse width was used during these single-pulse tests to provide the equipment enough time to return to its resting state between measurements is crucial. The number of electrical pulses the memristive nociceptor has already received determines how it will react to a series of pulses. Schematics Fig. 6c–e depict a train of 8, 10 and 15 V pulses of 50 ms pulse width and a series of pulses with 10 V amplitude and of varying widths (25 and 100 ms), together with the corresponding output current pulses (in the same graph). We found that the device could only be turned on with a sufficiently long pulse width (1 ms in this case) and a fixed pulse amplitude (15 V). A longer pulse also increased the output current. It was discovered that the initial current jump took place after some time Fig. 6a's inset. The response of the device to an electrical pulse train depends significantly on how many pulses it receives. Additionally, it suggests that more pulses are needed to turn on the device if the amplitude is lower. The creation of conducting routes within the switching layer is responsible for the "threshold" quality shown with the memristive nociceptor, which is in line with what has been demonstrated in its anatomical equivalent. These top-to-bottom electrode-bridging pathways need to be generated with enough amplitude and duration.

Following each applied pulse, the current generation progressively rises, indicating that the electrons inserted by the pulsations are progressively occupying the empty trap levels and allowing more steady conduction for succeeding pulses.

Moreover, it was noted that the current increased up to the saturated point, suggesting that the current devices could be used to mimic the "no adaptation" characteristic of nociceptors, in which a neuron does not adjust to more repeated unpleasant biasing because the sensitivity of the nociceptor slowly decreases with prolonged contact to outside stimuli.⁶⁸ The human body must experience these phenomena, which is analogous to the nociceptor's "no adaptation" characteristic, in order to defend itself from repeated damaging shocks.



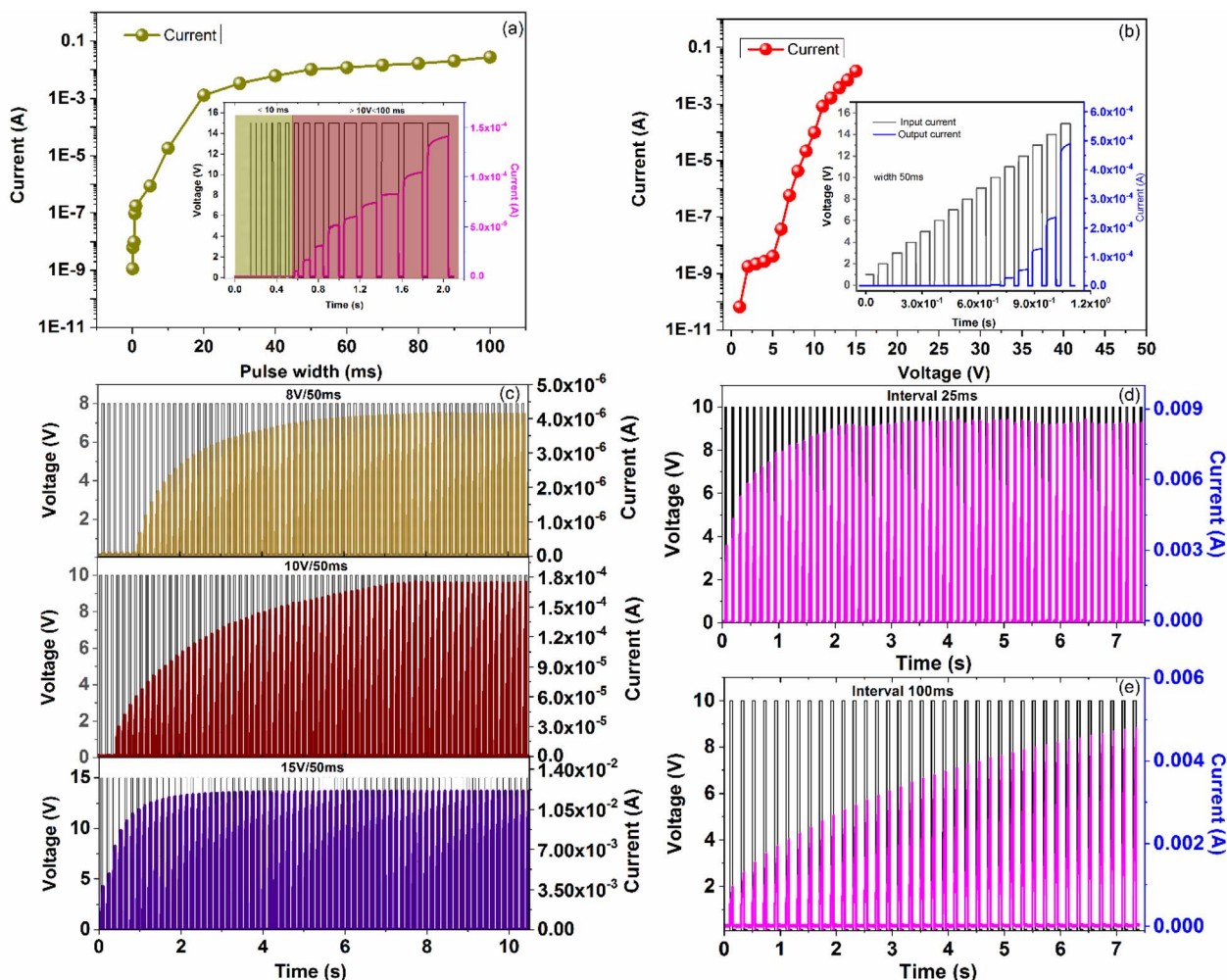


Fig. 6 Nociceptive behavior. (a) Corresponding output current at different pulse width. A succession of voltage pulses with amplitude 15 V (dark yellow curve) of different pulse lengths (1 ms to 100 ms) with interval 10 ms and the corresponding output currents (pink curve) [inset]. (b) Corresponding output current at different applied voltages. The output currents that correspond to a sequence of various voltage with a pulse width of 50 ms (black curve; 1.0 to 15 V, time interval 10 ms) are shown in the blue curve. An increased output current is caused by a greater input voltage [inset]. (c) The devices achieve the saturation state after a series of 50 ms, 8, 10, and 15 V pulses are delivered; there is no adaptation response. (d and e) Pulse response of memristors with intervals of 25 ms and 100 ms with amplitude of 10 V. The adaptation rate of rapidly and slowly respectively.

3.4 Sensitization function in a memristive nociceptor

A nociceptor is said to be injured when it is subjected to strong enough stimuli to cause damage. Following injury, a nociceptor will exhibit an enhanced response at a lower threshold, which is referred to as “hyperalgesia” and “allodynia”.^{68–70} This is illustrated schematically in Fig. 7a. We initially applied the high value pulses of 0 V, 5 V, 10 V, and 15 V with a pulse width of 20 ms, creating a modification that imitates the injury or damage to the nociceptor device in order to illustrate the “sensitization” property of a P⁺⁺-Si/CZO/Au memristive nociceptor. We evaluated our device’s current response to different input stimuli, as shown in Fig. 7b, to see if it had sustained different levels of “damage.” It is evident from Fig. 7c and d that the “injured” nociceptors have a larger output current and that the input voltage affects their maximum output current. Interestingly, when the output

current rose, the threshold voltage moved towards the lower value. This shows that a lower threshold voltage is desired to activate more gravely damaged devices in order to recreate allodynia and hyperalgesia in the nociceptor. The behaviour of the threshold shift has been validated by many series of pulse tests, as Fig. 7d illustrates. Further evidence of how the device’s threshold alters with the high voltage pulse, comes from the fact that the figure of pulses necessary to switch on the device is significantly lower and the generation current is significantly greater in an injured situation (after five 10 V pulses). This phenomenon is linked to the nociceptors’ apparent key characteristic—their great sensitivity during the relaxation period. Nociceptor protection is the process by which nociceptors react to pre-injury stimuli and then more rapidly and accurately identify stimuli that may be harmful.



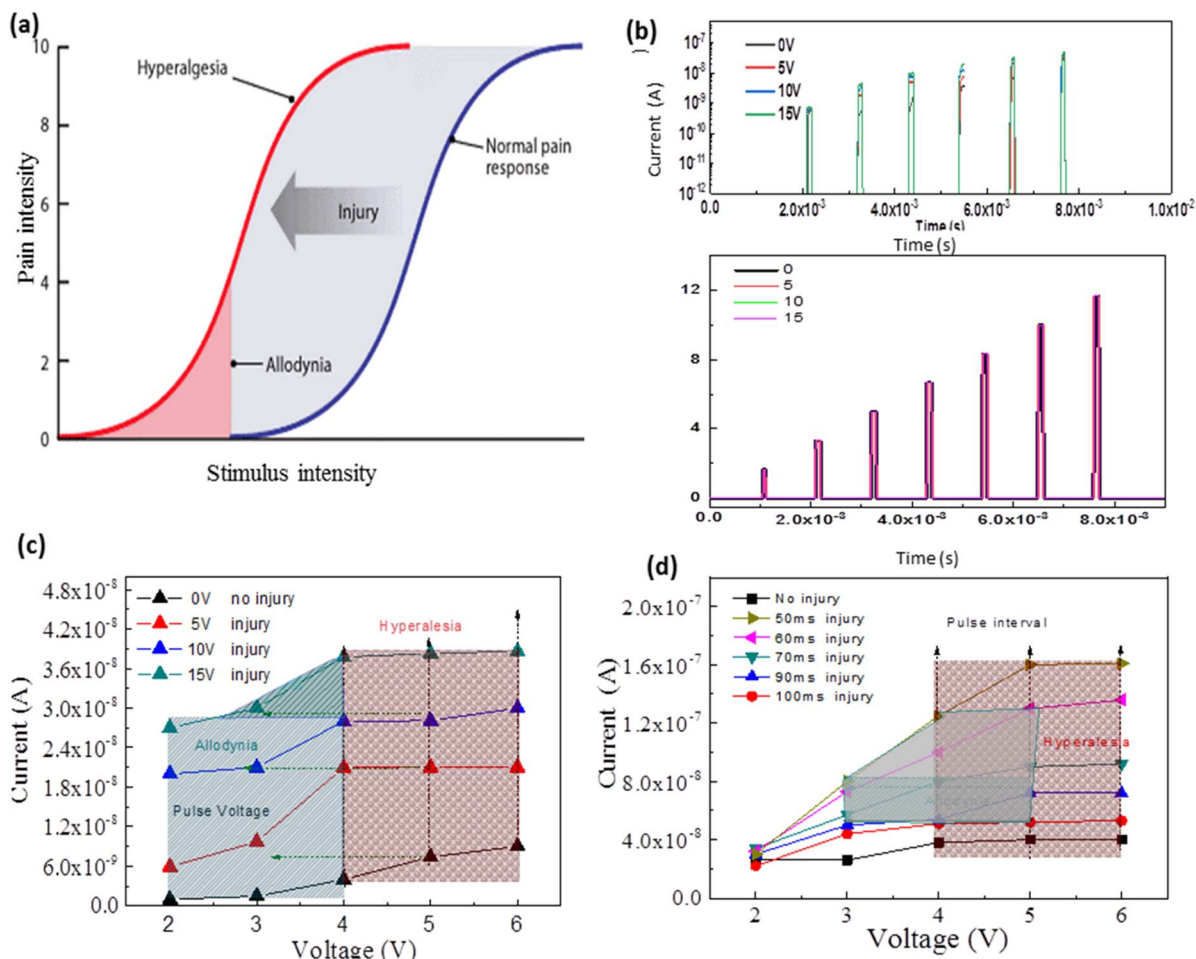


Fig. 7 A memristive's nociceptive behaviour (a) schematic illustration showing the characteristics of hyperalgesia and allodynia in response to increased stimuli intensity in both damaged (injury) and normal pain circumstances. (b) A series of pulses with an amplitude ranging from 2 to 7 V in the input voltage. The nociceptor was exposed to the following pulses, each with a 2 ms pulse width: 0 V, 5 V, 10 V, and 15 V (2 ms) (such as in an injury occurrence). The output current that matches. (c) The current response showed signs of both allodynia and hyperalgesia for a series of pulses from 2 to 6 V (20 ms) after high-amplitude pulses (10 and 15 V pulse, 20 ms) were stimulated. (d) The reaction of the current to a series of pulses from 2 to 6 V (20 ms) showed both hyperalgesia and allodynia features when different pulses (0 V, 5 V, 10 V, and 15 V 20 ms pulse) with variable time intervals were stimulated.

4 Conclusions

We have demonstrated improved memristive behavior CZO system, it is also used in neuromorphic computing to emulate synaptic response. In response to a quantum tunnelling with defect assistance process at high voltage and a Schottky partial process at low voltage, current flows in the CZO device. At different stages, there is also active charge trapping and detrapping with surface trap energy levels. Therefore, surface imperfections that contribute to the CZO device's high conductivity can be reduced. Additionally, at low electric fields, the imperfections in the layer between surfaces offer localized trap locations for electron skipping, and at high electric fields, they facilitate the tunnelling of electrons through the barrier and into the ZnO nanowire's conduction band. Based on analogue and digital switching characteristics, the relative resistive switching behavior of the devices was examined by evaluating the I - V characteristics and installing conduction models. Future useful

memory device applications can be expanded thanks to this device study. The result is a voltage-dependent threshold switching performance that can be used to simulate every important characteristic of a nociceptor, such as "hyperalgesia," "relaxation," "no adaptation," and "threshold."

Author contributions

Naveed Ur Rahman, Aziz Ullah, Muhammad Adil Mahmood, Nasir Rahman, Rajwali Khan: performed the experiment and wrote the original paper. Mohammad Sohail, Shahid Iqbal, Nizomiddin Juraev, Khaled Althubeiti, Sattam Al Otaibi, Rajwali Khan: discussed the results and implications. Naveed Ur Rahman and Rajwali Khan performed and analyzed the XRD spectrum. All authors commented on and approved the final version of the manuscript. Finally, Sattam Al Otaibi has successfully helped and guides us in comments addressing and removing grammatical errors.



Conflicts of interest

The authors declare no interest conflict. They have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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