

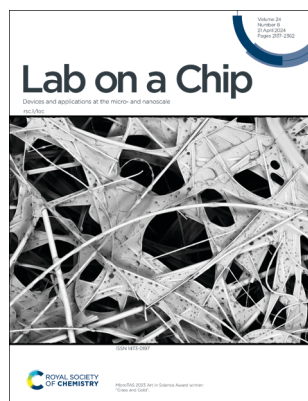
Lab on a Chip

Devices and applications at the micro- and nanoscale
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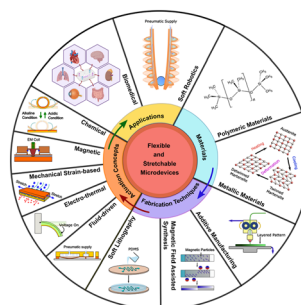
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and David R. Myers*



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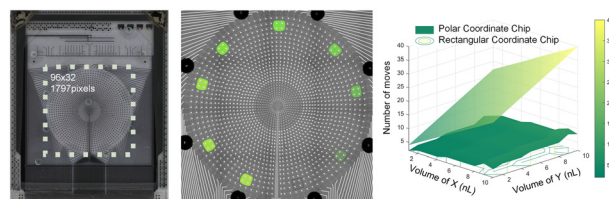


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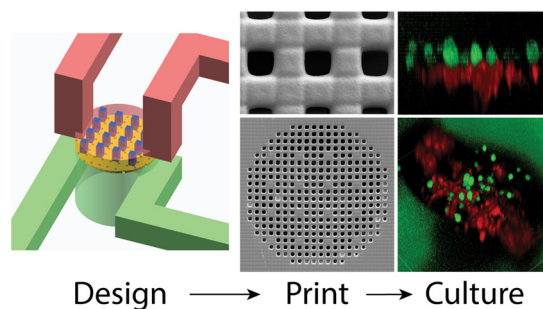
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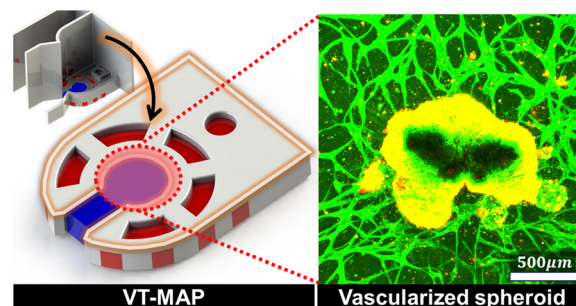
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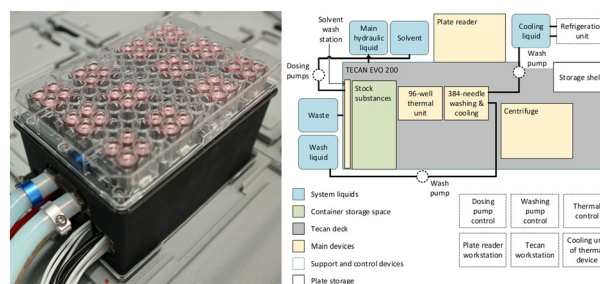
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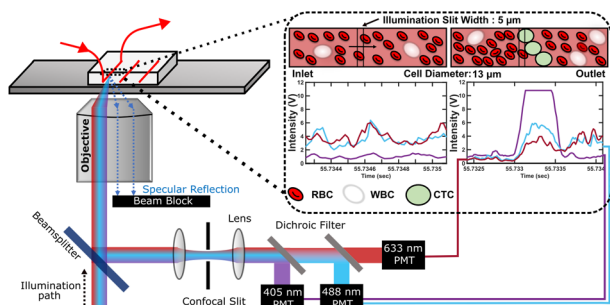
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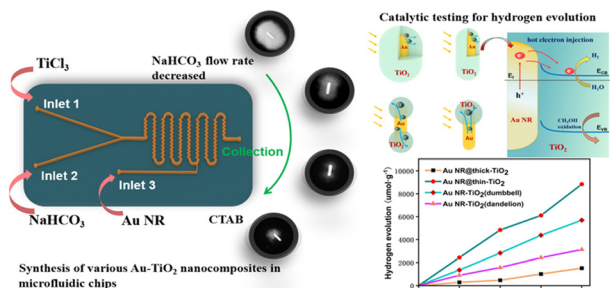
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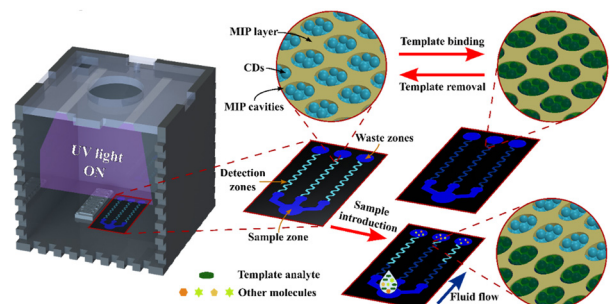
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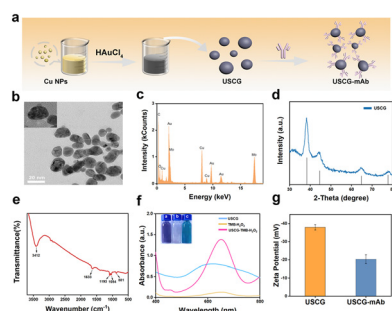
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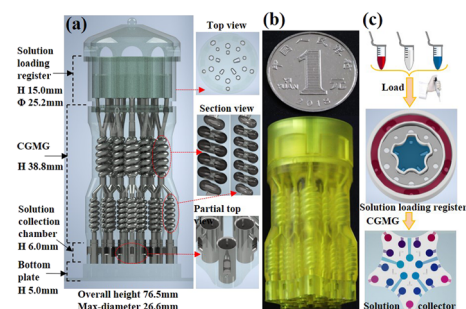


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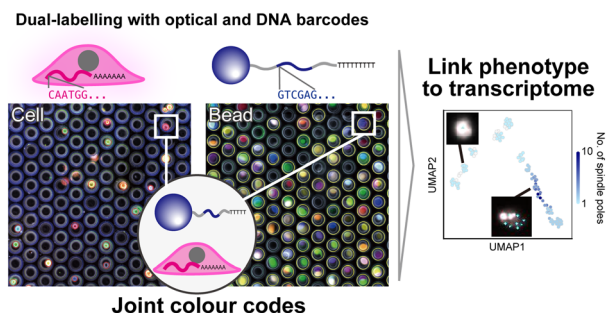
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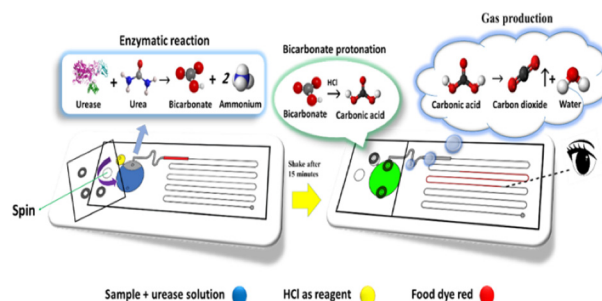
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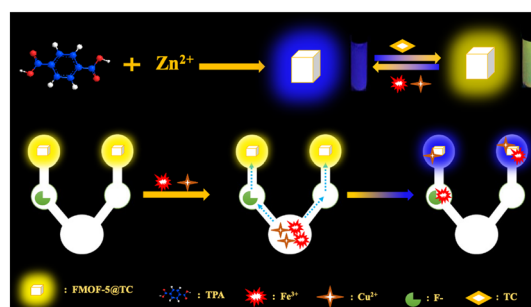
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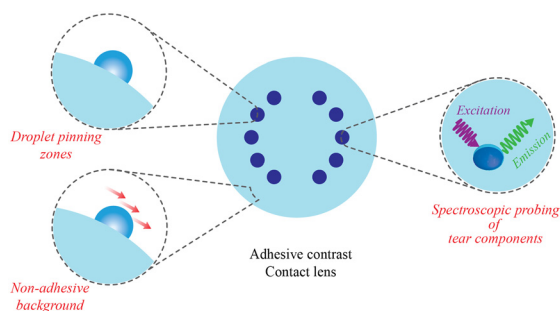
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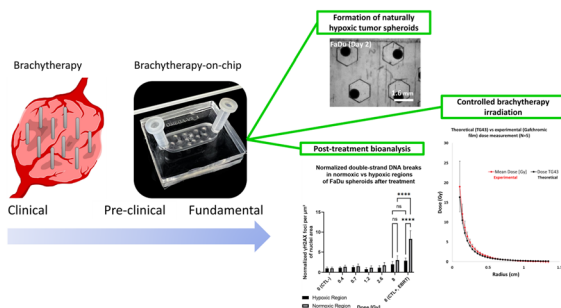
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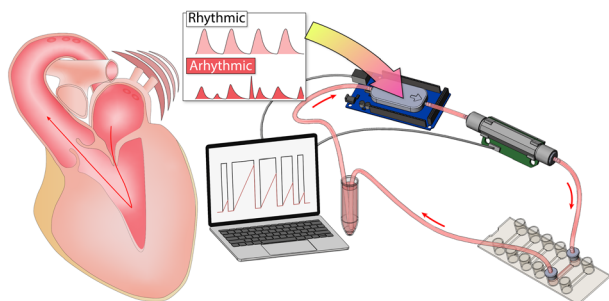
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