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# Controlling barrier height and spectral responsivity of p–i–n based GeSn photodetectors *via* arsenic incorporation

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GeSn compounds have made many interesting contributions in photodetectors (PDs) over the last ten years, as they have a detection limit in the NIR and mid-IR region. Sn incorporation in Ge alters the cut off wavelength. In the present article, p–i–n structures based on GeSn junctions were fabricated to serve as PDs. Arsenic (As) is incorporated to develop n-GeSn compounds *via* a metal induced crystallization (MIC) process followed by i-GeSn on p-Si wafers. The impact of As and Sn doping on the strain characteristics of GeSn has been studied with high resolution transmission electron microscopy (HRTEM), X-ray diffraction and Raman spectroscopy analyses. The direct transitions and tuning of their band energies have been investigated using diffuse reflectance UV-vis spectroscopy and photoluminescence (PL). The barrier height and spectral responsivity have been controlled with incorporation of As. Variation of As incorporation into GeSn Compounds shifted the Raman peak and hence affected the strain in the Ge network. UV-vis spectroscopy showed that the direct transition energies are lowered as the Ge–As bonding increases as illustrated in Raman spectroscopy investigations. PL and UV-vis spectroscopy of annealed heterostructures at 500 °C showed that there are many transition peaks from the UV to the NIR region as result of oxygen vacancies in the Ge network. The calculated diode parameters showed that As incorporation leads to an increase of the height barrier and thus dark current. Spectral response measurements show that the prepared heterojunctions have spectral responses in near UV and NIR regions that gives them opportunities in UV and NIR photodetection-applications.

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## Introduction

Direct transition semiconducting materials are involved in photodetection applications. Highly responsive photodetectors generate electron–hole pairs *via* direct photonic transition of electrons from the valence band to the conduction band by only photon excitation. However, in indirect semiconducting materials, a phononic transition is required using another photon, and then band-to-band photonic transition occurs.<sup>1,2</sup> GeSn compounds exhibit a direct band gap transition because of the

incorporation of Sn in Ge networks that modulates the *T* and *L* valley of the conduction band.<sup>3</sup> The energy difference between the two values is then equal to zero and the transition is undergone directly.<sup>4,5</sup> As a result of the modulated band gap in GeSn, their compounds have made a good contribution in mid-IR and NIR photodetection applications throughout the last decade.<sup>6</sup> Many attempts investigated responsivity enhancement and adjusting the cut off wavelength of GeSn based PDs.<sup>7,8</sup> J. Werner *et al.* compared Ge PDs and GeSn based PDs with 0.5% Sn content. The photodetector consisted of an i-GeSn layer sandwiched between a p-Ge bottom layer and n-Ge cap layer on a Ge/Si virtual substrate. They measured responsivity with a value of 0.1 A W<sup>−1</sup> with a cut off edge wavelength of 1.55 μm compared to the Ge PD with 0.03 A W<sup>−1</sup> responsivity and higher dark current and a shorter cut off wavelength with a value of 1.5 μm.<sup>9</sup> M. Oehme *et al.* studied the effect of Sn content increasing in i-GeSn layer capped by n-Ge on p-Ge. They varied the Sn content from 1.9% to 4% and found the cut off wavelength increasing with increasing Sn content from 1600 nm up to 2000 nm and the dark current increases with the increasing of Sn content.<sup>10</sup> M. Oehme *et al.* used multi quantum wells configurations instead of the GeSn layer. They altered the thickness of the GeSn well (*d*<sub>GeSn</sub>) from

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6 nm up to 12 nm. They found that the cut off wavelength increases with the increase of the thickness of GeSn well since the smaller thickness of the well causes quantum confinement and widens the energy gap. The responsivity also increased as increasing the Sn metal.<sup>11</sup> Kuo-Chih Lee *et al.* used a new configuration in which i-GeSn of a thickness of 163 nm is deposited on Ge virtual substrate and on both sides of i-GeSn layer, layers of p and n-Ge are deposited. They get a high responsivity of value  $0.25 \text{ A W}^{-1}$  at a cutoff wavelength of 1550 nm.<sup>12</sup> Harshvardhan Kumar *et al.* fabricated homojunction of p-i-n GeSn layers with 0%, 3%, 6% and 9% of Sn content. They found that the cut off wavelength increased with the increase of Sn content starting from 2000 nm to 2500 nm and the responsivity increased up to  $3.5 \text{ A W}^{-1}$ .<sup>13</sup> Xiang Quan Liu *et al.* deposited a layer of GeSn on Ge virtual substrate. They altered the concentration of Sn *via* adjusting the growth temperature in a molecular beam epitaxy reactor. They reached 16.3% of Sn content at growth temperature equal 150 °C and Sn crucible temperature of value 760–835 °C. They get cut off wavelength up to 4  $\mu\text{m}$ .<sup>14</sup>

In this work, metal induced crystallization of thermally evaporated amorphous Ge layers was employed to incorporate the Sn for tensile Ge network and As as n-type dopant during the low vacuum level annealing process. As a result of low vacuum level, oxygen is incorporated inside Ge networks. We get direct transition doped Ge. The effect of As incorporation on the tensile strain of Ge network has been investigated. Controlling the barrier height and so on the dark current *via* As doping are studied and how As incorporation reduces the energies of the direct transitions. Edge detection and responsivity of the prepared p-i-n junctions are also demonstrated.

## Materials and methods

### p-i-n junctions' fabrication

Four different configurations of semiconducting layers sandwiched between metal layers were deposited on single

crystalline p-Si substrate using thermal evaporation technique (EDWARDS E306A). These layers were deposited under a vacuum of  $6.5 \times 10^{-5}$  mbar and then annealed at 500 °C inside the evacuated jar with low vacuum level equal to  $4.6 \times 10^{-2}$  mbar for 24 h. The first configuration consists of 250 nm of silicon covered by 100 nm of Sn followed by 250 nm of Ge layer capped by 10 nm of As. As a result, metal induced crystallization (MIC) has occurred and Sn, As metals diffused in the semi-conducting layers forming an intrinsic GeSn layer covered by As doped Ge. Because of the low vacuum level during the annealing process, oxygen vacancies occupy the defects in Ge networks and formed  $\text{GeO}_x$ . The second configuration has the same silicon layer covered by Sn and the germanium layer capped with a Sn layer. After annealing, a 10 nm of As layer is deposited on the GeSn layer to form n-GeSn. The third configuration is the same as the first configuration except that a silicon layer is replaced by a Ge layer. The fourth configuration has the same structure as the second configuration but the silicon layer in the second configuration is also replaced by Ge layer. Four different structured p-i-n junctions are formed to serve as photodiodes as shown in Fig. 1.

### Investigation of strain in Ge crystallites formed by MIC

**High resolution transmission electron microscopy.** High resolution transmission electron microscopy was involved to investigate the Sn and As doped Ge strain crystallites formation during MIC process. A JEOL JEM-2100 plus HRTEM operating at 200 kV was used to calculate the *d*-spacing in the grown planes of strained Ge crystallites. Energy dispersion X-ray analysis (EDX) setup is attached to investigate the elemental analysis of the measured sample, which was peeled from the top surface of the fabricated film and dispersed in ethanol with ultrasonication for 15 minutes.

**X-Ray diffraction analysis.** A Shimadzu LabX 6000 X-ray diffractometer was utilized to investigate the formation of

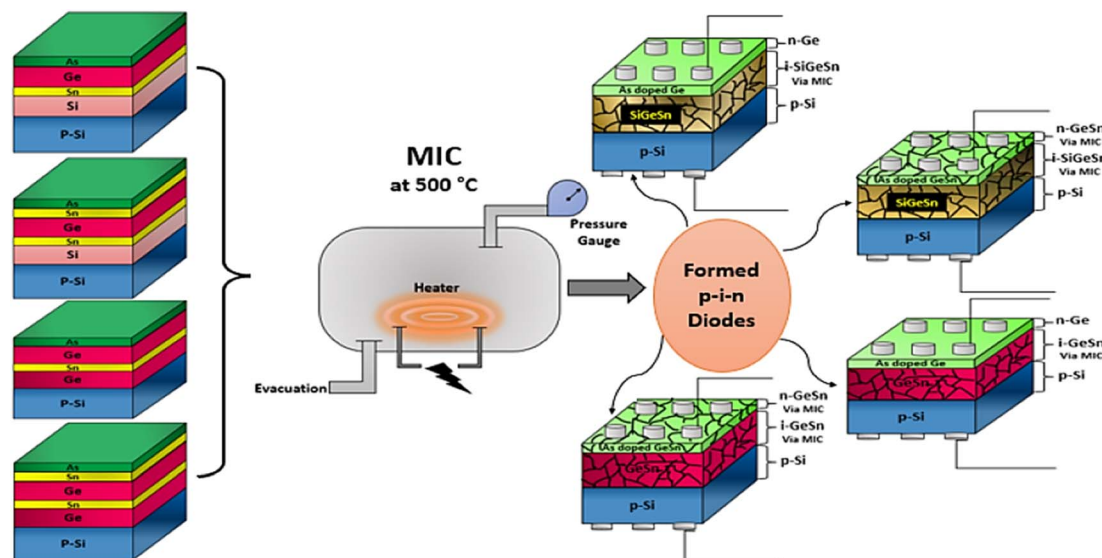


Fig. 1 Formation of GeSn based p-i-n diodes *via* metal induced crystallization (MIC).



GeSn crystallites as a result of Sn incorporation inside Ge network. *d*-Spacing, growth orientations, and microstrain were investigated using XRD measurements.

**Raman spectroscopy.** Raman spectroscopy is used to investigate the tensile strain Ge networks in the prepared p-i-n junctions in term of Raman shifting using Witec Raman confocal microscope with an excitation laser wavelength of 352 nm.

### Direct transition investigation

Direct transition of the formed compounds and their band gap as a function of As content were studied using diffuse reflectance spectrophotometer (UV-vis-NIR JASCO V-770). He-Cd laser (Kimmon – 325 nm) was used as excitation source for photoluminescence. The spectra are collected using iHR320 spectrometer (HORIBA) equipped with a Synapse CCD camera ranging between 300 nm to 1500 nm for detection.

### Hall effect, dark current and barrier height calculations

Negative charge carrier concentration as a function of As content was measured using Ecopia HMS 3000 Hall measurement system at room temperature  $\approx 298$  K. A Potentiostat METROHM Autolab 87070 was used to study the *I*-*V* characteristics of the formed p-i-n junctions biased by  $-5$  and  $+5$  V. Dark current and barrier height dependence on As were calculated.

### Spectral responsivity and cut off wavelength measurements

Spectral responsivity of the prepared heterojunctions has been measured using PVE300 system with 75 W xenon lamp of

spectral range between 300–1100 nm and 1100–2500 nm, respectively. The photodetectors used in the system are silicon PD with a detection range between 300–1100 nm and germanium PD with detection range between 800–1800 nm.

## Results and discussion

### HRTEM analysis

Low and high-resolution transmission electron microscopy were involved to investigate the Sn and As doped Ge crystallites during MIC process. Fig. 2a–c shows low and HRTEM micrographs of the annealed Ge/Sn/Ge/As multilayers on p-Si substrate. It is observed that there are crystallites of Ge formed after the layers interdiffusion during MIC process.<sup>15–17</sup> Fig. 2b and c shows multiple orientations of the grown Ge crystallites with *d*-spacing value of 0.58 nm of strained Ge (001). Fig. 2e and f shows the EDX spectra and the percentage of the elements forming the heterostructure in a selected area. The elemental analysis shows the existence of As and Sn metals that are incorporated in the Ge with an atomic percentage of 10.46% and 11.64%, respectively. Oxygen atoms with a percentage of 3.97% is detected during the annealing

Table 1 The calculated *d*-spacing of the strained Ge planes

Ge planes	$1/2r$ (1/nm)	<i>d</i> -Spacing (nm)
(001)	3.421	0.5846
(111)	5.593	0.3576
(333)	6.623	0.302
(220)	8.467	0.2362

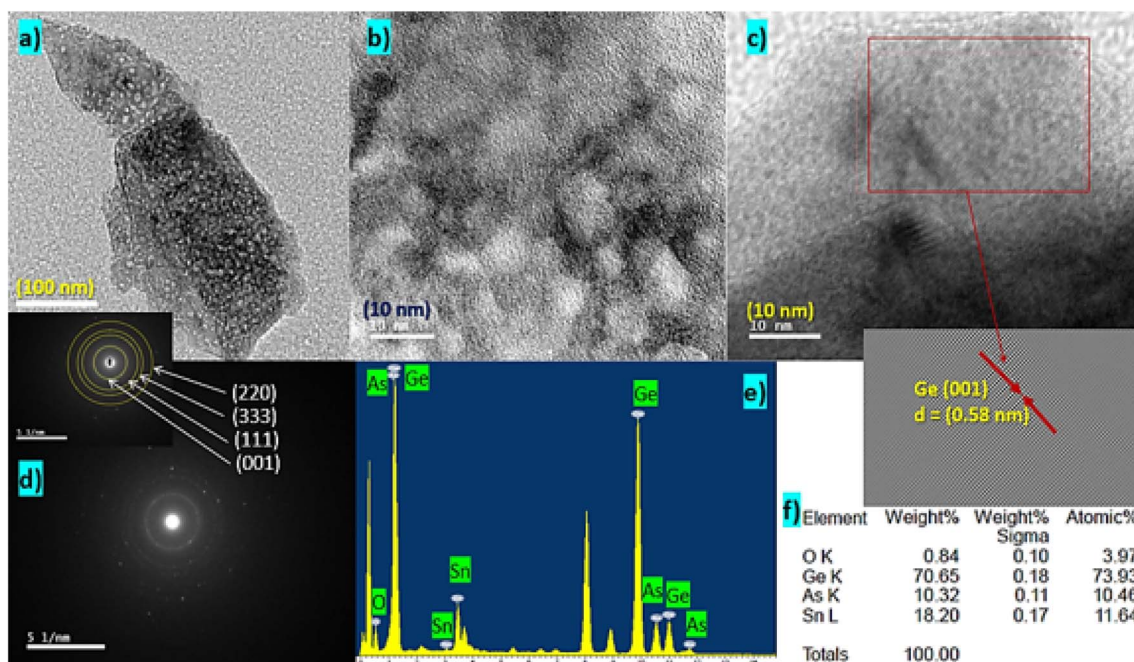


Fig. 2 (a–c) Low and HRTEM images of a prepared Ge/Sn/Ge/As configuration on p-Si substrate after MIC, (d) selected area electron diffraction, (e) EDX spectra, and (f) the atomic and weight percentage of the elements forming the sample.



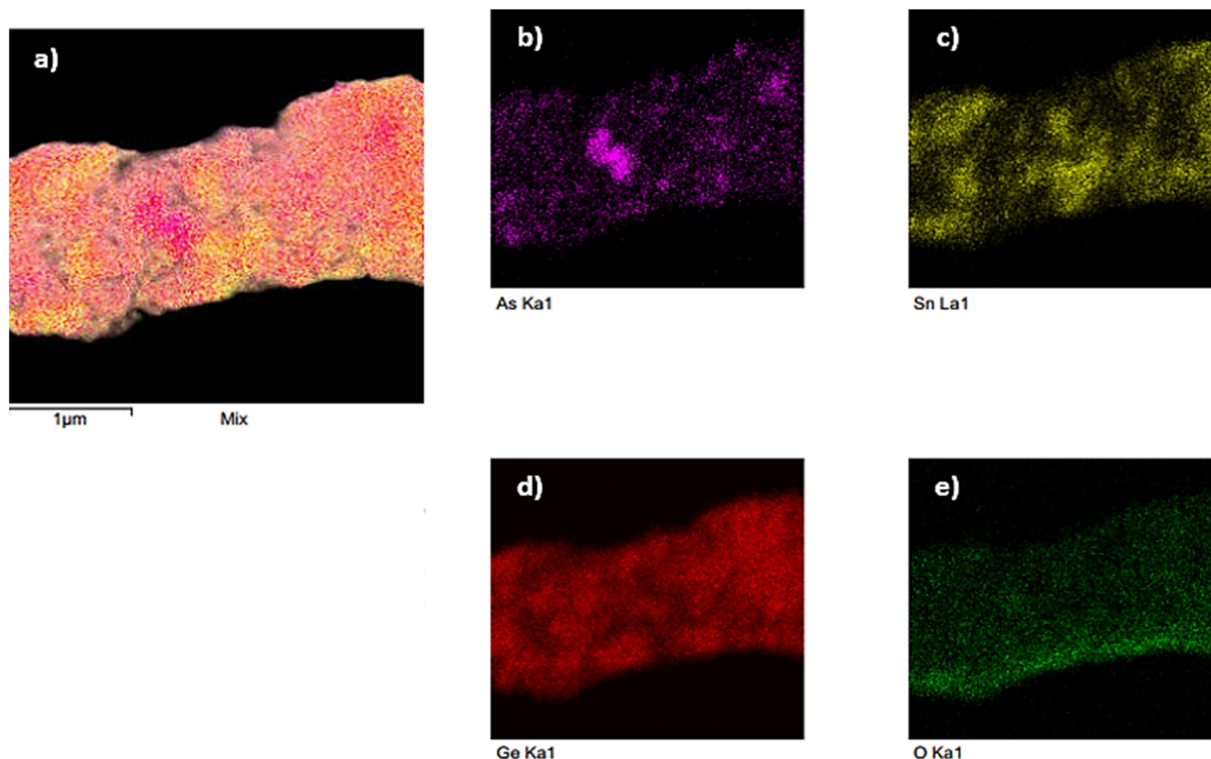


Fig. 3 Elemental mapping analysis of prepared Ge/Sn/Ge/As configuration annealed on p-Si.

process and filled the vacancies in the Ge network. Selected area electron diffraction (SAED) indicates that there are four orientations of (001), (111), (333), and (220) of the strained Ge planes doped with Sn (JCPDS card no. 65-0333).<sup>18</sup> The *d*-spacing values of these planes are estimated from the SAED. Table 1 summarizes the calculated *d*-spacing values of these interplanar spacing that are 0.5846, 0.3576, 0.302, and 0.2361 nm for the mentioned planes, respectively. These values of the strained planes are larger than the values of pure germanium (0.5513, 0.3276, 0.2935, and 0.2006 nm) for the same planes, respectively.<sup>18</sup> This indicates that Sn atoms with large diameter incorporated inside the Ge network and increased the *d*-spacing values in Ge network.<sup>19</sup>

Elemental mapping analysis was carried out for Ge/Sn/Ge/As annealed on p-Si. The mapping analysis shows uniform space distribution of the Ge, Sn, As, and O atoms that compose the prepared sample. Fig. 3a shows the mixture of the composing elements. Fig. 3b–e shows uniform distribution of As, Sn, Ge, and O, respectively.

### XRD analysis

XRD analysis was carried out for the Si/Sn/Ge/As and Ge/Sn/Ge/As configurations on p-Si annealed at 500 °C. Fig. 4 shows the XRD patterns of the two configurations. Ge (001), GeSn (111) broad peaks and GeSn (220) intense sharp peak were detected for both configurations.<sup>20–23</sup> The appearance of these planes matches with the HRTEM data. The most intense peak is GeSn (220) peak that is detected at  $2\theta = 43.8^\circ$  and  $43.72^\circ$  for Si/Sn/Ge/As and Ge/Sn/Ge/As configurations on p-Si annealed at 500 °C, respectively. Ge

(001) peak intensity is reduced in the second configuration in which the GeSn (220) peak increases. The second configuration's GeSn crystals seems highly oriented in (220) direction. Si (100) broad peak appears because of (100) p-Si oriented substrate that is covered by the deposited configurations.<sup>24</sup> The estimated *d*-spacing of the (220) oriented GeSn plane is 0.2221 and 0.2234 nm for the first and the second above mentioned configurations. The value of *d*-spacing from XRD analysis is very close to the *d*-spacing value of the GeSn (220) estimated from the SEAD measurements. The increase in the *d*-spacing for both configurations compared with the reference Ge is due to the large radii Sn atoms incorporation. This will cause compressive strain in Ge network.<sup>25</sup> The evidence for the compressive strain is the left shift of the GeSn peak (220). The value of the compressive strain in GeSn (220) plane was calculated using Williamson Hall equation  $\varepsilon = \beta/4 \tan \theta$ .<sup>26</sup> The values of the strain are  $4.37 \times 10^{-3}$  and  $4.01 \times 10^{-3}$  for the first and the second configuration, respectively. It is observed that the GeSn (220) in the second configuration (Ge/Sn/Ge/As) shifted to lower angle than in the first one (Si/Sn/Ge/Sn) as mentioned above. This is because of the higher thickness of the germanium layers in the second configuration. This causes relaxation in Ge network and incorporation more Sn atoms. due to Sn incorporation. This compressive strain relaxation makes the Ge network accept more Sn atoms that increase the structure compression.<sup>27</sup>

### Raman analyses

Fig. 5a–d shows the Raman spectra of the mentioned prepared configurations. A peak is detected for all configuration around



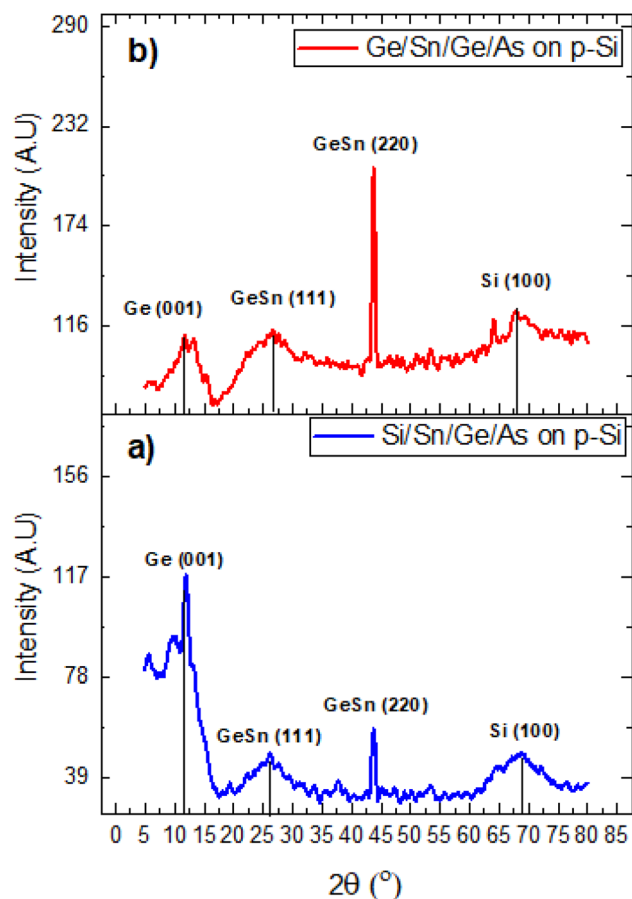


Fig. 4 XRD patterns of (a) Si/Sn/Ge/As and (b) Ge/Sn/Ge/As configurations on p-Si annealed at 500 °C.

300  $\text{cm}^{-1}$  which refers to GeSn mode.<sup>28</sup> The intensity of this peak is higher in configurations that contain Si buffer layer up to 7000 counts than the others don't have Si of intensity value  $\approx 2500$  counts. This is due to the solubility of Sn in Si layer is lower than in Ge during MIC because of the higher phase mismatching between Si and Sn than between Ge and Sn,<sup>29</sup> see Fig. 6. This reason caused higher incorporation of Sn in Ge in these configurations that contain Si buffer. Fig. 5a–d also shows a peak at 179  $\text{cm}^{-1}$  corresponding to GeAs mode.<sup>30</sup> The intensity of the GeSn peak is higher in configurations in which As is incorporated during annealing process. To explain the Raman shift in all configurations, the involved strains should be explained. In previous work, we studied the strain effect of Sn incorporation inside Ge network involved in identical Ge and Sn layers with the present work using XRD technique.<sup>15</sup> The interdiffusion between Sn and Ge interfaces was demonstrated using FESEM and HRTEM. It was shown that there is a compressive strain of Ge network in its interface with Si buffer due to the mismatching between Si and Ge lattice constants and atomic radii.<sup>19</sup> A compressive strain occurred due to the Sn incorporation inside Ge network. Sn has larger atomic radii that causes compression of the bond lengths of Ge network.<sup>31</sup> This results in shifting the GeSn peak towards lower wavenumbers.

Interstitial doping of As has an impact in Raman shifting.<sup>32</sup> The peak position of GeSn of the first configuration is at 293  $\text{cm}^{-1}$  in which As is incorporated during annealing while the peak position of the corresponding second configuration in which As isn't incorporated *via* annealing was detected at 305  $\text{cm}^{-1}$ . The GeSn peak of the other configurations is shifted from 308  $\text{cm}^{-1}$  to 304  $\text{cm}^{-1}$  which enhances the direct transition in Ge network.<sup>33</sup>

### Investigation of direct band gap using diffuse reflectance spectroscopy

Diffuse reflectance UV-vis spectroscopy was involved to investigate the direct transition in Ge crystallites in which Sn and As were incorporated during MIC process as well as introducing oxygen vacancies inside Ge networks. Kubelka–Munk model is used to estimate the values of the direct band gap of the prepared p–i–n heterostructures.<sup>34</sup> Fig. 7a–d shows plots of  $(kh\nu)^2$  versus  $h\nu$  of the prepared heterostructures. Weak direct transitions values are extrapolated at 1.05 eV to 1.47 eV for Si/Sn/Ge/As and Si/Sn/Ge/Sn/As respectively, see the insets of Fig. 7a and b. These weak direct transitions may correspond to the formed GeSn crystallites with oxygen atoms occupied the vacancies in Ge network. This oxidation of Ge incorporated with Sn makes the band gap wider.<sup>35</sup> The shifting of the transition towards low energy of 1.05 eV in Si/Sn/Ge/As than the other configuration is due to As incorporation during high temperature annealing forming shallow levels near conduction band that reducing the gap, See Fig. 8 and 9a in the manuscript.<sup>36</sup> These values of the transitions agree with the NIR (1255 nm) peak in PL measurements and the photoresponsivity edge in NIR at 1181 nm (1.0498 eV) as we will discuss in the following section.

Fig. 7c and d shows the direct transitions in Ge/Sn/Ge/As and Ge/Sn/Ge/Sn/As respectively. It is observed that a strong direct transition at 1.1 eV and 1.33 eV for the two mentioned configurations respectively.

Another direct transition at 2.9 eV and 3.38 eV is due to oxidation level during the annealing process under low vacuum forming  $\text{GeO}_2$  that has these wide band gaps as shown in Fig. 6.<sup>37,38</sup> This direct transition is due to strained GeSn formed crystallites with oxygen vacancies during low vacuum annealing process.<sup>39</sup> The transitions in the configurations that contain only Ge layers shift towards high energy than the configurations contain Si. This is because Sn has higher solubility in Ge than in Si thus the atoms of Sn layers will mostly diffuse in Ge rather than Si causing GeSn crystallites nucleation and growth in the above Ge and Sn layers on Si layer.<sup>16</sup> On the other hand, the atoms of Sn layers in the configurations contain only Ge layers diffuse in two Ge layers resulting in low Sn content in each Ge layer, see Fig. 6. As a result of low Sn content layers, the energy gap gets widened. Two other direct transitions are observed; one is at 2.5 eV, 2.17 eV for Ge/Sn/Ge/As and Ge/Sn/Ge/Sn/As respectively, and the other is at 3.1 eV and 3 eV for Ge/Sn/Ge/As and Ge/Sn/Ge/Sn/As respectively. These transitions are due to the formed  $\text{GeO}_x$  with different levels of oxygen vacancies at the two different Ge layers during the annealing process.<sup>38,39</sup>



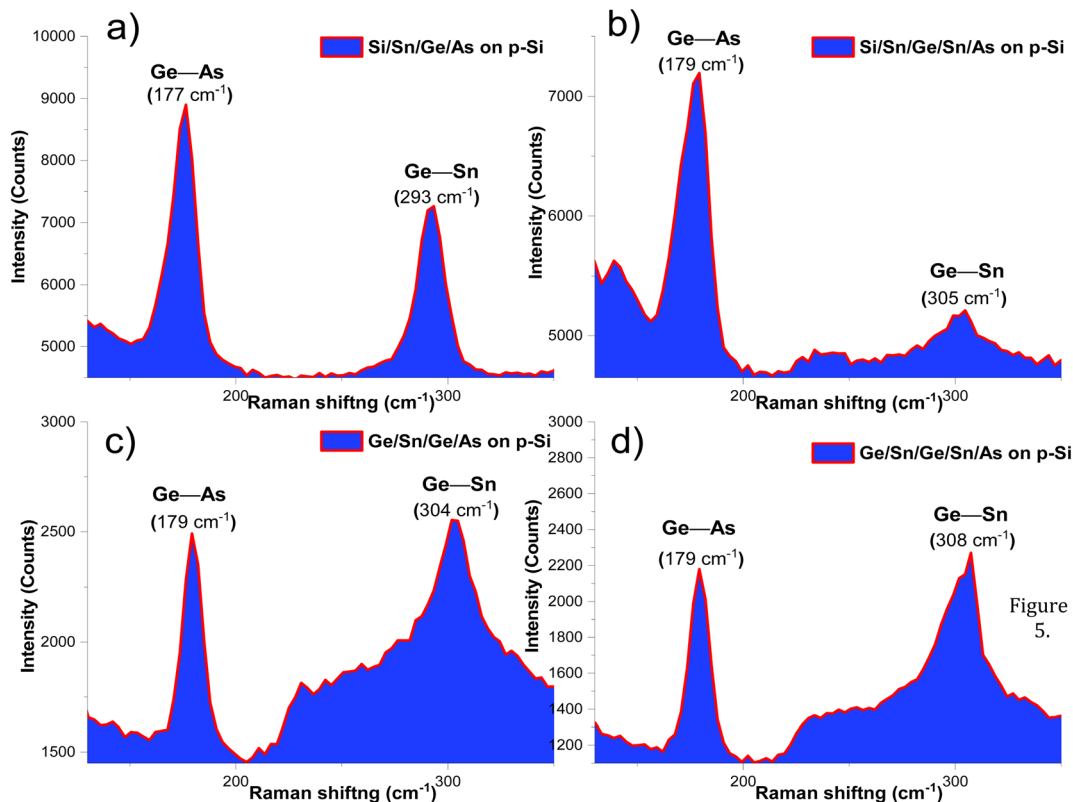


Fig. 5 Raman shifting of the prepared heterostructures (a) Si/Sn/Ge/As on p-Si, (b) Si/Sn/Ge/Sn/As on p-Si, (c) Ge/Sn/Ge/As on p-Si and (d) Ge/Sn/Ge/Sn/As on p-Si.

### Photoluminescence measurements

The Sn and As were incorporated in Ge networks within MIC process.<sup>40</sup> The role of Sn in the Ge networks is to form GeSn compound that has a direct transition band gap as Sn incorporation modulated the energy difference between *L*-valley and *Γ*-valley.<sup>41</sup> The energy of *Γ*-valley increases that is aligned to the valence band forming direct transition band gap in the strained Ge networks doped with Sn and the energy gap gets lower. The incorporation of As on the surface of the prepared structure formed n-GeSn layer with shallow levels of As charge carriers inside the energy gap causes the band gap get narrower, See

Fig. (9a). Fig. 9b shows the photoluminescence spectra of the prepared p-i-n structures. Three main overlapped peaks over wide range of spectra between 550 nm to 1500 nm (visible-NIR) is observed. The first peak is detected at 776 nm which is due to the p-Si base substrate after thermal oxidation and few contributions of poly-Si crystallites formed *via* MIC.<sup>42</sup> The second peak is at 1026 nm that is due to oxidized Ge network doped with Sn.<sup>43</sup> A 1255 nm low intensity peak is detected due to the oxidized GeSn doped with As that introduces shallow levels within the energy gap that makes it get narrower.<sup>36,43</sup> This gives these p-i-n structures the chance to serve as optically pumped

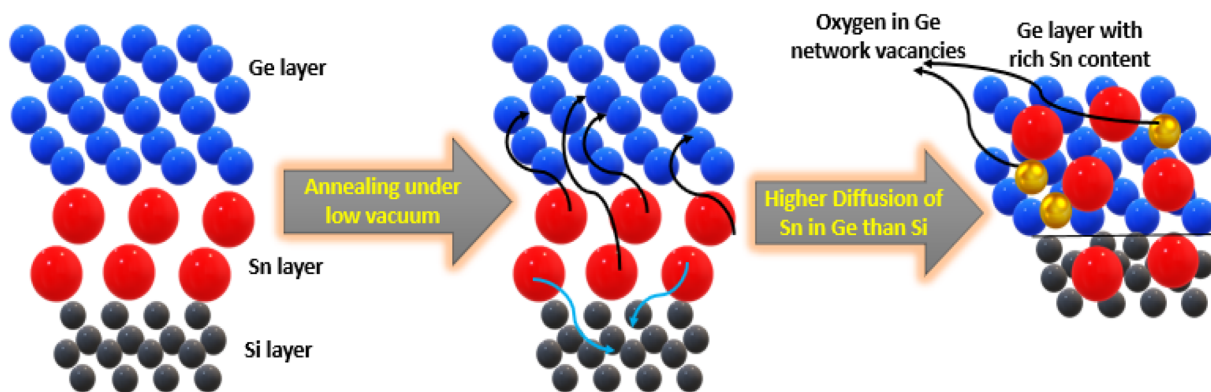


Fig. 6 Sn solubility in Ge rather than Si.



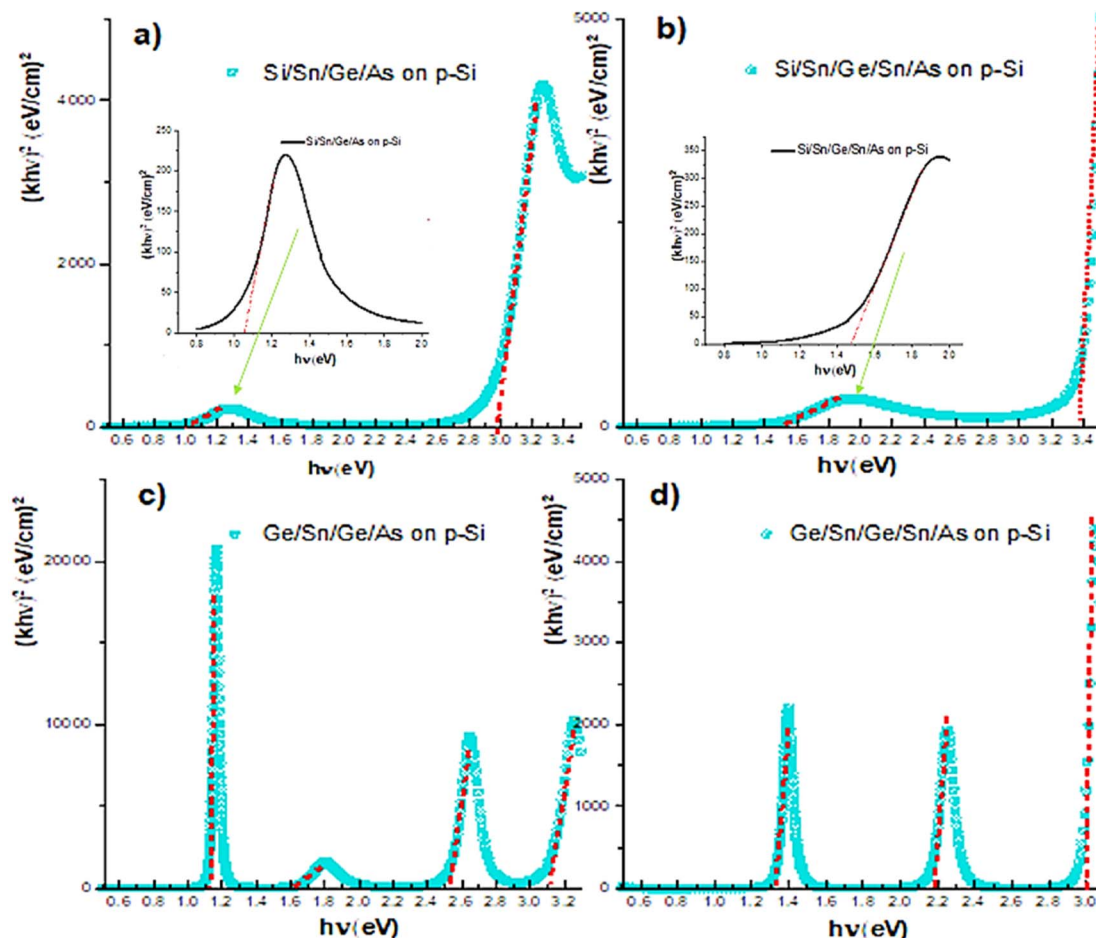


Fig. 7  $(kh\nu)^2$  versus  $h\nu$  plot of the prepared heterostructures.

white light and NIR source.<sup>15</sup> It is observed that the heterostructures containing Si layer have higher intensity of PL peak due to the enhancing of Si in MIC process of Ge as we discussed before.

#### Dark current and barrier height calculations using IV characteristics investigations

After the annealing process under low vacuum level, MIC process occurred and As is diffused in the formed strained GeSn compound. The resultant heterostructures consisted of n-GeSn doped with As above i-GeSn or i-SiGeSn formed on p-Si base substrate to get p-i-n junctions. Ag back and front grid electrodes were deposited on the p-Si and n-GeSn layer for Hall effect and IV characteristics measurements. Hall effect measurement was carried out by Ecopia HMS 3000 Hall measurement system to determine the negative charge carrier concentration due to As addition to germanium. Hall effect measurements revealed that the negative charge carrier concentration was  $-2.465 \times 10^{14} \text{ e cm}^{-3}$  with high electron mobility. The electron mobility was  $1.219 \times 10^3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . This high electron mobility is due to the high annealing temperature up to 500 °C. This high annealing temperature releases the strain in germanium and causes large

accumulation of germanium crystallites resulting in reducing grain boundaries.<sup>44,45</sup> IV characteristics of the resultant junctions were carried out as shown in Fig. 10 using a METROHM Autolab 87070 potentiostat system.

Fig. 10a–d shows the IV measurements for the fabricated heterostructures (n-GeSn/i-SiGeSn/p-Si) and (n-GeSn/i-GeSn/p-Si) biased by  $\pm 5 \text{ V}$ . Turn-on voltage of the four junctions is estimated from Fig. 10 and so the dark current at  $-5 \text{ V}$ . Table 2 summarizes the calculated diode parameters for the prepared p-i-n configurations. Fig. 11 shows a plot of the current in logarithmic scale versus voltage. It is found that the dark current for Si/Sn/Ge/As, Ge/Sn/Ge/As and Ge/Sn/Ge/Sn/As is very low in range of  $10^{-7}$ – $10^{-8} \text{ A cm}^{-2}$  compared to other works<sup>46–49</sup> except Si/Sn/Ge/Sn/As on p-Si that has higher dark current of value  $4.87 \times 10^{-4} \text{ A cm}^{-2}$  at  $-3 \text{ V}$ . According to Raman shifting measurements, this configuration has lower bonding between Ge and Sn that causes free Sn clusters to serve in the dark current.

The turn-on voltage has values ranged between 3.35 V to 3.95 V. It is found that the turn-on voltage decreases in the configurations (Si/Sn/Ge/As and Ge/Sn/Ge/As on p-Si) in which As is deeply incorporated inside GeSn during the annealing process that increases the n-type charge carrier and thus reduce depletion region.<sup>50</sup>



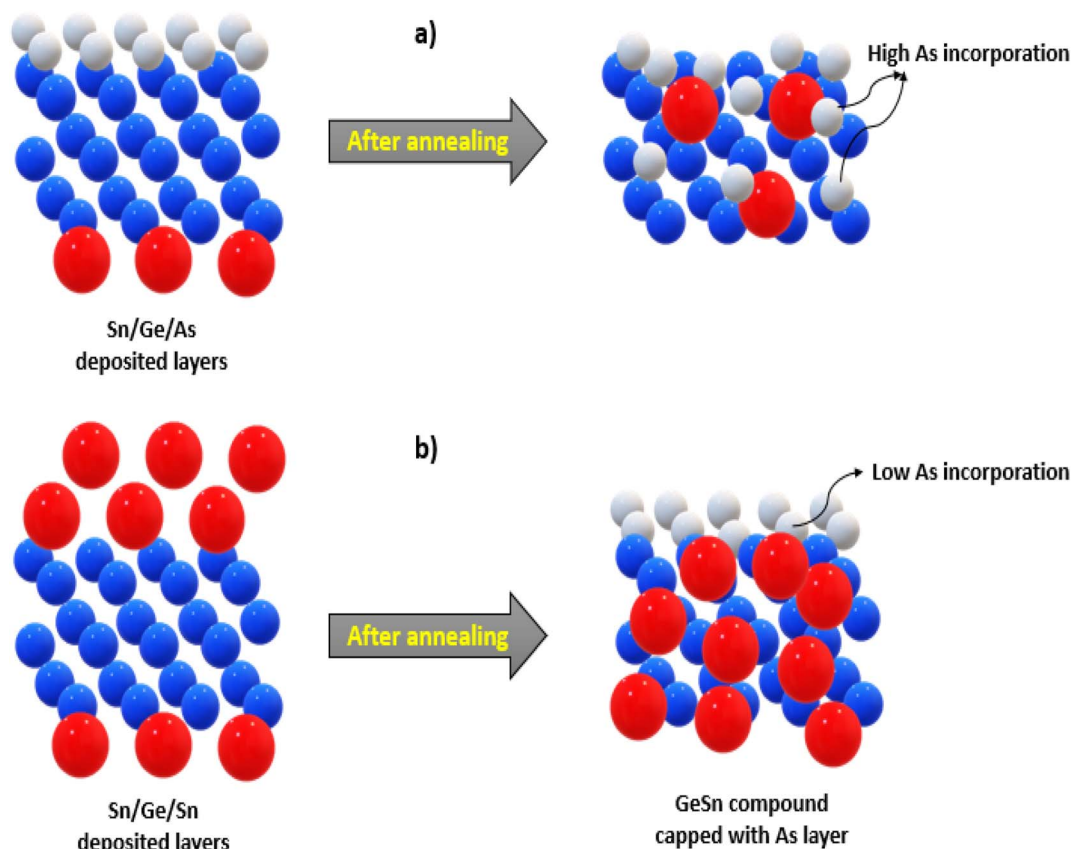


Fig. 8 Deep As incorporation inside Ge network during annealing process.

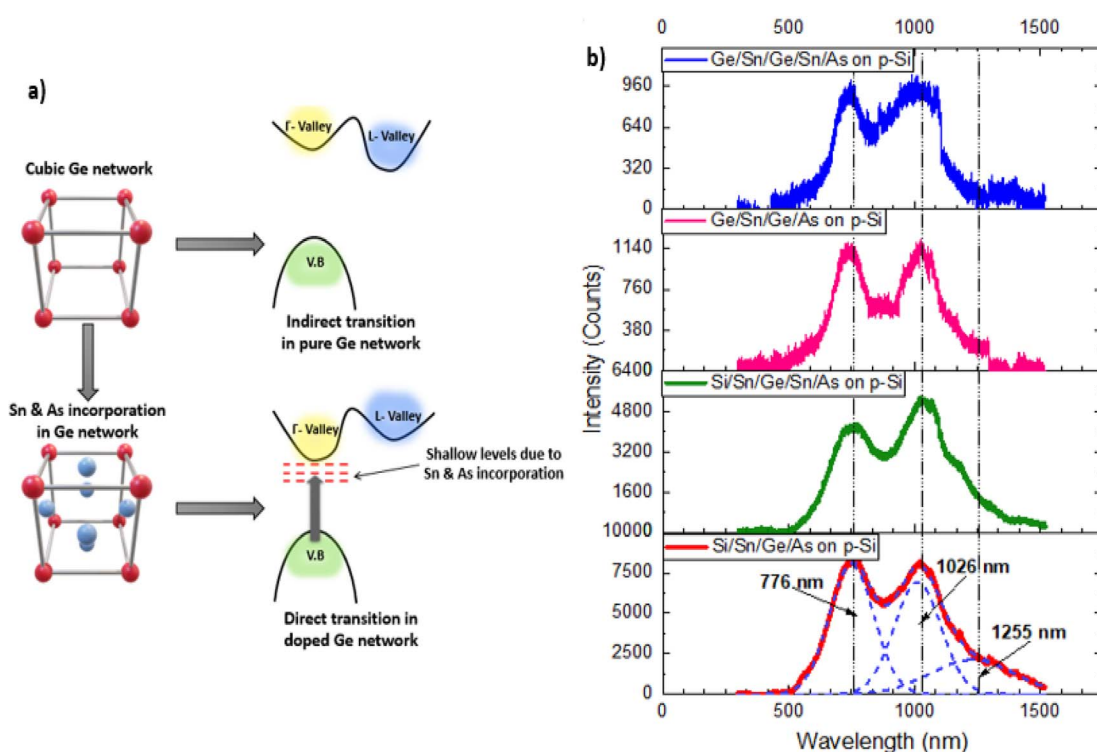


Fig. 9 (a) Indirect to direct transition band gap due to Sn and As incorporation in strained Ge network. (b) Photoluminescence spectra of the prepared heterostructures.

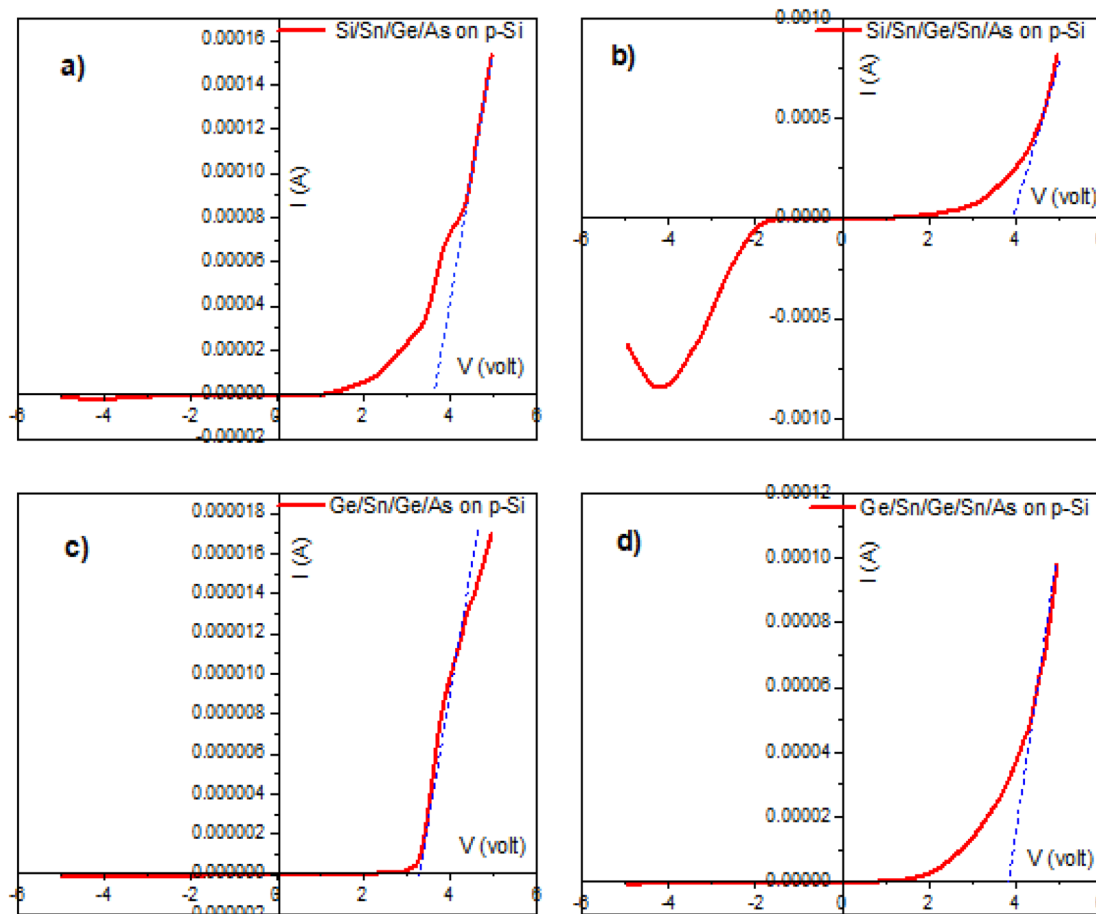


Fig. 10  $I$ - $V$  characteristic plot of the prepared heterostructures in  $\pm 5$  V forward and reverse biasing (a) Si/Sn/Ge/As on p-Si, (b) Si/Sn/Ge/Sn/As on p-Si, (c) Ge/Sn/Ge/As on p-Si, and (d) Ge/Sn/Ge/Sn/As on p-Si.

The diodes quality factors have been calculated using Schottky diode equation<sup>51</sup> at low voltage (region 1) and high voltage (region 2, see Fig. 12a-d).

$$I_D = I_s \left[ \exp\left(\frac{V_D}{nKT}\right) - 1 \right] \quad (1)$$

The values of the quality factor are estimated from the slope of  $\ln(I)$  versus  $V$  plot as seen in Fig. 12. They are listed in Table 2. It is observed that the values are near ideality at low voltage region. The configurations that contain only Ge semiconductor layers (Ge/Sn/Ge/As and Ge/Sn/Ge/Sn/As on p-Si) have ideality

factors of values 1.33 and 1.07 that are near ideal diode factor = 1 rather than the other configurations contain Si layers. The non-ideal factors of the prepared junctions that have values above 2 up to 5.61 at higher voltage region due to the tunneling effect of the conduction electrons of Sn and As metals at high voltage that increases the saturation dark current increasing the ideality factor above the ideality.<sup>52</sup>

To get a high response photodetector, the photocurrent must be higher above the reverse dark current. Higher barrier height prevents the reverse diffusion current from the n-type to the p-type.<sup>53</sup> Thus, the depletion region barrier height is responsible for the high-quality photodetectors. The barrier heights of the prepared junctions are estimated using Richardson equation:<sup>51</sup>

Table 2 The calculated diode parameters of the prepared p-i-n junctions

Sample	Turn-on voltage (V)	Quality factor "n"		Barrier height $\phi_B$ (eV)	Dark current at $-3$ V ( $A\ cm^{-2}$ )
		Region 1	Region 2		
Si/Sn/Ge/As on p-Si	3.65	1.73	2.94	0.77	$5.29 \times 10^{-7}$
Si/Sn/Ge/Sn/As on p-Si	3.95	2.34	2.28	0.61	$4.87 \times 10^{-4}$
Ge/Sn/Ge/As on p-Si	3.35	1.33	5.62	0.84	$8.26 \times 10^{-8}$
Ge/Sn/Ge/Sn/As on p-Si	3.87	1.07	2.51	0.81	$1.98 \times 10^{-7}$



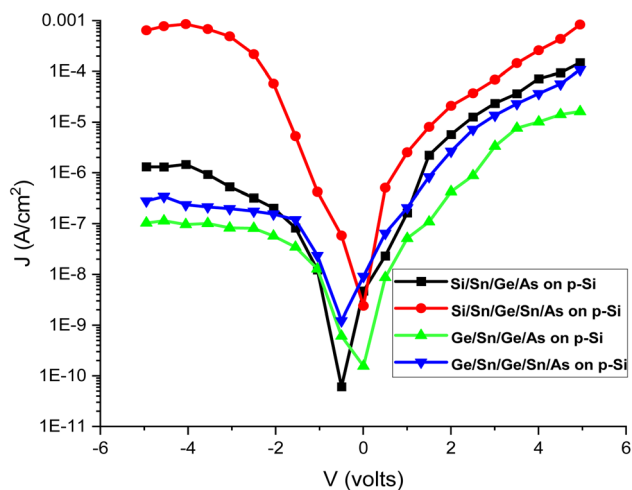


Fig. 11 The current density in logarithmic scale versus voltage of the prepared p-i-n junctions.

$$I_s = AA^*T^2 \exp\left(\frac{-q\Phi_b}{KT}\right) \quad (2)$$

where  $A$  is the area of the diode,  $A^*$  is Richardson constant,  $q$  is the charge of electron,  $K$  is Boltzmann constant,  $T$  is temperature in Kelvin and  $\Phi_b$  is the barrier height. The barrier heights of the prepared p-i-n junctions are calculated and listed in Table 2. It is seen that the barrier heights of the prepared junctions range between 0.61 to 0.84 eV and are higher than the barrier height of germanium-based diodes, which have values in the range of 0.2–0.3 eV.<sup>54,55</sup> This gives the prepared samples

a good chance to contribute to germanium based photo-detection applications.

### The responsivity measurements of the prepared GeSn based PDs

Responsivity measurements are carried out to investigate the detection region of the prepared p-i-n based GeSn heterostructures. The measurements reveal that the prepared heterostructures have a response near UV portion radiation as seen in Fig. 13. This can be explained as germanium networks doped with Sn and As of the prepared samples contain oxygen vacancies forming  $\text{GeO}_x$  that has wide band gap that pushes UV spectrum to generate photocurrent<sup>56</sup> as discussed in photoluminescence, UV-vis spectroscopy and Raman spectroscopy. Fig. 13 shows that the response decreases from near UV region to the visible. This gives the prepared p-i-n GeSn hetero-junction the opportunity to be utilized as UV photodetectors. Fig. 14 shows that the samples have responsivity in NIR region from 800 nm to 1200 nm of values close to that in UV region. This response in NIR region is corresponding to the GeSn doped with arsenic with cut off wavelength at 1181 nm.

In this work, we aim to get spectral responsivity in different regions of spectra. Low vacuum conditions were involved during preparing these heterostructures. As a result of different levels of oxidation, the band gap values varied across the prepared heterostructures. Spectral responsivity measurements reveal that the prepared heterostructures at low vacuum condition have spectral response in both UV and NIR regions with cutoff wavelength at 1181 nm. It is known that Ge and GeSn based photodetectors have spectral responsivity in mid IR

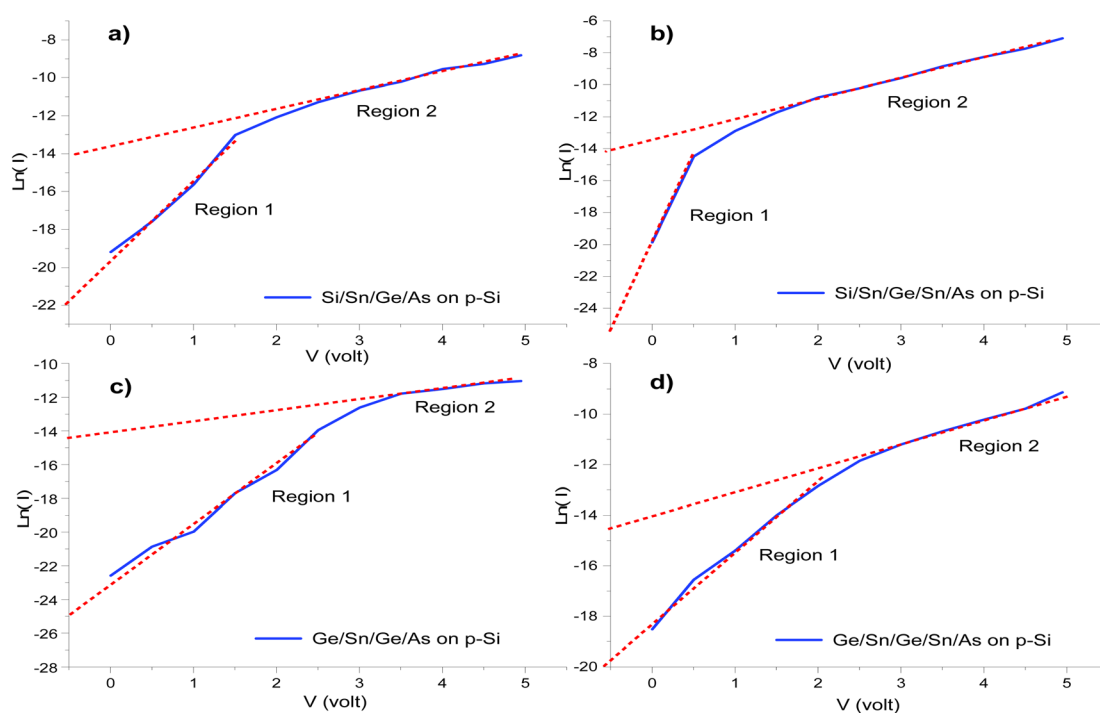


Fig. 12 Plot of  $\ln(I)$  versus  $V$  for the prepared junctions. (a) Si/Sn/Ge/As, (b) Si/Sn/Ge/Sn/As, (c) Ge/Sn/Ge/As, and (d) Ge/Sn/Ge/Sn/As on p-Si.



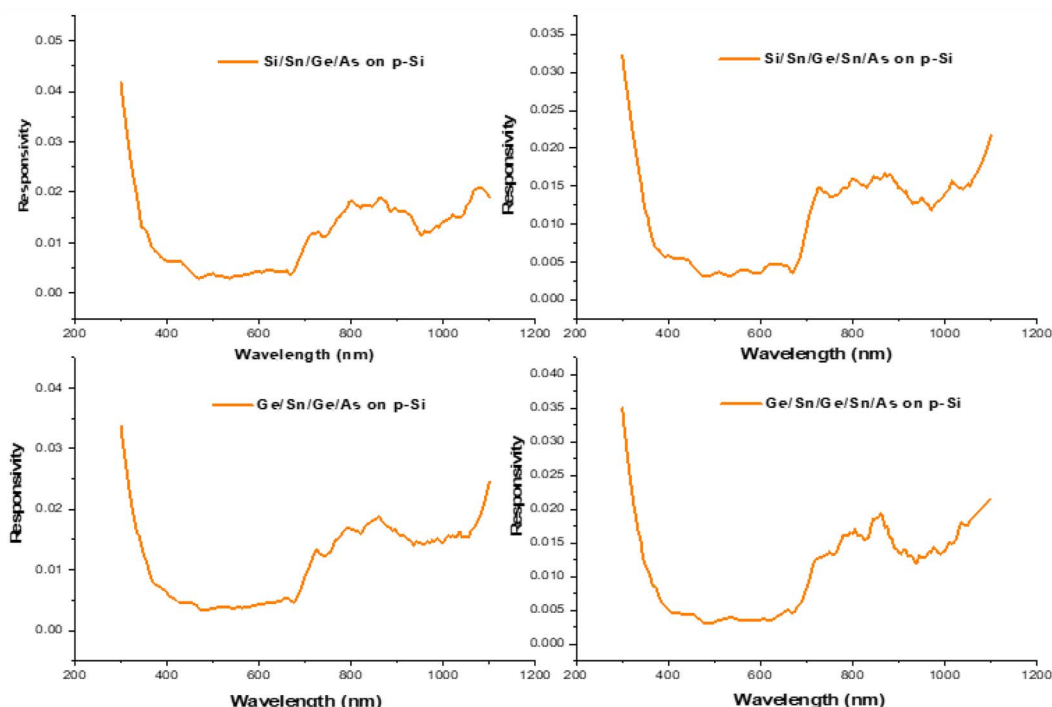


Fig. 13 Responsivity of the prepared heterojunctions in (300 nm–1200 nm) spectral range.

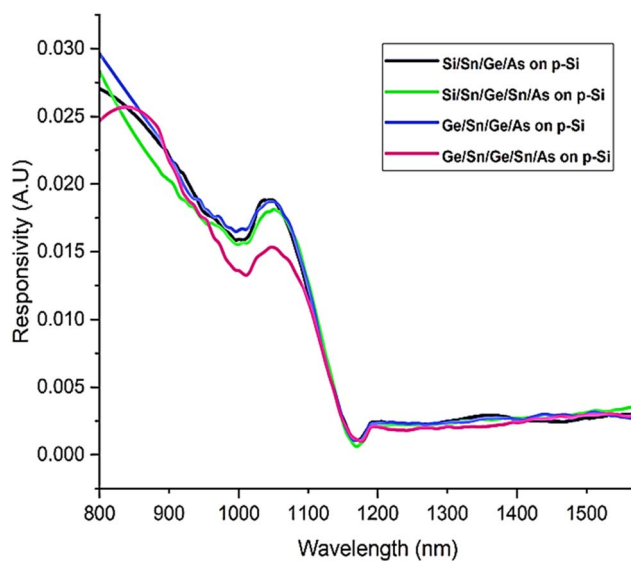


Fig. 14 Responsivity of the prepared heterojunctions in (800 nm–1600 nm) spectral range and cut off edge.

region with cut off wavelength in the range  $\approx (1600\text{--}3600\text{ nm})$ .<sup>57</sup> Here, the oxygen filled the Ge crystallite vacancies widens the band gap across the prepared heterostructures as shown in UV-vis spectroscopy and photoluminescence measurements. This shifts the cut off wavelength of responsivity from mid IR (1600–3600 nm) region to NIR region at (1181 nm). This also gives these prepared heterostructures the chance to be used as UV and IR photodetectors.<sup>58</sup>

## Conclusions

We fabricated two region (UV and NIR) p–i–n based GeSn photodetectors with the aid of the simple and low-cost metal induced crystallization of germanium technique. This technique served in oxygen incorporation that widened band gap up to 3.38 eV that enabled the prepared PDs to detect spectra below 300 nm. MIC also served in Sn and As incorporation and formation of strained Ge crystallites with  $d$ -spacing  $\approx 0.22\text{ nm}$  for the highly oriented Ge (220) plane. This caused modulation of strained Ge crystallites band gap make it get narrower down to 1.05 eV. This gives the prepared PDs the ability to detect in NIR region with cut off wavelength near 1200 nm. Diode IV characteristics revealed that the dark current for Si/Sn/Ge/As, Ge/Sn/Ge/As and Ge/Sn/Ge/Sn/As is very low in range of  $10^{-7}$  to  $10^{-8}\text{ A cm}^{-2}$ , their ideality factors are near 1 and the barrier heights of the junctions are between 0.61 to 0.84 eV that higher than the barrier height of germanium-based reference diodes that make them promise in photodetection applications.

## Data availability

The data that support the plots and results within this paper are available from the corresponding authors upon reasonable request.

## Conflicts of interest

The authors declare no competing interests or non-financial interests.



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