


Cite this: *RSC Adv.*, 2023, 13, 5437

The influences of AlGa_N barrier epitaxy in multiple quantum wells on the optoelectrical properties of AlGa_N-based deep ultra-violet light-emitting diodes†

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The growth conditions of the AlGa_N barrier in AlGa_N/AlGa_N deep ultra-violet (DUV) multiple quantum wells (MQWs) have crucial influences on the light output power of DUV light-emitting diodes (LEDs). The reduction of the AlGa_N barrier growth rate improved the qualities of AlGa_N/AlGa_N MQWs, such as surface roughness and defects. The light output power enhancement could reach 83% when the AlGa_N barrier growth rate was reduced from 900 nm h⁻¹ to 200 nm h⁻¹. In addition to the light output power enhancement, lowering the AlGa_N barrier growth rate altered the far-field emission patterns of the DUV LEDs and increased the degree of polarization in the DUV LEDs. The enhanced transverse electric polarized emission indicates that the strain in AlGa_N/AlGa_N MQWs was modified by lowering the AlGa_N barrier growth rate.

Received 20th November 2022

Accepted 6th February 2023

DOI: 10.1039/d2ra07368d

rsc.li/rsc-advances

1. Introduction

Deep ultraviolet (DUV) light has shown its importance in many fields, such as water purification, UV photolithography, high-density optical data storage, and portable chemical/biological agent detection/analysis systems.^{1–5} Furthermore, UV communication has been presented as a promising candidate for military wireless communication.^{6,7} A record-breaking data rate has been reported continuously in the past few years.^{8–11} Many applications require the light output power of the UV light source to be as high as possible. Especially, the output power of the UV light source essentially determines the transmission distance of UV communication, and its bandwidth has a major impact on the data rate.¹² The light output efficiency of AlGa_N-based DUV light-emitting diodes (LEDs) has been improved by the optimization of the epitaxy techniques for improving the material quality of AlN.^{13–17} Although the efficiency of DUV LEDs has been greatly improved, their performance is still hindered by high defect density, poor carrier injection, and low carrier

confinement. Many studies have reported strategies of defect reduction *via* substrate or template,^{18–20} methods of high carrier confinement, and hole injection.^{21–35} Low light extraction efficiency (LEE) is another challenge in achieving high efficiency for DUV LEDs. The LEE of the DUV LEDs has been improved by utilizing the flip-chip design,^{36,37} a patterned sapphire substrate,^{20,38} a patterned p-type layer,³⁹ and the nanowire structure.^{40,41}

AlGa_N/AlGa_N multiple quantum wells (MQWs) are the main active layers in DUV LEDs. The quality of AlGa_N/AlGa_N MQWs dominated the optoelectrical characteristics of DUV LEDs, such as leakage current and light output power. Furthermore, the defect reduction of AlGa_N/AlGa_N MQWs would improve the internal quantum efficiency (IQE) of DUV LEDs. The growth conditions of epitaxy directly affect the quality of AlGa_N/AlGa_N MQWs. Previously, Bryan *et al.*⁴² reported that the growth condition of the V/III ratio could impact the IQE of AlGa_N/AlGa_N MQWs. (The V/III ratio in metalorganic chemical vapor deposition (MOCVD) is normally defined as the ratio of the molar flow rate of ammonia (NH₃) to the total molar flow rates of trimethylgallium (TMGa) and trimethylaluminum (TMAI).) They found that AlGa_N/AlGa_N MQWs grown with high V/III ratio could achieve a high IQE of ~80%. The same group also reported that surface kinetics influence the growth of AlGa_N in terms of Al composition inhomogeneity and crystal quality.⁴³ Therefore, the growth conditions are crucial during the growth of AlGa_N/AlGa_N MQWs. During the growth of AlGa_N/AlGa_N MQWs, the condition of Al_{0.7}Ga_{0.3}N barrier might strongly affect the growth of the following Al_{0.4}Ga_{0.6}N wells. In this

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† Electronic supplementary information (ESI) available. See DOI: <https://doi.org/10.1039/d2ra07368d>


study, we varied the growth rates of the $\text{Al}_{0.7}\text{Ga}_{0.3}\text{N}$ barrier to investigate the influences on the electrical and optical properties of DUV LEDs.

2. Experimental

Epitaxial wafers were prepared on a 2-in (0001) sapphire substrate by MOCVD (Thomas Swan close-coupled showerhead 31×2 in). The source materials of gallium (Ga), aluminum (Al), and nitrogen (N) were TMGa, TMAI, and NH_3 , respectively. The n-type doping source was silane (SiH_4), and the p-type doping source was bis(cyclopentadienyl)magnesium(II) (Cp_2Mg). Initially a 20 nm-thick AlN nucleation layer was deposited on sapphire at 500 °C, and a 3 μm -thick undoped AlN layer was grown at 1250 °C. The screw and edge dislocation densities of the AlN epitaxy were $1.03 \times 10^8 \text{ cm}^{-2}$ and $1.64 \times 10^9 \text{ cm}^{-2}$, respectively. Following the AlN layer, a 20-pair AlN (2.5 nm)/ $\text{Al}_{0.8}\text{Ga}_{0.2}\text{N}$ (2.5 nm) superlattice and a 0.5 μm -thick $\text{Al}_{0.68}\text{Ga}_{0.32}\text{N}$ layer were grown as transition layers. A 1.5 μm -thick $\text{Al}_{0.6}\text{Ga}_{0.4}\text{N}:\text{Si}$ layer was grown as an n-type layer. The active layer was a five-pair $\text{Al}_{0.7}\text{Ga}_{0.3}\text{N}:\text{Si}$ (12 nm)/ $\text{Al}_{0.4}\text{Ga}_{0.6}\text{N}$ (2 nm) MQWs structure grown at 1050 °C, and a 2 nm-thick undoped $\text{Al}_{0.65}\text{Ga}_{0.35}\text{N}$ was grown as the last barrier. The $\text{Al}_{0.7}\text{Ga}_{0.3}\text{N}$ barriers grown at different growth rates were obtained by modifying the molar flows of TMGa and TMAI, and the molar flow of silane was also adjusted to fix the doping concentration. The growth rates of the $\text{Al}_{0.7}\text{Ga}_{0.3}\text{N}$ barriers were 900, 400, and 200 nm h^{-1} , respectively. After the MQW structure, an electron blocking layer, $\text{Al}_{0.8}\text{Ga}_{0.2}\text{N}$ (7 nm), and three-pair $\text{Al}_{0.8}\text{Ga}_{0.2}\text{N}:\text{Mg}$ (7.5 nm)/ $\text{Al}_{0.7}\text{Ga}_{0.3}\text{N}:\text{Mg}$ (3.5 nm) were grown. The p-type layer consisted of a 10-pair $\text{Al}_{0.8}\text{Ga}_{0.2}\text{N}:\text{Mg}$ (1 nm)/ $\text{Al}_{0.48}\text{Ga}_{0.52}\text{N}:\text{Mg}$ (1 nm) superlattice, a 20 nm-thick Al-grading AlGaIn:Mg layer (Al composition from 47% to 25%), and 10 nm-thick GaN:Mg layer.

The chip processes were performed to fabricate $500 \mu\text{m} \times 500 \mu\text{m}$ flip-chip DUV LEDs. The mesa area was revealed by the inductively coupled plasma (ICP) etching process, and Ti (20 nm)/Al (120 nm)/Ti (100 nm)/Au (50 nm) was subsequently deposited as the n-contact. Indium tin oxide (20 nm)/Al (100 nm) was deposited on the p-GaN as the p-contact. Through the thinning and dicing processes, the DUV LED chip was flipped and mounted on the AlN submount by AuSn eutectic bonding. The epitaxial structure and the schematic of the flip-chip DUV LED are shown in Fig. S1(a) and (b) in ESI,[†] respectively. A Keysight B1500 semiconductor parameter analyzer was employed to measure the current–voltage (I – V) characteristics of the flip-chip DUV LEDs for high- and low-current measurements. A calibrated integrating sphere and a spectrometer (Ocean Optics USB2000) were used to measure the output power and the emission spectra at room temperature. Moreover, the surface morphology of the AlGaIn/AlGaIn MQWs was measured by atomic force microscopy (AFM, Bruker Dimension Icon).

3. Results and discussion

The fabricated DUV LEDs with AlGaIn barrier grown at 900, 400, and 200 nm h^{-1} are denoted as BG900, BG400, and BG200,

respectively. Fig. 1 reveals the forward I – V characteristics and dynamic resistances of the AlGaIn-based DUV LEDs with different barrier growth rates. BG900, BG400, and BG200 have forward voltages (V_f) of 5.994, 5.915, and 5.948 V at 100 mA, respectively. The V_f of the DUV LEDs decreased slightly after the decrease of the AlGaIn barrier growth rate. In addition, the dynamic resistances at 350 mA of DUV LEDs with different AlGaIn barrier growth rates were in the range of 19.6–19.8 Ω . The inset of Fig. 1 shows the reverse I – V curves of the DUV LEDs, and the reverse leakage currents at -15 V of BG900, BG400, and BG200 were -0.2 , -1.15 , and $-2.49 \mu\text{A}$, respectively. The reverse leakage current at -15 V increased with the decreasing AlGaIn barrier growth rate. By reducing the AlGaIn barrier growth rate, the forward I – V changed slightly, but the reverse leakage current increased. The cause of the increased reverse currents of DUV LEDs with low AlGaIn barrier growth rates remains unclear.

The light output power–current (L – I) and external quantum efficiency–current (EQE– I) curves of the DUV LEDs with different AlGaIn barrier growth rates are presented in Fig. 2(a). The light output powers of all the DUV LEDs increased initially with the current and then reached the maximum output power at certain currents. The light output powers and EQEs of the DUV LEDs increased with decreasing AlGaIn barrier growth rate for all the driving currents. The 100 mA light output powers of BG900, BG400, and BG200 were 10.01, 14.01, and 15.51 mW, and the corresponding EQEs were 2.26%, 3.16%, and 3.49%, respectively. The 100 mA light output power enhancement was 40% by reducing the barrier growth rate from 900 nm h^{-1} to 400 nm h^{-1} , and the 100 mA light output enhancement could reach the largest value of 55% when the barrier growth rate was reduced to 200 nm h^{-1} . The enhancements of the maximum light output power were 55% and 83% for the barrier growth rates of 400 and 200 nm h^{-1} , respectively, compared with the barrier growth rate of 900 nm h^{-1} . The driving currents for the maximum light output power increased with the decrease of the barrier growth rate. The peak EQEs of BG900, BG400, and BG200 were 2.48%, 3.87%, and 4.27%, respectively. The corresponding improvements of EQE were 56% and 73% for the reductions of the barrier growth rates from 900 nm h^{-1} to 400

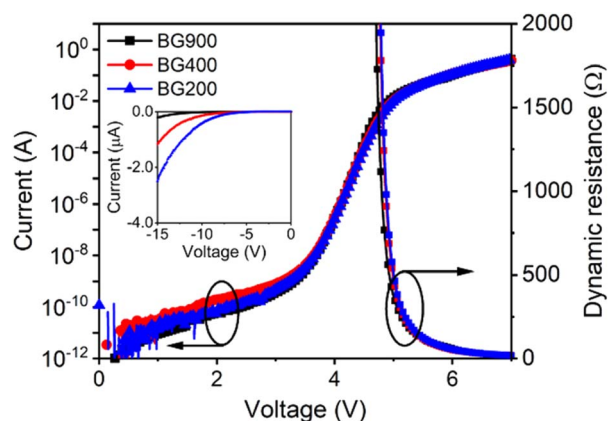


Fig. 1 Semi-log forward I – V curves of the DUV LEDs varying AlGaIn barrier growth rate. The inset of figure is linear reverse I – V curves.



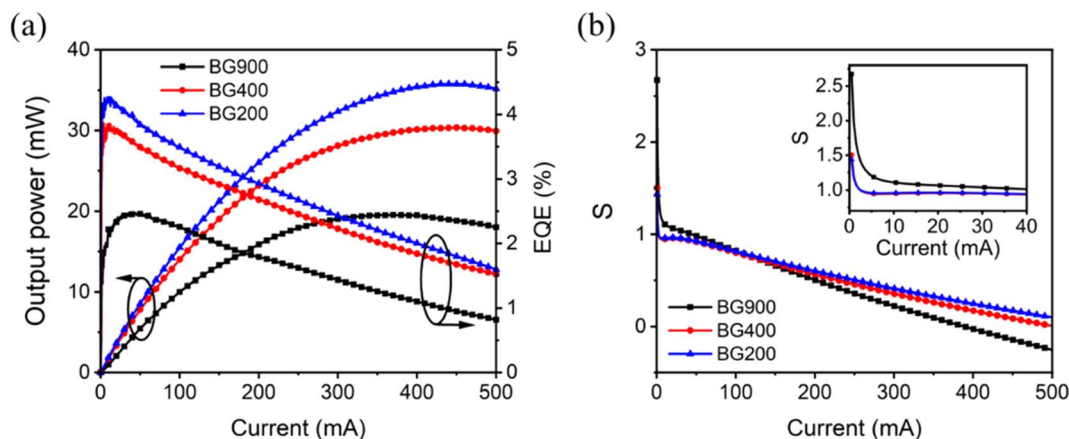


Fig. 2 (a) Light output power–current (L – I) and external quantum efficiency–current (EQE– I) curves of the DUV LEDs varying AlGaIn barrier growth rate. (b) S –current (S – I) curves of the DUV LEDs varying AlGaIn barrier growth rate. The inset of (b) is S – I curves at low injection current.

and 200 nm h^{-1} , respectively. Furthermore, the efficiency droops (defined as $1 - \text{EQE}_{450\text{mA}}/\text{EQE}_{\text{peak}}$) of the DUV LEDs were improved by decreasing the barrier growth rate. The efficiency droops of BG900, BG400, and BG200 were 62%, 57%, and 58%, respectively. The enhancements of the light output power and EQE of the DUV LEDs could be attributed to the quality improvement of the low AlGaIn barrier growth rate.

The L – I curves were further analysed by $\text{dlog } L/\text{dlog } I$ (ref. 44) (S is defined as the differential of the $\log L$ – $\log I$ curve) to obtain the S – I curves to further elucidate the dominant carrier recombination process in the DUV LEDs with different AlGaIn barrier growth rates. BG400 and BG200 presented lower S values than BG900 in the low injection current region (<40 mA), as presented in the inset of Fig. 2(b). The reduction of the S value in the low current region implies that the non-radiative recombination *via* defect was suppressed by lowering the barrier growth rate. In the mid and high injection current regions (>150 mA) in Fig. 2(b), the S values of BG400 and BG200 were larger than that of BG900, and all these S values were less than 1. The dominant carrier recombination process in this current range would be the carrier overflow, and a low S value implies severe carrier overflow. Therefore, a high AlGaIn barrier growth rate would have a large carrier overflow. A low AlGaIn barrier growth rate would have good carrier confinement in AlGaIn/AlGaIn MQWs, which would effectively reduce the carrier overflow process and improve the efficiency droop.

The surface morphology of AlGaIn can be changed by the growth conditions. The high growth rate induced 2D nucleation in the steps and the AlGaIn growth tended to be in 3D growth mode.⁴³ The high growth rate might increase the surface roughness of the AlGaIn barrier. Fig. 3 shows the AFM images of the MQWs with different AlGaIn barrier growth rates. The root mean square (RMS) surface roughness (R_q) of the AlGaIn/AlGaIn MQWs increased from 3.95 nm to 6.94 nm when the AlGaIn barrier growth rate was increased from 200 nm h^{-1} to 900 nm h^{-1} . The high growth rate increased the surface roughness of the AlGaIn barrier. This phenomenon might deteriorate the interface quality of the AlGaIn/AlGaIn MQWs and weaken the IQE. Moreover, the high growth rate of the AlGaIn barrier might

enhance the inhomogeneity of the Al composition of the barrier, which might also decrease the IQE. Given the fixed ammonia molar flow during the growth of MQWs, the reduction of the AlGaIn barrier growth rate also implies the increase of the V/III ratio (from 1900 to 6300) in AlGaIn growth. Bryan *et al.*⁴² reported that the high V/III ratio during the growth of AlGaIn/AlGaIn MQWs could suppress the midgap defect luminescence due to the reduction of the point defects. Therefore, the AlGaIn barrier grown at low growth rate could have a better surface quality and fewer defects than that grown at high growth rate. These phenomena could result in the enhancement of the 100 mA light output power and the reduction of the S value in the low current range of DUV LEDs. Moreover, the improved interfaces of AlGaIn/AlGaIn MQWs due to a low AlGaIn barrier growth rate would improve the MQWs' carrier confinements, which could suppress the carrier overflow and enhance the S value of DUV LEDs in the mid and high current regions.

To further understand the impacts of the AlGaIn barrier growth rate on the optical properties, far-field emission pattern and polarization measurements were taken. The far-field emission patterns of the DUV LEDs are presented in Fig. 4. The DUV LED with the highest growth rate, BG900, showed the lowest light intensity at each measured angle. The light intensities were augmented at all angles with the reduction of the barrier growth rate. The increases in light intensity in the far-field emission patterns are consistent with the enhancements of the light output power for the DUV LEDs with decreasing barrier growth rates. In addition, the enhancement of the light intensity at 90° was higher than those at 30° and 150° . The reduction of barrier growth rate primarily enhanced the normal emission and led to a shrink in the emission angles (the angle width at half maximum intensity) of the DUV LEDs. The normal light intensity to lateral light intensity ratio of the DUV LEDs might be related to the strain in the AlGaIn/AlGaIn MQWs due to the band structure change. By reducing the barrier growth rate, the surface roughness of the MQWs was diminished, and the strain in the MQWs might be altered at the same time. The large enhancement of the normal light intensity of the DUV LEDs

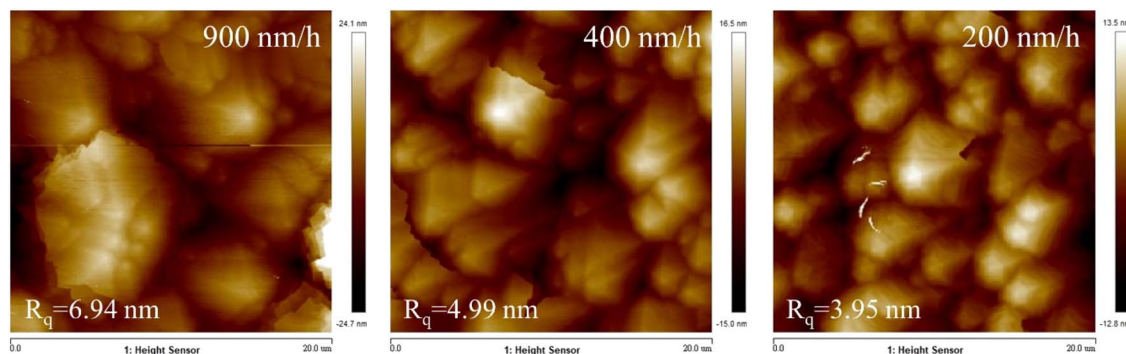


Fig. 3 AFM images of the DUV LEDs varying AlGaIn barrier growth rate.

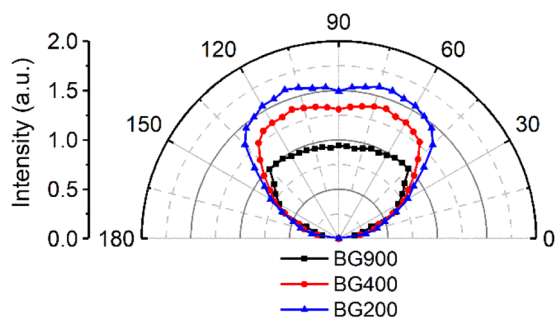


Fig. 4 Far-field emission patterns of the DUV LEDs varying AlGaIn barrier growth rate.

with low barrier growth rates might be attributed to the change of the strain in the MQWs.

The light emission of the AlGaIn-based DUV LEDs contained both TE- and TM-polarized lights, which originated from different band-to-band transitions. Many studies reported that the TE- and TM-polarized lights of AlGaIn-based LEDs mainly propagate in the normal (perpendicular to the c -axis, $E \perp c$) and lateral directions (parallel to the c -axis, $E \parallel c$), respectively.^{40,41} Given that the AlGaIn barrier growth rate affected the normal light intensity, polarization-dependent electroluminescence (EL) was performed to investigate the change of the TE- and TM-polarized lights of the DUV LEDs. A spectrometer was used to measure the EL spectrum of the sample at an angle of 10° to the bottom surface of the sample. The TE- and TM-polarized EL emissions could be distinguished by the polarizer (a Glan-Taylor prism) inserted between the lenses. The EL spectra of the TE- and TM-polarized lights of all the DUV LEDs are provided in Fig. S2 in the ESI.† The degree of polarization (DOP) is defined as $DOP = (I_{TE} - I_{TM}) / (I_{TE} + I_{TM})$, where I_{TE} and I_{TM} are the integrated intensities of the TE- and TM-polarized EL spectra, respectively. Fig. 5 shows that the DOP in the DUV LEDs varied with the AlGaIn barrier growth rates. The DOP increased with decreasing AlGaIn barrier growth rate. The result indicates that the low AlGaIn barrier growth rate augmented the TE-polarized emission in the DUV LEDs. Zhang *et al.*⁴⁵ reported that the compressive strain in the AlGaIn/AlGaIn MQWs could enhance the TE-polarized light emission. With the decrease of the AlGaIn barrier growth rate, the increase of DOP could be attributed to the increase of the compressive strain in the MQWs. Given that

the low AlGaIn barrier growth rate enhanced the TE-polarized emission of the DUV LEDs, the normal light intensity of the far-field pattern was significantly increased, and the emission angle was also shrunk, as shown in Fig. 4.

To understand how the growth rate affected the AlGaIn barrier, the AlN (104) X-ray diffraction reciprocal space mapping (RSM) was performed on the sample of a 400 nm-thick $Al_{0.7}Ga_{0.3}N$ barrier layer grown with different growth rates to analyse the strain. The detailed epitaxial structure is presented in Fig. S3.† The structure from AlN to n-AlGaIn layers was retained so that the situation of strain before growing MQWs was the same as that in the DUV LED structure. The AlN (104) RSMs of AlGaIn barrier layers grown with different growth rate are presented in Fig. 6, and it was found that the contour lines of the AlGaIn barrier layer shifted along the relaxation line, which meant that the strain was partially relaxed. All AlGaIn barrier layers grown with different growth rates showed partial relaxation. According to the RSM data, the calculated Al composition, in-plane strain (ϵ_a), and relaxation of in-plane strain (R_a) of AlGaIn barrier layers grown with different growth rates are summarized in Table 1. The ϵ_a and R_a are defined as

$$\epsilon_a = \frac{a_{\text{actual}} - a_{\text{relaxed}}}{a_{\text{relaxed}}}$$

and

$$R_a = \frac{a_{\text{actual}} - a_{\text{strained}}}{a_{\text{relaxed}} - a_{\text{strained}}}$$

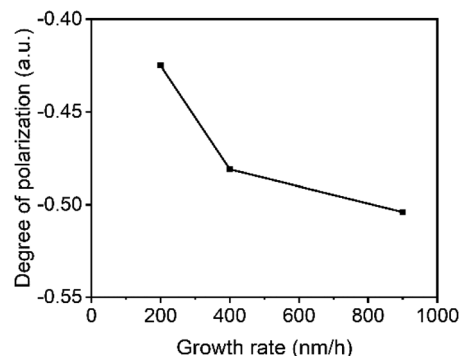


Fig. 5 Dependence of DOP at 100 mA current on AlGaIn barrier growth rate.



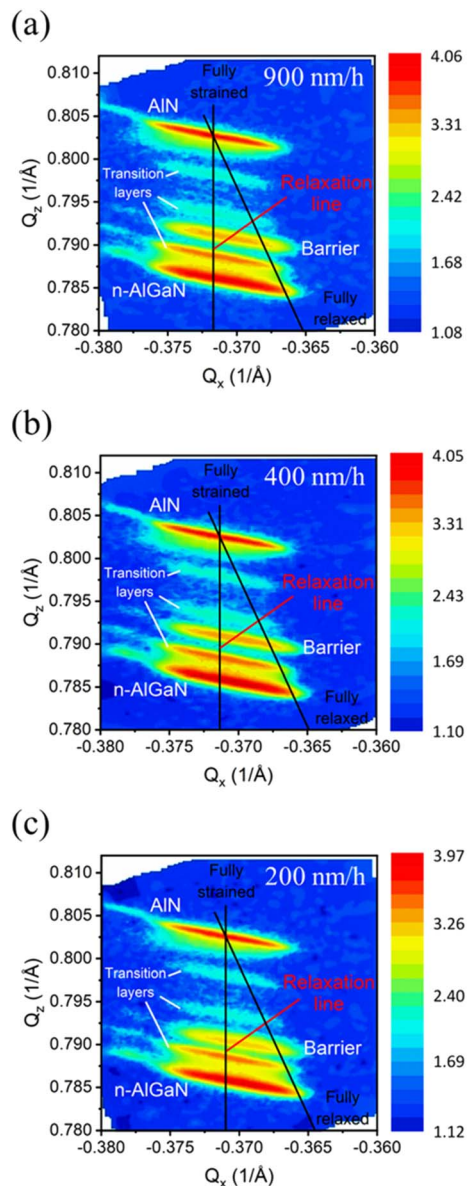


Fig. 6 The reciprocal space mappings of AlN (104) reflection plane of barrier layers with growth rate (a) 900, (b) 400, and (c) 200 nm h⁻¹.

where a_{actual} is the actual measured lattice constant, a_{relaxed} is the fully relaxed lattice constant, and a_{strained} is the fully strained lattice constant. The lattice constant a_{strained} is set to be a_{AlN} under the condition of pseudomorphic growth on AlN buffer layer. In almost the same Al content, the value of R_a

increased with increasing the AlGa_N barrier growth rate. In other words, the AlGa_N barrier grown at low growth rate tended to sustain the in-plane strain. Therefore, the AlGa_N barrier grown at high growth rate would be prone to relax its lattice constant compared to the AlGa_N barrier grown at low growth rate. Furthermore, the surface morphology of MQWs changed with varying barrier growth rates, as presented in Fig. 3. This phenomenon implied that there could be a change of strain even at the 60 nm-thick total barrier thickness in MQWs. Therefore, with lowering the barrier growth rate, the compressive strain in the barrier increased, and the lattice constant of the barrier shrank compared to the AlGa_N barrier grown at high growth rate. The well layer on the AlGa_N barrier grown at low growth rate was under larger compressive strain than the well layer on the AlGa_N barrier grown at high growth rate. The compressive strain altered the band structure of MQWs, so the TE-polarized emission was enhanced, and the DOP in the emission increased.

Finally, we compared our results to several previous reports.^{46–48} The flip-chip DUV LEDs with similar emission wavelengths are listed in Table 2. Zhang *et al.* proposed a dislocation filter (DF) layer to improve the unfavourable step-bunching morphology of AlN layer grown at a high growth rate.⁴⁶ The DF layer made a transition from step-bunching to step-flow morphology, which made it feasible to grow high quality AlN at high growth rate. Hu *et al.* proposed a superlattice electron deceleration layer (SEDL) to improve the performance of DUV LED.⁴⁷ The SEDL could decrease the electron injection current density and increase the hole injection current density. Hence, the radiative recombination rates in MQWs were raised by SEDL. Sun *et al.* reported the performance of DUV LED grown on nano-patterned sapphire substrates (NPSS) in combination with the optimization of V/III ratio and Si doping in MQWs.⁴⁸ They also found that the IQE of MQWs was improved from 56.2% to 70.3% when the V/III ratio was increased from 500 to 1000. However, further increasing V/III from 1000 to 2000 in MQWs slightly reduced the IQE from 70.3% to 69.2%. This phenomenon could be attributed to the degradation of crystal quality of MQWs under high V/III ratio. Even if the AlGa_N MQWs were grown at high V/III ratio, the crystal quality was also important. In our study, the V/III ratio was increased by lowering the growth rate of the AlGa_N barrier. According to the AlN (104) RSM results, the AlGa_N barrier grown at high growth rate tended to relax the strain, and the relaxation of strain usually caused defects. In other words, the barrier grown at low growth rate tended to sustain the strain, which caused fewer defects. Therefore, by lowering the growth rate, the V/III ratio

Table 1 The calculated Al compositions, in-plane strains, and relaxation of in-plane strain of barrier layer grown with different growth rates

Growth rate of barrier (nm h ⁻¹)	Al composition (%)	In-plane strain, ϵ_a (%)	Relaxation of in-plane strain, R_a (%)
900	69.9	−0.46	40.1
400	70.2	−0.48	37.6
200	69.5	−0.53	31.4



Table 2 The performances of the AlGaIn-based DUV LEDs

Ref.	Techniques	Structure of LED chip	Wavelength (nm)	MQWs pairs	Maximum EQE (%)	Maximum power enhancement (%)
46	Dislocation filtering (DF) layer	Flip-chip	276.6	5	2.28	40.4
47	Superlattice electron deceleration layer (SEDL)	Flip-chip	275	5	3.43	200
48	Optimization of V/III ratio and Si doping in MQWs and nano-patterned sapphire substrates (NPSS)	Flip-chip	276.1	10	4.01	n/a
This work	Reduction of barrier growth rate	Flip-chip	279	5	4.27	83

was increased without degrading the crystal quality of the AlGaIn barrier. Both effects were beneficial to the optical properties of MQWs. Our DUV LEDs with AlGaIn barrier grown at low growth rate presented the maximum EQE of 4.27% and the maximum power enhancement of 83%, which were comparable to the previous reports. Furthermore, the DOP of DUV LEDs was increased by reducing the growth rate of the AlGaIn barrier in MQWs.

4. Conclusions

In summary, the growth conditions of the AlGaIn barrier in the AlGaIn/AlGaIn MQWs presented crucial influences on the light output power of the DUV LEDs. The reduction of the AlGaIn barrier growth rate improved the surface roughness of the AlGaIn/AlGaIn MQWs, and the smooth surface could be favourable to enhancing the IQE. Furthermore, the accompanying high V/III ratio during the growth could lessen the AlGaIn barrier defects. The light output power enhancement could reach 83% when the AlGaIn barrier growth rate was reduced from 900 nm h⁻¹ to 200 nm h⁻¹. Furthermore, lowering the AlGaIn barrier growth rate enhanced the normal light intensity in the far-field emission patterns and increased the DOP in the DUV LEDs. The enhanced TE-polarized emission implies that the strain in the MQWs was compressive through the decrease of the AlGaIn barrier growth rate.

Conflicts of interest

There are no conflicts to declare.

Acknowledgements

We extend our gratitude to the National Science and Technology Council, Taiwan for their financial support under Grant Numbers: MOST 111-2221-E-006-033-MY3, MOST 107-2221-E-006-186-MY3, MOST 110-2221-E-006-153 and MOST 110-2218-E-006-025-MBK. We would also like to thank the core facility centre of the National Cheng Kung University for the support of the SEM observation and ICP etcher.

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