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Thermal degradation of the bulk and interfacial traps at 85 °C in perovskite photovoltaics†

Alan Jiwan Yun,^a Seokjoo Ryu,^a Jiheon Lim,^a Jinhyun Kim^b and Byungwoo Park^{*,a}

The facile formation of defects in halide perovskite has recently been regarded as the main bottleneck for both the efficiency and stability of perovskite solar cells (PSCs). Therefore, understanding and controlling defects and traps in PSCs is essential to achieving stable devices. Herein, the thermal degradation of perovskite solar cells at 85 °C is studied in terms of electronic traps and device performance, of which the correlations are discussed. In particular, the shifts and changes in both energetic and spatial distributions of electronic defects are observed by capacitance plus impedance analyses under thermal stress. As the energy level and density of deep traps are quantitatively investigated, both the relaxation and degradation of the traps are identified at different timescales. Additionally, the trap densities are individually traced by positions during thermal degradation, where distinct evolutions are visualized. Notably, the traps are measured dominant at the interface between the perovskite and electron-transport layer (ETL). However, LiF incorporation mitigates the electronic traps by an order of magnitude at both interfaces throughout the thermal degradation, indicating that LiF incorporation reduces the initial trap density and suppresses the further formation of traps near the interfaces.

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1. Introduction

Despite descent performance and cost efficiency as photovoltaic materials, organometal halide perovskite currently inheres a limitation in terms of the easy formation of defects owing to its solution-processed fabrication.^{1–9} Although various defects and traps are expected in terms of their origins and energy/depth positions, they are commonly accused of causing anomalous phenomena by hindering the generation and transport of photocarriers in PSCs.^{7,9–20} Moreover, these defects are reported as the prime origin that deteriorates the long-term stability of PSCs under practical conditions, *i.e.* 1 sun illumination and/or 85 °C temperature.²¹ Owing to the low activation energy of ion migration, the defects in the perovskite not only cause microscopic degradation of materials, such as phase transition and decomposition, but also affect carrier dynamics and energy levels at the interfaces.^{18,22–25}

Therefore, controlling the defects either in the bulk or at the interfaces has recently been considered alpha and omega for commercializing PSCs. Various strategies have been ardently reported to passivate the traps and defects of polycrystalline perovskite with great improvements in stability against air, light, and heat.^{26–35} For example, Jeong *et al.* modified perovskite with pseudo-halide anion formate (HCOO[−]) to control the anion vacancies at the grain boundaries and surfaces of the film.³⁶ Li *et al.* doped alkali fluorides into the perovskite and achieved 1000 h stability at 85 °C retaining over 80% of the initial efficiency with the spiro-OMeTAD-based hole-transport layer (HTL), confirming that the degradation of PSC depends more on perovskite defects than on the electrode materials.³⁷ Several recent tactics utilize additional ionic species into the perovskite without noticeable modification of its macroscopic properties, which leaves the insight that adjusting a morsel of defects is what significantly matters in forbidding the degradation of PSCs.^{22,36–42} Hence, studying the degradation of nanoscale defects is becoming increasingly important during the degradation of PSC, and systematic investigations on electronic trap properties must be conducted to achieve efficient and stable photovoltaic materials/devices.^{43–46}

In this study, the thermal degradation of PSCs is scrutinized in three aspects: photovoltaic performance, trap density of states (tDOS), and depth-profile trap density. The measurements were conducted for two-type solar cells, reference and

^aDepartment of Materials Science and Engineering, Research Institute of Advanced Materials, Seoul National University, Seoul 08826, Korea.

E-mail: byungwoo@snu.ac.kr; Fax: +82-2-885-9671; Tel: +82-2-880-8319

^bDepartment of Chemical and Materials Engineering, The University of Suwon, Hwaseong 18323, Korea

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metal–fluoride modified, before and after 85 °C stored over 20, 50, 120, 250, 400, 600, and 1000 h. By adopting both impedance and drive-level capacitance profiling (DLCP) analyses, the energetic and spatial distributions of the electronic traps in PSC devices are examined. Shifts and evolution of the traps are quantitatively observed in terms of energy level and spatial position as thermal degradation occurs, and their mechanisms and correlations with the photovoltaic parameters are discussed in this study.

2. Results and discussion

To investigate the temporal evolution of three aspects of PSCs, solar cells were prepared by fabricating two types of active materials: bare $\text{Cs}_{0.05}(\text{FA}_{0.83}\text{MA}_{0.17})_{0.95}\text{Pb}(\text{I}_{0.83}\text{Br}_{0.17})_3$ (CsFAMA) and LiF-doped CsFAMA (CsFAMA:LiF) (Fig. 1(a)). With the triple-cation perovskite (quite stable at 85 °C), it is expected that the changes in nanoscale trap properties essentially determine the overall degradation of solar cells, while the micro-/macro-scale material degradation is insignificant.^{47–50} Additionally, to avoid the degradation of electrodes being a determining factor for device stability, thermally stable PTAA was adopted as a hole-transporting material instead of spiro-OMeTAD.⁴⁵ The incorporation of alkali fluorides has been reported to effectively improve the long-term stability of PSCs by controlling the defects/traps in the bulk and at the grain boundaries/interfaces.^{30,37,45} In particular, the LiF-incorporated perovskite does not exhibit a significant transformation in morphology or crystal phase at 85 °C.^{30,45} The performances of both CsFAMA and CsFAMA:LiF are compared in Fig. 1(b), showing slight increases in short-circuit current density (J_{SC}), open-circuit voltage (V_{OC}), and fill factor (FF), while the J - V hysteresis effect decreases in CsFAMA:LiF. The improved photovoltaic process in CsFAMA:LiF is also certified by its enhanced quantum efficiency and steady-state current, as shown in Fig. S1.† Moreover, after 1500 h of storage at 85 °C, the CsFAMA:LiF device retains up to 91% of its initial PCE, with the bare CsFAMA showing 81% retention (Fig. 1(c)). Notably, the presence of LiF successfully restrains the thermal

degradation of solar cells, and further measurements and analyses were conducted on the devices during the thermal stability tests to significantly understand the degradation mechanisms of PSCs with/without LiF.

First, the change in each photovoltaic parameter was traced as a function of the post-annealing time at 85 °C. The parameters demonstrated in Fig. 2(a–e) are extracted from the J - V characteristics of more than 10 devices each for CsFAMA and CsFAMA:LiF to certify that the results are reproducible. As shown in Fig. 2(a–e), V_{OC} , J_{SC} , PCE ($= \eta$), and FF exhibit better retention in CsFAMA:LiF than in CsFAMA. (Note that the PCE retention in Fig. 1(c) is better than that in Fig. 2(c), which is attributed to the different exposures to air during each measurement.) However, the J - V hysteresis index ($\text{HI} \equiv (\eta_{\text{rev}} - \eta_{\text{for}})/\eta_{\text{rev}}$) decreases at the early stage, but soars after ~ 250 h, indicating down and up behaviors in the population of mobile ionic defects.^{9–11,16,30,51} Interestingly, quite high HI is measured in CsFAMA after 1000 h, while that of CsFAMA:LiF remains low, which implies that LiF notably suppresses the heat-induced formation of mobile defects. In Fig. 2(f) for CsFAMA, the formation of PbI_2 is observed mostly near the ETL through cross-sectional SEM after 1000 h at 85 °C, while it is barely visible in CsFAMA:LiF. This is somewhat consistent with the previous report on the formation of PbI_2 at 85 °C, where the growth of PbI_2 impurities was suppressed by the LiF incorporation, observed by the plane-view SEM and XRD.^{30,45} However, no significant differences were identified by the cross-sectional SEM in the scale of hundreds of nanometers in this study.

To ensure that the stability variations originate from the redistribution of ions *via* migration, interdiffusion, *etc.*, the elemental depth profiling in the solar cell (Fig. S2†) was investigated before/after 85 °C annealing by secondary ion mass spectroscopy (SIMS). No significant changes in the distribution of constituents were observed after 250 h, and the additional Li^+ and F^- ions in the CsFAMA:LiF were majorly identified near the interfaces between the perovskite and charge-transport layers. Considering that the solar-cell performance strongly depends on the trap states at the interfaces, differences in the stabilities of the parameters (Fig. 2(a–e)) are

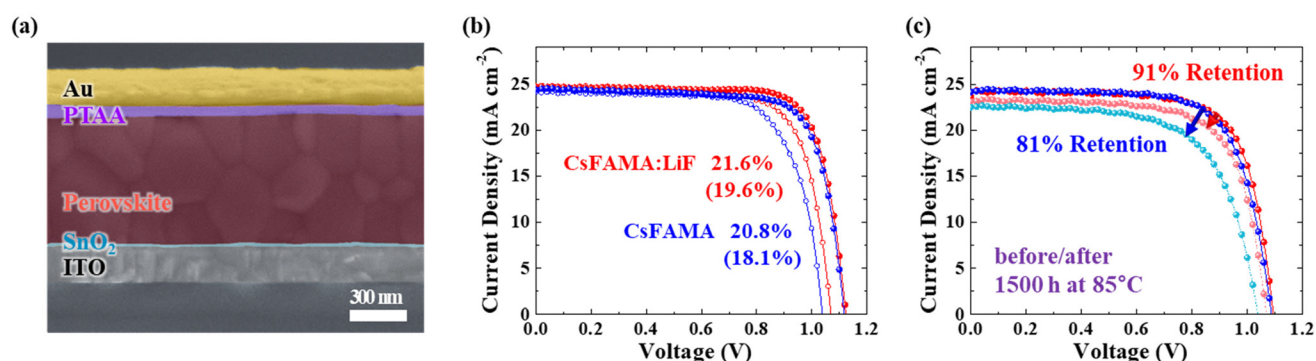


Fig. 1 Modification of the perovskite solar cell by LiF incorporation. (a) The architecture and dimension of the solar cell by SEM: ITO/ SnO_2 /perovskite/PTAA/Au. (b) The J - V curves of the champion devices with reverse scan (solid) and forward scan (open) and (c) the stability of the solar cells stored at 85 °C in N_2 for 1500 h, with (red) and without (blue) LiF.

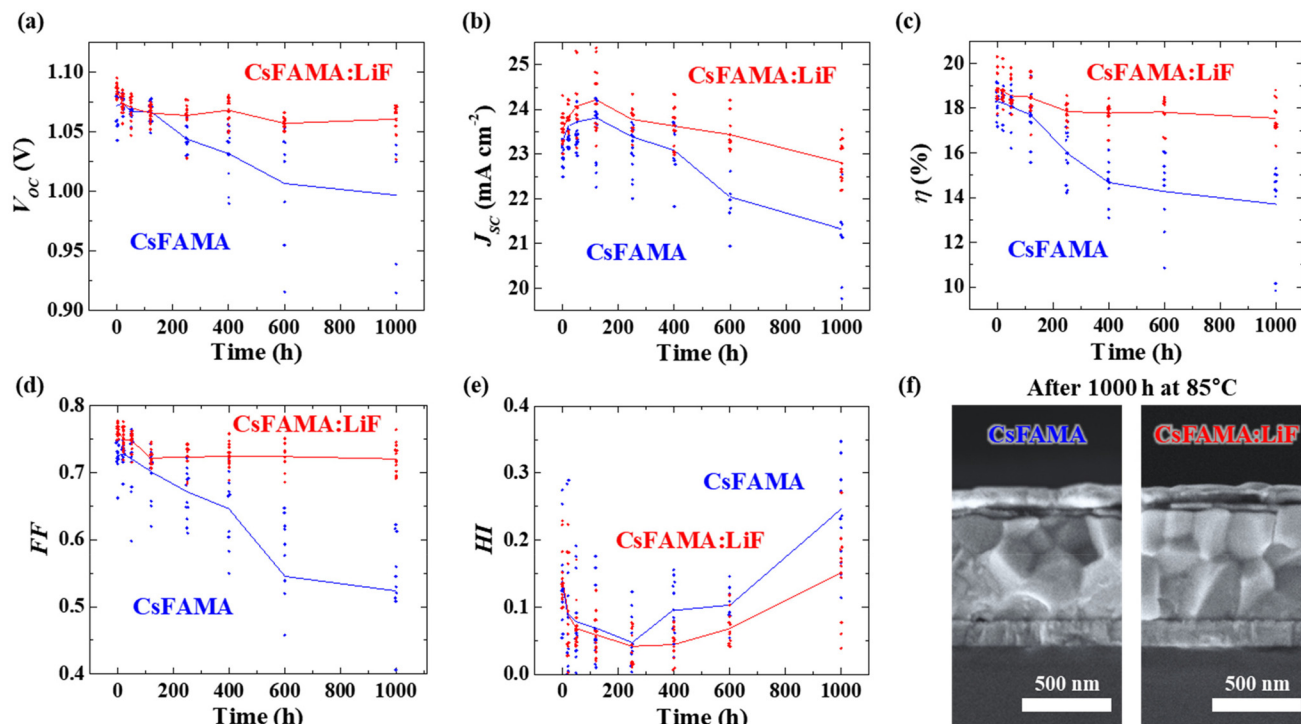


Fig. 2 Thermal stability of the photovoltaic parameters. (a) The open-circuit voltage (V_{oc}), (b) short-circuit current (J_{sc}), (c) power-conversion efficiency (η), (d) fill factor (FF), and (e) hysteresis index (HI) of the perovskite solar cells with/without LiF, as a function of post-annealing time at 85 °C. (f) Cross-sectional SEM images of the devices after 1000 h of the thermal test.

expected to be closely related to the trap formation and passivation near the interfaces without/with LiF.^{37,40,52–57}

To investigate the transformations of trap properties underneath the degradation of solar-cell performance, the impedance $Z(\omega)$ of the device is explicitly measured (Fig. S3†). Comparing the Nyquist plots of the PSCs without/with LiF as a function of the post-annealing time, it is noted that the radius of the second semicircle in the CsFAMA:LiF device remains stable even after 1000 h of heating. The second-semicircle radius indicates the recombination resistance of the carriers in the perovskite, where its decrease in the CsFAMA device implies the additional formation of electronically active defects.⁵⁸ Furthermore, as previously reported, the derivative of capacitance ($C^Z \equiv [1/i\omega Z(\omega)]_{\text{real}}$) with respect to the AC frequency delivers essential information about the trap density of states [tDOS $N_t^Z(\omega)$] in a diode^{37,52–54,59,60} (Fig. S4†). By adopting impedance analysis, the energy levels E_ω (with respect to the bandedge) and densities of deep/shallow traps are monitored as thermal degradation occurs, and these energetic trap-distribution spectra are demonstrated in Fig. 3(a). Even before the post annealing at 85 °C, the trap density in the deep-trap region ($E_\omega > 0.4$ eV) is almost half lower in CsFAMA:LiF than in CsFAMA (green symbols (0 h)). This certifies that LiF alloying successfully passivates the defects in solar-cell devices.

As thermal degradation proceeds, the trap density of states in both PSCs gradually shifts toward the bandedge with decreasing trap density in the deep-trap region (from green (0 h) to yellow (250 h)), which can be beneficial in terms of

photovoltaic performance.^{52,53,61,62} These favorable shifts of trap level and density, or the relaxations of traps, occur more immediately and drastically in CsFAMA:LiF. However, after a few hundreds of hours, the changes in trap level and density start to revert to the opposite direction with the increasing trap density and shift away from the bandedge (from yellow (250 h) to red (1000 h)). However, at 1000 h, the trap density of states ends notably shallow and reduces in CsFAMA:LiF more than in CsFAMA.

The phenomena observed by impedance spectroscopy for the evolution of tDOS $N_t^Z(\omega)$ during 1000 h are verified by fitting the trap levels and densities from ~20 individual devices (Fig. 3(b) and S5†). Two distinct evolutions are identified: the heat-induced relaxation of the traps (shift toward the bandedge and decreasing density) at the early stage from 0 to ~250 h, and the heat-induced degradation of the traps (shift away from the bandedge and increasing density) at the late stage from ~250 to 1000 h at 85 °C. This behavior of the trap properties somewhat resembles those of J_{sc} and HI illustrated in Fig. 2, where the enhancement at the early stage is followed by the gradual deterioration after ~200 h.

The relaxation of the electronic traps at the early stage of post annealing is more likely to be induced by local changes in chemical bonds among the atoms near the interfaces^{25,42,57,58,63} instead of ionic redistributions within the solar cell (Fig. S2†). With this viewpoint, the accelerated relaxation in CsFAMA:LiF suggests that excessive F^- and Li^+ ions at the interfaces may boost the elimination of interfacial defects

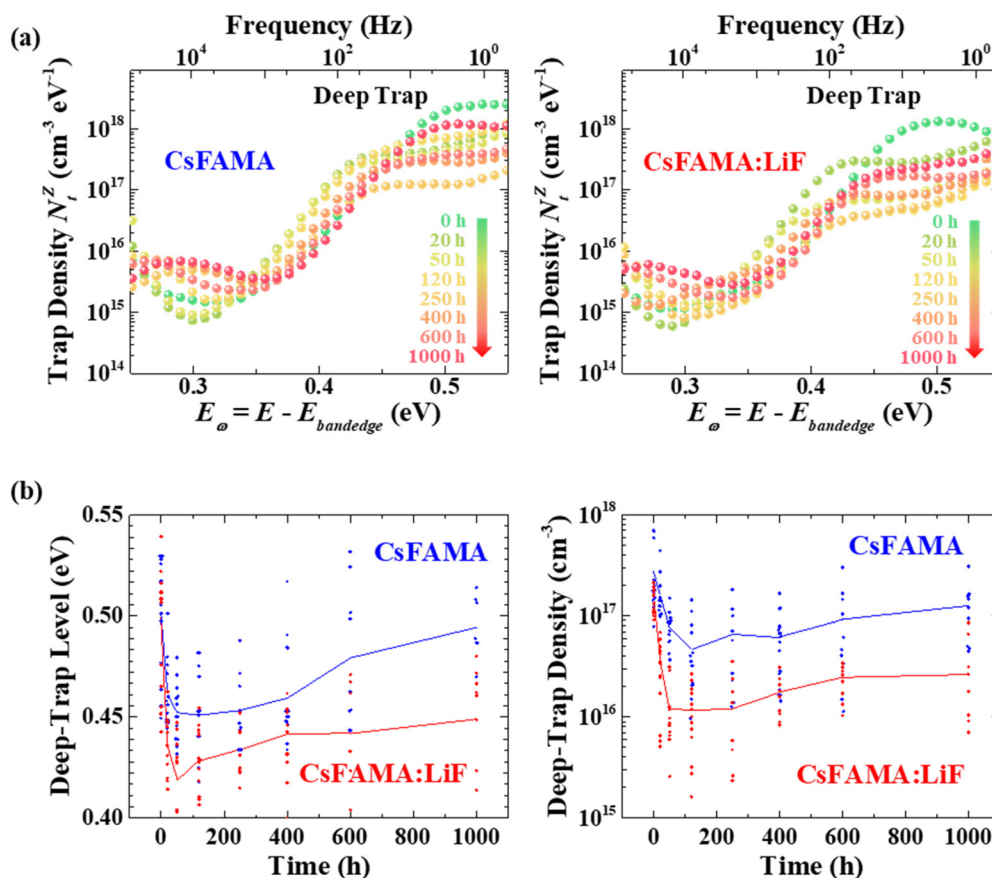


Fig. 3 Evolution of the electronic traps $N_t^Z(\omega)$ analyzed by impedance spectroscopy. (a) The spectra of the trap density of states [tDOS $N_t^Z(\omega)$] without and with LiF incorporation, aged under thermal stress at 85 °C. (b) The evolution of the deep-trap levels and integrated trap densities of PSCs, measured for ~ 20 devices.

at 85 °C, possibly by occupying the residual vacancies and/or passivating the dangling bonds, which are commonly reported behaviors of alkali-metal cations and fluoride anions.^{31,37,39} Moreover, the degradation of traps from ~ 250 to 1000 h is greatly suppressed by LiF (Fig. 3(b)). It should be noted that LiF forbids the additional formation of traps under thermal stress, but the underlying mechanisms must be further clarified.

As discussed above, the trap density of states in PSC showed complicated evolutions at 85 °C, and deeper investigation of the traps is needed with consideration of their origins and positions. Therefore, drive-level capacitance profiling (DLCP) was adopted to observe how traps evolve in terms of spatial position during thermal degradation. The DLCP analysis measures the capacitance of the device as a function of both DC bias and AC perturbation at a given frequency, and it allows derivation in the carrier concentrations as a function of spatial depth (Fig. S4†).^{6,18,64,65} The band diagrams based on the parameters of the solar cells utilized in this study are demonstrated in Fig. S6,†^{54,62,66–68} which schematically explains the significance of DC bias in the depth-profile trap analysis.

The depth-profile trap distributions obtained by the DLCP analysis [DLCP $N_t^C(x,\omega)$] are demonstrated in Fig. 4(a), which exhibits clear U-shaped spectra. It is assumed that the

measured concentration at high AC frequency ($f \cong 1$ MHz) contains only the free carriers in the device, and the trap density [$N_t^C(x,\omega)$] at low frequencies ($f \leq 100$ kHz or $E_\omega \geq 0.25$ eV) is estimated by subtracting the free carrier density from the total carrier concentration [$N_t^C(x,\omega) = N^C(x,\omega) - N_0^C(x,\text{MHz})$] (Fig. S4†).⁶ As thermal degradation proceeds, the bulk trap density consistently increases in both CsFAMA and CsFAMA:LiF. However, the evolutions of trap density at the interfaces include the ambiguity of growth and/or decay, depending on which sides the interfaces are at. To certify that the observation in Fig. 4(a) is the general case, ~ 10 devices for each type are analyzed and better monitored by mapping the spatial-distribution spectra in Fig. 4(b). Interestingly, as the devices are thermally degraded, the growth of the traps at the positions close to the interfaces ($\langle x \rangle \cong 75$ and 525 nm) is largely restrained in CsFAMA:LiF apparently owing to the LiF alloying. Consequently, the U-shape of trap distribution appears high and narrow in CsFAMA after degradation at 85 °C, while a relatively low and wide U-shape is identified in CsFAMA:LiF (Fig. S7†). It can be suggested that thinner layers of trap-rich regions are formed near the interfaces in CsFAMA:LiF, which could be responsible for its better charge-transfer functions through the interfaces from 85 °C stress.

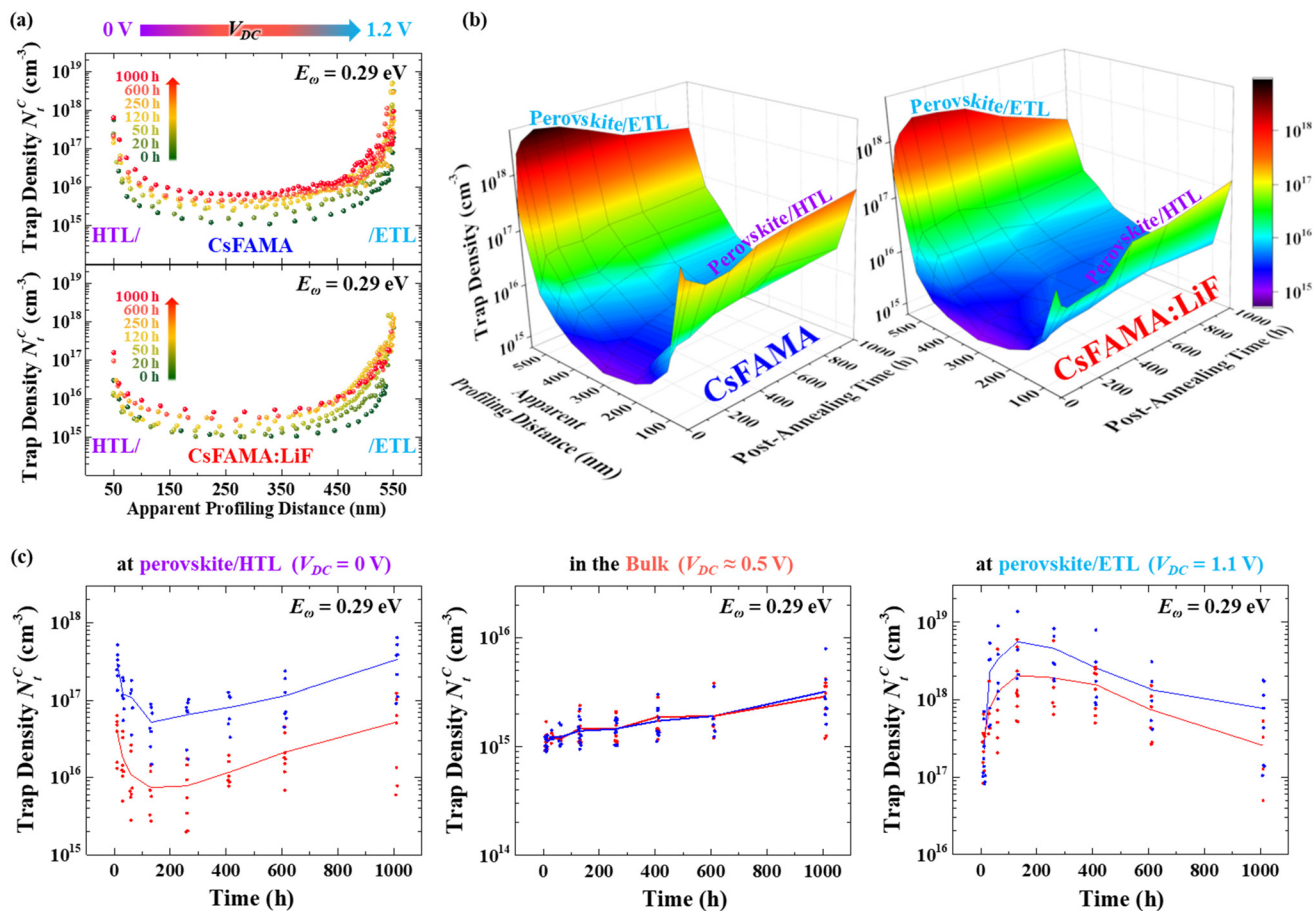


Fig. 4 Evolution in the spatial distribution of the electronic traps $N_t^C(x, \omega)$ analyzed by drive-level capacitance profiling. (a) Depth-profile trap density of the solar cells measured by the drive-level capacitance profiling analysis [DLCP $N_t^C(x, \omega)$] at 20 kHz, and (b) mapping of the trap distribution as a function of time during the thermal stability test at 85 °C. (c) The statistics of the trap densities measured at V_{DC} of 0, ~0.5, and 1.1 V. Data were collected from ~10 devices for CsFAMA and CsFAMA:LiF.

The statistical evolutions of the trap density at three representative positions (perovskite/HTL interface, bulk, and perovskite/ETL interface (Fig. S6†)) are individually traced, as depicted in Fig. 4(c) and S8.†^{6,18,69} The gradual decrease in $N_t^C(x, \omega)$ at the perovskite/ETL interface after 250 h may be attributed to the redistribution of traps, *i.e.* the migration of defects toward the perovskite/HTL interface. Additionally, the trap densities at perovskite/ETL are measured to be an order of magnitude higher than those at perovskite/HTL, which certifies that the defects at the perovskite/ETL interface are more dominant in quantity and their impacts on the stability of n-i-p PSC (Fig. 2(f)). However, the evolutions of trap densities $N_t^C(x, \omega)$ at the ETL and HTL interfaces show an opposite trend, which needs to be further explored. The DLCP analyses are also performed at various frequencies ($E_\omega = 0.25, 0.32$ and 0.36 eV (Fig. S8†)), and the evolutions of electronic traps at the given positions exhibit similar trends, as demonstrated in Fig. 4c ($E_\omega = 0.29$ eV).

The traps at the perovskite/HTL and perovskite/ETL interfaces are almost an order of magnitude lower in CsFAMA:LiF than in CsFAMA throughout 1000 h of post-annealing

(Fig. 4(c)). This confirms that LiF suppresses initial defect formation during the fabrication process, plays a role in healing residual defects, mitigates further degradation, and immobilizes additional defects at 85 °C. Moreover, the growths of the bulk trap densities do not show significant differences between CsFAMA:LiF and CsFAMA. The effects of the LiF incorporation are more confined at the interfaces, and the performance of photovoltaics mainly depends on the traps at the interfaces, where the densities of traps differ by orders of magnitude, rather than in the bulk.

The temporal analyses of the impedance and capacitance deliver a pile of unraveled information concerned with the 85 °C thermal degradation of PSCs. As discussed above, the transitions in the trap density of states and depth-profile trap distribution are observed, and their behaviors under thermal stress provide insights for effective strategies to control complex defects and traps. In addition, changes in some photovoltaic parameters are not fully explained by the evolution of the trap properties (from tDOS and DLCP) because the device performance is determined by the mixture of various factors in materials and interfaces simultaneously.

Moreover, the extended scientific messages are somewhat undisclosed under the possible measurement perturbation, which is currently a limitation of DLCP analysis methods for nanoscale trap properties. The observation of interesting phenomena in this study is expected to offer an important footstep for further studies on the evolution and degradation of electronic traps, which is key to further resolving the stability issues of hybrid perovskite-based photovoltaics.

3. Conclusions

The thermal degradations of CsFAMA and CsFAMA:LiF at 85 °C were systematically monitored for 1000 h in three individual aspects: photovoltaic performance, trap density of states, and depth-profile trap density. By performing impedance and DLCP analyses, the evolutions of the electronic traps are investigated in terms of energy level and depth position as a function of the post-annealing time. The heat-induced relaxation and degradation of traps are observed in this study, and the role of LiF in the middle of these phenomena is surmised. By tracing the shifts and changes in the trap distributions, F⁻ and Li⁺ ions are found to be involved in curing and immobilizing defects at the interfaces, where the defects tend to proliferate and shift toward the bulk. Although revealing the exact origins of the measured traps is still challenging owing to the absence of accurate analysis methods for the electronic defects in 3D nanoscales, the observed trap evolutions in both energetic and depth aspects offer an important step to unravel the underlying science beneath the degradation of perovskite solar cells.

4. Experimental section

Solar cell fabrication

The patterned ITO substrate (AMG) was cleaned by sonication in acetone, isopropyl alcohol, and deionized water for 20 min each. The substrates were treated with a UV-ozone cleaner (UVC-150, Omniscience) for 15 min. For the electron-transport layer (ETL), diluted SnO₂ nanoparticles in H₂O (Alfa-Aesar) were prepared with the addition of tetramethylammonium hydroxide (TMAH, Sigma-Aldrich) by 7.5 vol%. The SnO₂ ETL was deposited on the substrate by spin-coating the SnO₂ nanoparticles at 3000 rpm for 30 s and dried at 120 °C for 30 min on a hot plate. The surface of the 30 nm-thick SnO₂ nanoparticle layer was UV-ozone treated for 15 min.

0.065 M of cesium iodide (CsI, TCI Chemicals), 1.00 M of formamidinium iodide (FAI, Great Solar Laboratory), 0.20 M of methylammonium bromide (MABr, Great Solar Laboratory), 1.1 M of lead iodide (PbI₂, TCI Chemicals), and 0.2 M of lead bromide (PbBr₂, TCI Chemicals) were dissolved in a mixture of *n,n*-dimethylformamide (DMF, Sigma-Aldrich) and dimethylsulfoxide (DMSO, Sigma-Aldrich) (volume ratio of 4:1). For CsFAMA:LiF, 2.4 mM of LiF was added to the precursor solution. For the perovskite active

layer, the precursor solutions were spin-coated on the ITO/SnO₂ substrates with the spinning program: (1) 1000 rpm for 10 s and (2) 5000 rpm for 20 s sequentially. Anhydrous chlorobenzene (Sigma-Aldrich) was used as an antisolvent, 200 μL of which was dropped 5 s before the spinning program ended. The as-fabricated perovskite films were annealed at 100 °C on a hot plate for 30 min. For the polymer-based hole-transport layer (HTL), 17.5 mg of PTAA (33 kDa, MS Solutions) was dissolved in 1 mL of chlorobenzene (Sigma-Aldrich), and 6 μL of 4-*tert*-butylpyridine (tBP, Sigma-Aldrich) and 4 μL of bis(trifluoromethane)sulfonimide lithium salt (Li-TFSI, Sigma-Aldrich) solution (520 mg mL⁻¹ in acetonitrile) were added as dopants. The PTAA solution was deposited on the substrate by spin-coating at 3000 rpm for 30 s. Au was thermally evaporated onto the device, with the desired thickness of ~80 nm.

Characterization

Thermal stability was tested using a thermostat in an N₂-filled glove box. The devices stored at 85 °C in N₂ were cooled at room temperature for more than 2 h before the measurements and analyses. The voltage dependence of photocurrent density (*J*-*V*) and steady-state current density was measured using a potentiostat (CHI 604A, CH Instrument) under 1 sun illumination from a solar simulator (K-3000, McScience). A mask with an aperture of 0.09 cm² was utilized to control the active area during the *J*-*V* characterization. The voltage was swept in the reverse/forward directions from 1.2 to -0.1 V and *vice versa*, both at a rate of 100 mV s⁻¹. The solar simulator was calibrated for every measurement, and the solar cells were stabilized under AM 1.5 G for at least 1 min. The external quantum efficiency of the solar cell was measured using an incident photon-to-current efficiency measurement system (QEX7, CEXCI). The cross-sectional images of the perovskite solar cells were observed using a field-emission scanning electron microscope (Merlin Compact: Zeiss). For the depth profiling of elemental distribution, a time-of-flight secondary ion mass spectrometer (TOF-SIMS: ION-TOF) was utilized, Bi was used as a primary source, and Cs and oxygen were used as etching sources. Areas of the analysis and etching were 100 × 100 μm² and 500 × 500 μm², respectively. For the analysis of the trap density of states, the impedance of the device was measured using a potentiostat (Zive SP-1: WonATech), with AC perturbation of 20 mV amplitude and frequency ranging from 10⁻² to 10⁶ Hz. The impedance analysis was performed under short-circuit conditions without DC bias or illumination. Before the measurement, the samples were rested in the dark until the internal voltage dropped below 3 mV. Drive-level capacitance profiling (DLCP) was performed using an LCR meter (E4980A: Keysight). The capacitance of the solar cells was measured with varying DC and AC voltages from 0 to 1.1 V and from 5 to 60 mV, respectively. Each cycle was performed at frequencies of 1, 5, 20, and 100 kHz.

Author contributions

A. J. Y. and B. P. conceived and conceptualized this study. A. J. Y. planned and performed the experiments, and carried out the measurements and data analyses. S. R. helped the capacitance measurement and data analysis. J. L. helped the fabrication of the solar cells. J. K. and B. P. provided insights to write the manuscript. A. J. Y. and B. P. jointly wrote the manuscript. S. R., J. L., and J. K. helped correction of the manuscript. All authors discussed the results and contributed to the final manuscript.

Conflicts of interest

The authors declare no conflict of interest.

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