

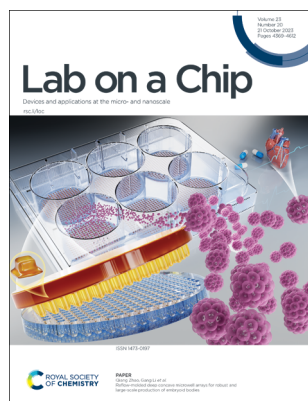
# Lab on a Chip

Devices and applications at the micro- and nanoscale  
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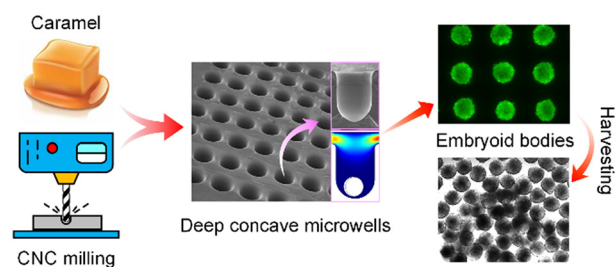
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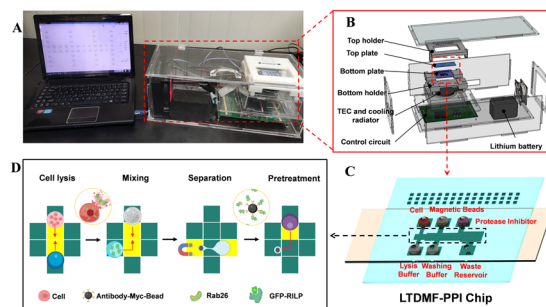
Xue Han, Qi Zhang, Hui He, Qiang Zhao\* and Gang Li\*



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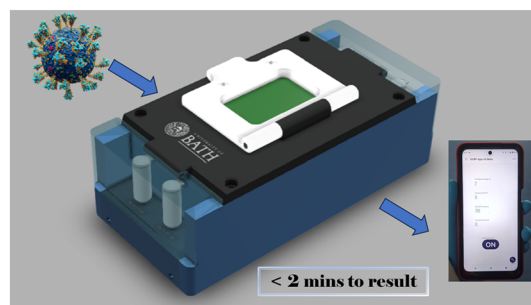


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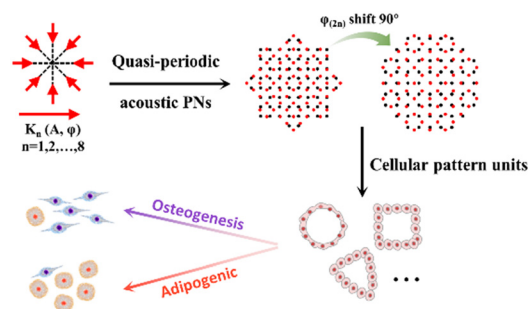
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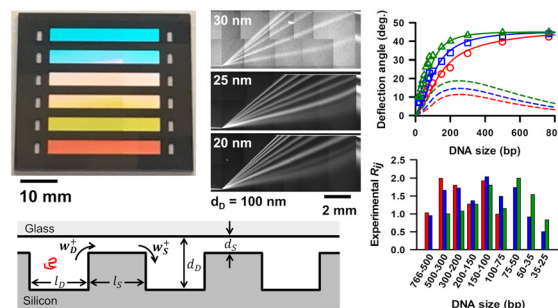
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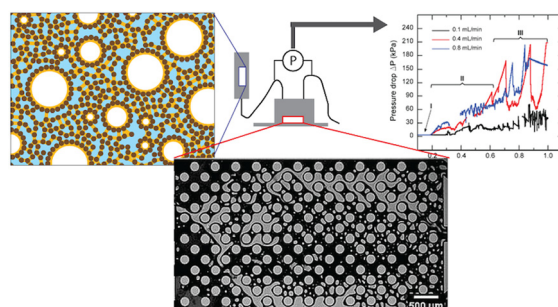
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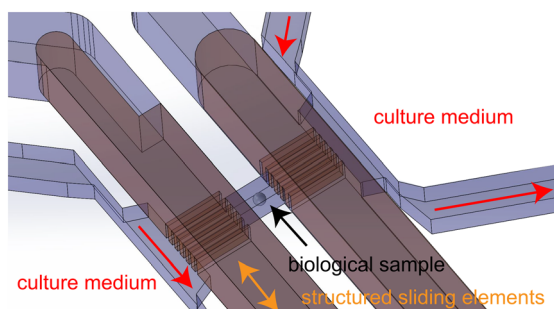
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Omotola Okesanjo, Guillaume Aubry, Sven Behrens,\* Hang Lu\* and J. Carson Meredith\*



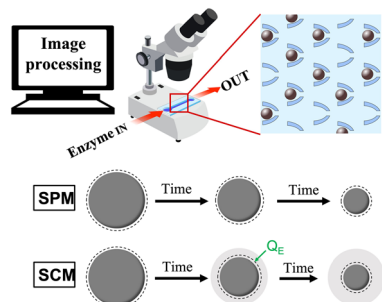
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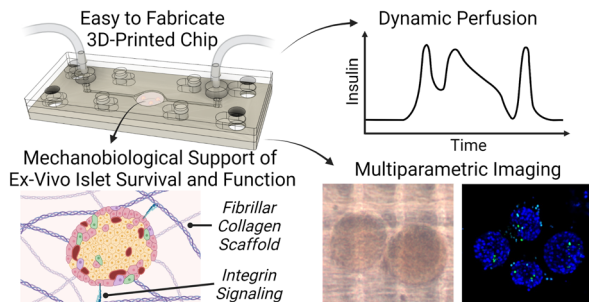
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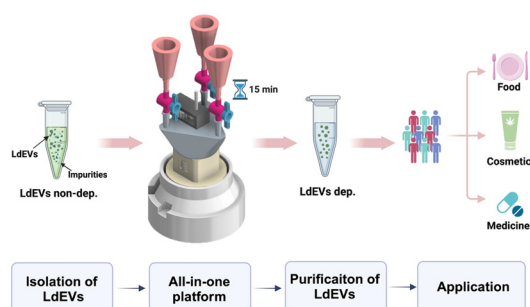
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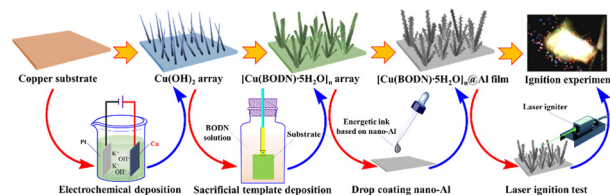


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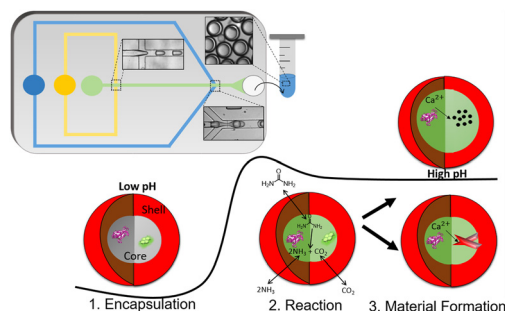
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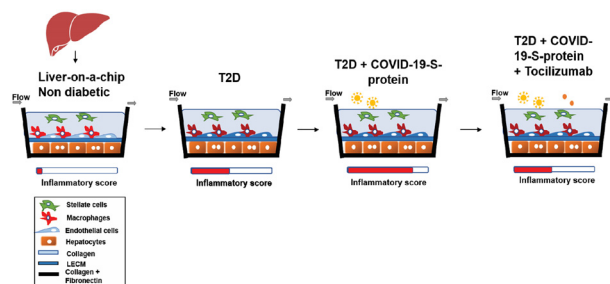
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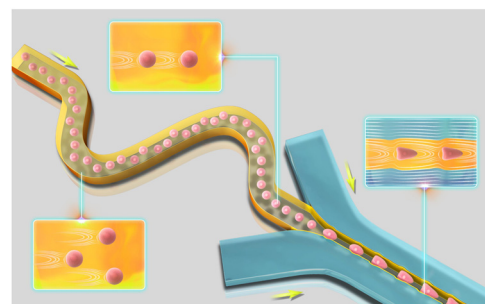
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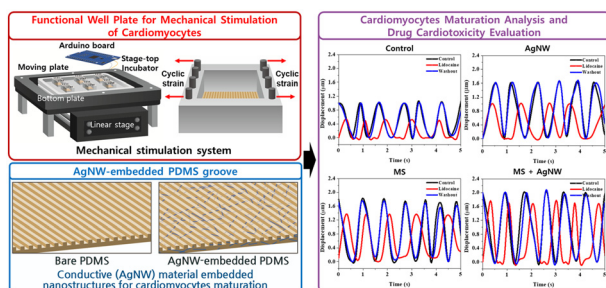
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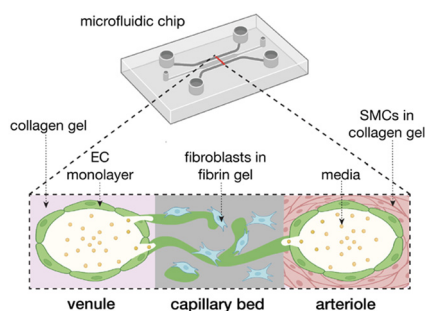
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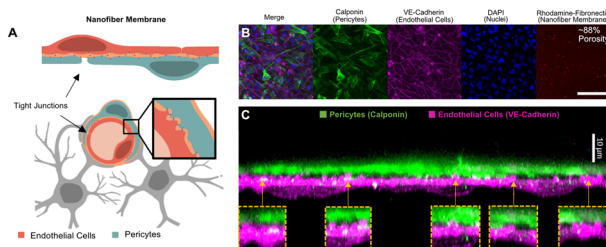
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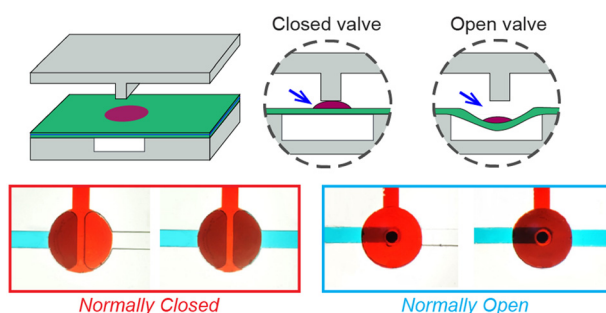
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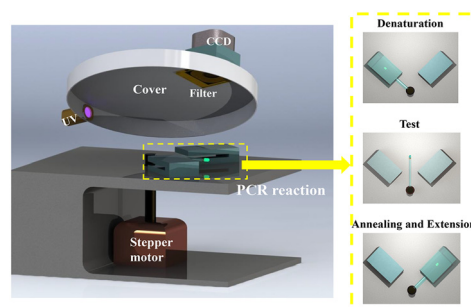


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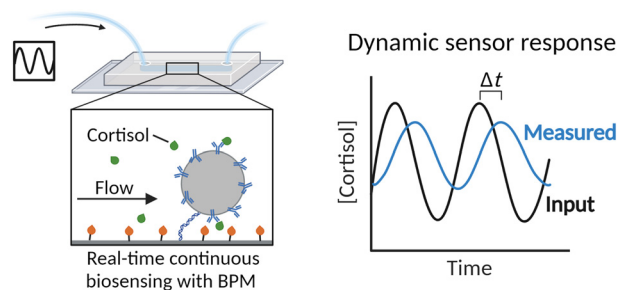
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