PAPER

Defect tolerant device geometries for lead-halide perovskites†

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The term "defect tolerance" is widely used in the literature to describe materials such as lead-halides perovskites, where solution-processed polycrystalline thin films exhibit long non-radiative lifetimes of microseconds or longer. Studies on defect tolerance of materials mostly look at the properties of the host material and/or the chemical nature of defects that affect their capture coefficients. However, the recombination activity of a defect is not only a function of its capture coefficients but also depends on the electrostatics and the design of the layer stack of a photovoltaic device. Here we study the influence of device geometry on defect tolerance by combining calculations of capture coefficients with device simulations. We derive generic device design principles which can inhibit recombination inside a photovoltaic device for a given set of capture coefficients based on the idea of slowing down the slower of the two processes (electron and hole capture) even further by modifying electron and hole injection into the absorber layer. We use the material parameters and typical p–i–n device geometry representing methylammonium lead halide perovskites solar cells to illustrate the application of our generic design principles to improve specific devices.

Introduction

With the recent interest in metal-halide perovskites for photovoltaic and optoelectronic applications, the term defect tolerance[1–8] has become omnipresent in the scientific literature. While it has been coined in the context of computational material screening,[6] lead-halide perovskites were the first incarnation of a defect-tolerant semiconductor with an antibonding valence band that works particularly well in solar cells and light emitting diodes.[5,7] One of the key reasons for this success was that solution processed polycrystalline layers of lead-halide perovskites in a wide range of stoichiometries and with varying cations and halides showed long charge carrier lifetimes,[9] high luminescence quantum yields[10–14] and subsequently high open-circuit voltages[14–17] and efficiencies $>23\%$[18–22] if used as a solar cell. In most classical semiconductors, intrinsic point defects (vacancy, interstitial, antisites) and other complex defects from unintended impurities (*e.g.* C, O in Si) can form deep defects in the bulk of the film as well as at grain boundaries, surfaces, interfaces and accelerate recombination.[23] The existence of these defects makes it usually quite difficult to achieve good

electronic properties without investing in complicated and (usually) energy-intensive fabrication techniques such as single crystal growth and epitaxy.[24] In halide perovskites, the impact of defects is less severe, because most intrinsic point defects create rather shallow energy levels[25,26] making non-radiative transitions less likely than in most other semiconductors. The prevalence of shallow defects is caused by the rather uncommon antibonding valence band[6] that causes most atomic orbitals to be in the conduction or valence band of the crystal. Thus, when bonds are broken (e.g. in the presence of a vacancy), the resulting orbitals are mostly inside the bands or close to the band edges[25,27,28] rather than in the middle of the bandgap as is the case for most covalent semiconductors. However, the defect tolerance of halide perovskites does not imply that defects are unimportant in these materials.[29,30] In contrast, non-radiative recombination *via* defects still reduces device performance below the values given by the Shockley–Queisser[31] limit with the topic of defect passivation[32–34] being central to current device optimization strategies. While the defect tolerance of a semiconductor is certainly an extremely important topic for understanding the success of lead-halide perovskites, so far there have been few attempts to discuss how the device geometry of a solar cell can affect the vulnerability of the device to defects.

Whether a defect causes substantial recombination not only depends on the density and energetic position of defects but also on the availability of electrons and holes to be captured by these defects. If for instance the material has an abundance of defects that preferentially trap holes, the capture of electrons is

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likely the rate-limiting step. If the device geometry is then designed such that there is an excess of holes in the absorber layer of the device, electron capture would be further reduced, and the total device efficiency may go up. However, changing the properties of the device geometry has a multitude of consequences given that the device geometry has to allow efficient charge extraction, therefore provide selectivity[35–37] at the different contacts, allow sufficient light absorption and efficient current flow minimizing resistive losses. Therefore, the perfect design of the device geometry under the explicit consideration of the properties of the dominant defects is a non-trivial task.

Here, we investigate the effect of device geometry on the recombination efficiency of defects, and hence on the efficiency of the solar cell. A review of well-studied materials[23] like Si,[38–41] GaAs,[42–44] CdTe[45,46] indicates that asymmetric capture coefficients are common and that its impact on recombination rates in combination with electron and hole concentration have been discussed in widely-used textbooks on the topic of recombination in semiconductors.[47] However, in the field of perovskite photovoltaics, measuring capture coefficients[48–50] and defect densities[48–50] is highly challenging due to the variety of different relevant perovskite materials, the low defect densities, the lack of actual doping and the presence of mobile ions.[51,52] Hence, for lack of better information, a near universal assumption of symmetric capture coefficients is made in studies on perovskite device simulation.[53,54] However, recent calculations[1,55] and fundamental arguments[56] show that defects most likely have asymmetric capture coefficients; *i.e.* a defect is not as likely to capture an electron as a hole or vice-versa. From a device point of view, the net impact of defects can be reduced by modulating the carrier concentration inside the device such that one of the capture rates is substantially slower than the other.[47] Modulating the carrier concentration by doping to minimize recombination losses is an obvious part of device optimization in technologies such as crystalline Si.[57] However, in emerging technologies such as halide perovskites, where intentional doping is challenging to achieve, the topic has so far received little attention.[32–34] This is despite the fact that modulation of carrier concentrations in the absorber layer is possible even without absorber-layer doping but alternatively by changing the properties of the electron and hole transport layers. Unfortunately, there is no one device geometry that will improve performance of all devices. However, the current work offers certain generic design principles which when implemented after identifying the dominant recombination levels will help reduce the recombination through the device.

This work explains how asymmetric device architectures improve open-circuit voltage of a device as seen from real device data[14] by mitigating defect-mediated recombination inside the device. It also discusses the role of the built-in-voltage in controlling the carrier concentration inside the device which then directly affects the solar cell device performance.[58,59] We highlight the importance of the electrostatic potential drop across the device[60] and how it alters the efficiency by either enhancing or mitigating the recombination through defects. We also show that the perovskite solar cell efficiency is limited by slow charge extraction due to the low-mobility organic transport layers[61] and can be improved if the low-mobility transport layers were to be replaced with higher-mobility transport layers. Besides, this work also considers the importance of atomistic calculations[1,55,62–66] or generalized models[56,67] in determining realistic values of capture coefficients and how these parameters can improve solar cell device simulation to help then go beyond the current state-of-the-art of symmetric capture coefficients.[53,54,61]

Theoretical background

Defect-mediated recombination

Defects occurring within the bandgap of a semiconductor enhance recombination in the device as they form an alternative recombination channel for the charge carriers besides band to band recombination. The theoretical description of this accelerated recombination *via* defects is based on work by Shockley, Read and Hall[68,69] and is therefore known as Shockley–Read–Hall (SRH) recombination. To help understanding the results and discussion in the remainder of the article, we will briefly introduce the key terminology of SRH recombination in the following.

Recombination *via* singly charged defects (donor-like or acceptor-like) occurs when the defect goes through a cycle consisting of two consecutive processes of electron capture and hole capture. The rate of recombination *via* such a defect depends on the electron capture rate (per unit time) nk_n [s^{-1}] and hole capture rate pk_p [s^{-1}], where n and p are the electron and hole concentration, and k_n [cm^3 s^{-1}] and k_p [cm^3 s^{-1}] are the electron and hole capture coefficient, respectively. The rate of recombination (per unit time and volume) [cm^{-3} s^{-1}] is then given as

$$R_{SRH} = N_T \eta_R, \quad (1)$$

where N_T [cm^{-3}] is the defect density of the semiconductor and the recombination efficiency η_R [s^{-1}] of a defect level, *i.e.* the number of recombination events occurring at a defect per unit time, is given as

$$\eta_R = \frac{k_n k_p (np - n_0 p_0)}{nk_n + pk_p + e_n + e_p}. \quad (2)$$

Furthermore, for defect levels lying well inside the two quasi-Fermi levels the emission coefficients are such that $e_n \gg nk_n$ and $e_p \gg pk_p$ and by assuming $np \gg n_0 p_0$, the recombination efficiency can be simplified as follows

$$\eta_R \approx \frac{nk_n pk_p}{nk_n + pk_p}. \quad (3)$$

Inside a device, the electron and hole carrier concentrations n_0 and p_0 in equilibrium, and n and p in typical operating conditions, respectively, are affected by the workfunctions and doping densities of the different layers in the device, whereas the capture coefficients $k_{n/p}$ are determined by the chemical nature and energetic position of a defect within the bandgap.

The electron and hole emission coefficients e_n and e_p depend on the respective capture coefficients and the position of the quasi-Fermi level splitting given by the typical operating conditions of the device.[56] Recombination efficiency is therefore affected by the combined properties of the device geometry and the nature of the defects.

However, SRH statistics is agnostic about the origin of the capture coefficients and makes no theoretical prediction about the values representative of $k_{n/p}$ in materials. In absence of a model to predict the values of capture coefficients, state-of-the-art device simulation of perovskite solar cells uses capture coefficients obtained from experimental methods[54] or heuristic assumptions[70] to calculate electron hole capture lifetimes $\tau_{n/p} = (N_T k_{n/p})^{-1}$. The frequently used assumption of symmetric capture coefficients $k_n = k_p$ for all defect levels within the bandgap in a device operating in a high injection scenario $n = p$ leads to a simplified picture where all defects within the two quasi-Fermi are equally recombination active. This behavior of defects is often used in textbooks for photovoltaics to illustrate the impact of capture and emission coefficients on the SRH recombination rate or recombination lifetime.[71,72] However, equal or at least similar values of the capture coefficients are not necessarily a typical or even a likely scenario[55,56] as has been discussed in the introduction.

To go beyond the state-of-the-art device simulation in perovskite photovoltaics involving symmetric capture coefficients, we either need more experimental data on capture coefficients of novel materials or, alternatively, models to calculate trap energy dependent capture coefficients. These can then be used in combination with SRH statistics to get a trap energy dependent recombination efficiency and SRH lifetime.

Capture coefficients

Recently we presented a generalized microscopic model,[56,67] within the limits of harmonic oscillator approximation, to calculate multiphonon capture coefficients using material properties of the host semiconductor and energetic position of the trap within the bandgap of the material. The model to calculate the capture coefficients is adapted from the work of Markvart[73–76] which uses the Wentzel, Kramers, Brillouin (WKB)[77–79] approximation to calculate the multiphonon transition rates between two states by tunneling. The capture coefficients so calculated are proportional to the inverse of exponential of the required number of phonons for a given transition (refer to eqn (S3) for $k_{n/p}$ in ESI:† the argument of exponential is negative when the phonon energy is sufficiently smaller than $2k_B T$). However, this model does not make any predictions about the value of the Huang Rhys factor. Therefore, we combined the approach of Markvart with work of Ridley,[80–82] which uses the so called 'quantum defect model' to calculate the Huang–Rhys factor by characterizing the defects' degree of localization by the quantum defect parameter, ν_T. The parameter ν_T, ($\nu_T = \sqrt{R_H^*/\Delta E_{min}}$) defined as the square root of the depth ΔE_{min} of any defect level E_T from the nearest band with respect to the depth of a shallow defect given by effective

Rydberg energy R_H^*, ensures that the higher the depth of the defect, the more localized it is. The quantum defect parameter ν_T diminishes as the depth of the defect ΔE_{min} from the nearest band increases and assumes the smallest value when the defect is at midgap. The Huang–Rhys factor S_{HR} measuring the displacement of the lattice from its mean position in the vicinity of the defect is an inverse function of ν_T and increases with increasing ΔE_{min}. Besides the type of coupling, (polar optical coupling for polar semiconductors or optical deformation potential coupling for elemental semiconductors or less polar semiconductors) also the charge state of the defect, μ, is considered in the calculation of S_{HR}. The Huang–Rhys factor is then used to calculate the capture coefficients of defects (see Table SI in ESI† for expressions)[56] in the material. The model we use therefore includes the effect of the depth of a defect twice. The depth affects the likelihood of a multiphonon transition by affecting the required number of phonons to bridge the energy gap between excited and ground state of a transition, and the use of the quantum defect model affects the transition probability by increasing the Huang–Rhys factor for more localized (i.e. deep) defects. As we show in more detail in the ESI† (see Fig. S1), the first effect (number of phonons needed for the transition) is by far the dominant one. Detailed accounts of the various models available for calculating the multiphonon capture coefficients and notes on their relations to each other, as well as their validity can be found in the work of Jaros.[83] To consider the Coulomb attraction between an oppositely-charged defect state and a captured carrier, the Sommerfeld factor s_a is multiplied with the electron capture coefficient k_n of a donor-like defect and with the hole capture coefficients k_p of an acceptor-like defect. When the trap energy dependent capture coefficients are used as inputs to the SRH theory, the energy dependence of the recombination efficiency is reflected.

It is evident from the expression of the capture coefficients given in Table SI of the ESI† that $k_{n/p}$ are inversely dependent on the depth ΔE of a trap via its dependence on the number of phonons. The depth ΔE of the trap is always measured with respect to the band edge from which the carrier is captured as shown in Fig. 1a. This ensures that the hole capture coefficient k_p decreases rapidly as the defect level moves away from the valence band edge E_V, whereas the electron capture coefficient k_n decreases as the defect moves away from the conduction band edge E_C, as shown in Fig. 1b. The faster than exponential decay of capture coefficients with respect to ΔE, measured from the respective bands of transition, makes k_n and k_p highly asymmetric except for around midgap, where the two are comparable or equal. The generalized model for capture coefficients predicts that in methylammonium lead iodide (MAPI) perovskite even defects lying within 200 meV on either side of midgap have k_n and k_p values differing from each other by 10^5 to 10^2 orders of magnitude as shown in Fig. 1b. This idea of asymmetric capture coefficients[55] contrasts with the usual assumption of symmetric capture coefficients[54,70] for any defect level and has a huge impact on the recombination efficiency of a device.
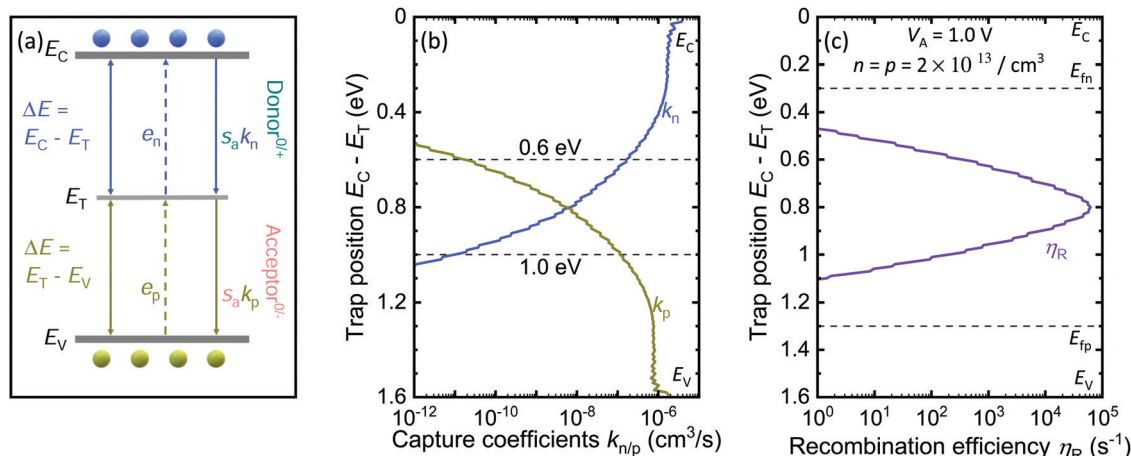
Fig. 1 Trap energy dependent capture coefficients and recombination energy. (a) Schematic indicating the depth of trap $\Delta E = E_C - E_T$ for transitions taking place from conduction band or $\Delta E = E_T - E_V$ for transitions taking place from valence band. The electron capture coefficient k_n and emission coefficient e_n, hole capture coefficient k_p and emission coefficient e_p. The k_n of a donor-like trap and k_p of an acceptor-like trap are multiplied with Sommerfeld factor s_a to account for Coulomb interaction between oppositely charged defect state and captured carrier. (b) The capture coefficients $k_{n/p}$ (cm^3 s^{-1}) as functions of trap position measured from the edge of the conduction band. The capture rates are obtained by multiplying the electron and hole carrier concentration with the electron and hole capture coefficients. The carrier concentration is considered to be equal and calculated at an applied voltage $V_A = 1.0$ V. (c) The recombination efficiency η_R (s^{-1}) as a function of trap position $E_C - E_T$.

In a simple scenario of high-level injection of carriers $n = p$, the capture rates nk_n and pk_p follow the same decaying nature of the respective coefficient with respect to ΔE measured from band edge of transition. Inserting these rates into eqn (2) leads to the result shown in Fig. 1c, where within the two quasi-Fermi level splitting, set by the applied voltage V_A, η_R is given by the slower of the two rates except for midgap where the two rates are symmetric and a combination of the two rates gives η_R. In the literature, often the adjectives "deep" and "shallow" are used in the context of defects to characterize how detrimental a defect is expected to be. Defect-tolerant materials are - in this context - often defined as materials that are unlikely to form many "deep" defects. However, a deep defect is not clearly defined. It could be defined by the energetic distance to the nearest band edge, by the degree of localization (the more localized, the deeper) or by a functional argument, *i.e.* the more detrimental, the deeper. In this paper, we follow the third approach by using the recombination efficiency η_R giving the number of recombination events at a defect per unit time to characterize a "deep" defect.

Accordingly, a defect is classified as "deep" when the defect is most detrimental for device performance. The most detrimental situation is reached, when the defect satisfies the two criteria:

$$nk_n = pk_p \tag{i}$$

$$nk_n \gg e_n \text{ and } pk_p \gg e_p. \tag{ii}$$

The second criterion is valid for defects occurring inside the two quasi-Fermi level of the device. The first criterion then determines the n and p values which make the electron and hole capture rate equal and thus leading a defect to its most detrimental state. In our simple scenario of $n = p$ the first

criterion is satisfied only at midgap thus making a midgap defect the most recombination active or "deep" defect.

However, the observation that only midgap defects are "deep" is a result of equal electron and hole concentration and will change as soon as we consider asymmetric carrier concentrations inside a device. It is crucial to realize that even though the capture coefficients are intrinsic properties of the defect, the recombination activity is not. The recombination efficiency of a defect is determined by the n and p values and hence can change with the change in carrier concentration within the device. Inside a device the same defect level can vary in recombination activity depending upon its position within the device. The significance of these two criteria is that, when satisfied, it indicates an occupation probability of the defect $f_T \sim 0.5$, and maximum recombination efficiency for the defect. When the second criterion is still valid, but n and p values are such that $nk_n \neq pk_p$, the recombination efficiency of the particular defect is limited by the slower of the two rates. From a device perspective, the defects satisfying the two criteria are also likely to be the most detrimental of all defect levels in the device, at that particular working condition. The energy and position dependence of η_R inside a device elucidates that not all defects are equally detrimental to device performance and whether a defect is "deep" or not is determined by the working condition of the device.

To understand the realistic impact of asymmetric capture coefficients of defects inside a device, we need to go beyond the simple scenario of $n = p$ and use asymmetric capture coefficients obtained from our general model based on the harmonic oscillator approximation in combination with a photovoltaic device simulator to map the recombination efficiency of trap levels as a function of both trap depth ΔE and position inside the device.

Results and discussion

All photovoltaic device simulations presented here, were performed using the Advanced Semiconductor Analysis (ASA) software,[84,85] an integrated opto-electronic tool developed by the Photovoltaic Materials and Devices group at TU Delft. The full list of material parameters and simulation settings used for all the results presented here can be found in Table II of the ESI.†

The criteria for "deep" defects discussed in the previous section are subject to the harmonic oscillator approximation. However, within this approximation, they are also valid in situations where $n \neq p$ and $k_n \neq k_p$. Thus, any defect satisfying the second criterion and having asymmetric capture coefficients can act as highly recombination active site when $nk_n = pk_p$. In Fig. 2, we use the capture coefficients calculated in Fig. 1b using the harmonic approximation for MAPI to map the recombination efficiency as a function of n/p and k_n/k_p. The key insights we gain are:

(i) The further below the defect is from midgap, the higher the electron concentration should be to obtain $nk_n = pk_p$. Alternatively, when a defect is more likely to capture a hole, $k_n \ll k_p$; the lower the electron concentration n is compared to the hole concentration p ($n \ll p$), the lower the recombination efficiency η_R will be.

(ii) The more the defect is above midgap, the higher the hole concentration should be to obtain $nk_n = pk_p$. Alternatively, when a defect is more likely to capture an electron, $k_n \gg k_p$; the lower the hole concentration p is compared to the electron

concentration n ($n \gg p$), the lower the recombination efficiency η_R will be.

(iii) The closer the defect is to midgap, the more comparable the carrier concentration should be to obtain $nk_n = pk_p$. Alternatively, when a defect is equally likely to capture a hole or an electron, $k_n \approx k_p$; either $n \gg p$ or $n \ll p$ will lead to a lower recombination efficiency η_R.

Hence, while dealing with asymmetric capture coefficients, it is better to have asymmetric carrier concentrations, which keeps the rates of capture vastly different from each other and prevents the situation of $nk_n = pk_p$.

The insights gained so far are general and can act as guidelines to investigate different device geometries of different materials. A particularly interesting test-case to study is the class of lead-halide perovskite solar cells. Here, the absorber is fairly intrinsic and the ratio n/p throughout the absorber depends to a large degree on properties of the electron and hole transport layers. For instance, the low permittivity of organic electron or hole transport layers, may cause a large drop of the electrostatic potential over the transport layers causing a smaller drop of electrostatic potential over the absorber layer. While this has been noted in the literature, so far, the impact of device electrostatics on recombination rates has not been discussed in much detail.

A perovskite solar cell (PSC) typically consists of an active layer sandwiched between an electron transport layer (ETL) and a hole transport layers (HTL) before the cathode and anode layer, respectively. In an ideal scenario, the ETL transports the electrons from the device to the cathode while blocking the holes and the HTL transports the holes from the active layer to the anode while blocking the electrons. As a result of the selectivity achieved by employing materials with different electron affinity, the difference of the workfunctions of the anode and cathode leads to a built-in-voltage V_{bi} across the device. At an optimum working condition, the electrostatic potential drop across the device is the net difference between the built-in-voltage V_{bi} and the applied voltage V_A and the electrostatic potential then determines the electron and hole concentration inside the device.

We employ a device structure, with a perovskite absorber layer of 300 nm sandwiched between two generic charge transport layers each of 20 nm thickness as shown in Fig. 3. The ETL and HTL are symmetric differing only in their value of electron affinity E_A. The HTL/perovskite and ETL/perovskite interfaces are modelled by two additional layers each of 2 nm and characterized by material parameters of the absorber layer. However, the bandgap of this interface layer is given by a combination of the bandgap and the electron affinity of the two materials forming the interface as shown in Fig. 3. We also assume a constant trap density $N_T = 10^{15}$ cm^{-3} across the absorber and interface layers (see Table II in ESI† for material parameters). We then perform certain thought experiments realizing different n/p ratios through our generic device by changing the electrostatic potential drop across the absorber and the contacts to correlate those variations in the ratio $n(x)/p(x)$ to the efficiency of the solar cell.
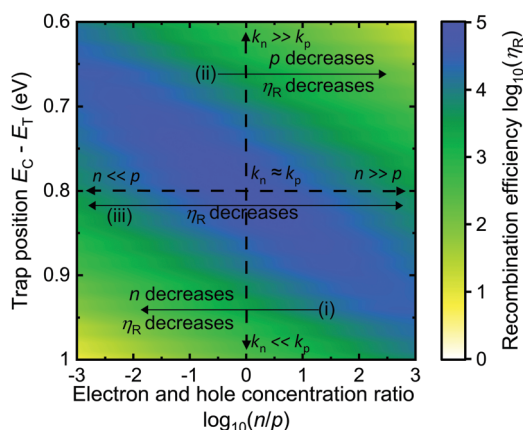

Fig. 2 The variation of recombination efficiency η_R as function of realtive values of electron and hole concentration (n/p) and realtive values of electron and hole capture coefficients k_n and k_p, respectively, with the change in energetic position of the traps. Recombination efficiency decreases when the slower of the two capture rates decreases in magnitude. For a particualar defect level with fixed capture coefficients this is determined by the carrier densities. (i) When the defect is below the midgap level and closer to the valence band, η_R decreases from right to left with decrease of n with respect to p. (ii) When the defect is above the midgap level and close to the conduction band, the trend is reversed and η_R decreases from left to right with decrease of p with respect to n. (iii) The closer the defect is to midgap level, the more symmetric are coefficients and η_R decreases on either decreasing n compared to p or p compared to n.
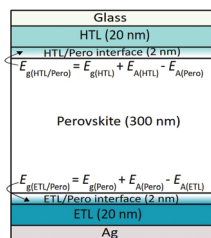
Fig. 3 p–i–n device structure of the perovskite solar cell under study. The generic hole transport layer (HTL) and electron transport layer (ETL) are symmetric differing only in their electron affinity (E_A). The interface layers are characterized by parameters of the absorber except for their bandgaps are given according to expressions shown in the figure.

Symmetric device with varying built-in-voltage:

The built-in-potential is determined by the energetic mismatch between the workfunctions of the anode and the cathode. When the Schottky barrier height at both the contacts are zero, the built-in-potential is equal to the bandgap of the absorber. When the workfunctions of both the metal contacts are such that there is a non-zero Schottky barrier at the ETL side and as well as the HTL side, then the built-in-potential is $qV_{bi} = E_{g(abs)} - \xi_p - \xi_n$, where ξ_p and ξ_n are the Schottky barrier heights at the HTL/anode interface and the ETL/cathode interface, respectively. We consider two scenarios such that in one $\xi_p = \xi_n = 0$ eV; $qV_{bi} = 1.6$ eV and in another $\xi_p = \xi_n = 0.2$ eV; $qV_{bi} = 1.2$ eV. In our symmetric device, the hole concentration p and the electron concentration n vary such that $p \gg n$ towards the HTL side and $n \gg p$ towards the ETL side leading to a continuously varying n/p ratio inside the device.

In Fig. 4a and b we plotted the band diagram of the two devices and then superimposed on it the logarithm of the absolute value of recombination efficiency η_R as a function of both the position inside the device and the position of the defect as measured from the conduction band edge. To explain the effect of the change in qV_{bi} on the recombination efficiency we also plot the electron and hole concentration through the device at $qV_{bi} = 1.6$ eV (solid blue curves) and at $qV_{bi} = 1.2$ eV (dashed pink curve) in Fig. 4c. When $qV_{bi} = 1.6$ eV, the hole to electron concentration ratio varies in range $p/n \approx 10^4$ to 10^{-4} from the HTL side to the ETL side as shown in Fig. 4c. This suggests that any defect that has $k_n/k_p \approx 10^4$ has the potential to act as a "deep" defect at the HTL side and so does a defect with $k_n/k_p \approx 10^{-4}$ at the ETL side by satisfying the condition $nk_n = pk_p$. In case of MAPI, as plotted in Fig. 1b, one such defect is about 0.6 eV from the conduction band with $k_n/k_p \approx 10^4$ whereas another one is about 1.0 eV away from the conduction band with $k_n/k_p \approx 10^{-4}$. Hence these two defects even with highly asymmetric capture coefficients satisfy the "deep" defect criteria of $nk_n = pk_p$ in different regions of the device. As we go inside the device from the HTL side the hole to electron concentration ratio decreases such that $10^4 > p/n > 10^{-4}$. This implies that as we move inside the device from the HTL side to the ETL side, any defect within 200 meV on either side of midgap, having capture coefficients such that $10^4 > k_n/k_p > 10^{-4}$,

can potentially be turned into a "deep" defect or at least highly recombination active defect when the criteria $k_n/k_p \approx p/n$ is satisfied at a position inside the device. This highly recombination active region appears in the shade of blue in Fig. 4a and η_R peaks around midgap at an approximate value of 10^5 s^{-1}.

In comparison to $qV_{bi} = 1.6$ eV, when non-zero Schottky barriers are introduced, the electron quasi-Fermi level move ξ_n energy away from the conduction band at the ETL/metal interface and the hole quasi-Fermi level moves ξ_p away from the valence band at the HTL/metal interface. This leads to reduction of the electron concentration towards the ETL and hole concentration towards the HTL. However, the change in qV_{bi} increases carrier concentration everywhere else inside the device as well as the minority carrier concentration towards the selective contacts (holes towards ETL and electrons towards HTL). The change in both absolute value as well as relative value of electron and hole concentration has two effects on the recombination activity of the device.

In Fig. 4c, when $qV_{bi} = 1.2$ eV, the carrier concentration varies such as $p/n \approx 10$ to 10^{-1} from the HTL side to the ETL side, which implies that only defects with coefficients such that $k_n/k_p \approx 10$ to 10^{-1} are potential "deep" defects or the most detrimental of all the defects inside the device. Thus, according to the coefficients determined from harmonic oscillator approximation for MAPI (see Fig. 1b), defects within a 50 meV energy range on either side of midgap can be turned into "deep" defects by the electron and hole concentration at a particular position inside the device when $qV_{bi} = 1.2$ eV. The narrowing down of the range of "deep" defects on reducing the built-in-voltage is related to the change of the relative values of electron and hole concentration inside the device. However, even though the range of "deep" defects narrowed down, comparing Fig. 4a and b reveals that the recombination efficiency of all defects increased and $\eta_R \approx 10^6$ s^{-1} around midgap due to increase in the absolute values electron and hole concentration as shown in Fig. 4c. As a result of reducing the built-in-voltage, the recombination efficiency of below midgap defects ($k_n \ll k_p$) at every position inside the device increases by the amount of increase in electron concentration. The same is true for defects above midgap ($k_n \gg k_p$) but in this case, the increase in recombination efficiency is determined by the increase in the value of hole concentration.

In Fig. 4d we show the effect of the change qV_{bi} and hence carrier concentration more explicitly by choosing to look at the recombination efficiency for two specific defect levels in MAPI with capture coefficients determined from the harmonic oscillator approximation. The defect level 1.0 eV away from the conduction band has asymmetric capture coefficients ($k_n \approx 10^{-11}$ cm^3 s^{-1} and $k_p \approx 10^{-7}$ cm^3 s^{-1}) and the one 0.8 eV away from the conduction band has symmetric capture coefficients ($k_n \approx k_p \approx 5 \times 10^{-9}$ cm^3 s^{-1}). Both defect levels have a constant defect density of $N_T = 10^{15}$ cm^{-3} across the active and the interfaces layers. The increase in electron concentration n upon decreasing qV_{bi} increased the rate of electron capture and hence η_R of the defect 1.0 eV away from midgap through most of the device. However, the case of the defect at midgap with
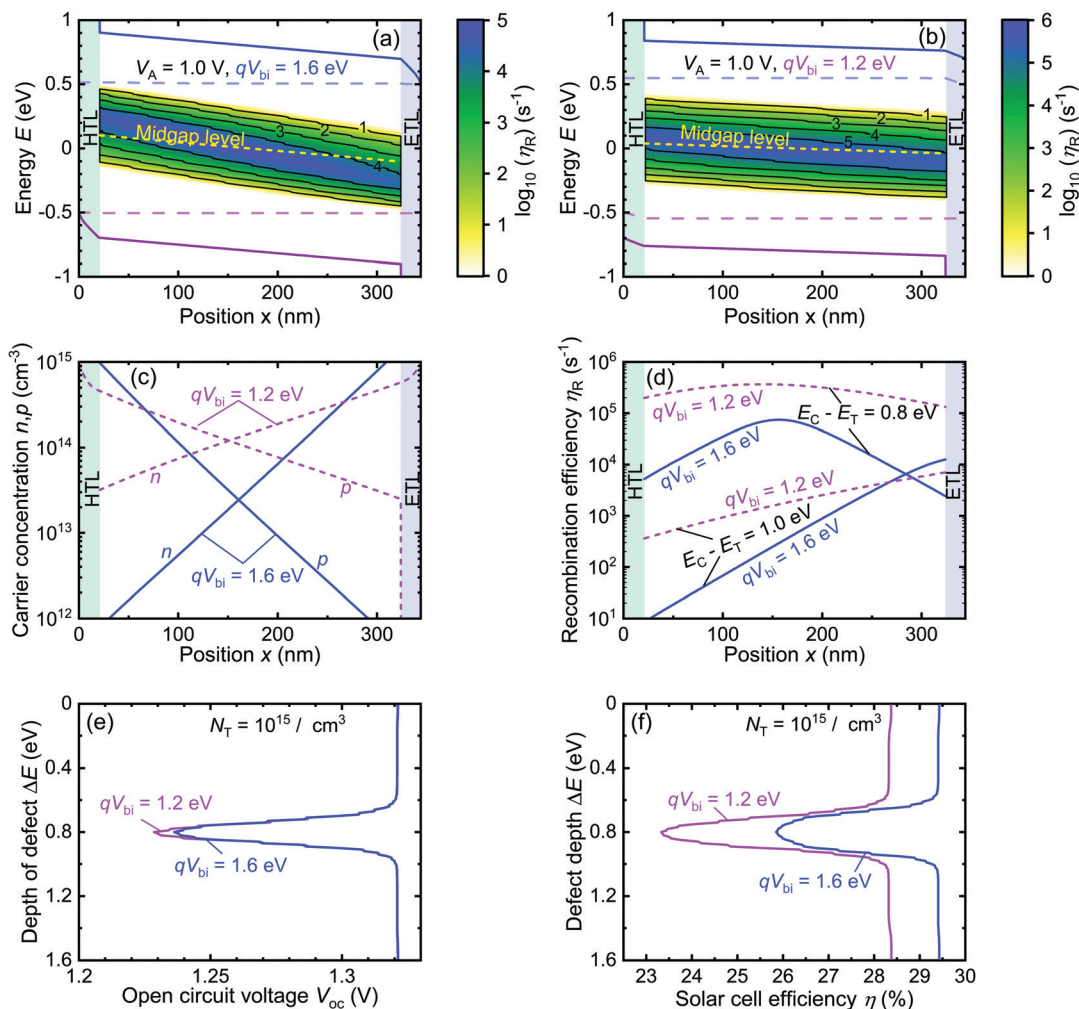
Fig. 4 The effect of changing the built-in-voltage V_{bi} across the device on SRH recombination and subsequently on the device efficiency. (a) Recombination efficiency η_R as a function of position inside a device and the energy level ($E_C - E_T$) of the trap calculated from the conduction band when qV_{bi} = 1.6 eV. This data is superimposed on the band diagram of the device at an applied voltage V_A = 1.0 V. (b) Same as before but for qV_{bi} = 1.2 eV. (c) The electron and hole concentration inside the device when qV_{bi} = 1.6 eV and qV_{bi} = 1.2 eV. (d) The SRH recombination rate R_{SRH} of defects 1.0 eV and 0.8 eV away from the conduction band for qV_{bi} = 1.6 eV and qV_{bi} = 1.2 eV. (e) Open circuit voltage V_{oc} plotted as a function of the position of the defect from the conduction band for qV_{bi} = 1.6 eV and qV_{bi} = 1.2 eV. (f) Solar cell efficiency η(%) plotted as a function of the position of the defect from the conduction band for qV_{bi} = 1.6 eV and qV_{bi} = 1.2 eV.

symmetric values of capture coefficient is a bit more complicated. When qV_{bi} = 1.6 eV, n and p are vastly different, making the electron and hole capture rates very different even though the coefficients are symmetric and thus η_R is determined by the slower of the two rates through most of the device. On the contrary, when qV_{bi} = 1.2 eV, the comparable n and p values yield very similar electron and hole capture rates having higher values compared to the capture rates at qV_{bi} = 1.6 eV and thus, η_R is determined by a combination of both capture rates.

In Fig. 4e and f we show the overall effect of the change in built-in-voltage on the open-circuit voltage V_{oc} and efficiency of the device η (%). When the SRH recombination rate exceeds the direct recombination rate $R_{SRH} > R_{direct}$, V_{oc} and efficiency η (%) decrease from their respective values given by the radiative limit. In the two devices discussed above this criterion is satisfied both by defects at midgap with symmetric coefficients

and defects away from midgap with asymmetric capture coefficients. This deviation from the radiative limit within 200 meV on either side of midgap results in a drop of V_{oc} and η (%) in this range. The drop in the values of V_{oc} and η (%) is higher when qV_{bi} = 1.2 eV compared to when qV_{bi} = 1.6 eV because of the higher R_{SRH} in the low qV_{bi} case. Even though the range of "deep" defects shrunk with the decrease in qV_{bi}, the actual overall recombination increased through the device due to the increase in the electron and hole concentration through the device. It should be noted that even beyond the 200 meV range on either side of midgap the non radiative recombination rate R_{SRH} increases substantially due to the increase in carrier concentration inside the absorber when qV_{bi} decreases from 1.6 eV to 1.2 eV, and the increased non-radiative recombination also leads to the drop in the solar cell efficiency η (%) beyond the 200 meV range on either side of midgap.

Thus, it is evident from the discussion above, that a built-in-voltage that helps in achieving relatively lower values of n and p inside the device also leads to relatively lower magnitudes of defect-mediated recombination as well as direct recombination. The rule is that if changing the built-in-potential qV_{bi} leads to the decrease in even one of the carrier concentrations, then the capture rate of that carrier by all defect levels will also decrease. And if the capture rate that decreases due to decrease in carrier is the recombination limiting rate, then the recombination efficiency of the defect levels will decrease and as a result the efficiency of the device would increase. The results discussed above were for defect levels in the bulk. Devices limited by surface recombination are a bit more involved but show similar trends as shown in Fig. S2 and S3 of the ESI.† Also, the effect of surface recombination on device performance is shown in Fig. S4 (ESI†). Nevertheless, it is obvious that it is better to have vastly different electron and hole concentration inside the device so that the recombination rate can be slowed down by inhibiting one of the capture processes. However, even when qV_{bi} is high, defects within 200 meV on either side of midgap act as deep defects. This will be particularly severe from a device perspective if a device has higher concentrations of defects around midgap since this will result in very high levels of defect mediated recombination. Also, in the cases we studied, due to continuously varying electron and hole concentration, the recombination efficiency of every defect level varies continuously inside the device and so does the position of "deep" defects both in energy and position inside the device. To circumvent such scenarios, we study a device geometry such that $n \ll p$ or $p \ll n$ throughout the device.

Asymmetric devices

To test our hypothesis about asymmetric carrier concentrations throughout the device we chose a device architecture with flat bands across the absorber layer by assuming absorber permittivity $\varepsilon_{pero} \rightarrow \infty$. Such a device with symmetric ETL and HTL layer gives $n = p$ across the absorber and the interface layers and is expected to be highly detrimental for devices with defects at or around midgap. However, the recombination activity of defects close to midgap can be improved if we dope one of the contact layers as it would result in $n \gg p$ or $p \gg n$ across the absorber depending on whether we dope the ETL layer with donor dopants or the HTL layer with acceptor dopants, respectively. If we want to induce an asymmetry of electron and hole concentrations in an intrinsic semiconductor by choosing asymmetric properties of contacts, we have to rely on the contacts being able to connect the majority carrier quasi-Fermi level at the contact with the one at the edge of the absorber without any substantial gradient in quasi-Fermi level. This requires either low thicknesses or sufficiently high mobilities in the undoped electron or hole transport layer. Because this cannot be taken for granted, we will first discuss the scenario where the contact layer mobilities are sufficiently high to minimize any voltage drops. In a second step, we discuss the opposite scenario where these mobilities are low enough to cause a substantial voltage drop.

In Fig. 5a and b, we plot the logarithm of the absolute value of recombination efficiency of a symmetric and asymmetric flat-band device, respectively, superimposed on the band diagram of the respective device. In the symmetric device, the electron and hole carrier concentrations are equal and constant $n = p$ (as indicated by the position of the two quasi-Fermi level from their respective bands) between the two interface layers, resulting in the recombination efficiency peaking around midgap and decreasing rapidly beyond, as shown by the blue shaded region of Fig. 5a. In the asymmetric device shown in Fig. 5b, the ETL layer has a donor dopant concentration of $N_{d(ETL)} = N_{C(ETL)}$ and results in $n \gg p$ between the two interfaces. This asymmetric carrier concentration leads to reduced recombination efficiency for all defects above midgap and within 100 meV below midgap, while increasing the same for defects which are about 100–300 meV below midgap. This is so because at midgap where $k_n \approx k_p$, $n \gg p$ makes $nk_n \gg pk_p$ and as a result η_R is limited by the slower hole capture rate pk_p and, thus, the defects around midgap do not anymore satisfy the "deep" defect criterion of $nk_n = pk_p$. However, the "deep" defect criterion is now satisfied by defects which have capture coefficients such that $k_n/k_p \approx p/n$, which in this case are defects situated about 100–300 meV below midgap.

In Fig. 5c and d we compare V_{oc} and η (%) of the two flat-band devices. Also, to put their performance in perspective with that of device geometries discussed in Fig. 4, we chose two devices having contact layer mobility $\mu_{ETL/HTL} = 10$ cm^2 V^{-1} s^{-1} and absorber layer permittivity $\varepsilon_{pero} = 33.5$. The choice of permittivity makes sure that the electron and hole concentrations vary continuously through the device. One of the two devices with $\varepsilon_{pero} = 33.5$ have symmetric contacts, *i.e.* undoped ETL or HTL (dashed purple curve) while the ETL of the other device is doped with a donor concentration of $N_{d(ETL)} = N_{C(ETL)}$ (solid purple curve). It is observed by comparison of the solid and dashed purple curves in both panels (c) and (d) that asymmetric contacts resulting in more asymmetric carrier concentration improve the V_{oc} and η (%) of midgap defects even when the n and p vary continuously inside the device. However, making the geometry such that $n \gg p$ across the device inhibits the recombination of midgap defects even further. At midgap the maximum V_{oc} and η (%) are given by the solid blue curves in Fig. 5c and d, respectively. The dashed blue curves in the same figures, representing the flat band symmetric ETL and HTL resulting in $n = p$ throughout the absorber layer, intdicates the worst V_{oc} and η (%) at midgap. The reference lines in Fig. 5d at 0.6 eV, 0.8 eV and 0.9 eV away from the conduction band show that within 200 meV above midgap and 100 meV below midgap, the solar cell efficiency η (%) improves as a function of the asymmetry between the electron and hole concentration inside the device.

However, even though $n \gg p$ helped reduce recombination through defects around and above midgap, it increases the recombination through defects 100–300 meV below midgap where $k_n \ll k_p$. In a device where most defects are positioned such that $k_n \ll k_p$, the HTL layer must be acceptor doped so that the electron concentration in the device remains small ($n \ll p$), thus decreasing the electron capture rate $nk_n < pk_p$.
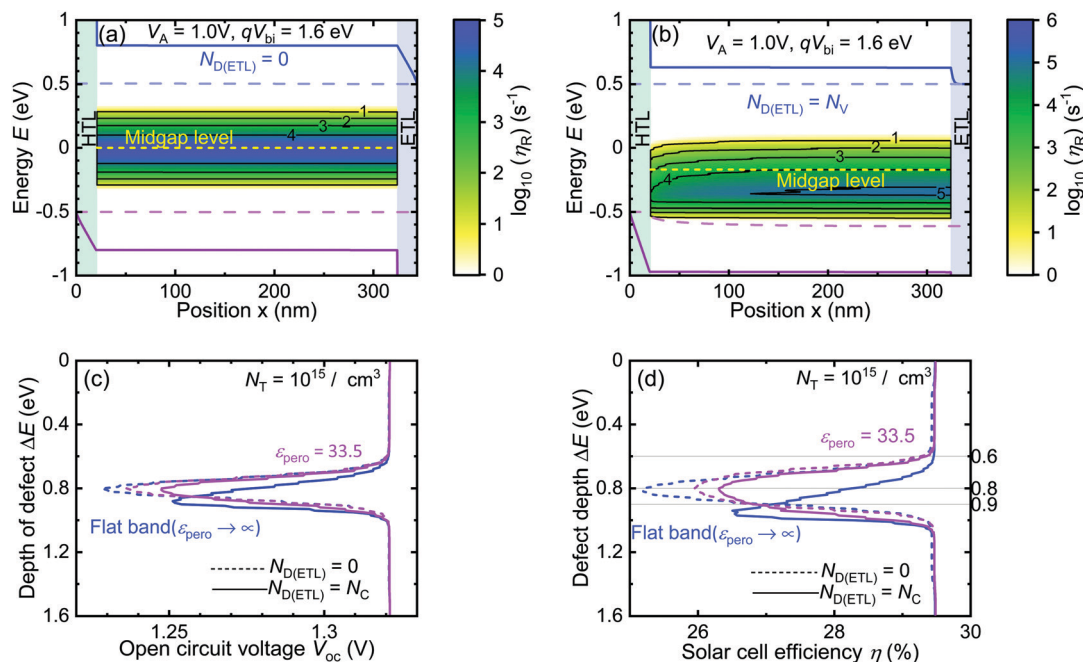
Fig. 5 The Effect of constant but asymmetric electron and hole concentration inside a device with μ_{pero} = 30 cm^2 V^{-1} s^{-1} and $\mu_{ETL/HTL}$ = 10 cm^2 V^{-1} s^{-1} on solar cell efficiency. (a) Defect mediated recombination efficiency η_R (s^{-1}) as a function of position inside the device and the energy level ($E_C - E_T$) of the trap calculated for a flat-band device with symmetric ETL and HTL layer such that $n = p$ and is superimposed on the band diagram of the same device. (b) Same as before except now the ETL layer is doped at a donor concentration of $N_{d(ETL)} = N_C$. (c) Comparison of open circuit voltage V_{oc} of devices with absorber permittivity ε_{pero} = 33.5 (purple curves) and $\varepsilon_{pero} \to \infty$ (blue curves). The devices with symmetric (undoped) ETL/HTL are shown by dashed curves and the devices with asymmetric (doped ETL, $N_{d(ETL)} = N_{C(ETL)}$) are represented by solid curves. (d) Solar cell efficiency of the same four devices mentioned in panel (c) is compared.

Therefore, the way to deal with asymmetric capture coefficients is to have asymmetric carrier concentration inside the device so that one of the capture rates is substantially slower than the other and $nk_n \neq pk_p$. However, to determine which geometry would improve the device efficiency, it is important to identify the dominant defect levels responsible for majority of the non-radiative recombination in the device by corroborating experimental data with first-principle studies of capture coefficients. The knowledge of the capture coefficients of the dominant defect levels can then be used to choose the appropriate scheme of asymmetry in the device that successfully reduces the recombination efficiency of the dominant defect levels. In the following section we put this idea into practice and validate our hypothesis against real device data.

Recombination through iodine interstitials

Liu *et al.*[14] reported an open circuit voltage exceeding 1.26 V for inverted planar MAPI solar cells by carefully optimizing the hole transport layer and electron transport layer to an asymmetric champion device. In MAPI structures, iodine interstitial defects have been long suspected to be the dominant defect level[4] and Zhang *et al.*[55] calculated the capture coefficients of iodine interstitial defects from first principles. Therefore, let us assume that iodine interstitials are the dominant defect in MAPI and use the capture coefficients calculated in Zhang *et al.* Under these two assumptions we are able to make predictions on how the device geometry should affect device performance.

We therefore simulate MAPI devices with an iodine interstitial defect level and varying asymmetries in the device geometry that modulate the relative efficiency of electron and hole injection.

We fabricated a MAPI solar cell of p–i–n structure with organic charge extraction layers poly(triarylamine) (PTAA) as the hole transport layer (p) and [6,6]-phenyl-C$_{61}$-butyric acid methyl ester (PCBM) as the electron transport layer (n). The PTAA layer thickness is ∼16 nm whereas the PCBM thickness is varied as ∼14 nm, 32 nm, 45 nm, and 60 nm. The current voltage curves are measured on a class AAA solar simulator providing a power density of 100 mW cm^{-2}. The V_{oc} increases with increase in PCBM thickness due to suppression of recombination at interfaces and in the bulk leading to an improvement in V_{oc} as well as the efficiency as shown in Fig. 6.

Recently Zhang *et al.*[55] performed first-principles calculations to determine the electron and hole capture coefficients of iodine interstitial defects about 0.48 eV away from the conduction band occurring in methylammonium lead iodide perovskite structures. Even though the iodine interstitial defect is energetically closer to the conduction band, they found the electron capture rate to be substantially slower as compared to the hole capture rate. The slowing down of the electron capture results from the strong anharmonicity in the potential energy surface of the system consisting of a positively charged iodine interstitial I$_i^+$, an e$^-$ at the conduction band minimum (CBM) and a h$^+$ at the valence band maximum (VBM) as shown in
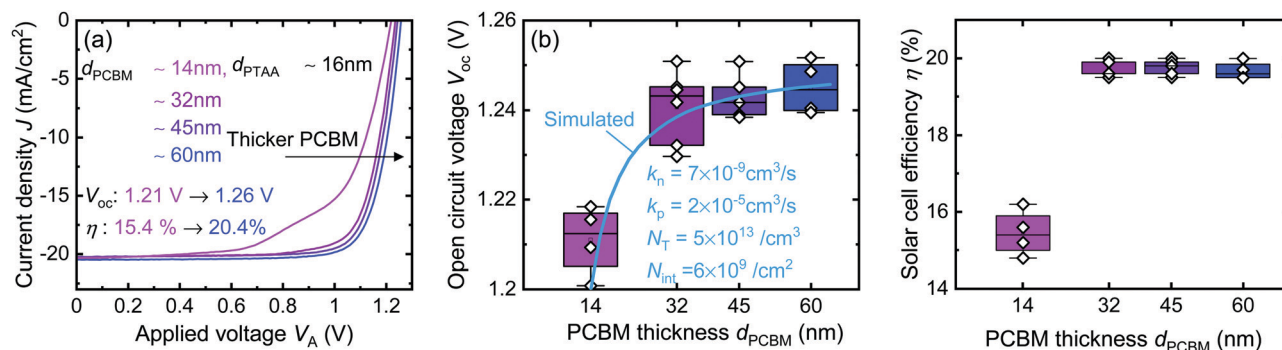
Fig. 6 (a) Experimental $J-V$ characteristics of inhouse fabricated standard MAPI cells showing the increase of open circuit voltage V_{oc} with the thickness of the ETL. (b) Statistical data of open circuit voltage. The white diamonds represent experimentally observed values of V_{oc} from which the statistical analysis is drawn. Each box represents results from three batches of cells made with the same processing parameters. All processing parameters are the same for all samples except for concentration of the solution used for the PCBM deposition. The light blue line shows the dependence of V_{oc} on ETL thickness as observed from simulation with the trap density chosen to obtain the best fit to experimental data. (c) Statistical data of solar cell efficiency.

Fig. 7. From a semiclassical perspective, this anharmonicity results in an energetic barrier ΔE_n^+ between the potential energy surface (PES) of the two systems ($I_i^+ + e^- + h^+$ and $I_i^0 + h^+$) which an electron needs to overcome to transition from the CBM (green curve) to the defect level (orange curve). On the contrary, the electron "sees" no such barrier while transitioning from the defect level to the VBM (blue curve), thus making the hole capture rate substantially faster. To study the combined effects of asymmetric geometry and the asymmetric capture coefficients of an iodine interstitial defect on perovskite solar cell performance, we take $k_n = 7 \times 10^{-9}$ cm^3 s^{-1} and $k_p = 2 \times 10^{-5}$ cm^3 s^{-1} as reported by Zhang *et al.*[55] from first-principles calculations and simulate a symmetric and an asymmetric device for comparison.

In Fig. 8a and b, we plot the band diagram of the symmetric and the asymmetric device, respectively, along with the iodine interstitial defect level 0.48 eV away from the conduction band. In the symmetric device, both the transport layers are identical with thickness $d_{HTL,ETL} = 20$ nm with $\mu_{HTL,ETL} = 10^{-2}$ cm^2 V^{-1} s^{-1}, whereas in the asymmetric device the ETL has a thickness of $d_{ETL} = 50$ nm, and HTL thickness is $d_{HTL} = 20$ nm with the mobility remaining same as before. As a result of this asymmetry, the electrostatic potential drop across the ETL increases and that across the absorber layer and the HTL decreases. The decrease in the potential drop in the absorber layer results in reduced electron concentrations inside the absorber and thus to maintain charge neutrality the hole concentrations in the absorber layer increases as shown in Fig. 8c. Now since the iodine interstitial defect has $k_n \ll k_p$, the smaller the electron concentration n is compared to the hole concentration p, the better it is for the device as it makes $nk_n \ll pk_p$. This is shown in Fig. 8d where a comparison of the solid blue curve and dashed purple curve reveals that the electron capture rate nk_n, which is the recombination limiting rate in this case, decreases with the introduction of the asymmetry. Even though the hole capture rate increases with increasing hole concentration, the "deep" defect criterion $nk_n = pk_p$ is completely avoided inside the asymmetric device. The recombination efficiency of the defect
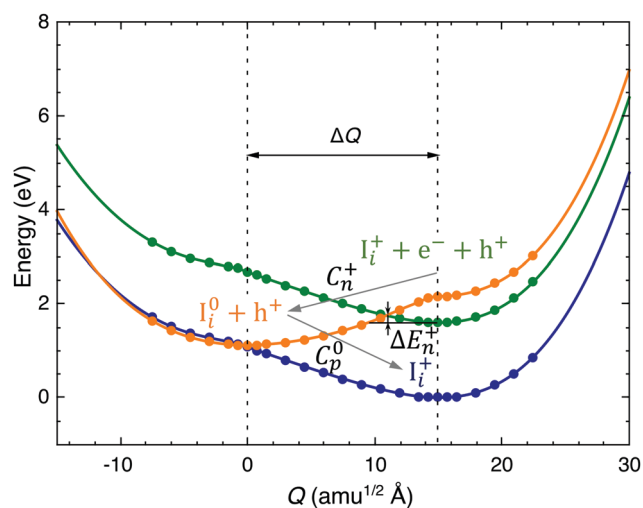

Fig. 7 The figure shows the potential energy surfaces for charge state transitions as a function of a generalized configuration coordinate. The green curve shows the potential energy surface of a system with an electron (e^-) at the conduction band minimum, a hole (h^+) at the valence band maximum and an iodine interstitial defect in its unoccupied state I_i^+. The I_i^+ defect captures an e^- from the conduction band and transitions to the I_i^0 state represented by the orange curve. Semi-classically the process of transition from $I_i^+ \rightarrow I_i^0$ needs to overcome a potential barrier of ΔE_n^+ as determined by the intersection of the potential energy surfaces (green and orange curves) of the two charge states. The defect I_i^0 then captures a hole from the valence band and relaxes the system back to I_i^+ represented by the blue curve. The small barrier of ΔE_n^+ resulting from the strong anharmonicity of the potential energy surface of I_i^+ makes the electron capture process slower in comparison to the hole capture process as there is no such barrier between the orange and the blue curve. Reprinted figure with permission[35] from Xie Zhang *et al.*, *Phys. Rev. B*, **101**, 140101(R) ©2020, American Physical Society. (https://doi.org/10.1103/PhysRevB.101.140101).

level in the asymmetric device represented by the purple open symbols also decreases and is entirely determined by the electron capture rate nk_n. In Fig. 8e we obtain the non-radiative recombination rate R_{SRH} through the iodine interstitial defect level for a defect density of $N_T = 10^{15}$ cm^{-3} across the
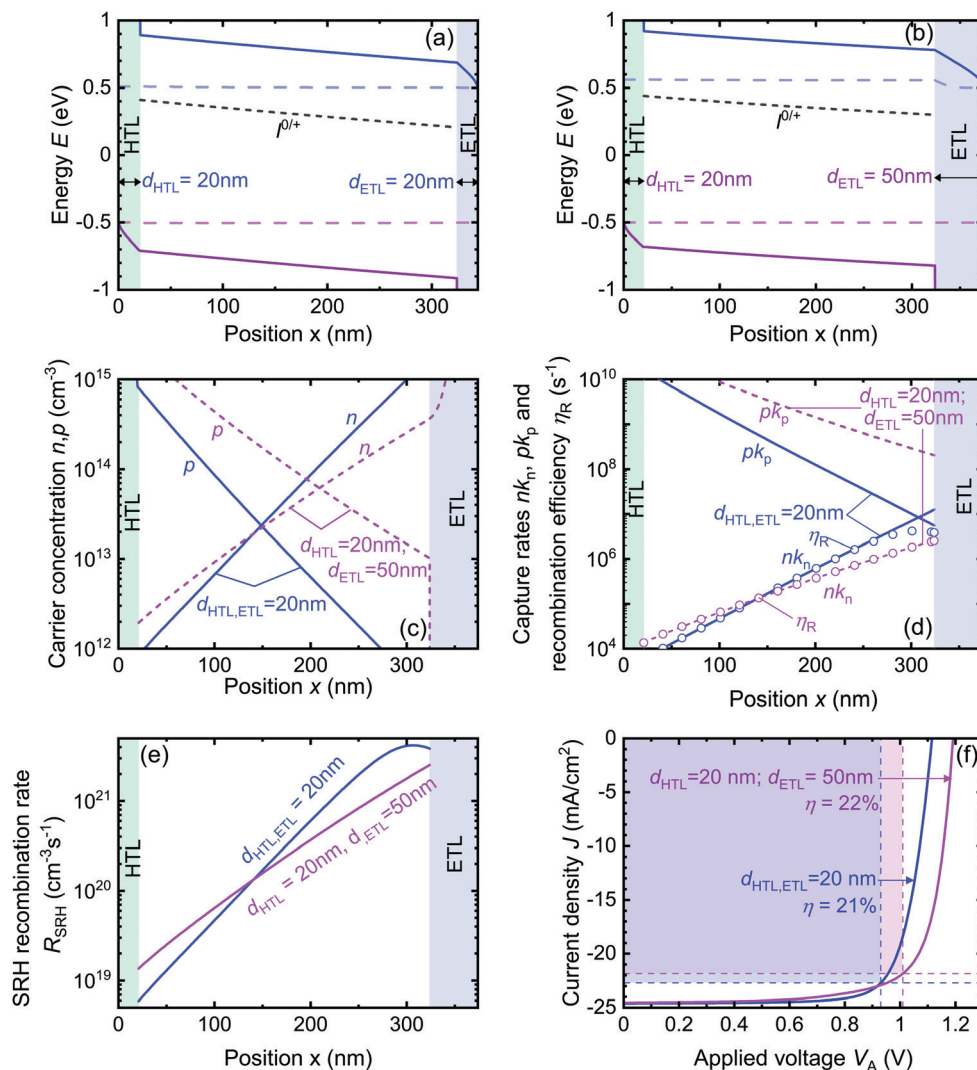
Fig. 8 Comparison of a symmetric and an asymmetric device with an iodine interstitial defect level. (a) The band diagram of the symmetric device with $d_{HTL,ETL}$ = 20 nm along with the iodine interstitial defect level 0.48 eV from the conduction band. (b) The same but for the asymmetric device which has d_{HTL} = 20 nm and d_{ETL} = 50 nm. (c) The electron concentration n and hole concentration p through the symmetric is represented by the curves in blue and that through the asymmetric device is represented in purple. (d) The electron and hole capture rates nk_n and pk_p of the symmetric device is shown by the solid blue curves, and that of the asymmetric device are shown by the dashed purple curves. The respective recombination efficiency η_R are shown by the open symbols. (e) The Shockley–Read–Hall recombination rate R_{SRH} for a defect density of $N_T = 10^{15}$ cm^{-3} across the absorber and the interfaces for the symmetric (blue curve) and the asymmetric device (purple curve). (f) The J–V curve for the symmetric (blue curve) and the asymmetric device (purple curve).

interfaces and the absorber, and we see a substantial decrease in the amount of non-radiative recombination. The reduced non-radiative recombination leads to an improvement in performance of the PSC as corroborated by the increase of V_{oc} by approximately 100 meV and device efficiency η (%) by more than 4.7% as shown in Fig. 8f. Fig. 8f reproduces the same trend of increase in V_{oc} with ETL thickness as studied from the experimental results in Fig. 6.

We used the same set of capture coefficients ($k_n = 7 \times 10^{-9}$ cm^3 s^{-1} and $k_p = 2 \times 10^{-5}$ cm^3 s^{-1}) and assumed a field free absorber to mimic the screening of the electric field by mobile ions in perovskite solar cells to reproduce the trend of experimentally observed V_{oc} as a function of PCBM thickness. We obtained the best fit (light blue line in Fig. 6b) between the

experimentally observed V_{oc} and simulated V_{oc} for a combination of absorber defect density of $N_T = 5 \times 10^{13}$ cm^{-3} and an interface defect density of $N_{int} = 6 \times 10^9$ cm^{-2} at both interfaces. This correspond to an electron lifetime of $\tau_n \approx 2$ μs in the absorber and a surface recombination velocity $S_n = 42$ cm s^{-1} for electrons, where S_n is analytically defined as $S_n = N_{int} \times k_n$. The electron capture coefficient being much smaller compared to the hole capture coefficient, the electron capture acts as the rate limiting step at open circuit. However, even though we reproduce the increase in V_{oc} with PCBM thickness due to reduced defect mediated recombination, this is not evidence that the asymmetric capture coefficients have to be the only or the major cause of this trend. Additional effects such as e.g. better coverage of rough perovskite surfaces may be superimposed, whose effects we would

not be able to disentangle from the effects of asymmetric capture coefficients.

Effect of the transport layer mobility on the device efficiency

The carrier selective transport layers (TLs) on either side of the perovskite absorber layer are responsible for the transport of the photogenerated charge carriers from the absorber layer to the extracting electrodes. However, the organic TL suffer from low mobilities such that $\mu_{HTL,ETL} \approx 10^{-5}$–$10^{-2}$ cm^2 V^{-1} s^{-1},[86,87] which is orders of magnitude lower than the mobility exhibited by the perovskite layer $\mu_{pero} \approx 1$–10 cm^2 V^{-1} s^{-1}.[88] These low mobilities of the state-of-the-art transport layers act as

an efficiency limiting factor for PSCs.[87] To investigate this relationship between TL mobility and device efficiency we choose to look at both symmetric as well as asymmetric perovskite device structures with a single defect level given by the iodine interstitial defect as was used in the previous section. In Fig. 9a to d we plot the band diagrams of four different device geometries at three different values of TL mobilities such that $\mu_{HTL,ETL} = [10^{-5}$ cm^2 V^{-1} s^{-1}, 10^{-2} cm^2 V^{-1} s^{-1}, 10 cm^2 V^{-1} s^{-1}] to show the position of the quasi-Fermi level as a function of the transport layer mobilities.

In Fig. 9a and b, we show the band diagrams of the undoped device geometries as for $\varepsilon_{pero} = 33.5$ and $\varepsilon_{pero} \rightarrow \infty$, respectively.
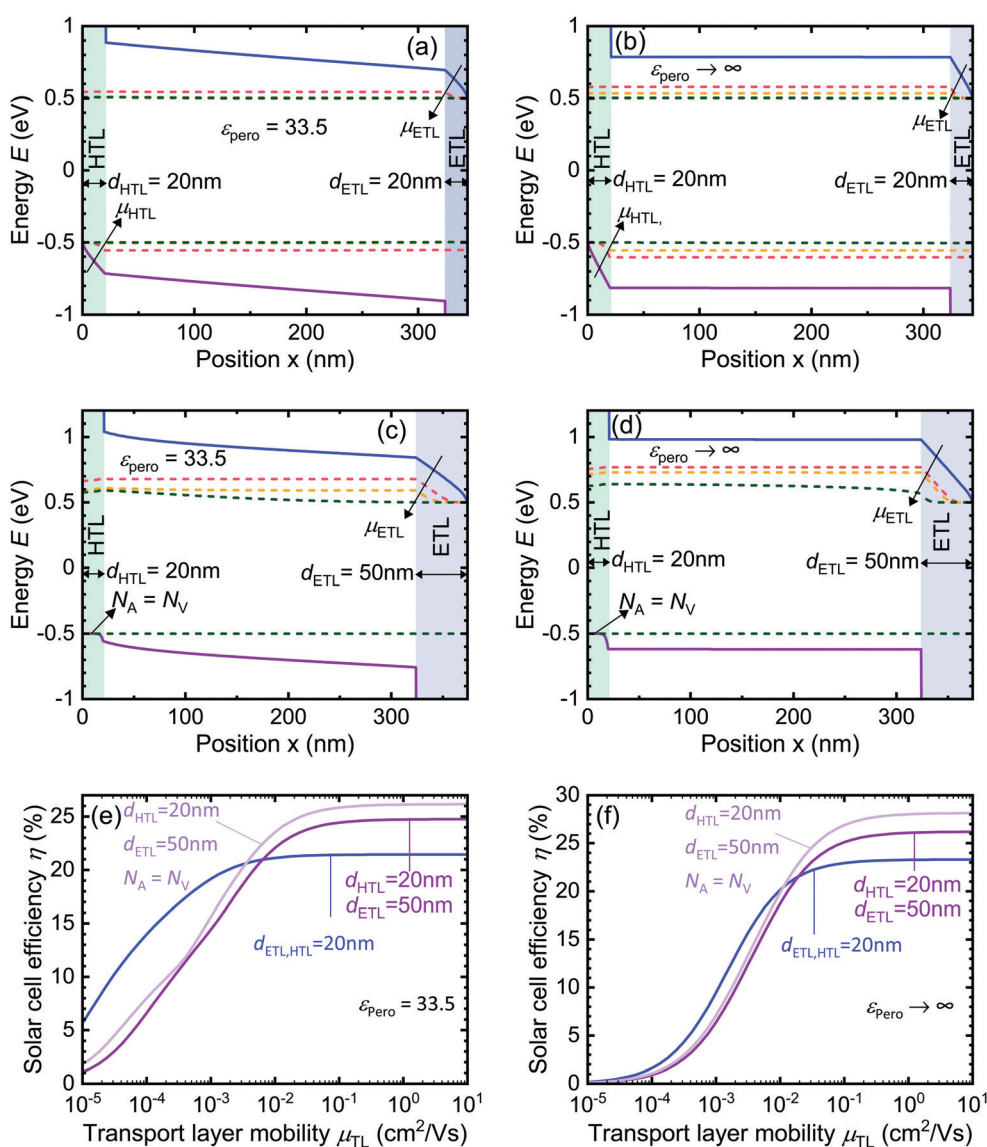

Fig. 9 Effect of transport layer mobility on the efficiency of the device. (a) Band diagram of a symmetric device with $\varepsilon_{pero} \rightarrow 33.5$, $d_{HTL,ETL} = 20$ nm and with one defect level at 0.48 eV away from the conduction band at three different values of transport layer mobility $\mu_{HTL,ETL} = [10^{-5}$ cm^2 V^{-1} s^{-1}, 10^{-2} cm^2 V^{-1} s^{-1}, 10 cm^2 V^{-1} s^{-1}]. (b) Same as in (a) but now the permittivity of the absorber $\varepsilon_{pero} \rightarrow \infty$. (c) Band diagram of an asymmetric device with $\varepsilon_{pero} \rightarrow 33.5$, $d_{ETL} = 50$ nm, $d_{HTL} = 20$ nm, acceptor doped HTL layer and with one defect level at 0.48 eV away from the conduction band at three different values of transport layer mobility as before. (d) Same as in (c) but now the permittivity of the absorber $\varepsilon_{pero} \rightarrow \infty$. (e) The efficiency η (%) of the different solar cell geometries with $\varepsilon_{pero} \rightarrow 33.5$ as a function of transport layer mobility. (f) Same as in (e) but for solar cell geometries with $\varepsilon_{pero} \rightarrow \infty$.

As we increase the TL mobility from 10^{-5} cm^2 V^{-1} s^{-1} to 10 cm^2 V^{-1} s^{-1}, the electron quasi-Fermi level (E_{fn}) and hole quasi-Fermi level (E_{fp}) do not bend along the curvature of the ETL conduction band and HTL valence band, respectively. At high mobilities due to faster charge extraction, we get completely flat quasi-Fermi levels throughout the device. As a result of the faster transport of carriers through the TL, electron and hole concentration inside the device decrease, thereby inhibiting recombination. This enhances the efficiency of the device as shown by the blue curve in Fig. 9e and f for ε_{pero} = 33.5 and $\varepsilon_{pero} \to \infty$, respectively. It is important to note the sensitivity of the recombination and hence the device efficiency on the carrier concentration inside the device. In Fig. 9a, the E_{fn} and E_{fp} for $\mu_{HTL,ETL}$ = 10^{-2} cm^2 V^{-1} s^{-1} and $\mu_{HTL,ETL}$ = 10 cm^2 V^{-1} s^{-1} overlap and this in turn makes the efficiency of the device almost constant when the TL mobility varies between $\mu_{HTL,ETL}$ = 10^{-2} cm^2 V^{-1} s^{-1} and 10 cm^2 V^{-1} s^{-1} as shown in Fig. 9e.

In Fig. 9c and d the asymmetric device has a longer ETL and a doped HTL such that $N_A = N_V$. As a result of the doping of the HTL, the E_{fp} is pinned to the valence band and does not move away from the valence band even when the TL mobility is increased. However, the E_{fn} moves away from the conduction band and we see a significant improvement in the efficiency of the device as shown by the light purple curve in Fig. 9e and f. This is because the recombination through the defect level, which in this case is the iodine interstitial level, is limited by the slower of the two capture rates. The decrease of the electron concentration inside the device with the increase of TL mobility reduces the electron capture rate within the device, thus improving the device efficiency.

When the TL mobilities $\mu_{HTL,ETL} < 10^{-2}$ cm^2 V^{-1} s^{-1} and are thus much slower than the perovskite mobility, even introducing the asymmetry in the device would not necessarily improve the device efficiency as seen in both Fig. 9e and f. The poor TL mobility results in slow charge transport leading to charge accumulation inside the transport layers as denoted by the bending of quasi-Fermi level splitting along the curvature of the conduction band or valence band of the TL in Fig. 9a–d. As an effect of the bending of the quasi-Fermi level along the bands, the carrier concentration inside the absorber layer is much higher and the efficiency is lower.

Conclusion

Defects play a key role in limiting conversions efficiencies of photon energy to electrical energy by enabling alternate channels of carrier recombination for excited carriers to escape from their respective bands before they could be extracted at the contacts. The recombination rates depend on the actual values of the capture coefficients for electrons and holes and on their densities. Because capture of electrons and holes are two processes that have to happen in series to allow for a single recombination event, it is always the slower of the two processes that limits the time constant for recombination. The capture rates depend on the relative concentrations of

electrons and holes and thereby also on the device geometry and the electrostatics of the solar cell. A device geometry that maximizes the volume where one capture rate is particularly slow and therefore limits the total recombination rate would therefore be one that reduces the relative impact of defects on device performance. It would be a defect tolerant device geometry. The answer to the question which device geometry is best does, however, not have a generic answer. We have to distinguish between different situations. In particular, we have to consider the ratio of the capture coefficients, *i.e.* how easy or difficult is it for a certain defect to capture one carrier relative to capturing the other. Defect assisted recombination is often modelled using equal capture coefficients for electrons and holes, however, we show here that this is in general a rather unlikely scenario. Evidence for this claim comes both from calculations using the harmonic approximation but also from literature data of actual defects in halide perovskites. With the assumption of asymmetric capture coefficients of a defect, we can modify a device geometry in such a way that the density of the carrier associated with the slower of the two capture coefficients decreases inside the absorber layer. This decrease will then slow down recombination. The implementation of these generic design principles can be done in a variety of ways and will depend on the device and the dominant defect levels in question.

After discussing the general principles of defect tolerant device geometries, we focus on one relevant example of recombination in methylammonium lead halide based solar cells. We base our calculations on assuming that the capture coefficients calculated by Zhang *et al.* for iodine interstitials are correct and dominating recombination in the lead-halide absorber layer. Since Zhang *et al.* predicted higher capture coefficients for holes than for electrons, the recombination could be further reduced by slowing down the slower of the two rates (the electron capture) by reducing the electron concentration and increasing the hole concentration. This can be done in various ways with p-type doping being the most obvious approach from a conceptual point of view. Due to their ionic nature, the creation of stable doping profiles in halide perovskites is however challenging. Thus, we show how changing the thickness of charge transport layers can be used to modulate the electron and hole concentrations and thereby improve photovoltaic performance. We find that slightly higher thicknesses for the ETL than for the HTL should be beneficial for device performance. This interestingly agrees with experimental findings in MAPI solar cells with high open-circuit voltages.

Experimental section

Device fabrication

Materials. Methylammonium iodide (MAI) was purchased from Greatcell Solar. Lead acetate trihydrate (Pb(CH$_3$COO)$_2$· 3H$_2$O, >99.5%) and Bathocuproine (BCP, >99.8%) were purchased from TCI. Lead chloride (PbCl$_2$, >99.999%) was

purchased from Sigma-Aldrich Company Ltd. Poly[bis (4-phenyl)-(2,4,6-trimethylphenyl)amine] (PTAA, M_n = 17 900, M_w = 33 000) was purchased from Xi'an Polymer Light Technology Corp (China). [6,6]-Phenyl-C_{61}-butyric acid methyl ester (PCBM) was purchased from Solenne (Netherlands). Toluene (T), (purity of 99.8%), N,N-dimethylformamide (DMF, 99.8%), dimethyl sulfoxide (DMSO, >99.9%) and chlorobenzene (CB, 99.8%) were purchased from Sigma-Aldrich and used as received.

Device fabrication. The pre-patterned ITO substrates (2.0 × 2.0 cm^2) were bought from KINTEC (Hong Kong) and ultrasonically cleaned with soap (Hellmanex III), deionized water, acetone and IPA in succession for 10 min. The as-cleaned ITO substrates were treated with oxygen plasma for 12 min and transferred to a N_2-filled glovebox. 80 μl PTAA (2 mg ml^{-1} in toluene) solution was spin-coated onto the ITO substrates with a two-consecutive step program at 500 rpm for 4 s (with a ramping rate of 500 rpm s^{-1}) and 4500 rpm for 20 s (with a ramping rate of 800 rpm s^{-1}), then the samples were thermally annealed at 100 °C for 10 min and afterwards cooled down to room temperature. The PTAA layer thickness is ∼16 nm. The perovskite precursor solution prepared by mixing Pb(CH$_3$COO)$_2$·3H$_2$O (0.54 M), PbCl$_2$ (0.06 M), DMSO (0.6 M) and MAI (1.791 M) in DMF/DMSO was stirred at room temperature for 24 hours, heated to 75 °C for 20 min and filtered with a 0.45 μm PTFE filter prior to use. To fabricate the solution-processed perovskite layer, 120 μl precursor solution (RT) was spin-coated by a two-consecutive step program at 1400 rpm for 15 s (with a ramping rate of 350 rpm s^{-1}) and 6000 rpm for 40 s with a ramping rate of 767 rpm s^{-1}. The samples were immediately annealed on a hotplate at 75 °C for 2 min. Afterwards they were cooled down to room temperature. 60 μl PCBM solution (5, 10, 15 and 20 mg ml^{-1} in CB and toluene) was spin-coated on the top of perovskite layer at a speed of 1200 rpm for 60 s (with a ramping rate of 400 rmp s^{-1}) as electron transport layer (ETL). The thickness is 14 nm, 32 nm, 45 nm, or 60 nm respectively. For the drying of the PCBM layer the samples were left in an open Petri dish for 20 min, without additional annealing. Finally, 8 nm BCP (0.2 Å s^{-1}) and 80 nm Ag was thermally evaporated in a separate vacuum chamber (<5 × 10^{-6} Pa) through a metal shadow mask to define an aperture area of 0.16 cm^2 by the overlap of the ITO and the Ag.

Device characterization

Current–voltage-characterization (J–V). The current–voltage curves were measured on a calibrated AM1.5 spectrum of a class AAA solar simulator (WACOM-WXS-140S-Super-L2 with a combined xenon/halogen lamp-based system) providing a power density of 100 mW cm^{-2}. The forward scan (−0.1 V to 1.27 V) and the subsequent reverse voltage scan (1.27 V to −0.1 V) were each carried out at a scan speed of 100 mV s^{-1}, using a Series 2420 SourceMeter (Keithley Instruments). All measurements were carried out under inert atmosphere in a sealed, electrically contacted measurement box in glovebox. Each sample contains four solar cells with an active cell area of 0.16 cm^2.

Author contributions

Conflicts of interest

There is not conflicts of interest.

Acknowledgements

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