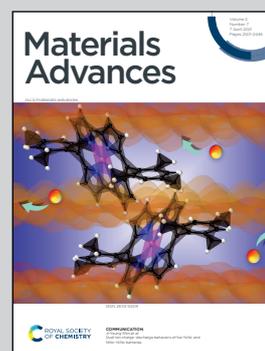


Showcasing research from the Fraunhofer R&D Center for Electromobility Bavaria at Fraunhofer Institute for Silicate Research ISC in Würzburg, Germany.

How interdiffusion affects the electrochemical performance of LiMn_2O_4 thin films on stainless steel

The Fraunhofer R&D Center for Electromobility Bavaria focuses on the development, testing, and optimization of materials, components, and processes for lithium solid-state batteries, among others. This study, in a joint effort with Empa – Swiss Federal Laboratories for Materials Science and Technology, examines the interdiffusion that occurs between electrode material and the substrate during high temperature crystallization, a necessary step during the sequential production of thin-film solid-state batteries. Indium tin oxide is an effective interdiffusion barrier leading to improved discharge capacities. Image copyright of Fraunhofer ISC (M. Geiling und K. Selsam).

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How interdiffusion affects the electrochemical performance of LiMn_2O_4 thin films on stainless steel†

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The sequential production process of thin-film solid-state batteries (TF-SSB) requires high temperatures up to 700 °C in order to achieve crystallized cathode films with high capacities and ceramic electrolytes with high ionic conductivities. This presents challenges for thermal interfacial stability between the electrode and electrolyte as well as electrode and substrate. Interdiffusion processes between the substrate and electrode at elevated temperatures are critical and may explain the low utilization of cathode active materials in TF-SSBs. This study examines the interdiffusion that occur between LiMn_2O_4 (LMO) thin films and the flexible stainless steel (StSt) substrate. Grazing incidence X-ray diffraction spectroscopy, time-of-flight secondary ion mass spectrometry and X-ray photoelectron spectroscopy reveal that during the crystallization at 600 °C and 700 °C, LMO undergoes lithium and manganese depletion whereas elements from the substrate (Fe, Cr, Al, Pt) diffuse into the LMO spinel structure. The combination of these effects, which are exacerbated by the higher crystallization temperature, results in a significant capacity loss during galvanostatic cycling. Various interlayers, including gold, platinum and indium tin oxide (ITO) were tested as conductive interdiffusion barriers. The use of ITO, unlike gold and platinum, as a barrier layer essentially prevents the interdiffusion, leading to an improved discharge capacity of 4.2 $\mu\text{A h cm}^{-2}$ (600 °C) and 5.1 $\mu\text{A h cm}^{-2}$ (700 °C). The results indicate that ITO is an effective interdiffusion barrier on flexible StSt current collector foils, which can enable higher performance TF-SSBs as well as cost effective roll-to-roll manufacturing.

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Introduction

Thin-film batteries are of particular interest for various applications including microelectronics, such as flexible microdevices, wearables, medical implants, *etc.* Thin-film solid-state batteries (TF-SSB) promise better cell performance in terms of energy density, cycle stability, cycle life and safety than conventional batteries. One of the key elements of solid-state batteries is the ceramic or glassy electrolyte, which replaces the liquid electrolyte in conventional lithium-ion batteries. Without liquids, the risk of leakage and other safety related issues can be reduced or eliminated.^{1–7} Furthermore, Li-ion conductive ceramics, *e.g.* $\text{Li}_7\text{La}_3\text{Zr}_2\text{O}_{12}$ (LLZO)^{8–11} or $\text{Li}_{1.3}\text{Al}_{0.3}\text{Ti}_{1.7}(\text{PO}_4)_3$ (LATP),^{12,13} and

glassy lithium phosphorus oxynitrides (LIPON)^{14,15} form a stable solid electrolyte interface (SEI) or have a higher potential window up to 5 V vs. Li/Li^+ , so that less degradation of the electrolyte is expected and the implementation of high-voltage active materials might be feasible.^{16–18} Li *et al.* demonstrated, for instance, a high-voltage $\text{LiMn}_{1.5}\text{Ni}_{0.5}\text{O}_4$ TF-SSB using a LIPON electrolyte with a capacity retention of 90.6% after 10 000 cycles and a coulombic efficiency of almost 100%. Although the performance was promising, the specific capacity of the cathode layer was about 83% of the theoretical value of 148 mA h g^{-1} .¹⁵ This lack of utilization of the active material has been reported for many thin-film battery studies. It is reasonable to presume that the high temperature crystallization process of the deposited active material may influence the electrochemical performance due to interdiffusion processes of substrate and active material elements. Li *et al.* used platinum (Pt) on aluminum (Al) as substrate. Further studies with Pt,^{19,20} gold^{21,22} (Au), stainless-steel²³ (StSt) or metal alloy²⁴ substrates have been published and show similar behaviour.¹⁵ Komaba *et al.* reported a relationship between the temperature of the crystallization process and the cycling

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performance of LMO thin-layers on a StSt substrate.²³ Diffusion of Fe and Cr atoms into the LMO layer was established *via* depth-profile X-ray photoelectron spectroscopy (XPS). Kim *et al.* slightly enhanced the utilization of LMNO on a Pt-coated StSt foil due to integration of an indium tin oxide (ITO) layer between Pt and StSt.²⁰ A survey of these publications demonstrates the high impact of substrate material and the crystallization temperature on the battery performance.

A cost effective production process for TF-SSBs is roll-to-roll manufacturing *via* sequential deposition and crystallization of the cathode and electrolyte on a flexible substrate. Each step requires a separate temperature treatment, which can cause interdiffusion. The highest temperature, ≥ 700 °C, is required after deposition of the ceramic electrolyte on top of the cathode layer to crystallize the ceramic electrolyte in order to achieve high ionic conductivities.^{11,25,26} These high temperatures enhance the interdiffusion processes at each interface and make their impact more significant.

This study takes a deeper look into the interdiffusion processes, their influence on the lattice structure of the active cathode material and on the subsequent electrochemistry. For this purpose, LMO cathode layers were deposited on flexible StSt foil *via* sol-gel dip-coating and crystallized at 600 °C and 700 °C. The three different conductive layers Pt, Au and ITO were tested as protection layers against interdiffusion between LMO and StSt. This systematic comparison elucidates the differences between the various substrates and reveals many phenomena like lithium depletion, interdiffusion, oxide layer formation and their impact on the electrochemistry. These results show that ITO is an excellent interdiffusion barrier between LMO and StSt and enables better electrochemical performance than using Au or Pt interlayers.

Experimental

Preparation of substrate and interlayers

The base substrate in this study was a flexible and heat-resistant ferritic chrome steel with 20 wt% Cr and 5 wt% Al (thickness = 30 μm , 1.4767, X8CrAl 20-5, H + S Präzisionsfolien GmbH). The StSt foil was cut into 5×5 cm² samples. Au and Pt interlayers were deposited on top of the StSt substrate *via* DC sputtering (Bulzer Union 040) in an argon atmosphere with a pressure of 0.1 mbar. The sample-target distance was 7 cm and the sputter current was set to 40 mA. The deposition times were 10 min for Au and 15 min for Pt to achieve similar layer thicknesses of *ca.* 100 nm. ITO layers were coated onto StSt using a sol-gel technique.²⁷ The solution was prepared by dissolving indium(III) acetate and diethanolamine in 1-butanol. This solution was refluxed for 10 min. Subsequently, ethoxyacetic acid and tin(II) 2-ethylhexanoate were added and refluxed for 1 h. The solvent was removed by a rotary evaporator and the sol adjusted to a defined solid content of 4.6 wt% by again adding 1-butanol. The coating of the ITO sol onto the StSt substrate was performed with a self-build dip-coater (Fraunhofer ISC) in a controlled air atmosphere with 25% relative humidity at 25 °C

and with a withdraw velocity of 200 mm min⁻¹. The ITO gel films were crystallized rapidly at 600 °C for 10 min (P330 muffle furnace, Nabertherm), which resulted in crystalline layers of *ca.* 100 nm with an electrical sheet resistivity of approx. 15 m Ω cm.

Preparation of LMO layer

The LMO active material was coated on the various substrates using dip-coating. The LMO sol was synthesized by dissolving stoichiometric amounts of lithium acetylacetonate and manganese(II) acetate tetrahydrate in a solution of 2-(2-methoxyethoxy) acetic acid and ethanol. The mixture was stirred for 30 min under reflux at 120 °C. The solid content was 10 wt%. The dip-coating procedure was performed in a controlled air atmosphere with 25% relative humidity at 25 °C. The substrates were immersed for 10 s in the sol solution, withdrawn with a constant speed of 50 mm min⁻¹ and immediately dried at 180 °C for 30 min. Subsequently, the LMO-coated substrates were rapidly annealed at 600 °C for 1 h or 700 °C for 1 h (P330 muffle furnace, Nabertherm) in an ambient air atmosphere. The samples were also rapidly cooled by opening the furnace at the crystallization temperature and removing the LMO-coated substrates.

Cell assembly

Before cell assembly, the backside of the coated StSt was polished to remove the oxide layer formed during the annealing step. Cathodes with a diameter of 16 mm (2.01 cm² area) were cut and dried in vacuum (10^{-2} mbar) at 110 °C for 10 h. Pouch cells were assembled in an argon-filled glovebox ($\text{H}_2\text{O} < 1$ ppm and $\text{O}_2 < 1$ ppm) with a Whatman GF/F glass microfiber separator and lithium metal as counter electrode. A mixture of ethylene carbonate and dimethyl carbonate (EC:DMC volume ratio of 1:1) with 1 M LiPF₆ was used as electrolyte (LP30, BASF).

Electrochemical characterization

Galvanostatic cycling with limited potential (GCPL) and electrochemical impedance spectroscopy (EIS) were carried out with a galvanostat (VMP-300, Biologic) in a temperature-controlled climatic chamber (Mettmert IPP260^{PLUS}) at 25 °C. After a rest step of 12 h for acclimatization, galvanostatic charging and discharging were performed with a current density of 30 $\mu\text{A cm}^{-2}$ between 4.4 V *vs.* Li/Li⁺ and 3.2 V *vs.* Li/Li⁺ for 24 cycles. After each constant current charge step, a constant voltage step at 4.4 V with the end criteria of either 5 min or a current density limit of 0.5 $\mu\text{A cm}^{-2}$ was executed. After the 24 cycles, the cells were charged at 30 $\mu\text{A cm}^{-2}$ to 4.4 V, followed by a constant voltage step at 4.4 V for either 3 h or until a current density limit of 0.75 $\mu\text{A cm}^{-2}$ was reached to obtain quasi-stable conditions for the EIS measurements. The EIS measurements were carried out at 4.4 V in the frequency range between 1 MHz and 1 mHz with an amplitude of 5 mV. Two cells of each sample type were tested. The rate capability test was performed on a battery cyler (Maccor 4000) with the same cell set-up and test protocol as described above. The current densities were varied between 200 $\mu\text{A cm}^{-2}$ (approx. 40C) and



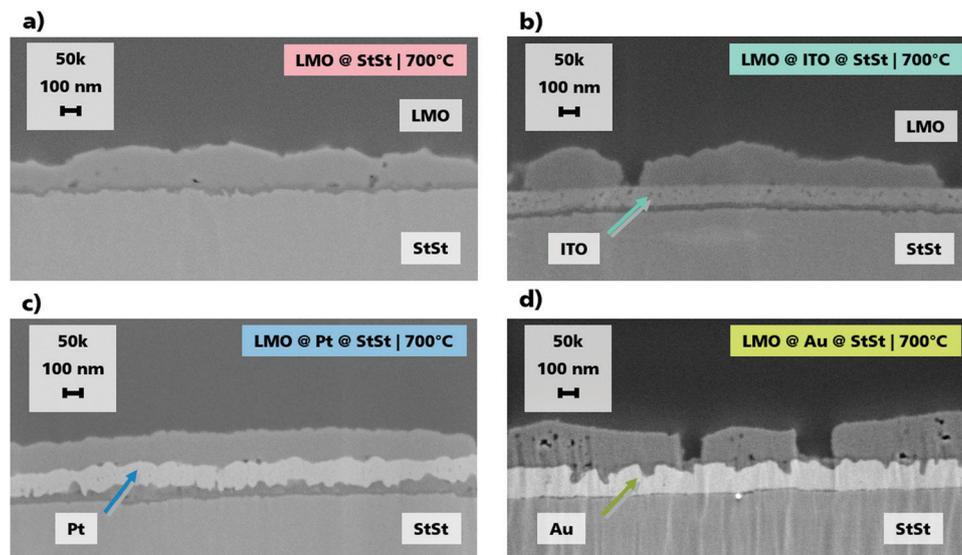


Fig. 1 SEM cross sections of (a) LMO@StSt (LS700), (b) LMO@ITO (LIS700), (c) LMO@Pt (LPS700) and (d) LMO@Au (LAS700) annealed at 700 °C for 1 h. LMO layer thicknesses are approx. 160 nm. Layer thicknesses of Pt, Au and ITO are approx. 100 nm.

$10 \mu\text{A cm}^{-2}$ (approx. 2C). These measurements were carried out in triplicate.

Structural characterization

Scanning electron microscopy (SEM) images have been collected on a ZEIS Supra 25 microscope with an accelerating voltage of 2.5 kV. The preparation of the cross sections were done by embedding the samples in an epoxy resin and polishing them with a JEOL SM-0910 cross-section polisher in argon atmosphere.

Grazing incidence X-ray diffraction spectroscopy (GIXRD) was performed on a PANalytical Empyrean diffractometer with $\text{CuK}\alpha$ radiation between 15° and $60^\circ 2\theta$ with a step size of 0.02° and a scan duration per step of 5 s. The fixed incidence angle of the radiation was 0.5° .

Time-of-flight secondary ion mass spectrometry profiles (ToF-SIMS) were measured in a ToF.SIMS5 system from ION-TOF. Depth profiling was performed by Cs^+ ion sputtering with an acceleration voltage of 1 kV and a current of 50 nA on an area of $300 \times 300 \mu\text{m}^2$. Bi^+ primary ions, with an acceleration voltage of 25 kV and a current of 2 pA on a $100 \times 100 \mu\text{m}^2$ area within the sputtering crater, were used for analysis.

X-ray photoelectron spectroscopy (XPS) measurements were performed with monochromatic $\text{Al K}\alpha$ X-ray source (Surface Science Instruments, S-Probe). Single scans were done with a 32 eV pass energy, which provides high-resolution scans with the full-width half maximum of 1 eV. The scans were calibrated with the C_{1s} peak at 284.60 eV.

Results and discussion

Morphology

The homogenous and dense ITO, Pt and Au interlayers with layer thickness of *ca.* 100 nm are shown in the SEM cross

sections in Fig. 1. The LMO layer thickness does not depend on the substrate and is approx. 160 nm. When no interlayer material is used, an additional region of dark contrast can be seen between the LMO and StSt, which is indicative of the formation of oxide layers during annealing. This formation is confirmed in the ToF-SIMS depth profiles as will be discussed below. Such areas of dark contrast are also seen when ITO and Pt are used as the interlayer materials. In contrast to the other three samples, when Au is implemented as an interlayer, the oxide layer formation takes place at the LMO and Au interface. Cracks between the layers are not observed, therefore a good contact between the layers is ensured. The top view SEM images (Fig. S1, ESI[†]) show an increase in particle size with elevated temperature for all samples.

Electrochemical performance

Galvanostatic cycling shows how the different interlayers and crystallization temperature influence the cell performance including the discharge capacity, coulombic efficiency and overvoltage (Fig. 2 and Table 1). With a crystallization temperature of 600 °C, LS600 has the lowest discharge capacity of $1.99 \mu\text{A h cm}^{-2} \pm 0.03 \mu\text{A h cm}^{-2}$ (Table 1). Through the use of an interlayer, the LMO discharge capacities of the LIS600, LPS600 and LAS600 are improved (Fig. 2a and Table 1). Elevating the crystallization temperature to 700 °C causes the LMO capacity on StSt or Au (LS700 and LAS700) to be negligible and on Pt (LPS700) to be reduced by a factor of three. In contrast, the LMO discharge capacity on ITO increases to $5.06 \mu\text{A h cm}^{-2} \pm 0.46 \mu\text{A h cm}^{-2}$ (LIS700). Furthermore, this combination had the highest coulombic efficiency ($97.3\% \pm 0.3\%$) (Fig. 2e) and capacity retention ($99.1\% \pm 2.3\%$) (Fig. 2b). Although LS600 and LAS600 have lower capacities than LIS700, the cycling behaviour is also stable. The capacity retention of LMO@Pt is substantially lower than all other samples (*ca.* 86% and *ca.* 46% for LPS600 and



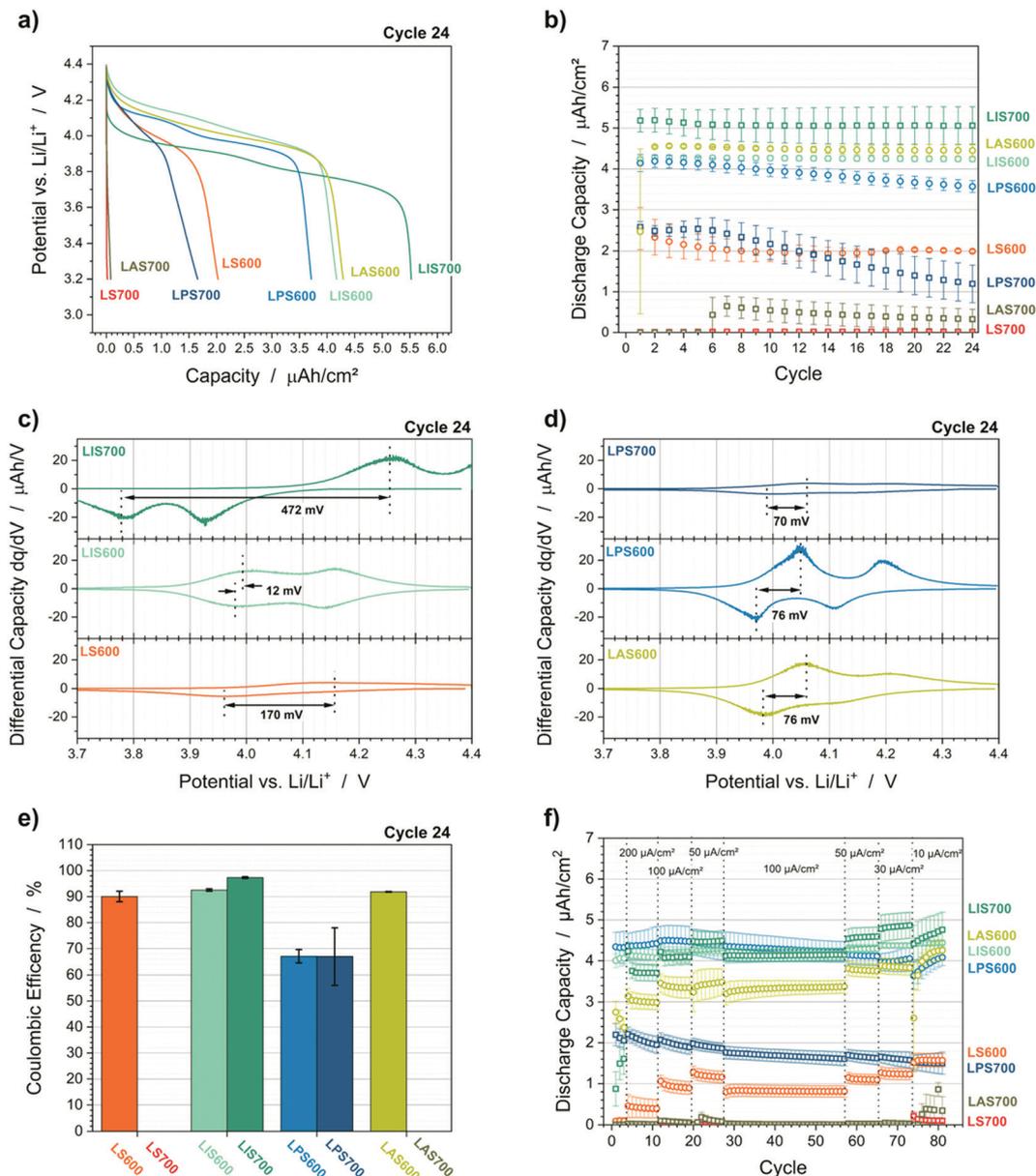


Fig. 2 GCPL results of the LMO@StSt (red), LMO@ITO (cyan), LMO@Pt (blue) and LMO@Au (green). (a) Voltage profile of cycle 24 (representative cells), (b) discharge capacity as a function of cycles, (c and d) differential capacity plots of cycle 24 (representative cells), (e) Coulombic efficiencies of cycle 24 and (f) discharge capacities during C-rate test.

LPS700, respectively). This capacity retention, combined with a low coulombic efficiency of *ca.* 67%, indicates irreversible side reactions during charge. This behaviour is confirmed by the performance during the rate capability tests (Fig. 2f). The rate capability is evaluated by the change in the LMO discharge capacity between the $100 \mu\text{A cm}^{-2}$ (Cycle 19) and $30 \mu\text{A cm}^{-2}$ (Cycle 73) constant current steps. LPS600 and LPS700 show a decrease of the LMO discharge capacity of 9% and 16%, respectively. This capacity fade is unique for the Pt cells and cannot be explained by polarization processes. Consequently, it further suggests irreversible side reactions. All other samples demonstrate the expected increase of the discharge capacity with lower currents due to the current dependence of

polarization processes of the LMO cathode and the ohmic resistance of the cell. The rate behaviour is reflected in the overvoltage from differential capacity plots, which is related to the concentration and charge transfer polarizations (Fig. 2c and d). The overvoltage is defined as difference between the peak positions of the first redox peak for charge and discharge. The overvoltage of LMO@ITO increases by a factor of 40 ($14 \text{ mV} \pm 2 \text{ mV}$ to $574 \text{ mV} \pm 143 \text{ mV}$) with elevated temperature, which results in an incomplete charge during the constant current step. LPS600, LPS700 and LAS600 show more moderate overvoltages in the range of *ca.* 66 mV to 79 mV.

A change in the ratio between the first and second redox peaks is also observable. This two-step redox process is related



Table 1 Electrochemical results obtained from galvanostatic cycling

| Sample | Discharge capacity cycle 24 ($\mu\text{A h cm}^{-2}$) | Coulombic efficiency cycle 24 (%) | Overtoltage cycle 24 (mV) | Capacity retention after 24 cycles (%) | Rate capability ^a (%) |
|------------------------------------|---|-----------------------------------|---------------------------|--|----------------------------------|
| 600 °C crystallization temperature | | | | | |
| LS600 (LMO@StSt) | 1.99 ± 0.03 | 90.1 ± 2.0 | 139 ± 30 | 97.3 ± 16.1 | 38 |
| LIS600 (LMO@ITO) | 4.22 ± 0.07 | 92.5 ± 0.4 | 14 ± 2 | 99.3 ± 0.2 | 6 |
| LPS600 (LMO@Pt) | 3.57 ± 0.15 | 67.1 ± 2.5 | 66 ± 9 | 86.4 ± 0.1 | n.a. ^b |
| LAS600 (LMO@Au) | 4.45 ± 0.16 | 91.8 ± 0.2 | 79 ± 3 | 97.9 ± 4.0 | 15 |
| 700 °C crystallization temperature | | | | | |
| LS700 (LMO@StSt) | No capacity | | | | |
| LIS700 (LMO@ITO) | 5.06 ± 0.46 | 97.3 ± 0.3 | 574 ± 143 | 99.1 ± 2.3 | 19 |
| LPS700 (LMO@Pt) | 1.19 ± 0.46 | 67.0 ± 11.0 | 76 ± 9 | 45.8 ± 13.6 | n.a. ^b |
| LAS700 (LMO@Au) | No capacity | | | | |

^a Ratio of discharge capacity 30 $\mu\text{A cm}^{-2}$: 100 $\mu\text{A cm}^{-2}$. ^b n.a.: negative values due to side reaction, not reasonable to analyse.

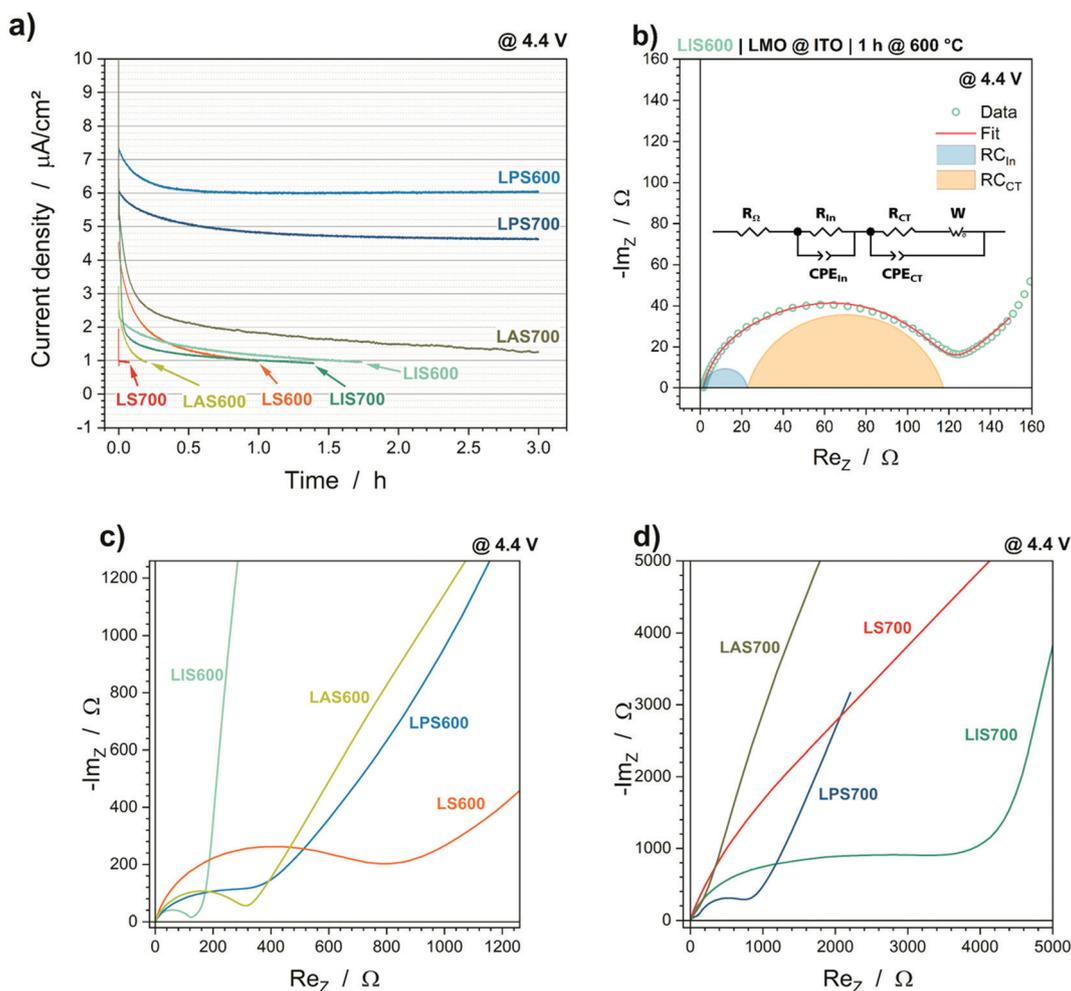


Fig. 3 (a) Current decay during the CV step @ 4.4 V vs. Li/Li^+ for 3 h and (b–d) Nyquist plots of the EIS data. (b) Fit of EIS data of LIS600 using the given equivalent circuit, (c) Nyquist plots of samples annealed at 600 °C and (d) Nyquist plots of samples annealed at 700 °C. LMO@StSt (red), LMO@ITO (cyan), LMO@Pt (blue) and LMO@Au (green).

to the ordering of half of the Li-ions on the tetrahedral 8a sites in the spinel lattice. The electrochemical extraction takes place *via* diffusion of the Li atoms from one 8a site to another over the 16c sites. The diffusion activation energies depend on the

atomic neighbours of the Li ion on the 8a-16c-8a diffusion pathway.^{28,29} Thus, a change in the ratio of the redox peaks might be explained by a change of the diffusion activation energies due to a change of the atomic neighbours in the crystal



lattice, *i.e.*, the substitution of Mn with elements of the interlayer or substrate. LIS600 shows a higher peak height for the second redox peak, which is reported in literature for LMO.²⁹ With Au and Pt interlayers, the second redox peak has lower maximum and peak area than the first. Furthermore, the second redox peak vanishes in the plot of the LS600 sample (Fig. 1c). This might suggest interdiffusion, which causes in substitution of Mn in the spinel lattice with substrate or interlayer elements for the Au, Pt and StSt samples. This phenomenon will be discussed below with the help of ToF-SIMS, GIXRD and XPS.

Electrochemical impedance spectroscopy

The overvoltages can be correlated with the concentration polarization and charge transfer polarization, which can be further evaluated *via* electrochemical impedance spectroscopy. The resistance of a possible insulating layer at the interface of LMO and the electrolyte R_{in} and the charge transfer resistance R_{CT} may be evaluated by fitting the impedance data with the equivalent circuit illustrated in Fig. 3b as demonstrated in the Nyquist plot in Fig. 3c and d. The resistances are summarized in Table 2. Further Nyquist plots including fit curves can be found in the supporting information (Fig. S3, ESI†).

The ohmic resistances R_{Ω} are similar, so the oxide layer formation between substrate/interlayer and LMO has, comparatively, little impact on the electrochemical performance. Significant differences are observable in the values of R_{in} and R_{CT} , which may be correlated with the overvoltages in the cycling experiments. When the samples are crystallized at 600 °C, LIS600 has the lowest R_{in} and R_{CT} , followed by comparable values from LAS600 and LPS600; the highest resistances occur for LS600. The morphology of the LMO crystallized at 600 °C is comparable (top view SEM images in Fig. S1, ESI†), which suggests that differences in the electrochemical active surface area are not responsible for this result. In contrast, there is an increase in particle size of the LMO layer crystallized at 700 °C on ITO, which leads to a lower surface area and the increase in R_{in} and R_{CT} . However, this increase cannot be completely explained by the change in morphology since R_{in} is a fifth of the total resistance for LIS600 and about a half for LIS700. Therefore, the formation of an insulating layer on the LMO surface with increasing temperature, which also depends on the interlayer material, is likely and will be discussed below.

Table 2 The ohmic resistance R_{Ω} , insulating layer resistance R_{in} and charge transfer resistance R_{CT} obtained by fitting the EIS data with the equivalent circuit as shown in Fig. 3b

| Sample | R_{Ω} (Ω) | R_{in} (Ω) | R_{CT} (Ω) |
|------------------------------------|---------------------------|-----------------------|-----------------------|
| 600 °C crystallization temperature | | | |
| LS600 (LMO@StSt) | 1.6 | 377.6 | 424.6 |
| LIS600 (LMO@ITO) | 1.4 | 21.3 | 94.7 |
| LPS600 (LMO@Pt) | 1.4 | 33.3 | 282.6 |
| LAS600 (LMO@Au) | 1.6 | 32.3 | 252.1 |
| 700 °C crystallization temperature | | | |
| LS700 (LMO@StSt) | No capacity | | |
| LIS700 (LMO@ITO) | 1.4 | 1397.0 | 2129.2 |
| LPS700 (LMO@Pt) | 1.8 | 88.6 | 806.1 |
| LAS700 (LMO@Au) | No capacity | | |

The charge transfer resistance R_{CT} is also highly dependent on the interlayer material. In addition to the high increase of R_{CT} with higher particle size, LPS700 shows low R_{in} and R_{CT} values, which suggest that the electrochemical reaction measured is not Li-ion extraction/insertion in the LMO cathode material, but a side reaction. In addition to the coulombic efficiency during cycling and impedance, this hypothesis is supported by the current decay during the CV step at the end of charge performed before the impedance measurement (Fig. 3a). The current density values of 6.0 $\mu\text{A cm}^{-2}$ and 4.6 $\mu\text{A cm}^{-2}$ throughout the entire CV step suggest that a faradaic current may be flowing for LPS600 and LPS700. In contrast, the cells containing the other interlayer materials have current density curves, which seem to be more capacitive in nature as they reach the current density limit of 0.75 $\mu\text{A cm}^{-2}$ in less than 2 h. This unique behaviour of the LMO@Pt samples might be related to the electrochemical dissolution of metallic platinum. The top view SEM images (Fig. S1, ESI†) show that there are cracks in the LMO layers, so that a direct contact between the Pt interlayer and the liquid electrolyte exists. The redox reaction of $\text{Pt} \rightarrow \text{Pt}^{2+} + 2\text{e}^{-}$ has a standard redox potential of 1.20 V vs. SHE (standard hydrogen electrode), which corresponds to 4.24 V vs. Li/Li⁺.³⁰ Even when considering an overpotential due to the use of an organic electrolyte and the deviation from the standard conditions, the electrochemical dissolution of Pt at 4.4 V vs. Li/Li⁺ is likely. In the case of a solid-state electrolyte combined with a dense cathode layer up to 1 μm , the risk of Pt dissolution would likely be small, but still present.

Compositional depth profiling by ToF-SIMS

Changes in discharge capacity, overvoltage and resistance suggest changes in the LMO spinel lattice during the crystallization process that depend on the interlayer and substrate material. Therefore, dedicated ToF-SIMS measurements were performed to detect which elements diffused in- and out-of the LMO active layer. Throughout this section, the detected mass-to-charge ratios (m/z) will be described by Li⁻ ($m/z = 7$ u), MnO⁻ ($m/z = 71$ u), AlO⁻ ($m/z = 43$ u), CrO⁻ ($m/z = 68$ u), InO⁻ ($m/z = 131$ u), Pt⁻ ($m/z = 195$ u), PtO⁻ ($m/z = 211$ u), Au⁻ ($m/z = 197$ u) and AuO⁻ ($m/z = 213$ u). FeO⁻ cannot be reliably analysed because it has the same m/z value as MnOH⁻ ($m/z = 72$ u).

Fig. 4 shows the ToF-SIMS depth profiles of the samples crystallized at 700 °C. The results of the 600 °C crystallized samples can be found in the supporting information (Fig. S4, ESI†). The sputter time is normalized to one for the interface between LMO and interlayer because of the different sputter yields for Au, Pt, ITO and StSt. The LMO-interlayer interface is defined as the intercept of the MnO⁻ signal and the interlayer element signal. Without an interlayer, *i.e.* the LMO@StSt sample, Fig. 4a demonstrates an overlap of the Li⁻ and MnO⁻ signals (from LMO) with the AlO⁻ and CrO⁻ signals (from StSt containing 20 wt% Cr and 5 wt% Al). This implies interdiffusion of the substrate elements into LMO, as well as Li and Mn into the substrate and results in Li depletion at the LMO surface. A Pt interlayer blocks the diffusion of Cr and Al into the LMO layer (Fig. 4c), but it cannot block Li diffusion. Furthermore, Pt is



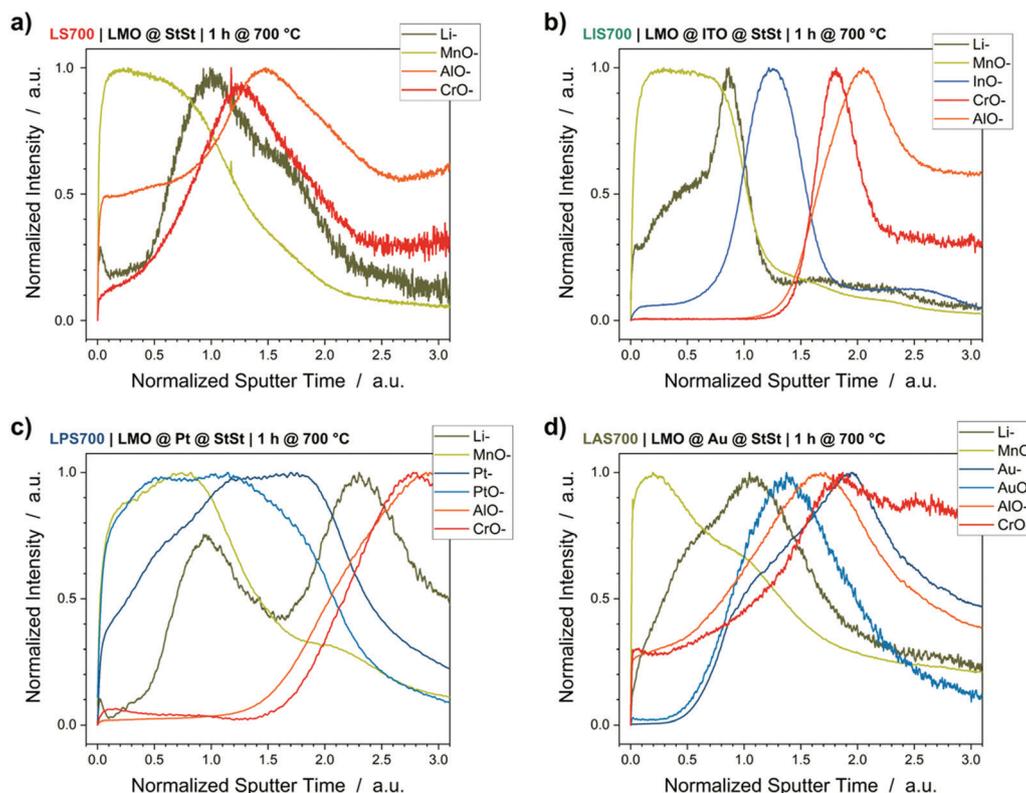


Fig. 4 ToF-SIMS depth profiles of samples annealed at 700 °C: (a) LMO@StSt, (b) LMO@ITO, (c) LMO@Pt and (d) LMO@Au. Sputter time is normalized to 1 for the interface between LMO and interlayer. LMO elements (Li^- and MnO^-) in green, StSt elements (AlO^- and CrO^-) in red and interlayer elements (InO^- , Pt^- , PtO^- , Au^- and AuO^-) in blue. Depth profiles of samples annealed at 600 °C are in the ESI† (Fig. S4).

present throughout the LMO layer. Fig. 4d illustrates significant overlap of the ion signals derived from the LMO layer, Au interlayer and StSt. The Au interlayer does not block the diffusion of the substrate elements Cr and Al into the LMO layer nor the diffusion of Li and Mn into the StSt substrate, which is also demonstrated by the overlap of all element signals in the Au interlayer region between 1 and 2 normalized sputter time. In contrast, the ITO interlayer demonstrates excellent interdiffusion blocking characteristics. Fig. 4b shows a clear separation between the signals of the LMO and the StSt substrate. The ITO interlayer, evident from the InO^- signal, seems to completely block Al and Cr diffusion and significantly reduces Li and Mn depletion. The SEM cross sections demonstrated the oxide layer formation (Fig. 1), which is confirmed here as peaks of CrO^- and AlO^- at the interface LMO/StSt, ITO/StSt or Pt/StSt. An Au interlayer, which cannot block Cr and Al diffusion, shows the oxide layer formation at the LMO/Au interface.

Crystalline phase

The effect of interdiffusion also affects the LMO crystallinity as can be deduced from GIXRD patterns (Fig. 5). LMO@StSt, LMO@Pt and LMO@ITO show crystalline LMO reflections and no mixed phases (Fig. 5a and b). LMO@Au exhibits crystalline LMO, but also the formation of AuLi ($2\theta = 28.9^\circ$ and 41.0° , ICSD 58523), AuLi_3 ($2\theta = 28.1^\circ$, ICSD 58524), Au_2Mn ($2\theta = 28.1^\circ$, 37.8° and 41.3° , ICSD 657181) and AlAu_2 ($2\theta = 20.0^\circ$, 38.1° and

43.3° , ICSD 606020) alloys due to interdiffusion processes (Fig. 5b). Additionally, MnO_2 (ICSD 20228) and MnO (ICSD 18006) phases are detected. The LMO hkl 111 reflection at approx. 18.720° 2θ shows a shift to lower 2θ values with increasing interdiffusion. In Fig. 5c, LIS600 can be viewed as a benchmark. The 2θ position of 18.720° is comparable with the literature value of 18.714° (ICSD 94340). Shifts of 0.053° (LIS700) up to 0.1848° (LS700) to lower 2θ degrees are observable with increasing temperature and depending on interlayer. These shifts can be correlated to the formation of other spinel phases, such as Mn_3O_4 , Fe_3O_4 , FeMn_2O_4 , MnFe_2O_4 or CrMn_2O_4 . The 111 reflections are reported as 18.022° (ICSD 31094), 18.467° (ICSD 77592), 18.040° (ICSD 28666), 18.237° (ICSD 56121) and 18.097° (ICSD 74707), respectively. According to this data and ToF-SIMS depth profiles, interdiffusion of substrate elements and Li depletion in the LMO layer lead to the substitution of Li and Mn in the LMO spinel lattice.

Changes in oxidation states from XPS measurements

The substitution of Li and Mn in the LMO spinel lattice with substrate and interlayer elements would lead to changes in the Mn oxidation states. The Mn oxidation states in the LMO lattice are 3+ and 4+. In MnX_2O_4 spinels ($X = \text{Fe}, \text{Cr}, \text{Al}, \text{Pt}, \text{Mn}$), the oxidation state of Mn is 2+ and of X is 3+. Therefore, increased amounts of Mn^{2+} and decreased amounts of Mn^{3+} and Mn^{4+} , as determined from the XPS Mn $2p_{3/2}$ peak could indicate the



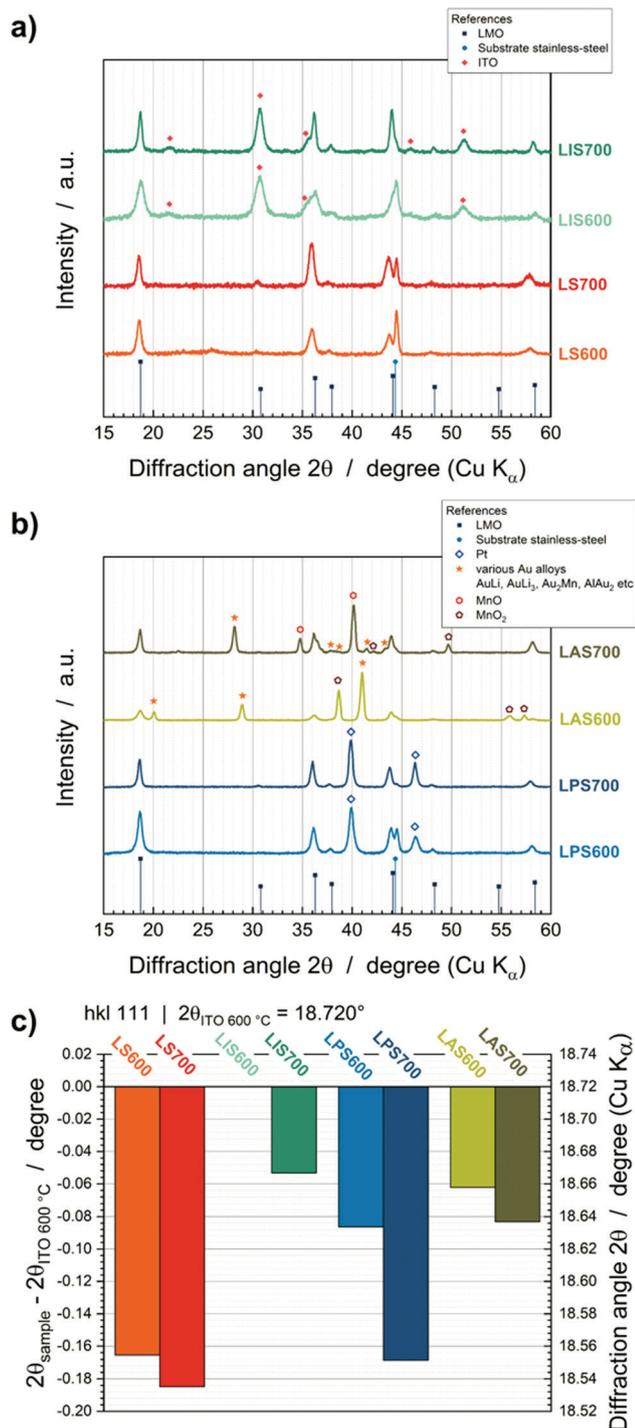


Fig. 5 (a) GIXRD patterns of LMO StSt (red) and LMO@ITO (cyan) and (b) GIXRD patterns of LMO@Pt (blue) and LMO@Au (green). (c) Analysis of shift of the reflex positions of all samples: LMO@ITO is set as benchmark.

presence of MnX_2O_4 spinel phases. For the LMO@StSt samples, an increase in Mn^{2+} species at the surface from 18% to 24% with higher annealing temperature is detected (Fig. 6a). Lower amounts of Mn^{2+} are detected if an ITO interlayer is present, *i.e.* 11% and 17% for LIS600 and LIS700, respectively. The decrease of Mn^{4+} species also indicates that the formation of MnX_2O_4

(X = Fe, Cr, Al, Pt, Mn) spinels are favoured at the surface. Furthermore, Fe is detected at the surface of the LMO@StSt samples (Fig. 6b). In contrast, the results for the LMO@ITO samples shows a low Fe signal for LIS700 and no Fe signal from the surface of LIS600.

Discussion

Interdiffusion processes between the LMO, the interlayer and the substrate cause changes in the LMO active material and therefore its electrochemical behaviour. Li and Mn diffuses into the interlayer and/or substrate, while the substrate elements (Fe, Cr, Al) and the interlayer element Pt diffuse into the LMO layer. This leads to substitution of Li and Mn in the LMO spinel lattice with Fe, Cr, Al and Pt. Li depletion leads to a change of the oxidation states of the Mn from 3+ and 4+ in the LMO spinel to 2+ and 3+ in MnX_2O_4 (X = Fe, Cr, Al, Pt, Mn) spinels.

Ion radii between 0.55 Å and 0.80 Å are necessary for substitution of Li and Mn in the LMO crystal lattice.^{31,32} Various ions derived from the substrate, such as Fe^{2+} (0.78 Å), Fe^{3+} (0.65 Å), Fe^{4+} (0.59 Å), Al^{3+} (0.54 Å), Cr^{2+} (0.80 Å), Cr^{3+} (0.62 Å), Cr^{4+} (0.55 Å), and Pt^{4+} (0.60 Å), fill this criteria.³³ As a result, any of these ions could diffuse into the LMO layer and were detected *via* ToF-SIMS. In contrast, Au^{3+} has an ion radius of 0.85 Å and consequently, Au diffusion into the LMO layer has not been detected. The general formula for a spinel structure is AB_2O_4 , where A and B have the oxidation states of 2+ and 3+, respectively. Therefore, spinel compositions like FeMn_2O_4 , CrMn_2O_4 or FeCr_2O_4 *etc.* are also possible. The presence of such spinel phases and MnX_2O_4 (X = Fe, Cr, Al, Pt, Mn) spinels would result in the formation of an insulating layer at the surface of the LMO layer. The qualitative comparison of the ToF-SIMS depth profiles of the LIS600 (Fig. S4b, ESI[†]) and LIS700 (Fig. 4b) samples indicates a lower Li amount at the surface of the LIS700 samples, which confirms a higher amount of Mn^{2+} -based spinel compositions. This insulating layer is then responsible for the increase in R_{in} and the overvoltage. The decrease in discharge capacities with higher interdiffusion degree is correlated with the thickness of the insulating layer. As the XPS measurements are only surface-sensitive, the thickness cannot be quantified.

The presence of spinel lattices with the composition LiMnXO_4 (X = Fe^{4+} , Cr^{4+} , Pt^{4+}) could explain the changes in the ratio of the first and second redox peaks in differential capacity plots. The substitution of the Mn^{4+} by other ions could change the diffusion activation energies for the Li ions, and thus the insertion/extraction process. Therefore, even the samples with interlayers not able to block interdiffusion have higher discharge capacities than the LS600 sample. At 700 °C, Au cannot block interdiffusion of StSt elements and Li depletion, while Pt tends to diffuse into the LMO layer. In both cases, the discharge capacities decrease. In contrast, ITO functions as interdiffusion barrier even at 700 °C and the increase in discharge capacity due to a higher crystallization degree of LMO is measurable. The higher overvoltage of LIS700 is induced by a number of factors including a decrease of the



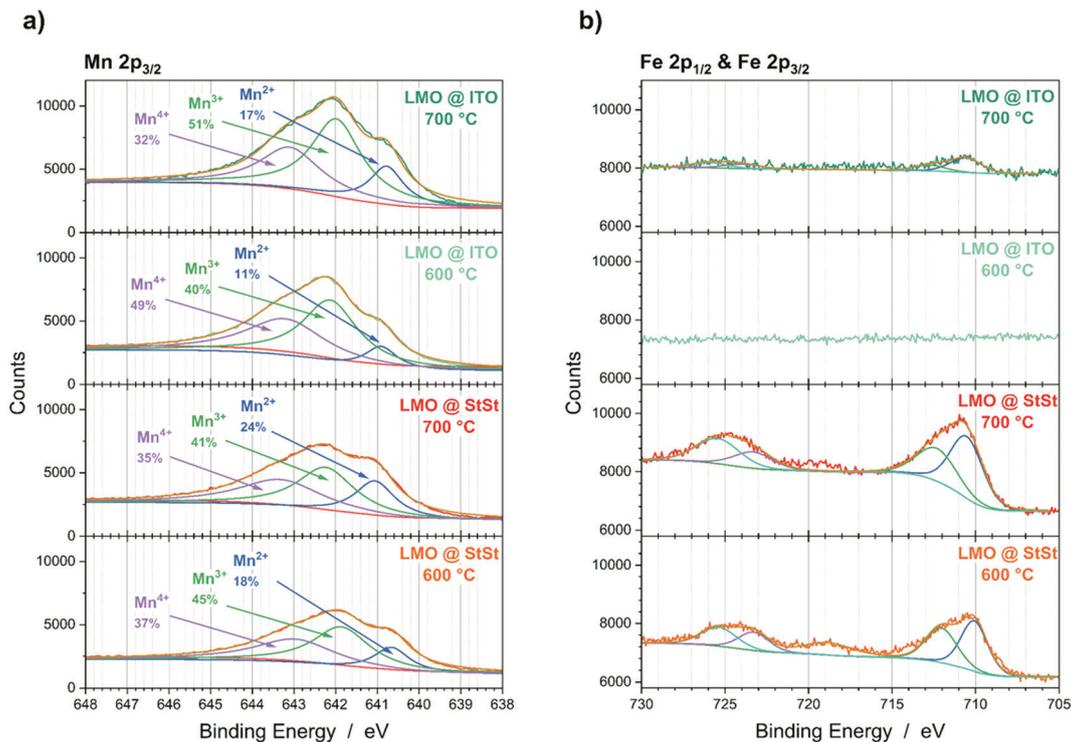


Fig. 6 XPS data of LMO@StSt and LMO@ITO samples annealed at 600 °C and 700 °C. (a) Mn 2p_{3/2} region and (b) Fe 2p_{1/2} and Fe 2p_{3/2} region.

electrochemical active surface area due to an increase in particle size, the formation of insulating layer and a small amount of Fe in the LMO lattice. The larger particle size, and thus the longer diffusion pathways, affect the charge transfer and concentration polarization at 30 $\mu\text{A cm}^{-2}$ (*ca.* 6C). In particular, the thickness and composition of the insulating layer has a significant impact on the overvoltage and electrochemical performance. This results in a higher overvoltage and a higher value for R_{In} .

Conclusion

The crystallization process of LMO on StSt leads to diffusion of Al, Cr and Fe into the LMO layer as well as the diffusion of Li and Mn into the substrate. Al, Cr and Fe replace Li and Mn in the spinel lattice and form new spinel compositions like MnX_2O_4 ($X = \text{Fe, Cr, Al, Pt, Mn}$) or LiMnXO_4 ($X = \text{Fe}^{4+}, \text{Cr}^{4+}, \text{Pt}^{4+}$). The loss of Li, the consequent change in the Mn oxidation states and the formation of an insulating layer at the LMO surface result in a reduction of the electrochemical performance including a decrease in discharge capacity and increases in R_{In} , R_{CT} and the overvoltage.

Three different conductive interlayers (Au, Pt and ITO) have been tested as potential temperature-resistant current collectors on StSt substrates. Au and Pt as an interlayer cannot satisfactorily suppress the interdiffusion processes. LMO@Pt and LMO@Au crystallized at a temperature of 700 °C show a significant decrease in the discharge capacities compared to the 600 °C crystallized samples. In contrast, ITO exhibits the best diffusion

barrier properties out of the three interlayers tested. Since ITO prevents Li and Mn depletion as well as interdiffusion of StSt elements (Fe, Cr, Al) into the LMO layer, the discharge capacity increases from 4.2 $\mu\text{A h cm}^{-2}$ (600 °C) to 5.1 $\mu\text{A h cm}^{-2}$ (700 °C).

The use of ITO as interlayer promises higher utilization of cathode active materials, which increases the performance and energy density of TF-SSBs. Its production *via* wet chemical processing, along with a flexible StSt foil as a substrate/current collector can enable cost-effective slot die roll-to-roll processes for TF-SSB manufacturing.

Conflicts of interest

The authors declare no conflict of interest. The funders had no role in the design of the study; in the collection, analyses, or interpretation of data; in the writing of the manuscript, or in the decision to publish the results.

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