

## CORRECTION

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## Correction: Revisiting thin silicon for photovoltaics: a technoeconomic perspective

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Zhe Liu,<sup>\*a</sup> Sarah E. Sofia,<sup>a</sup> Hannu S. Laine,<sup>a</sup> Michael Woodhouse,<sup>b</sup> Sarah Wieghold,<sup>a</sup> Ian Marius Peters<sup>a</sup> and Tonio Buonassisi<sup>\*a</sup>

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Correction for 'Revisiting thin silicon for photovoltaics: a technoeconomic perspective' by Zhe Liu *et al.*, *Energy Environ. Sci.*, 2020, 13, 12–23, DOI: 10.1039/C9EE02452B.

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The authors wish to amend the conflicts of interest statement to disclose the following patents:

United States of America Serial No. 16/624845, filed December 19, 2019.

Publication Number: US 2020-0127153 A1.

“Wire Interconnection For Solar Cells” by Anthony Buonassisi, Luke Meyer and Emanuel M. Sachs.

United States of America Patent No. US8969183B2, filed October 25, 2015.

“Method for producing thin layers of crystalline or polycrystalline materials” by Mark T. Winkler, Tonio Buonassisi, Riley E. Brandt, Michael J. Aziz, Austin Joseph Akey.

The Royal Society of Chemistry apologises for these errors and any consequent inconvenience to authors and readers.

<sup>a</sup> Department of Mechanical Engineering, Massachusetts Institute of Technology (MIT), Cambridge, MA, USA. E-mail: zheliu@mit.edu, buonassisi@mit.edu

<sup>b</sup> Strategic Energy Analysis Center, National Renewable Energy Laboratory (NREL), Golden, CO, USA

