



Cite this: *Sustainable Energy Fuels*,  
2020, 4, 852

# Roadmap for cost-effective, commercially-viable perovskite silicon tandems for the current and future PV market†

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A techno-economic analysis of perovskite-silicon tandem solar modules is presented, outlining the most viable pathway for designing cost-effective, commercially viable tandems. We explore the cost-performance trade-off for silicon bottom cells in perovskite-silicon tandems, and evaluate the potential of using low-cost, lower-efficiency silicon bottom cells, on the basis of levelized cost of electricity (LCOE), compared to the higher-efficiency, higher-cost bottom cells that have been the primary focus of most perovskite-silicon tandem research efforts. We fabricate a cost-effective four-terminal silicon-perovskite tandem using a low-cost multicrystalline bottom cell and calculate the device LCOE. We then extend this analysis by modeling performance and LCOE of similar tandems instead using high-efficiency silicon bottom cells, enabling direct comparison of a low-cost and a high-efficiency tandem. Lastly parametric analyses are performed to more broadly examine the bottom-cell cost-performance trade-off. We show that low-cost silicon, even at the detriment of efficiency, is the more likely path to make perovskite-silicon tandems commercially viable and enable future reductions in LCOE, given both current and near-future silicon technology. We lay out a clear economic motivation for pursuing low-cost silicon bottom cells in perovskite-silicon tandems, showing that they can achieve a 15–20% relative LCOE reduction compared to the single-junction sub-cells. This is a 2–3 times greater relative LCOE reduction compared with using high-efficiency silicon. Furthermore, we show that the advantage of using low-cost silicon bottom cells is robust to and benefits from expected market trends, such as falling system costs and advanced, low-cost manufacturing. This work provides a clear pathway to cost-effective tandems, outlines the benefits for existing multicrystalline silicon manufacturers to investing in tandem development, and points out a clear mismatch between commercial viability and current research efforts.

Received 30th July 2019  
Accepted 1st November 2019

DOI: 10.1039/c9se00948e

rsc.li/sustainable-energy

## 1. Introduction and background

Tandem architectures present a path to reach high solar cell efficiency with the potential to surpass traditional, single-junction (SJ) limits. Increasing efficiency is one of the strongest technical drivers to reduce the cost-per-watt of solar technology,<sup>1,2</sup> so as efficiencies of current mainstream technology approach the theoretical limit, novel technologies are needed to continue to improve efficiency and drive down the cost of solar electricity. This has led to significant interest in the development of tandem solar cells for terrestrial applications. A primary focus of recent tandem research efforts has been perovskite-on-silicon tandems, because (1) the tunable bandgap

of perovskites (typically from 1.5 to 2.2 eV) allows the creation of a potentially ideal bandgap top cell to couple with silicon, and (2) this material pairing enables leveraging the >95% market share currently held by silicon while utilizing the low-cost,<sup>3,4</sup> rapidly developing perovskite technology, together holding the promise to make tandems economically superior.

While perovskite-silicon tandems have these advantages, there are challenges to making a cost-effective tandem. Though tandems can achieve higher efficiencies, they are also more expensive and complex to manufacture. The efficiency gain of the tandem relative to the comprising single-junction (SJ) cells must be sufficient to justify the added manufacturing cost of integrating an additional sub-cell, such that the tandem is more economical than *either* comprising sub-cell on its own as a SJ. Generally, sub-cells that have similar SJ efficiencies and areal cell costs are most likely to produce a cost-effective tandem device, a property referred to as the ‘marriage of equals’.<sup>5</sup> This balance is difficult for perovskite-silicon tandems since they feature two technologies with very different manufacturing

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† Electronic supplementary information (ESI) available. See DOI: 10.1039/c9se00948e



approaches: perovskite using solution-processed thin-film deposition and silicon being a wafer-based technology. Thus, this tandem potentially combines a very low cost material with low capital expenditure (CapEx) with silicon solar cells that are more expensive and fairly CapEx intensive to manufacture. While a recent techno-economic analysis shows that silicon-based tandems are likely to be a financially viable technology in the future solar market,<sup>6</sup> there has been little exploration into what makes a silicon-based tandem optimally cost-competitive.

There are two clear design directions that can be taken for the silicon bottom cell in order to make a cost-effective perovskite-silicon tandem: (1) reducing the cost of the silicon bottom cell as much as possible to minimize its financial impact on the cell and reduce the overall module cost, or (2) maximizing the power output of the bottom cell to help justify its inclusion in the device stack and produce a higher efficiency module. The low-cost option has a clear advantage in promoting similar costs between the two sub-cells, as advised by the 'marriage of equals' principal, while the high-efficiency option maximally leverages efficiency improvement as a strong driver to reduce the cost-per-watt on a module and system level. Of course, achieving both a low-cost and high-efficiency cell would be ideal, there is generally a trade-off between efficiency and cost, since higher efficiency cell technology typically requires better material quality and more complex device architecture which increase manufacturing cost.

Many impressive perovskite-silicon tandem results have been achieved: the current record four-terminal (4T) (mechanically-stacked, electrically independent sub-cells) efficiency record is 26.4%,<sup>7</sup> and the current two-terminal (2T) (monolithically integrated, series-connected sub-cells) efficiency record is 25.2%.<sup>8</sup> (A 28% efficiency 2T device has been reported,<sup>9</sup> however no device details are published). These results, like many other published results, focus on maximizing efficiency by utilizing high-cost, monocrystalline silicon cells for the bottom cells with 1-sun efficiencies of 23.9 and 22.6%,<sup>8</sup> respectively. While demonstrating high efficiency devices is worthwhile to show the potential of this technology and push toward higher performance devices, the use of high-efficiency, high-cost bottom cells may not be the best path to cost-effective tandems, as this further exacerbates the cost discrepancy between the low-cost perovskite top cell and the more expensive, CapEx-intensive silicon bottom cell.

Furthermore, the bottom cell typically produces less than half of the total energy generated by a tandem, so the bottom cell performance has less impact on the overall tandem efficiency than the top cell. This further suggests the possible advantage of low-cost, multicrystalline silicon wafers, potentially employing low-cost crystallization techniques such as ingot casting and kerfless or direct-wafering techniques, for use in tandem applications rather than a high-cost, higher efficiency cell: in addition to being a better 'marriage of equals' match for perovskite cells, (1) the lower cost cell can more easily be justified by the small fraction of the tandem power they produce, (2) the tandem is less sensitive to variation in performance of the bottom cell due to their smaller role, making high-performance less valuable for a bottom cell. This is further motivated by multicrystalline silicon's 62% market share of solar production<sup>10</sup> and recent news of

kerfless Direct Wafer® technology transitioning into production scale.<sup>11</sup> Despite these indications that low-cost silicon is likely more viable for designing a cost-effective perovskite-silicon tandem, there have been almost no research efforts looking at multicrystalline silicon-perovskite tandems.

To explore whether low-cost silicon bottom cells do, in fact, have an economic advantage and more broadly understand how to best design these devices and direct research efforts, we use a techno-economic approach to explore the financial viability of perovskite-silicon tandem solar modules, and aim define the economically-best roadmap for the development of perovskite-silicon tandems. We compare two primary tandems: (1) the high efficiency tandem: a perovskite-silicon tandem utilizing a high-efficiency, high-cost heterojunction with intrinsic thin layer (HIT) monocrystalline silicon solar cell, and (2) the low-cost tandem: a perovskite-silicon tandem employing a low-cost, lower efficiency multicrystalline passivated emitter and rear cell (PERC) silicon solar cell. The explored tandems and SJ device architecture are shown in Fig. 1. First, we fabricate a 'low-cost' tandem using an industrially processed PERC mc-Si bottom cell, demonstrating the viability of a perovskite based tandem using a multicrystalline silicon bottom cell. We additionally model potential improvements to this device, as well as compare to a modelled high-efficiency tandem. Levelized cost of electricity (LCOE) is used to evaluate the relative viability of these two tandems, combining module manufacturing costs and energy yield calculations and considering current and potential future cost scenarios for both residential and utility scale systems. We then extend this approach to perform a few parametric analyses to understand the relative merits and potential viability of each tandem. By doing so, we propose a framework to understanding when to push toward maximizing the absolute device efficiency *versus* leveraging the tandem architecture to boost the efficiency of low-cost solar technology and thus making cheap solar cells viable to compete with high efficiency SJ technology and lowering the cost of solar electricity.

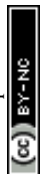
## II. Methodology

### A. Yield calculations and architectures

Energy yield calculations are performed using the method presented in ref. 12 and 13, employing hourly time-resolved, location-specific spectra generated using the Simple Model for Atmospheric Radiative Transfer of Sunshine (SMARTS) from the National Renewable Energy Laboratory<sup>14,15</sup>. We perform all calculations for three, climatically distinct locations: Albuquerque, NM (dry), Rapid City, ND (temperate), and Miami, FL (humid). We present only calculations for Albuquerque, NM in the main manuscript, as the results across different locations are very similar however calculations using the other two locations are included in the ESI.†

### B. LCOE calculations and cost model

The figure of merit used to evaluate each device architecture is the levelized cost of electricity (LCOE). LCOE is calculated by the equation,<sup>16</sup>



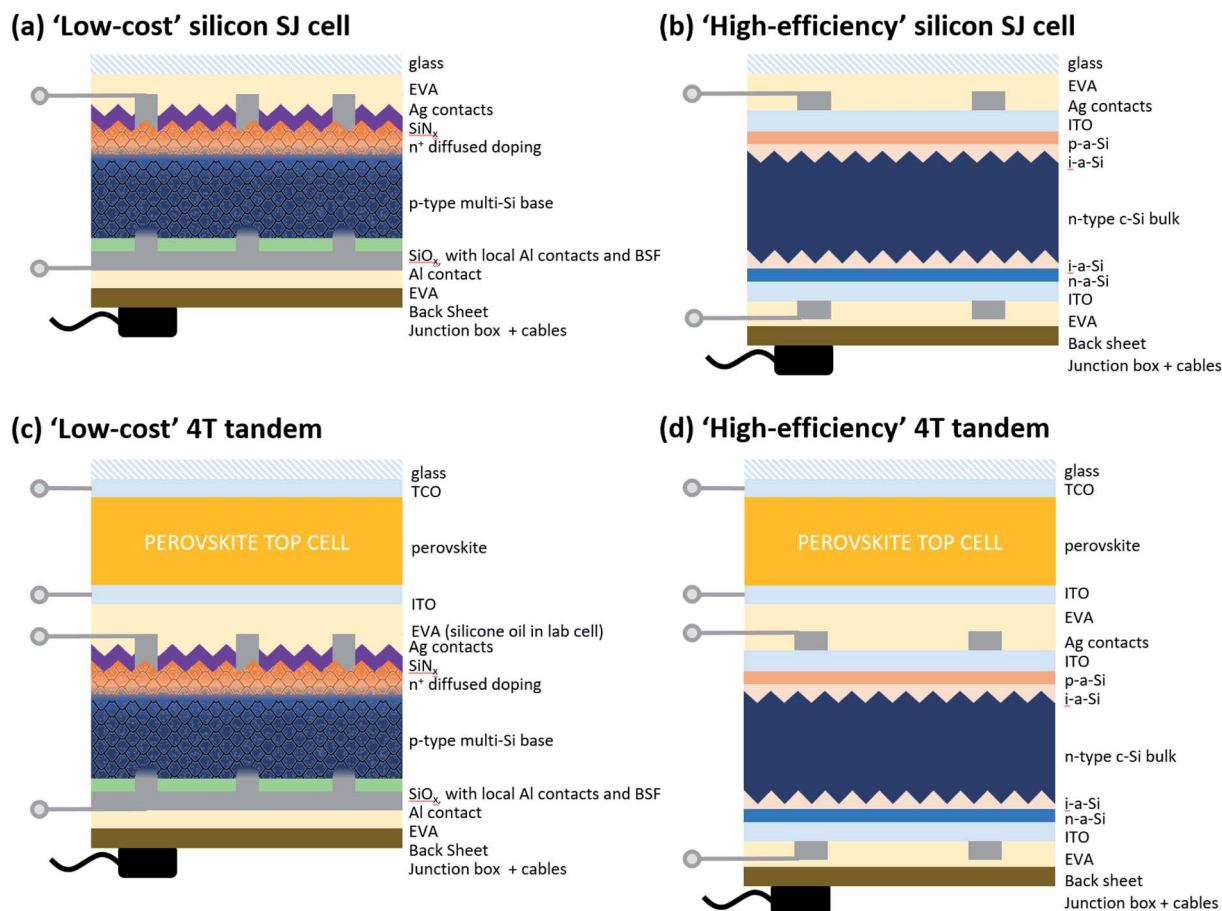


Fig. 1 Schematic of the two silicon single-junction modules and the two 4T tandems. These schematics indicate the full stack used for the module manufacturing.

$$\text{LCOE} = \frac{\text{total lifetime cost}}{\text{total lifetime electricity production}} = \frac{I + \sum_{i=0}^N \frac{\text{OM}}{1+r}}{\sum_{i=0}^N \frac{E(1-d)^i}{1+r}} \quad (1)$$

where  $I$  is the total initial investment to install the PV system (including cost of PV modules, racking, interconnects, labor, and permits),  $\text{OM}$  is the annual cost for operation and maintenance,  $E$  is the (un-degraded) annual energy output by the system as electricity,  $N$  is the system lifetime in years,  $d$  is the annual module efficiency degradation rate, and  $r$  is the nominal discount rate. The values of the financial input parameters are given in Table 1. An annual degradation rate of 0.5%, relative, is assumed for both the silicon and perovskite cells. This is consistent with current silicon degradation rates,<sup>17</sup> while perovskite have demonstrated significant stability issues.<sup>18</sup> Rather, the current longest reported stable lifetime of a perovskite solar cell is only 1 year.<sup>19</sup> For this analysis, however, we assume continued progress will be made on perovskite cells, as perovskite SJ cells and perovskite-silicon tandems will not be viable for long-term, grid-connected applications unless perovskite solar cells achieve similar degradation rates to silicon.

We define a manufacturing process flow for each tandem module and used this to develop a bottom-up module manufacturing cost model for the two four-terminal tandems based on existing cost models<sup>3,4,13,20</sup>. Using this model, we calculated the minimum sustainable price (MSP) assuming straight-line depreciation and a 14% internal rate of return (IRR).<sup>1</sup> The tandem and SJ module costs and MSPs are given in Table 2. See ESI† for a more detailed cost breakdown.

Lastly, to calculate the total cost of the installed system, we broke down PV system cost data from <sup>21</sup> into area, power, and project dependent costs<sup>22</sup> to project system installation costs for a given residential or utility PV system (Table 1). We also consider a hypothetical future reduced system cost scenario, presented in ref. 13, based on the costs presented in ref. 23 that aim to meet the United State Department of Energy SunShot goals. We assume a constant module area of 35.9 m<sup>2</sup> and 0.6 km<sup>2</sup> for all residential and utility systems, respectively.

To examine the benefit of the tandem architecture compared to the single-junction sub-cells on their own, we use the 'relative tandem advantage' as a metric. The relative tandem advantage is defined as,



**Table 1** Financial and system parameters and system installation costs used to calculate LCOE for residential and utility scale systems based on both current (2018) costs and a potential reduced system cost scenario representing a potential future cost breakdown (future reduced)

System costs and LCOE input parameters				
	Residential		Utility	
	2018	Future reduced	2018	Future reduced
Inflation rate	2.5%			
Manufacturing WACC	14%			
System nominal discount rate ( <i>r</i> )	7%		6%	
Operation and maintenance (OM) [\$ per kW per years]	\$21 per m <sup>2</sup>	\$10 per m <sup>2</sup>	\$13 per m <sup>2</sup>	\$7 per m <sup>2</sup>
Lifetime ( <i>N</i> ) [years]	25 years		25 years	
Annual degradation rate [% per years]	0.5%		0.5%	
Power-dependent cost [\$ per W]	0.28	0.18	0.07	0.07
Area-dependent cost [\$ per m <sup>2</sup> ]	131.05	71.98	75.82	60.81
Project-dependent cost [\$]	6341	2482	9.65 × 10 <sup>6</sup>	9.65 × 10 <sup>6</sup>
System size	35.9 m <sup>2</sup>	35.9 m <sup>2</sup>	100 MW	100 MW
Module mark-up	35%	24%	24%	9%

**Table 2** Device manufacturing minimum sustainable prices, in U.S. dollars per square-meter. See ESI† for a more detailed breakdown

Module manufacturing cost and prices	
	Module price [\$ per m <sup>2</sup> ]
'High-efficiency' Si SJ cell: HIT c-Si SJ	\$79.31 per m <sup>2</sup>
'Low-cost' Si cell: PERC mc-Si SJ	\$62.90 per m <sup>2</sup>
Perovskite SJ	\$38.69 per m <sup>2</sup>
'High-efficiency' 4T tandem	\$96.88 per m <sup>2</sup>
'Low-cost' 4T tandem	\$80.25 per m <sup>2</sup>

Relative tandem advantage =

$$\frac{\min(\text{LCOE}_{\text{Si}}, \text{LCOE}_{\text{perov.}}) - \text{LCOE}_{4\text{T}}}{\min(\text{LCOE}_{\text{Si}}, \text{LCOE}_{\text{perov.}})} \quad (2)$$

showing when the relative reduction in LCOE for a tandem compared to the lowest LCOE sub-cell as a single-junction is largest.

### III. Results and discussion

#### A. Device fabrication and modeling

We first fabricated the low-cost tandem, using an industrially processed PERC multicrystalline silicon solar cell from an industrial partner, laser cut to 0.8 cm<sup>2</sup>, and coated in 36 nm Al<sub>2</sub>O<sub>3</sub> by ALD for edge passivation. The small-area, masked (0.72 cm<sup>2</sup> aperture) cell efficiency was 17.2%. The open-circuit voltage (*V*<sub>OC</sub>) and the efficiency of the cut, small area cell was significantly reduced compared to the full, 6 inch wafer measurement, with the efficiency dropping from 19% to 17.2% and the *V*<sub>OC</sub> dropping from 0.65 to 0.61 V (with edge-passivation). This is believed to be due primary to (1) edge recombination becoming significant for a small area device, (2) dark-current due to a small masked area (about 1 mm wide strip on a side, 10% of wafer) reduction in shunt-resistance due to the wafer cutting. This cell was aligned and measured under a 1.7 eV semi-transparent perovskite top-cell, fabricated in substrate

configuration, with an efficiency of 13.1%. The resulting four-terminal, mechanically-stacked tandem had a total efficiency of 19.1%, with 6% produced by the bottom cell (Fig. 2 and Table 3). While this device performance is not record breaking, it demonstrates the potential of a low-cost, industrially-relevant mc-Si cell to act as a decent bottom cell and create a tandem that surpasses the sub-cell efficiencies.

Using the measured external quantum efficiency (EQE) and current-voltage (*JV*) curves, along with the cost models, we compute the LCOE for the SJ sub-cells and the tandem for current and future residential and utility scale systems (Table 3). We see that even our prototype tandem has the lowest LCOE in both residential cases. Our fabricated tandem, however, is far from ideal, and one clear path for improvement is to increase the perovskite top cell efficiency. Reported values up of to 16% for semi-transparent<sup>24</sup> and around 20% for non-transparent cells<sup>25,26</sup> of similar bandgap have been demonstrated, compared to our 13.1% cell. Additionally, the losses due to the small area such as edge effects and dark current from masking hurt the tandem performance and unrealistic for a full-size module. Thus, to explore the future potential of this tandem device, we model the performance and calculate the LCOE of the tandem and single-junction with bottom cell parameters reflecting full area silicon cell (see Table 4) and for the case of an 18% efficient perovskite cell with an average transmission of 80% (Table 3, second column). The transmission curve is modeled by scaling-up the sub-bandgap portion of the measured transmission curve of our experimental perovskite cell. See ESI† for more detail on the 18% top-cell. Further improvements to tandem bottom cell performance could be made by tailoring the silicon bottom cell device architecture for application as a tandem bottom cell, such as through optimizing anti-reflection coatings and bulk-resistivity for long-wavelength response. For this work, however, we simply show the potential of commercial silicon cells as a drop-in solution for tandems, without processing changes.

Lastly, in order to compare low-cost silicon-based tandems to tandems with a higher-efficiency silicon bottom cell, we





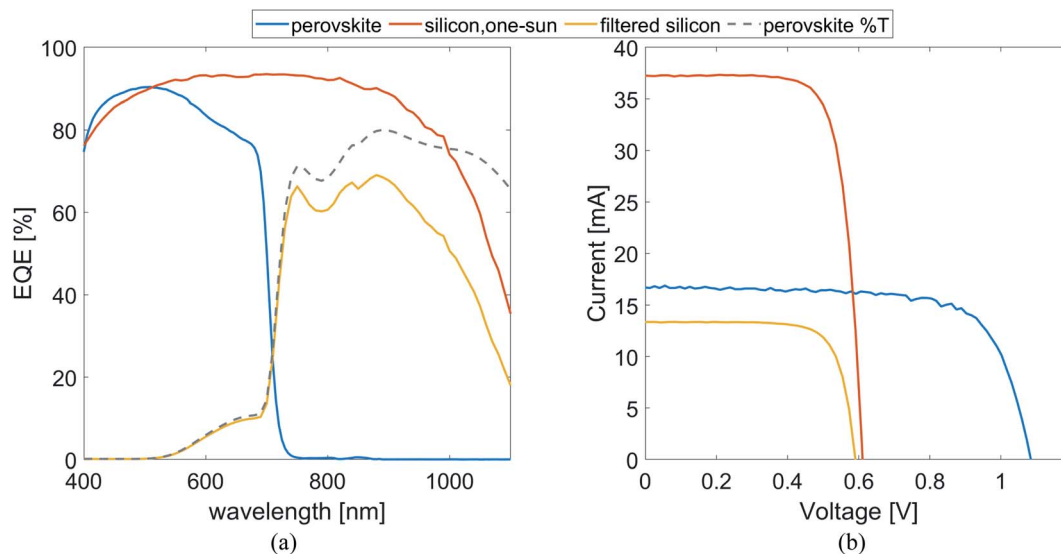


Fig. 2 (a) External quantum efficiency (EQE) and (b) current–voltage (*JV*) curve of the perovskite top cell, the silicon bottom cell under one-sun illumination, and the silicon bottom cell under one-sun filtered by the perovskite top cell.

model the performance of the tandems using the same two perovskite top cells (13.1% and 18% efficient) but with a high-efficiency, monocrystalline HIT bottom cell. We use published, record-efficiency front-and-back-contact HIT monocrystalline Si cell EQE and *JV* parameters<sup>27,28</sup> with modeled added module losses (Table 4) and calculate the bottom cell EQE as the product of the transmission curve and reported one-sun EQE. We then compute the LCOE for each of these modeled high-efficiency silicon-based tandems. All results are summarized in Table 3, totaling four devices comprised of different pairings of two perovskite top cells and two silicon bottom cells. We refer to the two tandems using mc-Si as ‘low-cost’ and the

two tandems using the HIT monocrystalline Si cells as ‘high-efficiency’.

We see that for both residential system scenarios, the experimental low-cost tandem with the 13.1% top cell is cost-effective relative to its sub-cells, and the low-cost tandem with the 18% top cell is cost-effective relative to its sub-cells in all four scenarios. The high-efficiency tandem with the 18% top cell is cost-effective in 2018 and reduced residential as well as 2018 utility systems, while the high-efficiency tandem with the 13.1% top cell is not cost-effective in any of the given scenarios. For utility scale systems, all tandems using the 13.1% top cell have an LCOE higher than both sub-cells, favoring the silicon SJ, while all tandems with the 18% top-cell have an LCOE lower

**Table 3** Efficiency for each considered measured or modelled tandem and the calculate LCOE. The LCOE is reported in the order: Residential 2018 (Res 2018), Residential Reduced System Cost Scenario (SS Res), Utility 2018 (2018 Util), Utility Reduced System Cost Scenario (SS Util). The lowest LCOE among the tandem and sub-cell single-junctions is in bold, and the lowest LCOE for each of the four system scenarios among all tandems and single-junctions across all four devices is underlined. See Table 4 for further detail on device efficiencies and IV parameters

Architecture	Low-cost tandem								High-efficiency tandem							
	Meas. perovskite (70% avg. sub-Eg T.)				Future perovskite (85% avg. sub-Eg T.)				Meas. perovskite (70% avg. sub-Eg T.)				Future perovskite (85% avg. sub-Eg T.)			
Si SJ efficiency	17.2%				18.1%				21.7%				21.7%			
Si bottom cell eff.	6.0%				8.1%				7.9%				9.3%			
Perovskite top cell eff.	13.1%				18%				13.1%				18%			
Tandem efficiency	19.1%				26.1%				21.0%				27.3%			
	<b>2018 Res</b>	<b>SS Res</b>	<b>2018 Util</b>	<b>SS Util</b>	<b>2018 Res</b>	<b>SS Res</b>	<b>2018 Util</b>	<b>SS Util</b>	<b>2018 Res</b>	<b>SS Res</b>	<b>2018 Util</b>	<b>SS Util</b>	<b>2018 Res</b>	<b>SS Res</b>	<b>2018 Util</b>	<b>SS Util</b>
Si SJ LCOE [ct kW <sup>-1</sup> h <sup>-1</sup> ]	12.7	7.2	4.7	4.2	12.0	6.8	4.4	3.8	10.7	6.2	4.1	3.6	10.7	6.2	4.1	3.6
Perovskite LCOE [ct kW <sup>-1</sup> h <sup>-1</sup> ]	14.6	7.9	4.9	4.2	10.9	5.9	3.6	3.2	14.6	7.9	4.9	4.2	10.9	5.9	3.6	3.2
Tandem LCOE [ct kW <sup>-1</sup> h <sup>-1</sup> ]	<b>12.1</b>	<b>7.1</b>	5.1	4.4	<b>9.3</b>	<b>5.5</b>	<b>3.6</b>	<b>3.1</b>	11.1	6.7	4.5	3.8	<b>9.0</b>	5.5	3.6	3.2



**Table 4** Summary of the IV parameters and efficiencies for the measured mc-Si cell, the modeled mc-Si module efficiency based on removal of loss due to small area and glass reflection loss. The IV parameters for the record-level, large-area HIT cell from Panasonic<sup>27,28</sup> as well as the expected module efficiency given added module loss and glass reflection. For each relevant case, the measured or modeled bottom cell and tandem efficiencies under the measured perovskite cell (13.1% efficiency with 70% average sub-bandgap transmission), and/or the modeled perovskite top cell (18% efficient with 80% average sub-bandgap transmission). Note that the average sub-bandgap transmission of 70% an 8% include any glass reflection loss

	$V_{OC}$ (V)	$J_{sc,1sun}$ (mA cm <sup>-2</sup> )	FF (%)	$\eta_{Si}$ SJ (%)	Measured perov. top cell ( $\eta_{perov.} = 13.1\%$ , 70% T)		Modeled perov. top cell ( $\eta_{perov.} = 18\%$ , 80% T)	
					$\eta_{bot,Si}$ (%)	$\eta_{4T}$ (%)	$\eta_{bot,Si}$ (%)	$\eta_{4T}$ (%)
Measured mc-Si cell	0.61	37.3	76	17.2	6.0	<b>19.1</b>	—	—
Modeled mc-Si mod	0.65	35.0	79	18.1	7.2	<b>20.3</b>	8.1	<b>26.1</b>
Published c-Si cell	0.75	39.5	83	24.7	—	—	—	—
Modeled c-Si cell	0.75	36.6	80	21.9	9.3	<b>13.1</b>	10.4	<b>28.4</b>

than their Si SJ but higher than the perovskite SJ. Lastly, looking at the lowest LCOE for each system scenario, the lowest LCOE device for current residential systems, among all considered, is the high-efficiency tandem using an 18% efficient top cell, however the low-cost tandem with the 18% top cell is favored in both current and future residential systems, as well as current utility systems. These results show that perovskite-silicon tandems can be viable and a tandem using a low-cost bottom-cell has potential to succeed, especially as system costs continue to fall. Even in utility systems where tandems tend to be less competitive,<sup>13</sup> the tandems with an 18% top cell reduce the LCOE compared to the silicon SJ.

## B. Parametric cost relations

We then extended this analysis to explore the effect of improving top-cell efficiency by comparing the two tandems and their comprising sub-cell single-junctions' LCOEs over varying perovskite efficiency, again for utility and residential systems for 2018 and the future, reduced system cost scenario (Fig. 3). The four devices explored are shown in Fig. 1. The tandem efficiencies are modeled using the two  $JV$  curve parameters for each silicon cell from the previous section and using a range of 70.5–90% average sub-bandgap transmission, the 70.5% transmission corresponding to the original measured perovskite transmission and the 90% transmission calculated by scaling the measured transmission curve (see ESI† for transmission curves). This corresponds to 7.2–9.0% efficiency from the PERC mc-Si bottom cell and 9.2–10.3% efficiency from the HIT c-Si bottom cell. This range of sub-bandgap transmission can also represent a range in relative low-light performance or other factors causing variations in bottom cell performance.

From these results, we see that as perovskite efficiency increases, the low-cost tandem LCOE decreases more quickly than the high-efficiency tandem, causing the high-efficiency tandem to have a smaller advantage compared to the low-cost tandem. As a result, the low-cost tandem LCOE is expected to become increasingly competitive, relative to the high-efficiency

tandem, as perovskite cells are developed and reach higher efficiencies. This is because an increase in efficiency becomes less valuable at higher baseline efficiencies,<sup>22</sup> making a higher efficiency top cell reduce the benefit of an improvement in the bottom cell.

In the 2018 residential case, the high efficiency tandem is the lowest LCOE option provided the perovskite top cell efficiency is above ~16% (comparing equivalent sub-bandgap transmission fractions), however the two tandems' LCOE remain quite close. For perovskite efficiencies above 16%, the tandem LCOEs are within 1.3% and 2.4% of each other for the 70.5% and 90% transmission cases, respectively, and get closer at higher perovskite efficiencies. For the reduced system cost scenario, however, the low-cost tandem has a lower LCOE than the high-efficiency tandem when the perovskite top cell efficiency is above ~16%, though the two tandems' LCOEs again remain close. These results suggest that general trends of improved perovskite efficiencies and continued reduction in system costs will increasingly favor the low-cost tandem over the high-efficiency tandem, however their relative LCOE values do not clearly favor either tandem since they are so close. For utility scale systems, the low-cost tandem always has a lower LCOE than the high-efficiency tandem, since the lower system costs of utility scale installations value low module costs more than efficiency, compared to residential. However, as the efficiencies of perovskite SJ solar cells improve, their LCOE becomes the lowest of all architectures. Additionally, we are using a fairly high bandgap perovskite top cell in this analysis compared to many experimentally demonstrated perovskite cells. This high bandgap favors the higher efficiency tandem since the bottom cell produces a larger fraction of the tandem power with a higher bandgap top cell since more light reaches the bottom cell, thus making a high efficiency bottom cell more valuable. A lower bandgap top cell, therefore, would cause the low cost tandem to be more beneficial relative to the high-efficiency tandem.

In addition to comparing the two tandems' LCOEs directly, a key factor to consider when assessing tandems is the relative



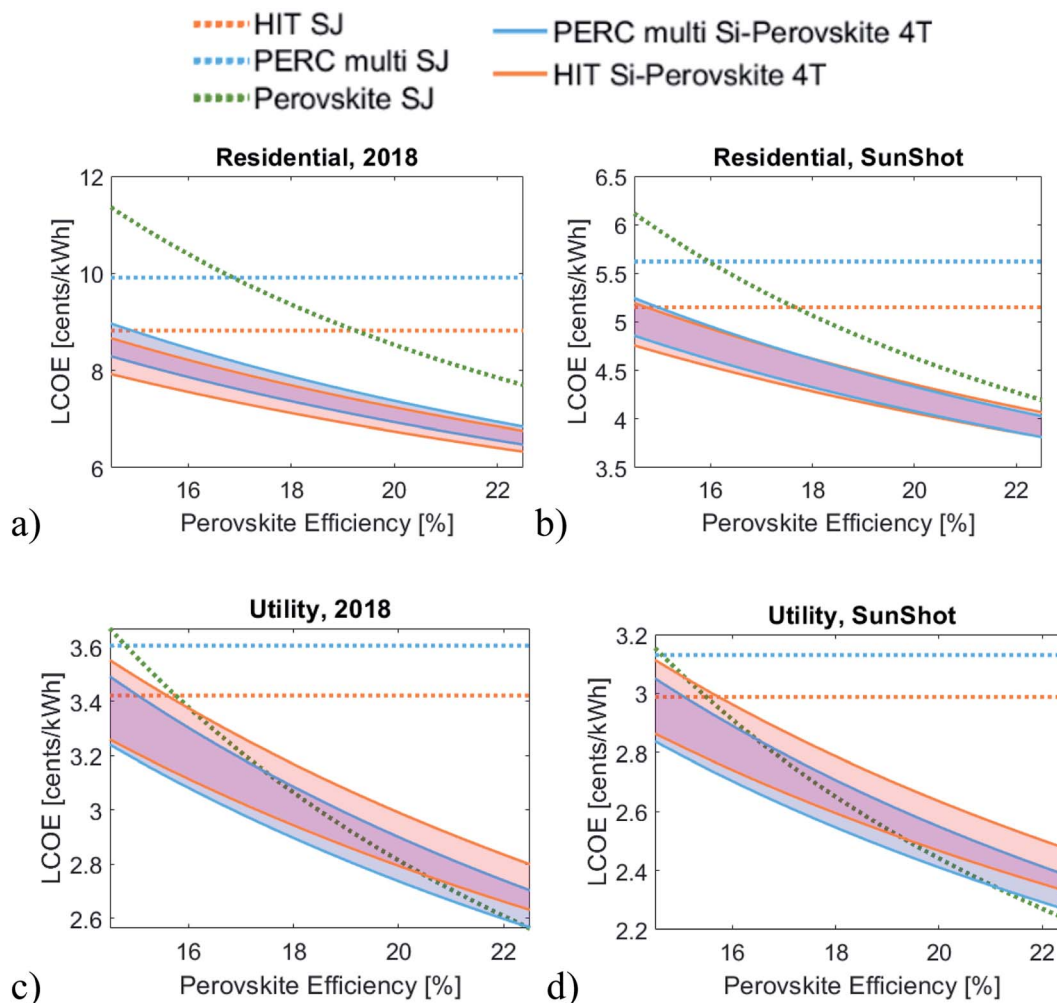


Fig. 3 LCOE of each single-junction and 4T tandem architecture versus perovskite assuming (a) 2018 residential cost, (b) reduced residential BOS cost scenario, (c) 2018 utility costs, (d) reduced utility BOS cost scenario. The two tandem LCOE values are calculated using a range of sub-bandgap transmission, 70.5–90%.

LCOE of each tandem to its comprising sub-cells. In particular, perovskite-silicon tandems must pose a significant LCOE advantage over their comprising silicon SJ solar cell, as silicon is the established, existing technology. We see that the low-cost tandem LCOE is always lower than the low-cost Si SJ LCOE in both system cost scenarios and both system scales, while the high-efficiency tandem requires a perovskite top cell efficiency above ~16% or ~18% for the 2018 and reduced system cost scenarios, respectively. Furthermore, the relative LCOE benefit compared to the Si bottom cell alone is much less for the high-efficiency tandem. For example, for a top cell efficiency of 18%, the low-cost tandem LCOE advantage compared to the Si sub-cell operated independently is 15% to 20% (for the 70.5% and 90% sub-bandgap transmission cases, respectively), whereas the high-efficiency tandem LCOE advantage is only 5% to 10%. This means that, though the high-efficiency tandem is cost-effective compared to its sub-cells, the relative benefit is small. Thus, the investment required to develop and commercialize these tandems using high-efficiency silicon may not be worthwhile. Investment in tandem technology that utilizes low-

cost, lower quality silicon bottom cells, on the other hand, provides a much greater opportunity.

We primarily use the perovskite costs based on the somewhat optimistic, forward-looking cost model from ref. 3, however, since perovskites have not yet been commercialized, there is uncertainty in their manufacturing cost. Another detailed cost model presented in ref. 29 uses a perovskite stack and fabrication process that is more conservative and predicts higher costs for the perovskite cell. To understand the impact of perovskite cell cost on our results, we find the maximum added cost of adding a perovskite top cell such that the perovskite-silicon tandem sustains a lower LCOE than the silicon bottom cell SJ alone (Fig. 4). We do this for both the high-efficiency and low-cost silicon bottom cells, across all system cost scenarios. This shows that the results for the low-cost perovskite cell are not very sensitive to perovskite cost – there is room for the perovskite to be more expensive while the tandem maintains the lowest LCOE, especially in the residential case. The high-efficiency bottom cell, on the other hand, is much closer to the threshold at which an increase in the perovskite cost makes



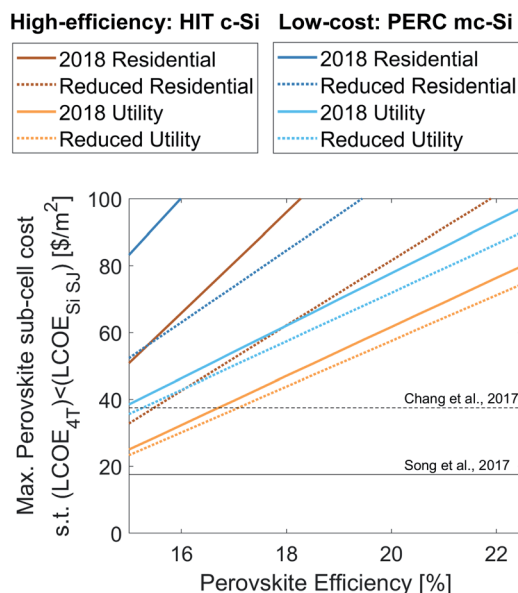


Fig. 4 Maximum added areal cost of adding the perovskite top cell to the bottom Si SJi bottom cell and the high-efficiency HIT c-Si cell in the cases of current and reduced system costs for residential and utility scale.

the tandem no-longer cost-effective, thus pushing the perovskite efficiency requirement up to about 17% for utility systems, if the costs are what is projected in ref. 4. In both cases, the LCOE advantage relative to the silicon SJ is reduced. Conversely, as the perovskite top cell becomes more expensive, the perovskite SJ LCOE increases more quickly than the tandem, thus making the tandem more beneficial relative to the top cell perovskite.

In order to more generally understand the trade-off between the bottom cell cost and efficiency, we model the LCOE of the perovskite-silicon tandem over a varying silicon cell areal cost and bottom cell efficiency (Fig. 5). This allows us to see both the trends of LCOE with bottom cell cost and efficiency, and how changing costs and performance over time will affect the tandem favorability. We vary the energy yield linearly with efficiency, using the high-efficiency silicon bottom cell energy yield as a baseline. The perovskite top cell is assumed to have a constant efficiency of 18% and an average sub-bandgap transmission of 85%. We additionally plot contour lines of the relative tandem LCOE advantage, as defined in eqn (1), colored corresponding to the magnitude of this advantage. Lastly, the HIT c-Si cell and PERC mc-Si cell are marked, along with the two corresponding 'advanced' Si cells with reduced silicon and silver usage.<sup>20</sup>

This figure illustrates the inherent challenge that tandems face: what leads to the largest relative benefit for tandems is not necessarily aligned with what leads to the lowest LCOE. However, since an almost free silicon cell that is over 20% efficient seem unlikely, we want to instead look at what bottom cell can best be optimized for both a low LCOE and a significant relative LCOE advantage to justify the investment in development and potential associated risk.

We see two distinct trends in the relationship between LCOE reduction and relative tandem advantage for different bottom cell efficiencies regimes. For lower efficiency silicon bottom cells that fall to the left of the central diagonal of the relative LCOE advantage contour plot in Fig. 5, efficiency improvements or cost reductions of the silicon cell will both reduce LCOE and improve the relative tandem advantage. This allows tandem and bottom cell development to work synergistically. The low-cost PERC mc-Si cell falls into this regime for both current and future residential scenarios. Starting from the low-cost silicon cell parameters, both improvements in efficiency and cost reductions help reduce LCOE and improve the tandem advantage, however further reductions in cost are more advantageous than efficiency increases as they improve the tandem advantage more quickly while also reducing the LCOE directly. We see that the 'advanced Si' cost savings greatly reduces the tandem LCOE and improves the relative tandem advantage. Additionally, there is room for the efficiency to fall with cost while still reducing the tandem LCOE and increasing the tandem advantage. For all scenarios except current residential, for a constant \$ per W equal to the low-cost silicon cell, reducing the cost and efficiency of the bottom cell improves the tandem LCOE and the tandem advantage. Efficiency gains for the low-cost cell will also reduce LCOE, but will only marginally increase (or potentially decrease, depending on the magnitude of the efficiency increase) the tandem advantage.

For higher efficiency bottom cells that fall to the right of the central diagonal of the relative LCOE advantage contour plot, increasing efficiency or reducing the cost of the silicon cell always reduces the tandem LCOE but also reduces the tandem LCOE advantage, making the tandem less desirable compared to its sub-cells. This is because in this regime, the silicon cell has a lower LCOE than the perovskite SJ, so the tandem must compete with the silicon SJ LCOE. Improvements in the silicon bottom cell, however, benefit the SJ more than the tandem. The high efficiency HIT c-Si cell falls into this regime for the 2018 residential case and on the edge of this regime in the future residential scenario. This means that as the high-efficiency silicon cell's cost decreases or efficiency improves, the high-efficiency tandem becomes less attractive because the Si SJ LCOE will fall more quickly than the tandem LCOE. For this to not be the case, a much better perovskite top cell would be required (but this may again cause the perovskite cell to then be more economically viable than the high-efficiency silicon single junction). Furthermore, we see that the 'advanced' multi- or monocrystalline silicon cells improves the LCOE advantage of the low-cost tandem over the high-efficiency tandem. This enforces that expected market directions further advantage the low-cost tandem.

For utility scale, both silicon cells are too expensive for the tandem to have the lowest LCOE. If the cost of the low-cost, mc-Si PERC cell is reduced by even a small margin, which can be achieved through kerfless wafers, however, the tandem will have a lower LCOE than either SJ solar cell. This is also true for the high-efficiency c-Si HIT cell, however the LCOE of the tandem and the silicon SJ would be very close, giving tandems little advantage.





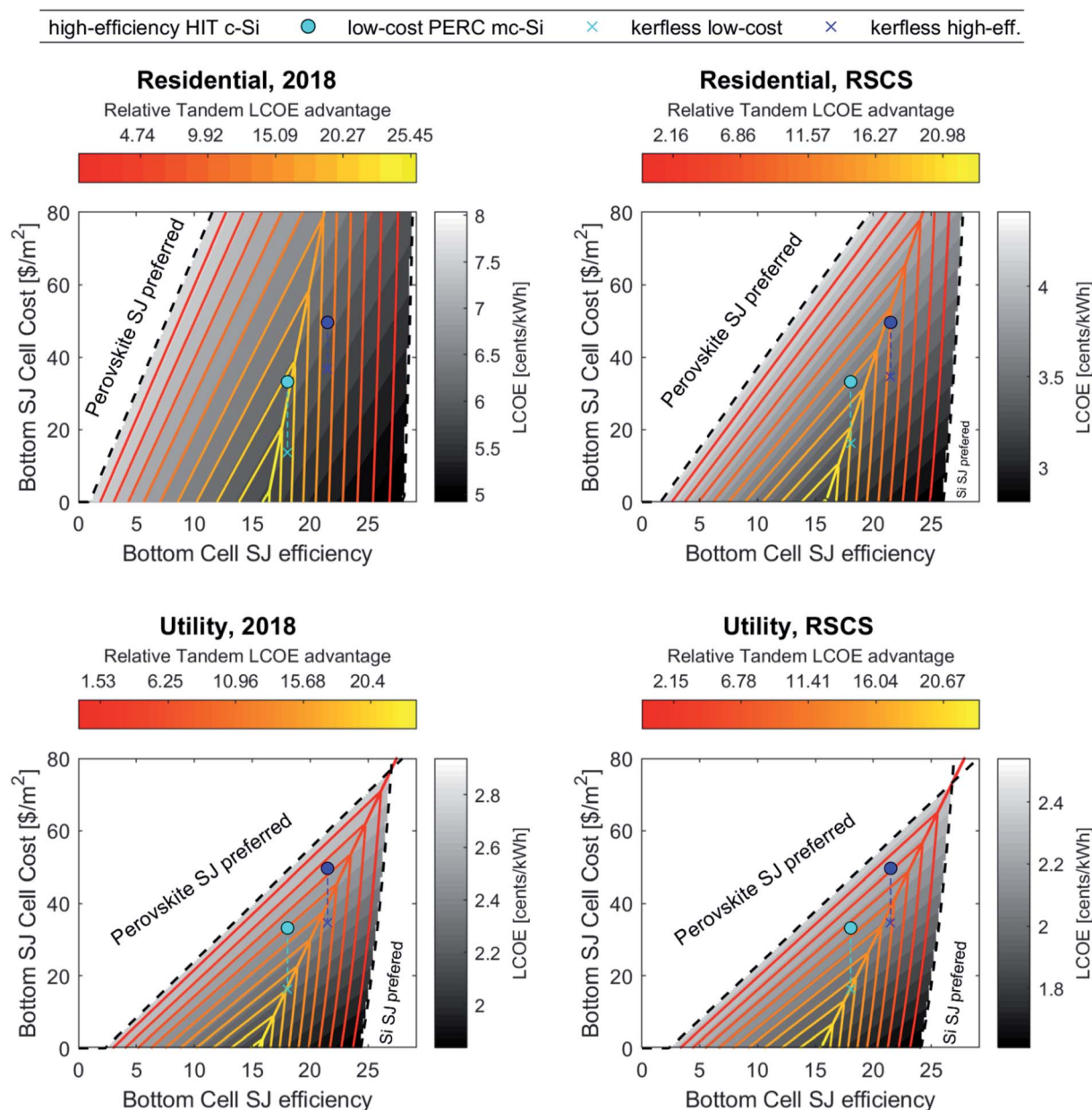


Fig. 5 The grey contour plot shows the LCOE of a perovskite-silicon tandem over a range of bottom cell single-junction efficiencies and areal costs, assuming an 18% perovskite top cell with average sub-bandgap transmission of 85%. The red-to-yellow lines trace contour lines of the relative LCOE advantage, *i.e.*  $\frac{\min(\text{LCOE}_{\text{Si}}, \text{LCOE}_{\text{perov.}}) - \text{LCOE}_{4T}}{\min(\text{LCOE}_{\text{Si}}, \text{LCOE}_{\text{perov.}})}$ . The regions left white indicate where one of the single-junction sub-cells (noted by the overlaid text) has a lower LCOE than the tandem. The low-cost and high-efficiency cells are marked, along with an 'x' indicating the cost 'advanced' multi- or monocrystalline silicon cells are used, respectively, with reduced silicon and silver usage.

### C. Considerations for investing in tandems

From the perspective of a silicon manufacturer that produces high-efficiency silicon modules, investing in perovskite tandems is likely not worthwhile as the benefit will be small and requires perovskite cell efficiency above 16% to maintain any benefit as system costs fall. If the efficiency of the perovskite top-cell on a module level reaches 20%, tandems look more appealing since the relative tandem LCOE advantage would exceed 15%, however, at this point, perovskite single-junctions

will likely be more viable than either the tandem or the silicon SJ. Furthermore, any improvement of the silicon cell's efficiency will make the relative tandem LCOE even less beneficial. If the Si-SJ module efficiency reaches ~25%, then the tandem and Si-SJ LCOE will be equivalent, assuming an 18% perovskite top cell efficiency and unchanged Si manufacturing cost. This parity-achieving efficiency decreases as the silicon manufacturing cost and system costs decrease.

From the perspective of a manufacturer of lower cost multi-crystalline silicon solar cells, investing in perovskites has the



potential to be greatly beneficial by reducing the LCOE of their product and opening up opportunities to compete in the residential market – an area of the solar market that multicrystalline silicon solar cells currently cannot compete in because of their low efficiency. The relative benefit of the tandem is fairly robust to potential uncertainty in perovskite cell manufacturing cost and efficiency, as well as to expected trends in system costs. Furthermore, adoption of technological advancements to lower costs such as reducing silicon and silver usage will increase the benefit of the tandem and the relative advantage over the high-efficiency tandem. The design of the bottom cell can also be optimized based on lower expected current-carrying requirements, making series resistance less important. Such reductions in multicrystalline silicon cells manufacture could enable these tandems to compete with the perovskite SJ in utility scale applications.

Lastly, from the perspective of the broader solar community and in the interest of reducing the LCOE of solar electricity generally, we see that both tandems provide a path to greatly reduced LCOE compared to the silicon single-junction technology, decreasing LCOE by 9–13% (depending on the scenario), compared to the lowest LCOE Si SJ and assuming an 18% efficient perovskite top-cell. The two tandems suggest similar resulting LCOEs, thus neither seems to obviously be more viable, however expect directions in the solar market, changes in manufacturing, and efficiency improvement of both sub-cells indicate the low-cost tandem will more easily achieve a lower LCOE.

Additionally, the multicrystalline silicon bottom cells have a large CapEx advantage which allows for much faster growth in manufacturing capacity.<sup>30</sup> Thus, low-cost silicon-perovskite tandems could enable a faster growing solar market – a necessity for near term terawatt scale installation of solar panels.<sup>31</sup> Furthermore, the low-cost silicon CapEx and growth rate will more closely match the CapEx and growth rate of perovskite solar cells than the high-efficiency silicon, making multicrystalline silicon and perovskite a better pairing.

## V. Conclusion

In this work, we perform techno-economic analysis to explore the trade-off of cost-*versus*-efficiency for silicon bottom cells in a perovskite-silicon tandem in order to define the optimal approach to make cost-effective perovskite-silicon tandems. We show that perovskite-silicon tandems can be made cost-effective, competitive, and provide sufficient benefits for investment by using current, available low-cost multicrystalline silicon technology, with further advantages from even lower cost kerfless wafer production. Furthermore, these tandems are robust to and benefit from expected technological advances and market trends. Transitioning silicon manufacturing to high-efficiency, high-cost architectures for use in tandems is shown to be in unnecessary and potentially disadvantageous for achieving competitive tandems that reduce LCOE.

We fabricate a perovskite-silicon tandem with a multicrystalline bottom cell to demonstrate the viability of using a low-cost silicon bottom cell. Our tandem reaches an efficiency of 19.1%, surpassing the values for either comprising sub-cell operated independently. We then compare, more generally

through modeling, perovskite-silicon four-terminal tandems that employ a high-efficiency, high-cost silicon bottom cell *versus* tandems with low-cost and lower-efficiency bottom cell. The figure of merit for this comparison is the LCOE for utility and residential scale installations for both current and future system cost scenarios. Through broader parametric cost analysis, we see that a low-cost mc-Si based tandem has great potential to reduce LCOE relative to the mc-Si single-junction, and possibly open mc-Si technology to new, more lucrative markets like high-efficiency rooftop markets.

In current residential systems, the high-efficiency tandem has a lower LCOE than the low-cost tandem, yet that advantage is small and, as system costs continue to fall, is lost entirely. The low-cost tandem has a lower LCOE than the high-efficiency tandem for utility systems. In all cases, as perovskite top-cell efficiency improves, the low-cost tandem has an increasingly competitive LCOE compared to the high-efficiency tandem. Furthermore, the low-cost tandem has a significantly lower LCOE than the low-cost silicon bottom cell, demonstrating a clear benefit to invest in transitioning to tandem technology, while the tandem using the high-efficiency silicon cell has an LCOE that is close to or potentially higher than the LCOE of the high-efficiency silicon single-junction cell. This demonstrates that there is a compelling argument to pursue perovskite-silicon tandems that employ low-cost, lower-efficiency bottom cells as the benefit of the tandem is more significant and more certain. Lastly, we map out a path towards optimizing for both device LCOE reduction and the relative benefit of a tandem compared to its sub-cells. A tandem using a high-cost, high-efficiency bottom cell, despite achieving a higher efficiency, is likely not economically favorable compared to its sub-cells or compared to the low-cost tandem. This finding stands in contrast with much of the perovskite-silicon tandem device development and motivates research to explore lower-cost, lower-efficiency silicon bottom cells for tandems that may not achieve record efficiencies but are likely to be more commercially viable.

## Conflicts of interest

There are no conflicts to declare.

## Acknowledgements

The authors thank Dr Kevin Bush for contributing cells early on for this work and Dr Adam Lorentz for his support and feedback. This work was financially supported by the DOE-NSF ERF for Quantum Energy and Sustainable Solar Technologies (QESST), by funding from Singapore's National Research Foundation through the Singapore MIT Alliance for Research and Technology's "Low energy electronic systems (LEES) IRG", and the funding from the Martin Family Society of Fellows for Sustainability.

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