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Controlled synthesis of few-layer SnSe₂ by chemical vapor deposition†

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Few-layer SnSe₂ has intrinsic low thermal conductivity and unique phase transition from amorphous to crystalline state under laser irradiation. It has been extensively used in the fields of thermoelectric conversion and information storage. However, the traditional precursors like tin oxide and organic compounds have either high melting points or complex compositions, and the improper deposition temperature of the substrate may lead to mixed products, which impedes controllable synthesis of high-quality few-layer SnSe₂. Here, we propose a chemical vapor deposition (CVD) method, in which the precursor evaporation and deposition have been controlled *via* the adjustment of precursors/substrate positions, which effectively avoided mixed product growth, thus achieving the growth of high-quality few-layer SnSe₂. The calculated first-order temperature coefficient of the A_{1g} mode is $-0.01549 \text{ cm}^{-1} \text{ K}^{-1}$, which is superior to other two-dimensional (2D) materials. Meanwhile, two exciton emissions from few-layer SnSe₂ have been found, for which the higher energy one (1.74 eV) has been assigned to near-band-gap emission, while the lower one (1.61 eV) may have roots in the surface state of SnSe₂. The few-layer SnSe₂ also exhibits large exciton binding energies (0.195 and 0.177 eV), which are greater than those of common semiconductors and may contribute to stability of excitons, showing broad application prospects in the field of optoelectronics.

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1. Introduction

As an important member of two-dimensional (2D) layered materials, the group-IV metal chalcogenides (GIVMCs) have advantages like abundant earth reserves, environmentally-friendly features and low cost.^{1–3} The GIVMC semiconductors have diverse electron band structures, and their band gaps correspond to a photoresponse spectrum ranging from ultraviolet to mid-infrared.^{4–7} Unlike element of other group, the metal atoms of GIVMCs semiconductors have +4 or +2 valence states, both states can coordinate with chalcogens atoms to form 2D materials. Especially for the metal element Sn, the formed chalcogenide SnSe₂ is a n-type indirect semiconductor with a hexagonal crystal structure similar to CdI₂, while the elements in the same group mostly form p-type indirect semiconductor with orthorhombic crystal structure.⁸ SnSe₂ is a promising material in the field of thermoelectricity, which has relatively lower lattice thermal conductivity compared to graphene.⁹ Meanwhile, the quick and reversible

transformation of SnSe₂ from crystal to amorphous under laser irradiation make it a typical phase change materials which can be used for information storage.^{10,11} What's more, SnSe₂ is also a nova material in the field of photoelectricity because of its high responsivity ($1.1 \times 10^3 \text{ A W}^{-1}$), high on/off ratio and fast response rate.¹² Despite the excellent properties mentioned above, there still exist challenges for SnSe₂ synthesis techniques.^{9,13,14} On one hand it is limited due to the precursor selection, general precursors like tin oxides have relatively high melting points,¹ while other precursors like organic compound [SnCl₄{ⁿBuSe(CH₂)_nSeⁿBu}] made by artificial cumbersome steps are extremely sensitive to moisture and oxygen,¹⁵ thus the evaporation of precursors is uncontrolled and affects the morphology of the SnSe₂ product. On the other hand, due to the improper deposition temperature of substrate, the obtained SnSe₂ product is usually mixed with a large number of impure phase (SnSe or Se).^{12,16,17} Thus till today, most of the current researches can only product multilayers or bulk SnSe₂.¹⁸ However, compared to bulk material, properties of single crystal in 2D few layers material can effectively avoid the influence of grain boundary.¹⁹ Additionally, there are also researches demonstrating that few layers samples have higher carrier mobility than their bulk counterparts.^{20,21} Therefore, the synthesis of few-layer SnSe₂ samples are important for further properties and applications exploration. At the present, only a few studies have achieved the preparation of few-layer SnSe₂. In these studies,

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Athanasios and co-workers²² fabricated few-layer SnSe₂ by molecular beam epitaxy (MBE) technique. Liu and co-workers²³ have synthesized single crystal of SnSe₂ by chemical vapor transport (CVT) method, then obtained few-layer SnSe₂ *via* mechanically exfoliated method. Zhai and co-workers¹² for the first time obtained high quality ultrathin SnSe₂ (with a thickness of 1.5 nm, corresponds to only two atomic layers) by chemical vapor deposition (CVD) method. However, problems like high instrument costs, low yield, uncontrollable size are still existing.

To address the issues mentioned above, based on the research of reaction thermodynamics, a CVD synthesis method with SnI₂ and Se as precursors has been proposed, in which the evaporation of precursors has been controlled *via* the sources position, which kept the vapor pressures consistent at the growth locations, while the deposition temperature has been controlled *via* the substrate position, which effectively avoided impure phase growth, thus achieving the growth of high-quality few-layer SnSe₂. Atomic force microscopy (AFM), Raman, photoluminescence (PL) and X-ray Photoelectron Spectroscopy (XPS) characterization reveal that the as-synthesized few-layer SnSe₂ (with a thickness of 1.34 nm) are of high quality with an indirect bandgap ≈ 1.74 eV, while subsequent variable temperature (77–298 K) Raman and PL measurements provide SnSe₂ lattice vibration and exciton emission information for further fundamental research and potential applications exploitation in thermoelectric and optoelectronics field.

2. Experimental section

2.1 Fabrication of SnSe₂

The SnSe₂ crystals were synthesized on mica substrates *via* atmospheric pressure CVD method, with the CVD procedures carried out using a Mini-Mite™ tube furnace, which is purchased from Lindberg/Blue M. Before the synthesis, high purity Se powder (99.5%, about 0.02 g) and SnI₂ powder (99.999%, about 0.01 g) were placed in two separate quartz boats, and placed 15 cm and 14 cm upstream from the center heating zone respectively in a quartz tube. All the chemicals were purchased from Alfa and used as received without further purification. To provide the growth locations for the synthesis product, the substrates were placed 10, 12, and 14 cm downstream from the center heating zone, respectively. Ar (20 sccm) and H₂ (5 sccm) were used as carrier gas and sent into the furnace for the whole synthesis process. During the synthesis, the furnace was firstly heated to 915 K in 15 min, and naturally cooled to room temperature after 10 min system stabilization.

2.2 Characterization methods

The optical images were taken with Olympus BX51 optical microscopy. The AFM images were taken with Bruker Multi-Mode 8 atomic force microscope. The XPS spectra were collected with ESCALAB 250Xi X-ray photoelectron

spectroscopy. The Raman and PL spectra were collected with Witec alpha 300 confocal Raman spectroscopy.

3. Results and discussion

High quality few-layer SnSe₂ were synthesized by CVD method, Fig. 1(a) illustrates the CVD process for few-layer SnSe₂ fabrication. During the growth process, we used SnI₂ and Se powders as precursors, and mica as substrate. Unlike most tin oxides with high melting points or organic compounds [SnCl₄{ⁿBuSe(CH₂)_nSe^mBu}] with complex composition, SnI₂ has a relatively low evaporation temperature (590.15 K) and a simple chemical composition. It can provide more uniform and stable growth conditions for the CVD process.¹² Mica is a 2D layered material without hanging bond on the surface, the migration barrier of the adsorbed atoms on mica surface is much smaller than that on silicon substrate, which permits rapid migration of the adsorbed atoms on the mica surface, thus contributes to a better CVD growth of few layers 2D materials. In contrast with mica substrate, it is easily to grow thick SnSe₂ flakes on silicon substrate due to lots of dangling bonds existing on its surface (as shown in Fig. S1†). The detailed experimental procedure is described in the Experiment section. Growth mechanism with two steps was proposed to describe the SnSe₂ growth process. The first step is the evaporation and diffusion of precursor sources. According to the results of thermodynamic calculations, the simulation results of SnI₂ and Se vapor pressures as a function of temperature are showed in Fig. 1(c) and (d), more details are showed in ESI.† Combining with the actual measured furnace temperature results (Fig. 1(b)), Se powder and SnI₂ powder were put in two separated quartz boats, and placed 15 cm and 14 cm upstream from the center heating zone, corresponding to the 540 K and 625 K respectively. After the transport of precursors vapor to the substrate position by carrier gas, atoms released by precursor sources occurred to adsorb and desorb on the substrate, the adsorbed Se and Sn atoms will migrate and react with each other, thus leading to nucleation and growth of SnSe₂. When the substrate was located at different positions, different number of adsorbed active atoms would desorb and evaporate into carrier gas.^{24,25} The rate of atoms migrations J can be described by eqn (1)

$$J = D \times \nabla C \quad (1)$$

where ∇C is the atoms concentration gradient, D is the atoms migration coefficient, which is related to the nature of the adsorbent, and the fact that relative atomic mass of Sn is larger than Se results in $D_{\text{Sn}} < D_{\text{Se}}$ at the same temperature. Noticing that the atoms migration coefficient D is also proportional to $e^{-E_m/kT}$ (where E_m is migration barrier energy; k is Boltzmann constant; T is temperature), it's easy to understand that the migration rate is reduced with the decrease of substrate temperature.²⁶ According to our experiment results, when the mica substrate was located at 12 cm downstream from the center heating zone, corresponding to the temperature of 750 K, the temperature permitted relatively suitable migration rate of the adsorbed atoms on the mica surface, and the SnSe₂ growth



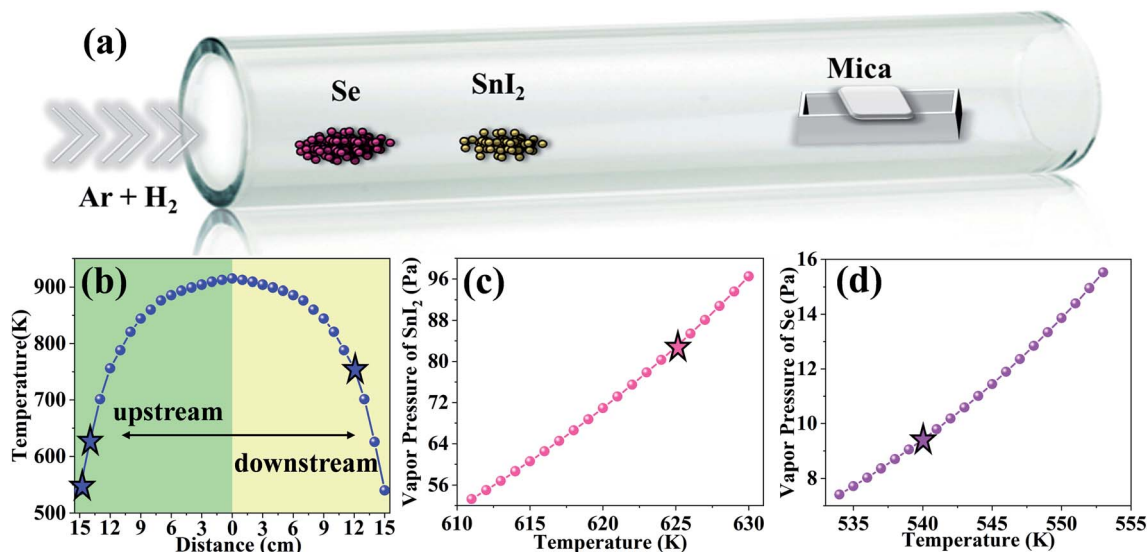


Fig. 1 Synthesis of SnSe₂ by chemical vapor deposition method. (a) Schematic illustration of the CVD process for the synthesis of the SnSe₂, where Se and SnI₂ powder were used as the precursors. (b) Temperature profile of the furnace. (The heating zone of furnace was set to 915 K. The star marks from left to right represent Se, SnI₂ and optimal mica locations, respectively. The distance of "0" represents the heating center of furnace.) (c) Vapor pressure dependence of SnI₂ with temperature. (The star mark corresponds to the vapor pressure at 625 K.) (d) Vapor pressure dependence of Se with temperature. (The star mark corresponds to the vapor pressure at 540 K.)

temperature was ensured synchronously, which benefitted few-layer SnSe₂ growth (Fig. S2(b)†). When the mica substrate was placed at 10 cm downstream from the center heating zone, corresponding to relatively higher temperature (820 K), Se atoms would react more vigorously with Sn atoms under fast atoms migration rate, meanwhile there were part of adsorbed Se atoms on substrate which would desorb into the carrier gas due to the high temperature.²⁵ Additionally, as the growth temperature of SnSe is higher than SnSe₂, it's easier to fabricate SnSe under a relatively high temperature. Therefore, thick SnSe with square shaped were fabricated on mica substrate (Fig. S2(a)†) at this occasion. In contrast, when the substrate was placed at 14 cm downstream from the center heating zone, which corresponds to lower temperature (625 K), the growth rate of the sample and the nucleation density decreased due to the slow atoms migration, only a few samples were collected on mica, as shown in Fig. S2(c)†. Based on the thermodynamics calculation and prediction, controllable few-layer SnSe₂ synthesis has been achieved.

Structural model of SnSe₂ is showed in Fig. 2(a), SnSe₂ presents a hexagonal crystal structure governed by van der Waals interactions between the layers, each layer consists of "Se-Sn-Se" three atomic planes with internal covalent bonding. Fig. 2(b) shows optical image of the SnSe₂ synthesized on mica substrate, showing their hexagon shape with lateral dimensions about 10 μm. AFM image of few-layer SnSe₂ is showed in Fig. 2(c), which reveals the sample thickness is ~1.34 nm, corresponding to two atomic layers SnSe₂. Raman spectroscopy was performed to provide information about the quality of as-prepared few-layer SnSe₂, and the corresponding result is shown in Fig. 2(d). Apparently, except mica peaks at 194 cm⁻¹, 271 cm⁻¹, 313 cm⁻¹, there are two peaks located at 115 cm⁻¹ and 184 cm⁻¹, corresponding to E_g mode and A_{1g} mode, which

are caused by the in-plane and out-plane vibrations of the Se atoms, respectively.²³ The intensity of E_g mode is significantly weak due to few scattering centers existing in few-layer

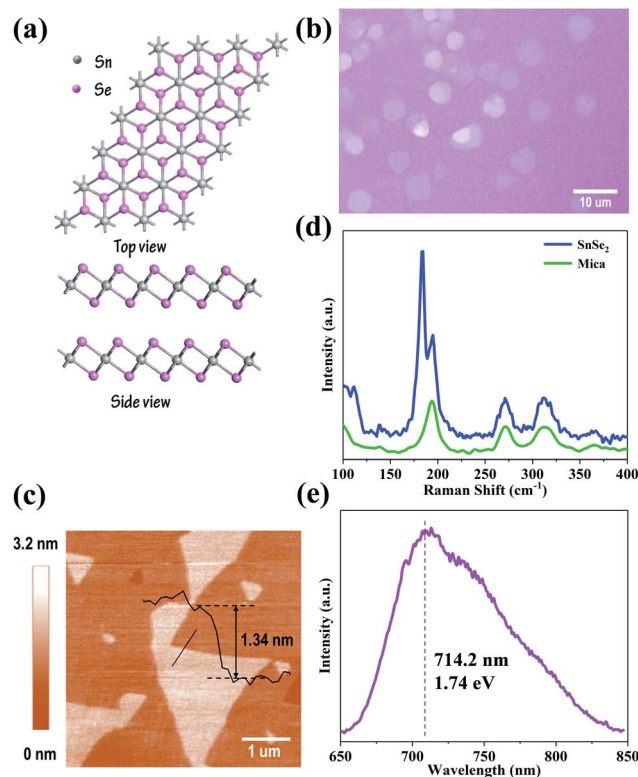


Fig. 2 (a) Structural model of SnSe₂. (b) Optical image of the SnSe₂ synthesized on mica. (c) AFM image of few-layer SnSe₂, the height profile shows a thickness of 1.34 nm (inset). (d) Raman spectra of SnSe₂ at room temperature. (e) PL spectra of SnSe₂ at room temperature.



SnSe₂.^{12,27} Raman mapping of A_{1g} mode shows the uniformity of the sample (Fig. S3(c)†), and the PL spectrum shows a peak centered at 714.2 nm (Fig. 2(e)), corresponding to a bandgap of 1.74 eV. All results above confirm that high crystalline quality few-layer SnSe₂ has been synthesized *via* the proposed CVD method in which the thermodynamics parameters have been precisely controlled.

With the purpose of investigating the elemental composition and stoichiometry of the as grown SnSe₂ on mica substrates, XPS was used. The binding energy is referenced to an adventitious C 1s peak at 284.6 eV. The typical XPS survey of SnSe₂ is showed in Fig. 3(a), in which the peaks of Sn 3d, Se 3d and C 1s were identified respectively. The peak of O 1s is found because of the mica substrate. Then XPS spectra of Sn 3d and Se 3d were furtherly performed. The energy for Sn 3d_{3/2} and Sn 3d_{5/2} (Fig. 3(b)) are located at 494.8 eV and 486.4 eV, respectively. According to previous studies,²⁸ the distance between these two peaks is 8.4 eV, revealing that the valence of Sn is +4. The spectra of the constituent element Se 3d is showed in Fig. 3(c), which splits into two states Se 3d_{3/2} and Se 3d_{5/2} through the peak fitting, relating to the binding energy of 54.58 eV and 53.88 eV respectively. Based on the analysis above, it also proved there is no presence of other oxidation states. The synthesis of high quality SnSe₂ on mica substrates by CVD method has been confirmed.

Better heat dissipation may be one of the biggest challenges in electronic devices, as the performance and service life of the device are greatly affected by the self-heating effect.^{29,30} For this problem, it is important to investigate thermal property of materials.¹³

Raman spectroscopy can be used to characterize the lattice vibrations of materials rapidly and non-destructively, and provide information of lattice thermal property. Hence, lattice vibrations of few-layer SnSe₂ were brought to research by temperature-dependent Raman test. There are several reasons for the temperature dependence of Raman modes position,

such as electron–phonon, anharmonic phonon–phonon interactions and thermal expansion.^{29–31} SnSe₂ is a 2D layered material, its interlayer van der Waals interaction tends to lead lower interfacial thermal conductivity. A_{1g} mode corresponding to the out-of-plane vibration mode, this interlayer vibration is sensitive to temperature, and largely contributes to the total lattice thermal conductivity, so that the temperature dependence of A_{1g} mode was investigated. In Fig. 4(a), we present Raman spectra of few-layer SnSe₂ at temperature ranging from 77 K to 298 K. With temperature increase, A_{1g} mode showed red shift, and the peak intensity increased, showing the general thermal effect of semiconductor. The Fig. 4(b) shows the typical plot of A_{1g} mode positions as a function of temperature for few-layer SnSe₂, which behaves linear relation from 77 K to 298 K. The Lorentzian fitting eqn (2) was used for calculating temperature coefficients (χ).^{32,33}

$$\omega(T) = \omega_0 + \chi T \quad (2)$$

where ω_0 is the A_{1g} position for temperature close to 0 K and χ is the first-order temperature coefficient of A_{1g} mode. This equation is widely used for describing temperature dependence of Raman shifts for nanomaterials. The position of SnSe₂ A_{1g} mode decreased by $\sim 3.4 \text{ cm}^{-1}$ when the temperature increased from 77 K to 298 K, thus the calculated first order temperature coefficient χ of A_{1g} is $-0.01549 \text{ cm}^{-1} \text{ K}^{-1}$, this result is consistent with the literature,³² which is superior to many other 2D materials. As the change in the A_{1g} mode is mostly due to the thermal expansion and volume contribution, based on its relatively high temperature coefficients result for A_{1g} modes, SnSe₂ can be used for monitoring local temperature in electronic operating device.³³

In the process of photoelectric conversion, the electrons are excited into the conduction band and the holes with positively charged are left in the valence band, the electrons and holes will form a bound state due to the strong coulomb interaction, which is called an exciton. In the semiconductor electronic devices, the existence of excitons plays a central role. The effective mass theory of 2D excitons has predicted that exciton binding energy of two layers SnSe₂ is 0.229 eV.²⁰ However, this value hasn't been experimentally verified. Therefore, the temperature-dependent PL measurements on the same SnSe₂ was carried out to investigate its exciton binding energy. When the temperature increases from 77 K to 298 K, the PL intensity

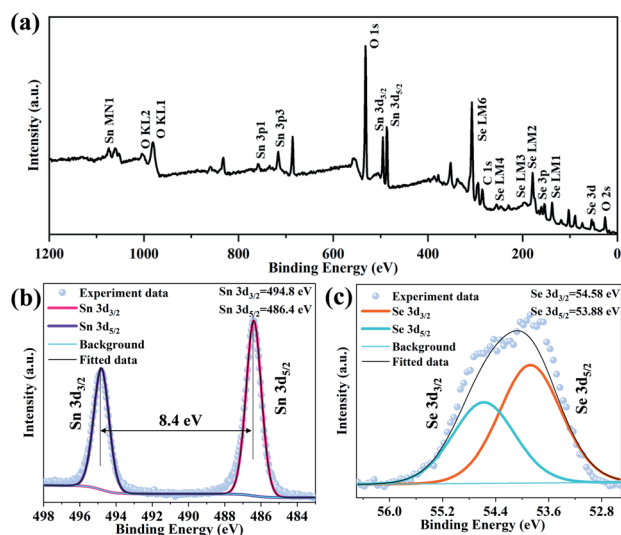


Fig. 3 XPS spectra of SnSe₂ for typical survey (a), Sn 3d (b) and Se 3d (c), respectively. No presence of other oxidation states is realized.

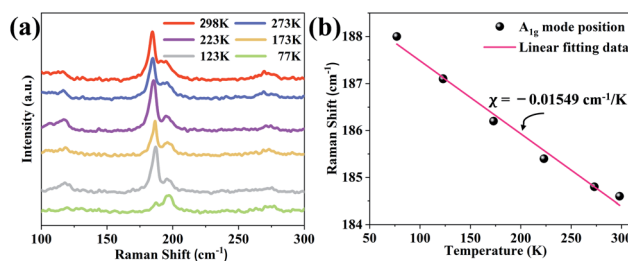


Fig. 4 Raman spectra of the (a) SnSe₂ measured in the temperature range between 77 K and 298 K. (b) Temperature dependence of SnSe₂ A_{1g} mode position.



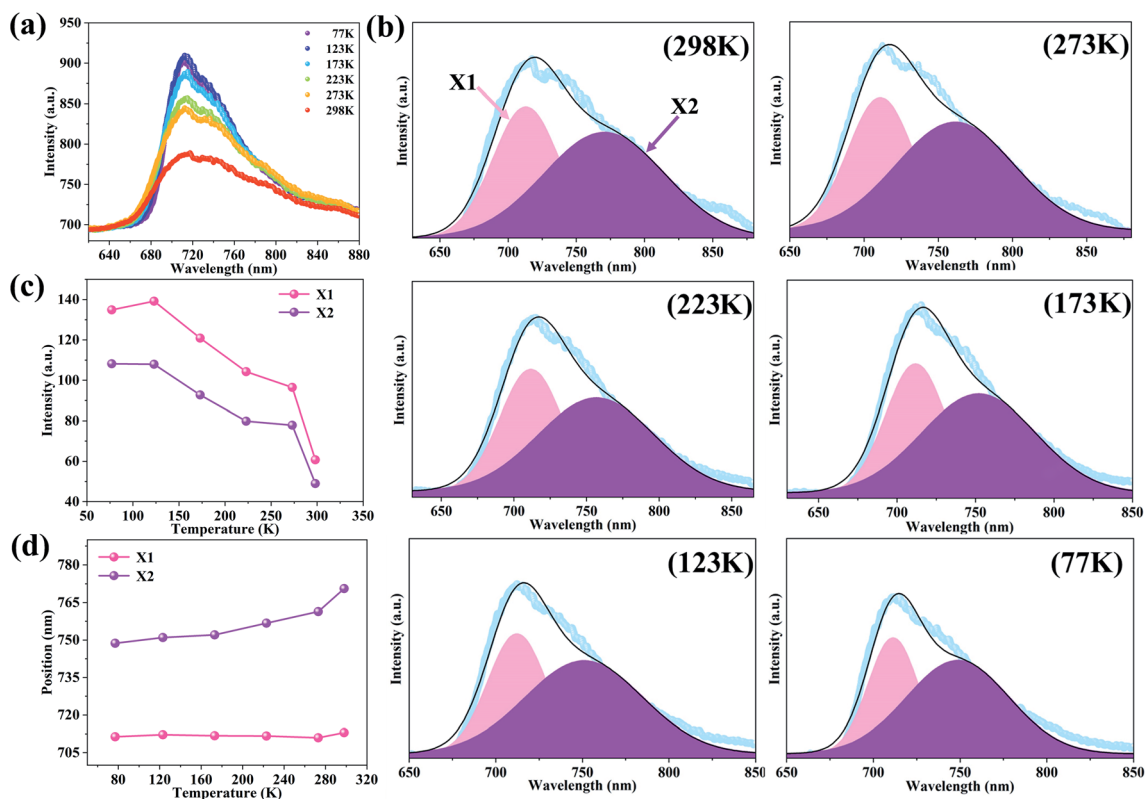


Fig. 5 PL spectra of the (a) SnSe₂ measured in temperature range from 77 K to 298 K. (b) Temperature dependent PL spectra fitted by the Gaussian distribution. (c) The intensity of X1 and X2 varying with temperature. (d) The position of X1 and X2 varying with temperature.

are generally weakened (Fig. 5(a)). There are some reasons for the weak PL intensity, one is that the SnSe₂ is a natural indirect semiconductor, the extremes of the conduction band and valence band correspond to different wave vectors k , which means the transition between bands is an indirect one, thus during the transition process, in addition to emitting photons, phonons are also involved, which consume a part of energy. Beyond that, unlike d electrons of Mo, the element Sn has out p electrons which are involved in the structural bonding, leading to very weak luminous intensity.^{13,34} When fitted by Gaussian functions, the temperature-dependent SnSe₂ PL peak can be deconvoluted with X1 and X2 peaks (Fig. 5(b)). Not only the overall PL intensity but also the intensities of X1 and X2 peaks were weakened with the temperature increasing, as shown in Fig. 5(c). It is attributed to the thermally activated nonradiative recombination due to the increased interactions.³⁵ That is, when the thermal motion of phonons is enhanced with the temperature rising, the nonradiative process will consume more photogenerated carriers, resulting in the weakened PL intensity. The temperature dependence of the PL intensity can be expressed by eqn (3),³⁶

$$I(T) = I_0 / (1 + A \exp[-E_B/k_B T]) \quad (3)$$

where I_0 is the PL intensity at 0 K, A is the fitting parameter, E_B is the exciton binding energy, and k_B is the Boltzmann constant. From the fitting curves in Fig. S4(a) and (b),† the exciton

binding energies of X1 and X2 peaks are calculated to be 0.195 and 0.177 eV respectively, which are greater than those of common semiconductors, and approximately equal to the calculated theoretical value 0.229 eV. The large exciton binding energy is helpful for stabilizing an exciton in a nanosystem.³⁷ In the Fig. 5(d), the PL positions of X1 increased ~ 2.02 nm when the temperature increased from 77 K to 298 K, which showed a redshift of energy ~ 0.005 eV. The X1 peak is attributed to the near-band-gap emission,³⁸ which is speculated to be associated with phonons interaction. However, as the phonons interaction is greatly weakened at low temperature, it can't reflect the bandgap change, resulting in no obvious change for X1 peak position. Meanwhile, for the PL position of X2 peak, with the temperature increase, this position increased from 748.7 nm to 770.6 nm, corresponding to an energy decrease from 1.66 eV to 1.61 eV. This phenomenon may result from the surface defect state of SnSe₂.^{38,39} The dynamics of exciton and scattering mechanism are still not clearly, which call for further investigations.

4. Conclusions

In conclusion, we obtained few-layer SnSe₂ by CVD method with SnI₂ and Se as precursors, achieving controllable precursors evaporation and avoid mixed products *via* adjust the positions of precursors/substrate. The temperature dependent Raman and PL tests of the products were carried out subsequently, and



the first-order temperature coefficient of A_{1g} mode was measured to be $-0.01549 \text{ cm}^{-1} \text{ K}^{-1}$, which is superior to other 2D materials, showing great potential for application in 2D electronic device. Meanwhile, we found two exciton emissions from few-layer SnSe_2 . The higher energy emission (1.74 eV) may be referred to near-band-gap emission, while the lower one (1.61 eV) may be affected by the surface state of SnSe_2 . The exciton binding energy of X1 and X2 are about 0.195 and 0.177 eV respectively, which may signify a relatively good stability against thermal dissociation of excitons. The few-layer SnSe_2 have a promising prospect for future thermoelectric and optoelectronics field. What's more, this synthetic method by precisely controlling the thermodynamics parameters can be extended to other 2D materials growth.

Conflicts of interest

There are no conflicts to declare.

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