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# Time dependence of electrical characteristics during the charge decay from a single gold nanoparticle on silicon

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In this work, we investigate the time dependence of trapped charge in isolated gold nanoparticles (Au-NPS) dispersed on n-Si substrates, based on the electrical characteristics of nano metal–semiconductor junctions. The current–voltage ( $I$ – $V$ ) characteristics have been analysed on a single Au-NP at different time intervals, using conductive mode atomic force microscopy (AFM). The Au-NPs have been characterized for their morphology and optical properties using transmission electron microscopy (TEM), ultraviolet visible (UV-vis) spectroscopy and scanning electron microscopy (SEM). The tunneling current is found to be a direct function of the trapped charge in the NP, due to the charge screening effect of the electric field at the NP/n-Si interface. The evolution of the  $I$ – $V$  curves is observed at different time intervals until all the trapped charge dissipates. Moreover, the time needed for nanoparticles to restore their initial state is verified and the dependence of the trapped charge on the applied voltage sweep is investigated.

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## Introduction

Due to the unique physical and chemical properties of gold nanoparticles (Au-NPs), they have been used extensively in nanoelectronics and biomedical applications.<sup>1–4</sup> Below 20 nm, the material characteristics are more dependent on their size and structure.<sup>5–9</sup> Namely, it has been reported that the photo-detection and electrical characteristics depend only on the size of the metal–semiconductor nano interfaces.<sup>10–12</sup> Because of their compatibility with organic compounds and ability to bond with reduced graphene oxide the Au-NPs have been used to improve the efficiency of semiconducting devices.<sup>13,14</sup> The Au-NPs, being chemically stable with high work function, are considered to be excellent candidates for charge trapping elements.<sup>15,16</sup> Therefore, rigorous investigation of the NPs electrical characteristics on Si substrates is crucial in this regard. This would involve the AuNP/Si interface electrical characteristics and the charge trapping effect, that are addressed in this work.

For various applications, Au-NPs can be synthesized using either Ostwald ripening or chemical synthesis.<sup>15,17–19</sup> In Ostwald ripening method, a very thin layer of gold is deposited on a suitable substrate followed by high temperature annealing process. The subsequent high temperature annealing process

causes the formation of NPs or NCs due to the minimization of surface energy.<sup>20–22</sup> Au-NPs fabricated using this technique are published in the literature.<sup>18,23</sup> However, due to different sizes and distributions, due to the difficulty in size control of nanoparticles and diffusion of metal content at the interface, this method is not a suitable candidate for device applications. Alternatively, solution processed Au-NPs synthesis is used in memory application.<sup>24,25</sup> By using solution-based synthesis, one can synthesize the targeted size of nanoparticles by choosing appropriate precursors as these customized colloidal solutions enable the use of NPs for different applications. The distribution of these NPs on the substrate can be controlled by different deposition methods like spin coating and dip coating.<sup>11,26,27</sup> Recently, the solution processed Au-NPs have been used for electron storage centers by properly embedding NPs in the carefully engineered blocking and tunneling layers of Al<sub>2</sub>O<sub>3</sub> on an n-Si substrate.<sup>26</sup> To characterize individually or discretely dispersed NPs on the substrate, atomic force and scanning tunneling microscopies (AFM and STM) are considered to be among the best tools. Consequently, the tip size plays a vital role in investigating the electrical characteristics of individual NPs. Therefore, the smaller the dimension of the nanotip is, the more accurate local characteristic data can be obtained. As a result, the fabrication of such well-defined nanotips has received a lot of attention from researchers in this field.<sup>28–30</sup>

Earlier, we have shown the significant effect of individual NPs deposited on a thin film passivated n-Si substrate on the enhancement of the electric field, and, in turn, on the reverse tunneling current through the nano-Schottky junction.<sup>11</sup> In this work, the effect of the charge trapped in the NP on the  $I$ – $V$

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characteristics of the nano Schottky junctions, formed between the NP and Si substrate, and the time evolution of the  $I$ - $V$  curves during the charge decay is studied. Moreover, this will help to assess and optimize the use of these metal nanoparticles in future memory based devices.

### Experimental approach

In order to investigate the charge trapping centers on the n-Si substrate, 15 nm diameter gold NPs dispersed in citrate are used. The citrate acts as a stabilizing agent for Au-NPs. The Au-NPs solution is initially sonicated for one hour to ensure elimination of agglomeration. After the sonication process, the Au-NPs are individually dispersed on n-Si substrate, resistivity 5 ohm-cm, through spin coating at a rate of 500 rpm. The n-Si wafer used is diced into 1 cm  $\times$  1 cm pieces, which are passivated for 30 s in hydrofluoric acid (HF) solution and then rinsed in de-ionized (DI) water. The dispersion of Au-NPs on the substrate is confirmed using a Scanning Electron Microscope (SEM) and AFM in AC mode. Furthermore, the atomic crystalline nature of Au-NPs is observed using a High-Resolution Transmission Electron Microscope (HRTEM). Ultraviolet visible spectroscopy is also carried out to confirm the size of Au-NPs. Finally, the electrical measurements are carried out using Asylum MFP3D-AFM system, by placing the AFM probe either directly on n-Si surface or on the pre-determined single Au-NP on the n-Si substrate. During the electrical measurements, the gold coated AFM tip is grounded, and voltage bias sweeps are applied on the substrate.

## Results and discussion

Fig. 1(a) illustrates the schematics of the electrical measurement setup using the AFM, where the tip is grounded and voltage is applied on the sample. Fig. 1(b) is the SEM image of gold-coated AFM tip with the approximate diameter of  $\sim 37$  nm. The SEM image is done at a working distance of 6 mm and an acceleration voltage of 15.0 kV. During the electrical measurement, a gold-coated metallic disc is attached to the sample with silver paste applied to its backside, to enhance the ohmic contact.

The size and crystal structure of the NPs are accurately characterized using UV-vis method and HRTEM with atomic resolution. Fig. 2(a) shows the AFM image of well-separated Au-

NPs on the n-Si substrate. The image is produced using an AFM tip with a resonance frequency of 261 kHz. The scan is carried out in the 1.4  $\mu\text{m} \times 1.4 \mu\text{m}$  region, with a scan frequency of 0.75 Hz. The sample is also characterized using SEM, as shown in Fig. 2(b), which again illustrates the individually dispersed Au-NPs. Fig. 2(c) shows the UV-visible spectroscopy of Au-NPs dispersed in citrate buffer solution. The spectrum shows the maximum absorbance peak at the wavelength of 523.5 nm, confirming the Au-NPs of size 15 nm.<sup>31,32</sup>

AFM and SEM micrographs confirm the nanoparticles are within an approximate diameter of 15 nm, and are well dispersed on the substrate. This discrete dispersion of Au-NPs is critical to investigating the electrical characteristics of the single nanocrystalline Au-NP. Fig. 2(d) shows the TEM image of Au-NP with a diameter of 14.9 nm. The crystal planes of Au-NP are shown with atomic crystal plane spacing ( $d$ -spacing) of 0.2317 nm.

The  $I$ - $V$  measurements are carried out either on the Au-NP or directly on the n-Si substrate, using AFM nano-probe at different locations. Fig. 3 (a) and (b) shows the first and second consecutive voltage sweeps on Au-NP for the complete  $-2$  V to  $+2$  V sweep and 0 to  $+2$  V sweeps respectively. Using a positive voltage sweep (reverse bias), the difference in tunneling current due to the charge trapping in Au-NP and the  $I$ - $V$  curves is shown. On the other hand, during the negative sweeps (positive bias), the  $I$ - $V$  curves are overlapping and the current is relatively small.

From Fig. 3 we can conclude that the tunneling current of the second sweep is smaller than that of the first sweep for voltage sweeps beyond 1.0 V. This reduction in tunneling current is attributed to the effect of charge trapped in the Au-NP during the application of first voltage sweep, which leads to a screening of the electric field at the NPs/n-Si interface.

In this work, we focus on the positive voltage (reverse bias) sweep where the tunneling current is dominant. Notably, in forward bias (negative voltage sweep) the current is very small, as shown in Fig. 3(b), due to forward conduction barrier and high contact resistance.<sup>11</sup>

Fig. 4 shows the  $I$ - $V$  measurements of the sample when placing the AFM nano-probe directly on the n-Si surface. The ( $I$ - $V$ ) curves for consecutive positive voltage sweeps (0.0 to  $+2.0$  V) are shown in Fig. 4, where the AFM tip is in direct contact with the n-Si substrate, and shows no change in the current during

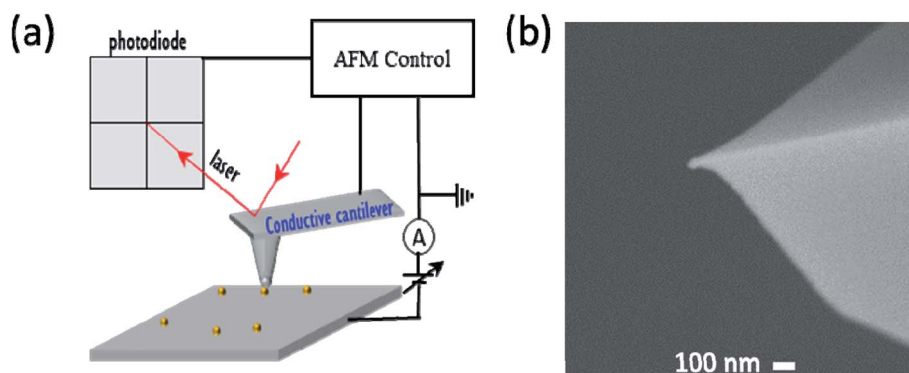


Fig. 1 (a) The schematics of electrical measurements setup in the atomic force microscope and (b) the scanning electron microscope image of the gold-coated AFM tip used for the electrical measurements.



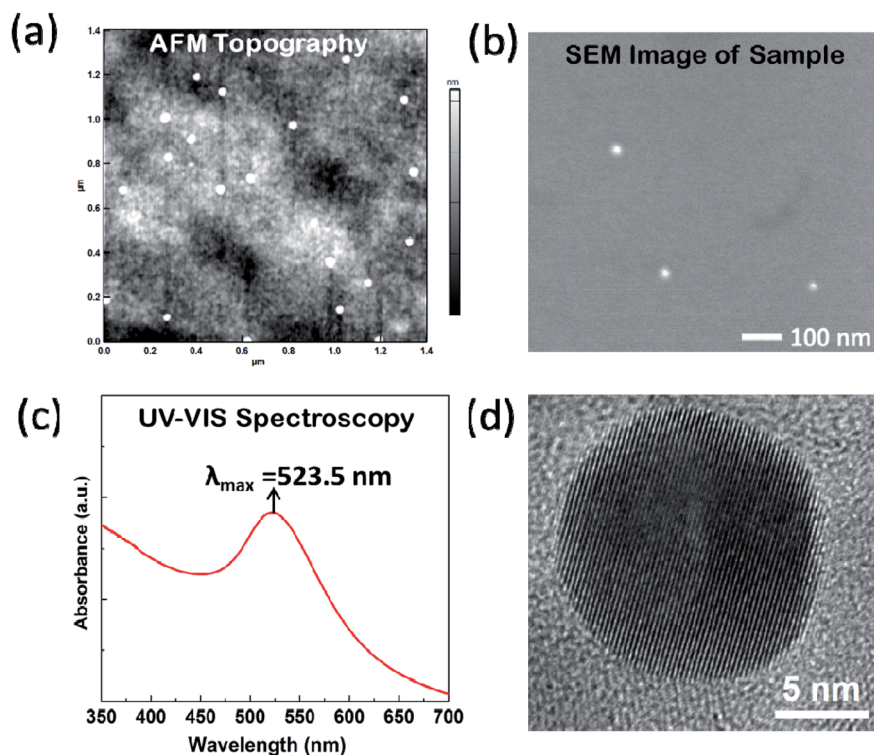


Fig. 2 (a) The AFM topography of the sample (b) the SEM images of the nanoparticles on the n-Si substrate (c) the UV-vis spectroscopy of gold nanoparticles and (d) the investigation of nanocrystalline nature of Au-NP using high resolution (HRTEM) imaging.

successive sweeps on n-Si substrate. It has been demonstrated that the tunneling current is a direct function of the electric field at the nano metal/Si junction, and the  $I$ - $V$  behavior is reproducible when using the same Au AFM probe on a clean n-Si surface.<sup>12,33</sup> Therefore, the drop in the tunneling current at the Au NP/n-Si interface at subsequent voltage sweeps, as shown in Fig. 3, is a result of the reduction of the interface electric field due to the charge screening effect from the Au-NP. This effect is not observed in the absence of NPs, as shown in Fig. 4.

For a further understanding of the tunneling current in Fig. 3, the  $I$ - $V$  characteristics on the Au-NP at different voltage ranges and time intervals are measured. The results showed the modulation

and irreversibility of  $I$ - $V$  curves with sequential sweeps are only observed when the probe is placed on Au-NPs. However, there is no change in  $I$ - $V$  characteristics with multiple voltage sweeps when the AFM tip is directly in contact with the n-Si substrate, as shown in Fig. 4. To assess the impact of different voltage sweep ranges on the charge storage, different voltage sweep ranges are carried out, as shown in Fig. 5, where Fig. 5(a), (b) and (c) shows the consecutive  $I$ - $V$  curves on Au-NPs with voltage sweeps in the range of 0 to +0.6 V, 0 to +1.0 V and 0 to +1.2 V respectively. Fig. 5(b) shows the sweeps up to +1.0 V do not pump enough charge into the Au-NP to impact the  $I$ - $V$  characteristics, therefore, this value (+1.0 V) is considered the minimum bias to pump enough charge to create

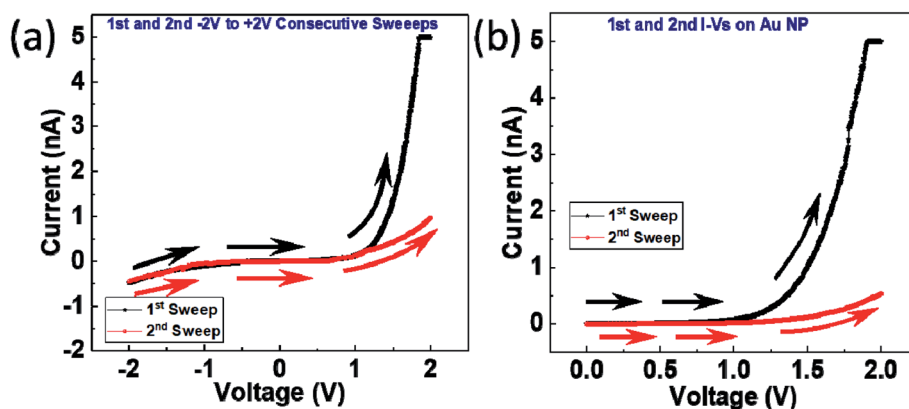


Fig. 3 The subsequent first and second sweeps on the Au-NP (a) from  $-2$  V to  $+2$  V (b) from 0 to  $+2$  V the direction of arrows shows the direction of voltage sweep.

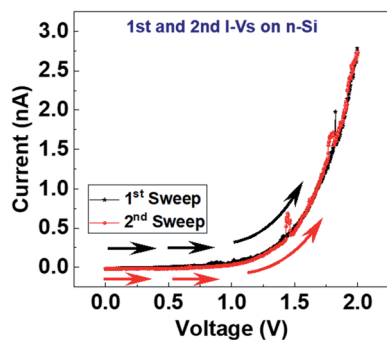


Fig. 4 The  $I$ - $V$  characteristics by placing tip directly on the surface and the direction of arrows show the direction of voltage sweeps.

this effect. For the consecutive voltage sweep of 0 to +1.2 V we observe a slightly smaller current level as compared to that of its previous sweep as shown in Fig. 5(c). The shift in overlapping of first and second consecutive sweeps shows a small amount of charge accumulation in the Au-NP.

The time dependence of the  $I$ - $V$  curves with a sweep range of 0 to +2.0 V, carried out at different time intervals, is shown in Fig. 6. Fig. 6(a) shows multiple  $I$ - $V$  sweeps, with intervals ( $\Delta t$ ) of 15 seconds (s), 30 s, 45 s and 60 s between the successive sweeps. At  $\Delta t = 60$  seconds from the previous sweep, we can readily observe that the  $I$ - $V$  curve coincides with the initial (first)  $I$ - $V$  sweep where there was no charge stored; indicating the decay of the charge stored due to the earlier sweep.

It is worth mentioning that while each  $I$ - $V$  measured during a sweep reads the current charge state of the Au-NP, it also injects

charge into the Au-NP (if the sweep is larger than +1.0 V). Therefore, if an immediate readout sweep is carried out, a lower current state is obtained due to the charge pumping introduced by the preceding sweep. If the sweep is done after longer intervals, the charge decay states (higher  $I$ - $V$  current) are observed and with given enough time it will get back to the non-charged state of high current. This is also captured in Fig. 6(b), which shows a different data set depicting the initial  $I$ - $V$  curve, the immediate  $I$ - $V$  measurement at  $\Delta t = 0$  s,  $\Delta t = 30$  s and again  $\Delta t = 0$  s from the previous sweep. In Fig. 6(b), the immediate  $I$ - $V$  measurement after any sweep (*i.e.*  $\Delta t = 0$  s and after 30 s dark green color) overlaps with the lowest current level, indicating the charging effect of any full range sweep (sweep voltage >1.0 V). Fig. 6(c) shows two overlapping time-delayed  $I$ - $V$  sweeps at the highest current, indicating no charge in the Au-NP, where the second sweep is carried out after 60 s from the first sweep. This indicates there is a charge decay time of  $\sim 60$  s. This evolution in  $I$ - $V$  behavior can be understood considering the charge screening effect from the NPs on the current reduction in Fig. 3, as explained earlier. However, during the charge decay from the NP, the screening effect becomes less, *i.e.* higher electric field, hence higher tunneling current. When the NPs is completely discharged the electric field is back to the maximum value and the initial tunneling current is observed, as in Fig. 6(a) and (c).

Finally, close observation of the difference in  $I$ - $V$  behaviors in Fig. 4 (when the AFM probe is placed directly on n-Si surface) and Fig. 3(b) (when the AFM probe is placed directly on n-Si surface), shows that charge trapping is strictly attributed to the existence of NP itself. On the other hand, if there is oxidation underneath Au NP, on Si surface, charge trapping effect will

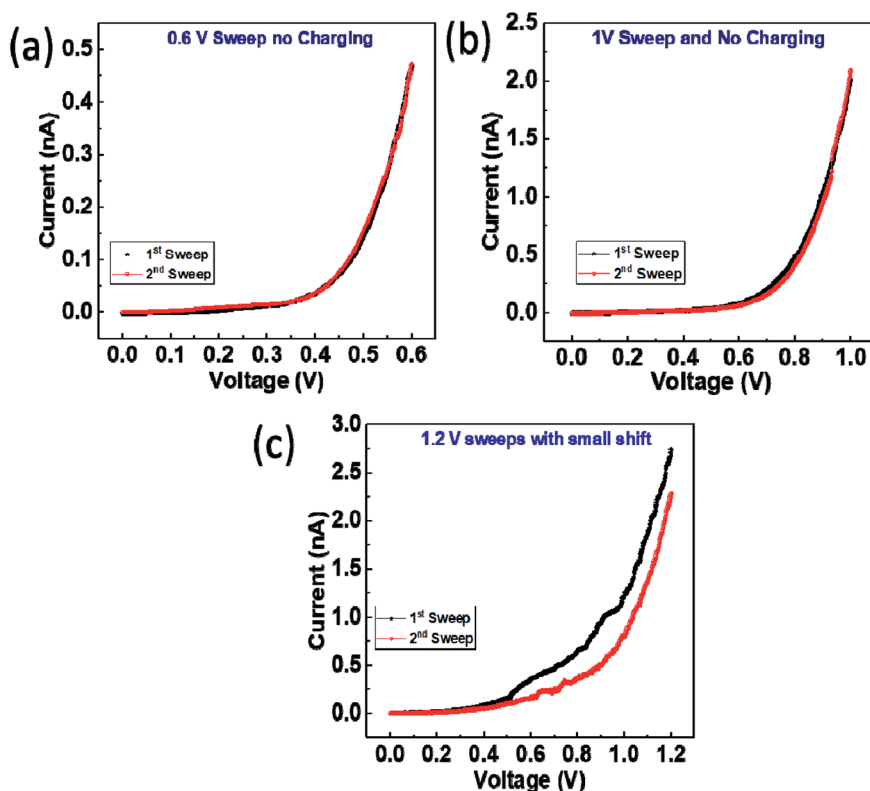


Fig. 5 The voltage-dependent  $I$ - $V$  measurements with (a) voltage sweep of 0–0.6 V (b) voltage Sweep of 0–1 V and (c) the sweep of 0 to 1.2 V.





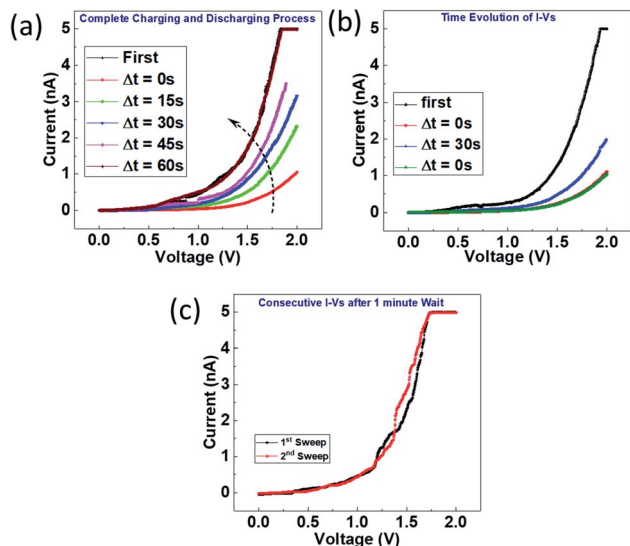


Fig. 6 The time-dependent  $I$ - $V$  measurements with (a) the complete charging and discharging process (b) the time-dependent voltage sweeps of 0 to +2 V (c) consecutive  $I$ - $V$ s with 1 minute time interval.

have been evident in Fig. 4 as well. Furthermore, the time evolution of the  $I$ - $V$  curves in Fig. 6 and the restoration of the NP initial state after waiting along enough time (one minute) also supports the NP charging effect. This indicates that the effect of oxidation on n-Si underneath Au-NPs is negligible, as the n-Si is passivated with hydrogen after HF cleaning.

## Conclusion

In summary, the time dependence of charge decay in individual 15 nm Au-NPs, as manifested in the  $I$ - $V$  characteristic, is studied. The UV-visible spectroscopy, HRTEM and SEM are used to accurately confirm Au-NPs size and distribution on the substrate. The consecutive  $I$ - $V$  measurements conducted on n-Si and Au-NP using gold-coated tip, confirmed the charging effect in the Au-NP. The results show that the irreversibility of  $I$ - $V$  curves is a characteristic of Au-NP due to charge trapping therein. Finally, this time-dependent analysis will facilitate the use of Au-NPs for different applications in nano memory devices.

## Conflicts of interest

There are no conflicts to declare.

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