


A carbon nanotube–graphene nanoribbon  
seamless junction transistor†Abhay A. Sagade \* and Ameya NyayadishCite this: *Nanoscale Adv.*, 2020, 2, 659Received 23rd December 2019  
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The nature and type of intramolecular junctions are very important for nanoelectronics. Here, a new way of fabricating seamless junctions between carbon nanotubes and graphene nanoribbons (GNRs) is demonstrated. Dielectrophoretically aligned multi-walled carbon nanotubes (CNTs) across metal electrodes are etched with an Ar ion beam at low pressure. We show that grounding of metal electrodes plays an important role in the etching of CNTs in contact with the metal electrodes. If electrodes are grounded, that portion of the CNT doesn't get etched due to the discharge of ions through the ground, and CNT to GNR conversion occurs in the gap region between the metal electrodes. Thus produced GNRs have a large aspect ratio of  $\sim 90$ , and Raman spectroscopy analysis shows that the distance between the defects is  $\sim 65$  nm. The CNT–GNR–CNT seamless junctions are ohmic in nature and the transistor shows a current on/off ratio of 27 with a hole mobility of  $350 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ .

## Introduction

Carbon nanotubes (CNTs) and graphene are two most important carbon allotropes that have been explored extensively in the field of nanoscience and nanotechnology. Theorists have predicted innovative properties of CNT–graphene intramolecular junctions and proposed new devices for magnetic memory, spintronics, diodes and transistors.<sup>1,2</sup> These junctions are also known to host a few quantum phenomena.<sup>3</sup> By using DFT calculations Leem *et al.* have predicted that a CNT–GNR junction based tunnel transistor can show sub-threshold swings of  $\sim 20 \text{ mV dec}^{-1}$ .<sup>4</sup> However, experimentally there are only a few attempts to fabricate these types of junction devices on the molecular scale.<sup>5</sup>

To best understand such junctions, fabrication of clean devices with individual entities is needed. Starting from

monolayer graphene and using shadow mask exposure to  $\text{O}_2$  plasma have produced high quality graphene nanoribbons (GNRs) with widths down to 4 nm.<sup>5</sup> High pressure sputter etching with  $\text{Zn}^1$  and  $\text{Ar}^{6,7}$  ions has been utilized to unzip CNTs to form GNRs. Wei *et al.* have reported only single seamless CNT–GNR junction formation from single-walled CNTs and explored gate voltage tuneable diodes and logic devices.<sup>1</sup> To date, these types of devices have been realized at channel lengths of few hundreds of nanometers only and hence charge transport at extended length scales is not known. Despite all these efforts, the experimental realization of CNT–GNR–CNT junctions is yet to be reported. Also, there are seldom reports on the formation of GNRs using multi-walled CNT ion etching at low pressures.

Ion beam etching (IBE) is a low pressure, anisotropic and neutralized dry plasma technique which is capable of defining surface features from tens of microns to the nanometer scale with figures of merit such as controlled etch rate, selectivity, uniformity, aspect ratio, and low substrate damage.<sup>8</sup> Particularly in industrial processing for carbon-based materials, it is known to have low electrostatic damage risk. Hence, for the first time, here a CNT–GNR–CNT seamless junctions are fabricated using multi-walled CNTs and Ar IBE. If metal electrodes are grounded during ion exposure, it is observed that the CNTs do not get etched in the region overlapping the metal contacts. The formed GNRs have a length/width ( $L/W$ ) ratio of  $\sim 90$ . Metallic CNTs form ohmic contacts with GNRs and show good current modulation in field effect transistors.

## Experimental methods

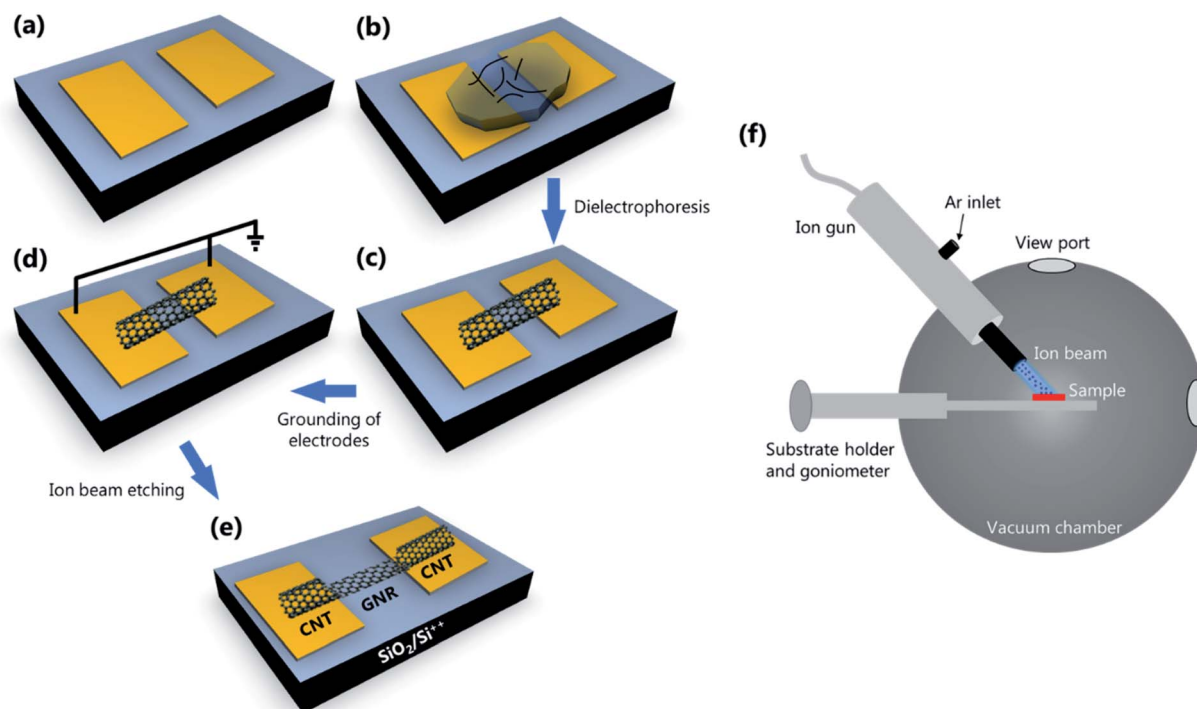
## Dielectrophoresis of CNTs

Multi-walled CNTs were obtained from Sigma Aldrich ( $>90\%$ ) and used as is. CNTs were then arranged by dielectrophoresis (DEP) across gold metal (30 nm thin) electrodes of gap length  $\sim 7 \mu\text{m}$  on a 300 nm  $\text{SiO}_2/\text{Si}^{++}$  substrate (Fig. 1).<sup>9</sup> A homemade set-up was used for DEP. CNTs were dispersed in isopropyl alcohol (IPA) by sonication for 1 hour to form a very dilute

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**Fig. 1** Steps involved in CNT alignment and ion beam processing. (a) Empty gold metal gap electrodes on a  $\text{SiO}_2/\text{Si}$  substrate, (b) a drop of IPA containing CNTs is placed across the electrodes, (c) aligned CNTs by dielectrophoresis, (d) grounding of metal electrodes, (e) formation of the CNT-GNR-CNT junctions after ion beam exposure, and (f) the set-up for IBE.

solution ( $1 \mu\text{g mL}^{-1}$ ). The deposition parameters such as voltage and frequency were varied from 2–3 V and 1–10 kHz, respectively. The chosen electric field was  $\sim 10^6 \text{ V m}^{-1}$  such that the DEP force was dominant over the Brownian motion of the MWNTs.<sup>10</sup> A  $1 \mu\text{L}$  drop of solution was introduced between the electrodes and the assembling time was up to 1 min. The motion of CNTs was monitored using an optical microscope. After alignment,  $100 \mu\text{L}$  of fresh IPA was used to wash out extra CNTs. Further, the substrate was heated at  $70^\circ\text{C}$  for 15 min to remove the solvent completely.

### Ion beam etching

The device substrate containing aligned CNTs was loaded in a vacuum chamber having a goniometer with a substrate holder, an electrical feedthrough, and an Ar ion gun (Omicron FDG 150). The base vacuum created was  $5 \times 10^{-7}$  Torr. Ion gun parameters were tuned to produce a beam of 0.5 cm diameter. The electrical connections from gold electrodes were carefully taken out for grounding ( $V_{\text{ground}} \approx 0.01 \text{ V}$ ). For etching, the ion energy was tuned from 3 to 4 keV in a vacuum of  $5 \times 10^{-5}$  Torr with a beam current of 1–3 mA. After exposure, the sample was kept in a high vacuum for 30 min to cool down.

### Characterization

CNT and GNR formation was probed at various stages using a Nova NanoSEM 600 instrument (FEI Co., The Netherlands). Scanning probe microscopy was carried out on an Innova Bruker instrument with a 30 nm Si tip in tapping mode. Raman

spectra were recorded by using a Horiba LABRAM HR Evolution with 633 nm. The image colour processing was performed on Mipar© software. Electrical measurements were acquired using a Keithley 4200 SCS in the ambient environment at room temperature.

## Results and discussion

Here, multi-walled carbon nanotubes (CNTs) are used due to their well-known metallic nature, long length and bendable properties. They were aligned across gold metal electrodes using dielectrophoresis (Fig. 1a–e).<sup>10</sup> Multiple devices were fabricated with a single CNT and a few CNTs across metal electrodes. After alignment, the substrate was exposed to a 3–4 keV Ar ion beam under  $5 \times 10^{-5}$  Torr to etch the CNTs (Fig. 1f). It should be noted that the CNT ends are in contact with the thin metal while the middle portion is touching the insulating oxide. It is well known that during sputter etching there is charging of the material as well as the substrate due to the formation of charged species such as ions and electrons. Hence, if any bias or grounding is applied, it is possible to guide these charges, which may affect the etching. To date, insufficient attention has been paid to these kinds of effects in nanoscale materials like CNTs. Usually, ion beams etch CNTs everywhere along their length independent of whether they are lying on a metallic or insulating substrate (see ESI Fig. S1†). We observed that during ion exposure the etching of CNTs overlapping the metal region depends on whether the metal contacts are connected to the ground or not (see ESI Fig. S2†). If they are not



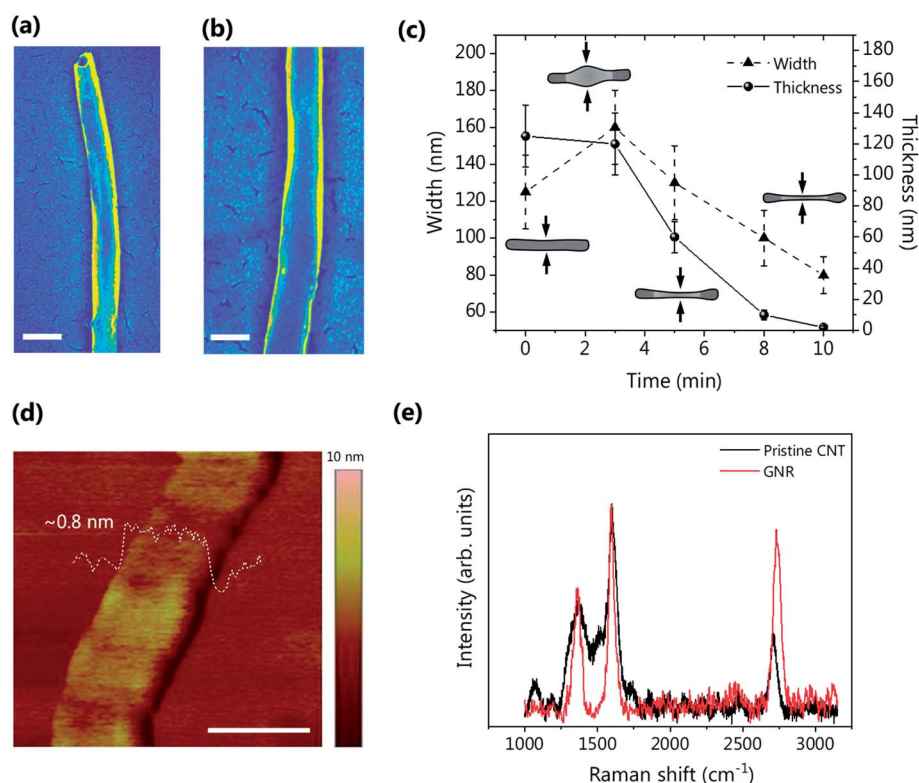
grounded, then charged ions etch CNTs in equal proportion along their whole length. In other case, if metal electrodes are grounded, then the portion of the CNTs away from the metal contact which is touching the insulating background is etched. This is because positively charged Ar ions are neutralized on the metal region and get removed immediately through the ground, but they do get reflected from the insulating area and remove carbon atoms in the CNTs. Note that the metal gap is sufficiently long (7  $\mu\text{m}$ ) compared to the diameter of CNTs ( $120 \pm 20$  nm) and the Ar ion diameter so that the formation of this kind of gradient is feasible. We found neither formation or change in potential nor currents at the ground electrode during this neutralization process (on a time scale of seconds), which eliminates the possibility of crowding of positive or negative charges which may affect the etching rate. Thus, CNT-GNR-CNT dual junctions are obtained (*vide infra*). At shorter channel lengths in the sub-micron region, this effect will be feeble due to the highly conducting nature of CNTs. Hence, this method is more suitable for making such molecular junctions of extended dimensions.

Structural analysis of GNR formation was carried out by scanning electron microscopy (SEM), atomic force microscopy (AFM) and Raman spectroscopy (Fig. 2). The etch time was varied from 0 s to 10 min. With an increase in exposure time, etched regions of the CNTs increase (Fig. 2a–c). In the false coloured SEM images the etched region appears bluish (similar

to the substrate) and the reactive edges are brighter and yellowish. Surprisingly, the etching rate is found to be similar for both with and without metal electrode grounding (see ESI Fig. S2†). The observed etching yield of 1 atom/ion matches well with Ar ion usage for carbon films.<sup>8</sup> The intermediate steps of etching are probed by SEM (see ESI Fig. S3†), which shows that after 3 min of exposure, there are formation of whiskers and opening of CNTs. With time, the opened side walls diminished and then there is a vertical etching of ribbons. For an etching time of 10 min, the minimum width and thickness are observed to be  $\sim 80$  nm and  $\sim 0.8$  nm, respectively (Fig. 2d). The formation of GNRs is further confirmed by Raman spectroscopy (Fig. 2e). The spectra show standard D, G, and 2D peaks for CNT and graphene.<sup>11</sup> The intensity ratio of 2D to G indicates the presence of 2 or 3 layers of graphene.<sup>12</sup> The ratio of the D to G peak area ( $A$ ) for our GNRs is  $A_D/A_G \sim 1$ –2, which is comparable to that of the GNRs obtained from CVD ( $\sim 1$ –5) and exfoliated ( $\sim 2$ –8) graphene etching with  $\text{O}_2$  plasma.<sup>13</sup> Further, the crystallite size ( $L_a$ ) is obtained from these Raman peaks using,

$$L_a = \frac{560}{E^4} \left( \frac{I_D}{I_G} \right)^{-1}$$

where,  $E$  is the energy of the excitation laser. It is calculated to be  $\sim 65$  nm which is close to the value of GNR width and similar to the values for good quality exfoliated graphene flakes.<sup>14,15</sup>



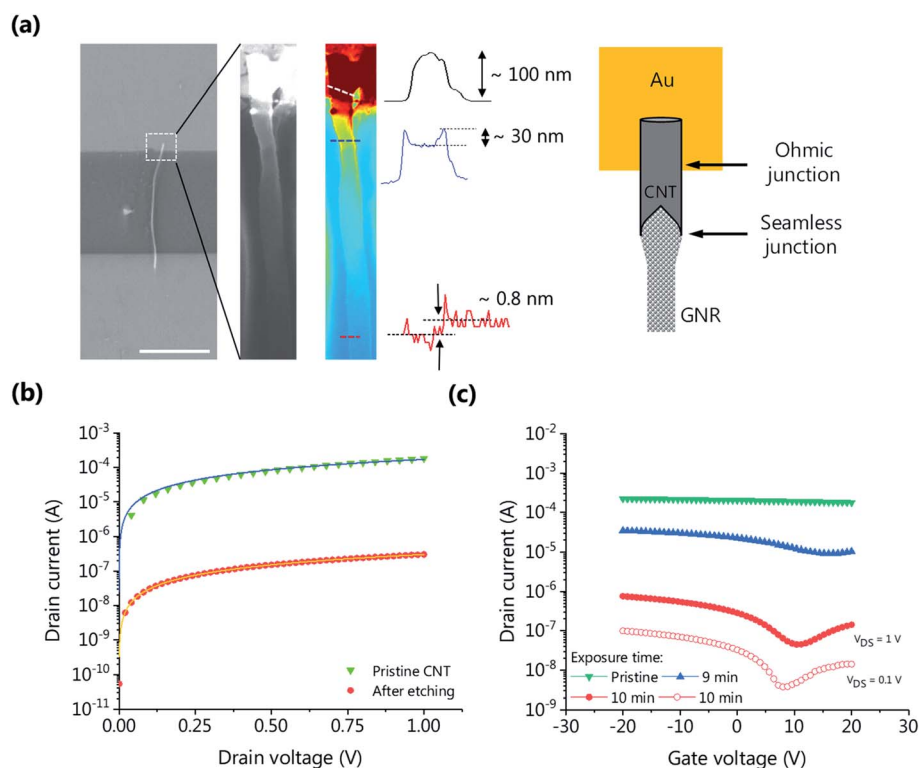
**Fig. 2** (a and b) False colour SEM images of CNTs after Ar ion exposure for 1 and 3 min, respectively. The etched regions of the CNTs are blue in colour which matches with the background. (c) Variation of CNT width and GNR thickness as a function of Ar ion exposure time. The arrows indicate the tentative positions where width is measured. The error bars correspond to measurements of several CNTs. (d) AFM image and (e) Raman spectra of GNRs are obtained for an ion exposure time of 10 min. Scale bars: (a) 200 nm, (b) 100 nm, and (d) 100 nm.



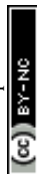
This assures long distance scattering free motion of charge carriers.

To characterize the transport properties of the CNT–GNR–CNT seamless junction, field effect transistor (FET) measurements were performed in air at room temperature. For a device with a single pristine CNT (Fig. 3a), the output current–voltage (*IV*) characteristics (Fig. 3b) are linear, indicating the metallic nature of CNT with a resistance of 5.85 K $\Omega$ . After etching for 10 min with grounding the electrodes, the formed GNR *IV* curve showed  $10^3$  times reduction in current but maintained its linearity. From the transfer *IV* curves it is evident that, as expected, the pristine CNT does not show a change in current with the application of the gate voltage (Fig. 3c). For an etching time of 10 min, good current modulation is observed with ambipolar conduction. The current on/off ratio is noted to be 27 for  $V_{DS} = 0.1$  V. To date, diverse experimental values have been obtained for current on/off ratios in GNR FETs, which are quite different from the theoretical predictions.<sup>16–18</sup> This is due to the variations in several experimental parameters such as contact resistance, interface contamination during fabrication, dielectrics used, measurement conditions, *etc.* Field effect mobility for hole and electron is obtained to be 350 and 130 cm<sup>2</sup> V<sup>−1</sup> s<sup>−1</sup>, respectively. These values are very good considering the large *L/W* ratio and the fact that the crystallite size is similar to the width.<sup>13</sup>

Identification of a suitable inorganic metal contact for the GNRs is an important task. Metallic CNTs themselves could be the best choice, which can maintain similar bonding and form a seamless junction.<sup>3</sup> This seamless junction has its own advantages for charge transport. We identified a unique way to fabricate it efficiently. In general, for IBE, grounding of active materials (target) has not been paid sufficient attention and its effects on the process outcome are largely unknown. It is important on the nano scale since formation of local charged or neutral zones may affect the shapes of materials. By using the simple concept of grounding and ion beam etching, we have successfully developed a process which produces a double seamless junctions of CNT–GNR–CNT. The fabricated device works as a FET with good charge carrier mobility over large distances. The Raman analysis of  $A_D/A_G$  and  $I_D/I_G$  showed that the distance between the defects is  $L_a \sim 65$  nm which is quite large and indicates that ion bombardment has not created large defects in the ribbon plane. Without grounding of electrodes, we observed uniform etching of CNTs to form GNRs even over metal overlapping regions (see ESI Fig. S2 and S4†). To enhance the understanding and working of this kind of device, we need additional experimental studies as well as theoretical analysis. Further, the nature of edges and their effect on transport can be investigated. The developed method has strong potential to obtain high throughput in fabricating these kinds of devices since dielectrophoresis of CNTs is possible at the industrial



**Fig. 3** (a) The grey colour SEM images of a single CNT aligned across metal electrodes before (left) and after etching (right, zoom-in) when metal electrodes were grounded. The false colour image is obtained by software processing of the right side SEM image which indicates variation of thickness along the length of the CNT from the metal edge. The line profiles are obtained from the dotted lines. The schematic depicts the formation of ohmic and seamless junctions between the metal, CNT and GNR. (b) Current–voltage characteristics of CNT and CNT–GNR–CNT devices are shown on the semi-log scale with a linear fit. (c) Transistor transfer characteristics of CNT and GNR at different ion exposure times.





scale. As theoretically predicted by Santos *et al.*<sup>2</sup> for a CNT–GNR–CNT junction, tubes act as completely transparent contacts and such systems are by themselves magnetoresistive devices with a large value of magnetoresistance. Hence, these seamless intramolecular junctions can act as efficient filters in valleytronics.<sup>19</sup>

## Conclusions

In summary, we have shown experimentally that the formation of CNT–GNR–CNT seamless junctions is possible with good reproducibility for transistor applications. Multi-walled CNTs are used as the source material and etched with an Ar ion beam to form GNRs. During exposure to the ion beam, if electrodes are grounded, the positive ions sputter etch the CNTs away from the CNT–metal junction, and without grounding, etching occurs uniformly over the whole length. Thus, a 2 or 3 layers GNR transistor with high aspect ratio shows good performance at room temperature. This work will pave the way to test experimentally novel phenomena for quantum computers and various applications of CNT–GNR junctions.

## Conflicts of interest

There are no conflicts to declare.

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