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# Effects of NaCl treatment on the performance and environmental stability of microporous SiO<sub>2</sub>-based thin film transistors

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In this letter, we report the effects of NaCl treatment on the performance and environmental stability of microporous SiO<sub>2</sub>-based thin film transistors (TFTs). It was found that appropriate amounts of NaCl treatment significantly improved the electric double layer (EDL) capacitance of such composite solid electrolytes from 1.9 to 4.7 μF cm<sup>-2</sup>. A highest field effect mobility of 42.8 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> was found for 1% NaCl treated microporous SiO<sub>2</sub>-based TFTs. However, 10% and 26.5% NaCl treated microporous SiO<sub>2</sub>-based TFTs showed good environmental stability of the *I*<sub>on</sub>/*I*<sub>off</sub> ratio with reasonable field effect mobility.

## Introduction

Microporous SiO<sub>2</sub>-based thin film transistors (TFTs) have attracted much attention in recent years due to their superior performance compared to the common SiO<sub>2</sub>-based TFTs. Microporous SiO<sub>2</sub> dielectrics have several advantages such as high specific capacitance, good environmental stability in ambient air and simple deposition methods at room temperature. Various SiO<sub>2</sub>-based TFTs have been reported in recent years.<sup>1–5</sup> The enhancement of the electric double layer (EDL) capacitance of SiO<sub>2</sub> dielectrics strongly influences the performance of SiO<sub>2</sub>-based TFTs.<sup>1–3</sup> The EDL capacitance of microporous SiO<sub>2</sub> dielectrics can be enhanced either by reducing the thickness of the SiO<sub>2</sub> film or by ionic treatment. It has been reported that microporous SiO<sub>2</sub> with appropriate amounts of LiCl and/or CaCl<sub>2</sub> treatment effectively enhanced the EDL capacitance of microporous SiO<sub>2</sub> dielectrics.<sup>4,5</sup>

For practical applications, the environmental stability of microporous SiO<sub>2</sub>-based TFTs should be considered. The environmental stability of field-effect mobility of 1% CaCl<sub>2</sub> treated microporous SiO<sub>2</sub>-based TFTs has been reported.<sup>5</sup> However, the information on the stability of on/off ratio of the TFTs which is an important factor for switching applications has been rarely mentioned. In this letter, we report the effect of NaCl treatment on the performance and environmental stability of microporous SiO<sub>2</sub>-based TFTs.

## Experimental

The entire process of the microporous SiO<sub>2</sub>-based TFTs fabrication was performed at room temperature. First of all, microporous SiO<sub>2</sub> gate dielectric with the thickness of 2 μm was deposited by plasma-enhanced chemical vapor deposition (PECVD) method on ITO glass and Si (100) substrates using SiH<sub>4</sub> and O<sub>2</sub> mixture as the reactive gases. Then, the samples were immersed in 0.1%, 1%, 10% and 26.5% NaCl aqueous solution, respectively. After 3 hours, these samples were taken out and dried in air ambient. Then, a 40 nm-thick ITO layer was deposited by RF magnetron sputtering method with a RF power of 100 W in Ar/O<sub>2</sub> (4 : 1) mixed ambient of 0.5 Pa as active channel. At last, highly conductive ITO layers were deposited by dc sputtering methods in pure argon ambient at 1.0 Pa as source and drain electrodes. The channel length and width-to-length ratio of the shadow mask were 80 μm and 12.5 : 1, respectively. The structural characterization of the microporous SiO<sub>2</sub> electrolyte film was investigated by field emission scanning electron microscopy (Hitachi S-4800 SEM). The electrical characterizations of the microporous SiO<sub>2</sub> dielectric and EDL transistors were investigated by an impedance analyzer (WK 6500B) and a semiconductor parameter analyzer (Keithley 4200 SCS) at room temperature in dark with a relative humidity of 60%.

## Results and discussion

Fig. 1(a) shows the schematic picture of ITO-based TFTs gated by microporous SiO<sub>2</sub> treated by NaCl. On account of immersing into NaCl aqueous solution for 3 hours, some protons enter the microporous SiO<sub>2</sub> and some anions adhere to the surface of the microporous SiO<sub>2</sub>, these anions induce image charges of equal density and opposite sign in the dielectric layer, which is similar to the case of EDL organic transistors gated by ionic liquids or

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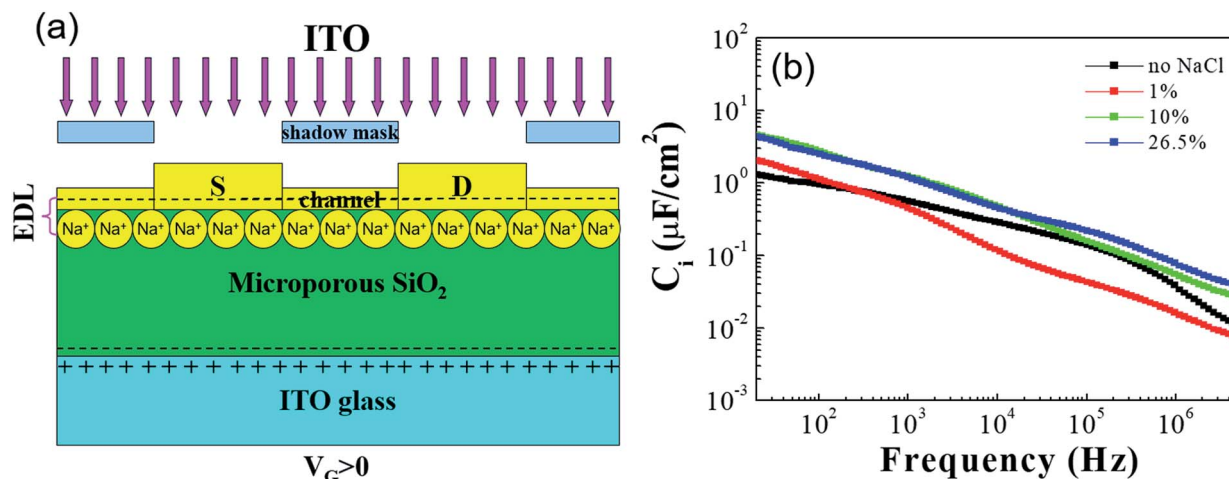


Fig. 1 (a) Schematic picture of ITO-based TFTs gated by microporous SiO<sub>2</sub> treated by NaCl. (b) Density dependence of the specific capacitance versus frequency of the microporous SiO<sub>2</sub> with and without NaCl treatment in the frequency.

solid state electrolytes.<sup>6,7</sup> Its transformation may be driven by proton transfer to and from a water molecule with very little permittivity temperature dependence.<sup>8–11</sup> The major advantage of the EDL effect is that the specific capacitance is exceptionally large which results in an ultrahigh current throughput at an ultralow operating voltage of 0.5 V. Fig. 1(b) shows the density dependence of the specific capacitance versus frequency of the microporous SiO<sub>2</sub> with and without NaCl treatment in the frequency range from 20 Hz to 5 MHz. Compared with the SiO<sub>2</sub> without NaCl treatment, the specific capacitance of microporous SiO<sub>2</sub> with 10% and 26.5% NaCl treatment has increased to 4.7 μF cm<sup>-2</sup> at 20 Hz and remains about 1 μF cm<sup>-2</sup> at 1 kHz. However, the specific capacitance of microporous SiO<sub>2</sub> with 1% NaCl treatment is about 2 μF cm<sup>-2</sup> and decreases rapidly. It is expected that this behavior is mainly due to the changing of Na<sup>+</sup> ions concentration in the films, which is similar to the incorporation of Na<sup>+</sup> ions into gate dielectrics.<sup>12–14</sup> The output characteristics of all the TFTs with and without NaCl treatment showed typical transistor behavior. Fig. 2 shows the transfer characteristics of the TFTs with and without NaCl treatment measured after different time interval of fabrication. The fresh measurement refers to the TFTs fabrication after 3 h of SiO<sub>2</sub> film deposition and the electrical properties were measured within 1 h of the device fabrication. To study the environmental stability of the TFTs with and without NaCl treatment, the devices were exposed to air without surface passivation and their electrical properties were measured after 1 week and 1 month of device fabrication. The saturation field effect mobility ( $\mu_{\text{sat}}$ ) was extracted by fitting a straight line to the plot of the square root of the drain current versus  $V_{\text{gs}}$ , on the basis of the following relationship in the saturation regime,  $I_{\text{ds}} = (W/2L)\mu_{\text{sat}}C_i(V_{\text{gs}} - V_{\text{th}})^2$ , where  $I_{\text{ds}}$  is the drain current,  $V_{\text{th}}$  is the threshold voltage,  $W$  and  $L$  are the channel width and length, respectively, and  $C_i$  is the capacitance per unit area of the gate dielectric. The large mobilities of depletion mode ( $V_{\text{th}} = -0.17$  to  $-0.35$  V) TFTs probably derive from the high charge

density in the channel, because the carrier mobility in polymer semiconductors is strongly dependent on carrier density, which result in increased trap filling and a general smoothing of electrostatic potential variations in the film due to trapped charge, these combined effects lead to higher carrier mobilities.<sup>15,16</sup> The electrical parameters of the TFTs are listed in Table 1. The mobility and  $I_{\text{on}}/I_{\text{off}}$  ratio of the drain current for the freshly prepared TFT without NaCl treatment were 12 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and  $3.4 \times 10^5$ , respectively. The threshold voltage ( $V_{\text{th}}$ ) was  $-0.1$  V and it was shifted in the positive direction with exposure to air. The  $I_{\text{on}}/I_{\text{off}}$  ratio of the TFT without NaCl treatment measured after 1 week and 1 month of air exposure were  $1.8 \times 10^5$  and  $6 \times 10^4$ , respectively. The mobility was significantly decreased with exposure to air. The TFT with 1% NaCl treatment showed a mobility of 41.8 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and the mobility was decreased to 15 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> with an  $I_{\text{on}}/I_{\text{off}}$  ratio of  $3.1 \times 10^6$  after 1 week aging. From Table 1 it is noted that after 1 month aging the TFT with 1% NaCl treatment showed a lower stability than the TFT without NaCl treatment. The mobility and  $I_{\text{on}}/I_{\text{off}}$  ratio for the freshly prepared TFT with 10% NaCl treatment was 64.5 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and  $2.5 \times 10^6$ , respectively. In this case, the mobility was decreased to 29.3 and 12 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> after 1 week and 1 month aging, respectively. The TFT with 26.5% NaCl treatment showed a mobility and  $I_{\text{on}}/I_{\text{off}}$  ratio of 36.5 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and  $3.5 \times 10^6$ , respectively, and the  $I_{\text{on}}/I_{\text{off}}$  ratio measured after 1 month aging was  $1.2 \times 10^7$ , which is about three orders of magnitude higher than the  $I_{\text{on}}/I_{\text{off}}$  ratio of TFT without NaCl treatment. It is noted that, the threshold voltage of the TFTs was shifted in positive direction after 1 month aging, which may be attributed to the reduction of defect or trap density at the gate dielectric and channel interface.

To understand the effect of air exposure on the stability of the TFTs, the subthreshold slope was calculated using the following function:  $S = \delta V_{\text{gs}} / (\delta \log_{10} I_{\text{ds}})$ . From Table 1, it is clearly seen that the subthreshold slope rapidly increased with air exposure duration in case of the TFT without NaCl treatment



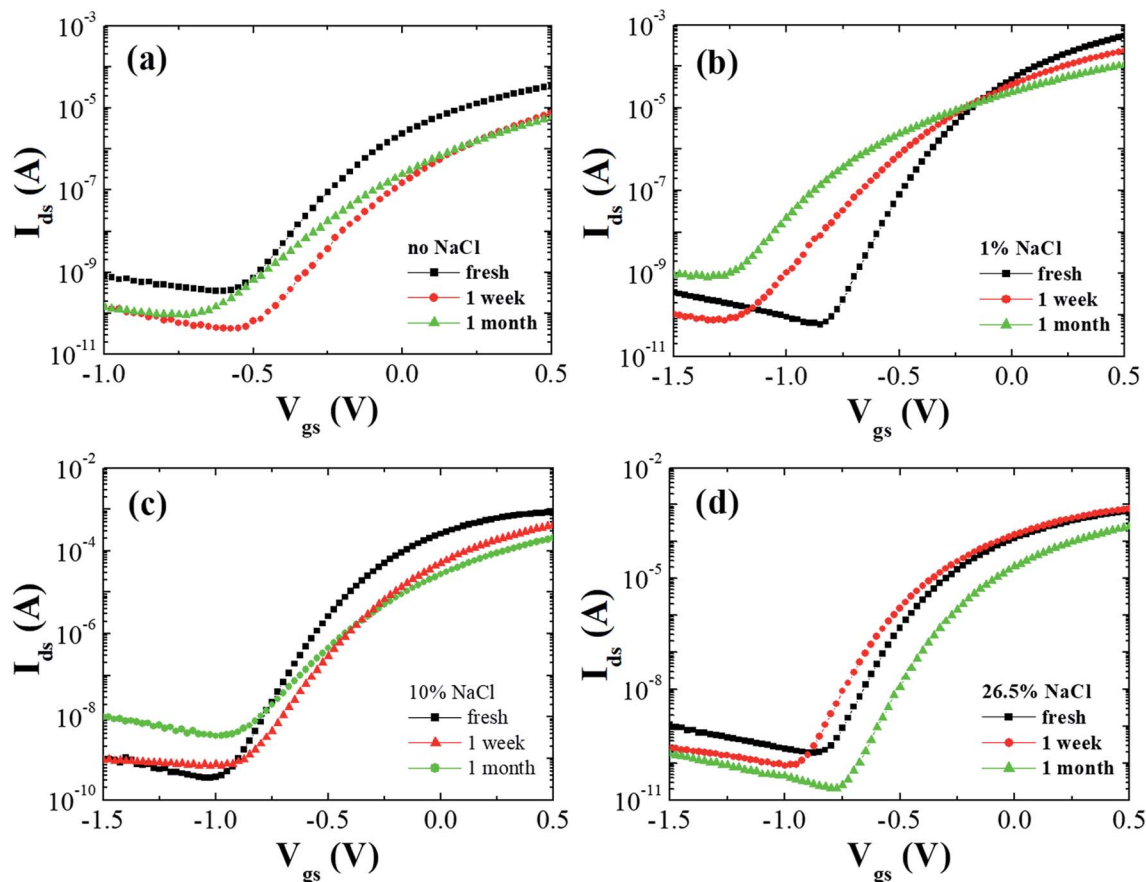


Fig. 2 Transfer characteristics of TFTs (a) without and with (b) 1%, (c) 10% and (d) 26.5% NaCl treatment measured after different durations of air exposure.

and a moderate increase of subthreshold slope was also observed for the TFTs with 1% and 10% NaCl treatment. However, almost constant subthreshold slope was observed for the TFTs with 26.5% NaCl treatment.

In order to further investigate the stability and reproducibility of the TFTs with and without NaCl treatment, the bias stress measurements have been performed. Fig. 3 shows the bias stress curves for transistors gated by microporous SiO<sub>2</sub> with and without NaCl treatment. After 10 minutes bias with  $V_{gs} = 0.5$  V and  $V_{ds} = 0.5$  V, transistor gated by 26.5% NaCl-treated microporous SiO<sub>2</sub> shows a small on current loss of 1.7%. While the transistor gated by microporous SiO<sub>2</sub> without NaCl

treatment shows an on current loss of 9.3%. For dynamic stress test (inset in Fig. 3), the transistor was repeatedly cycled between on and off states (period square wave pulses of  $V_{gs} = +0$  V to  $-1.0$  V and  $V_{ds} = 0.5$  V). TFT gated by NaCl-treated microporous SiO<sub>2</sub> maintained a current on/off ratio of  $10^6$  and without obvious current degradation after more than 2000 seconds operation. These results suggest that no chemical doping (*i.e.* Na<sup>+</sup> penetrate into the ITO channel) or chemical reaction occurs at the electrolyte/ITO channel interface when the gate potential is biased. As we know, if chemical doping or a chemical reaction occurs,  $I_{ds}$  will not return to its original value after gate scanning.<sup>17</sup>

Table 1 Extracted electrical parameters of TFTs with and without NaCl treatment after different durations of air exposure

NaCl content	Fresh				1 week aging				1 month aging			
	$\mu$ (cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> )	$I_{on}/I_{off}$	$V_{th}$ (V)	$S$ (mV per decade)	$\mu$ (cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> )	$I_{on}/I_{off}$	$V_{th}$ (V)	$S$ (mV per decade)	$\mu$ (cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> )	$I_{on}/I_{off}$	$V_{th}$ (V)	$S$ (mV per decade)
0	12	$3.4 \times 10^5$	-0.1	110	4.3	$1.8 \times 10^5$	0.04	120	3.6	$6 \times 10^4$	0.05	180
1%	41.8	$1.1 \times 10^7$	-0.2	100	15	$3.1 \times 10^6$	-0.28	150	5.7	$1.3 \times 10^5$	-0.35	160
10%	64.5	$2.5 \times 10^6$	-0.17	100	29.3	$7 \times 10^5$	-0.2	130	12	$6 \times 10^4$	-0.11	180
26.5%	32.6	$3.5 \times 10^6$	-0.35	90	36.5	$9.2 \times 10^6$	-0.37	90	24	$1.2 \times 10^7$	-0.16	90



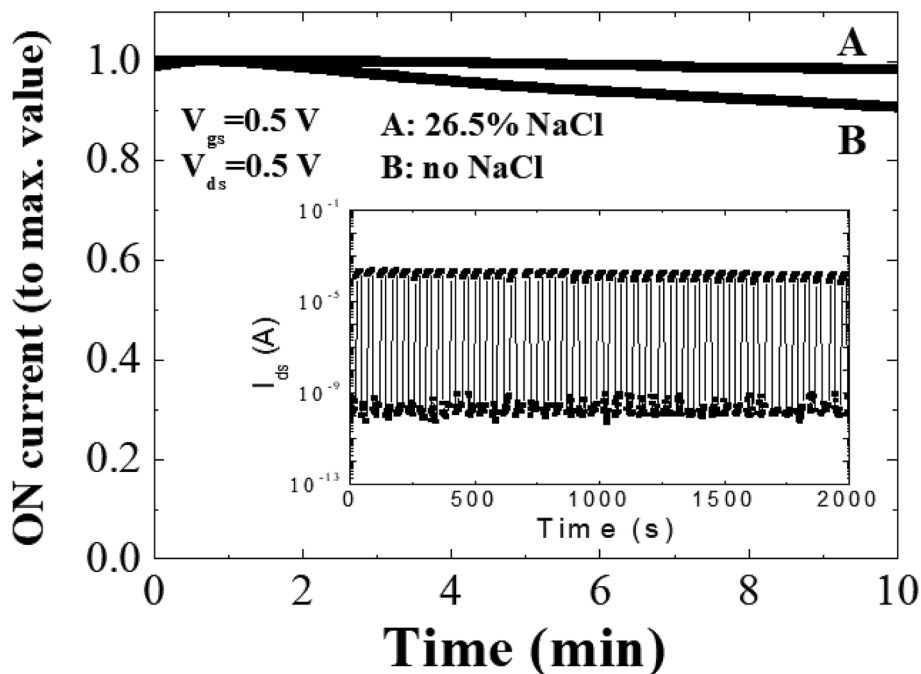


Fig. 3 Normalized ON current ( $I_{ds}$ ) measured versus time for EDL TFTs gated by microporous  $\text{SiO}_2$  with and without NaCl treatment. Inset shows the dynamic stress test of the TFTs gated by 26.5% NaCl-treated microporous  $\text{SiO}_2$ .

## Conclusions

In conclusion, we have investigated the effects of NaCl treatment on the performance and environmental stability of microporous  $\text{SiO}_2$ -based TFTs. The results showed that an appropriate amount of NaCl treatment can enhance the EDL capacitance of the microporous  $\text{SiO}_2$  dielectric, which are the key factors for obtaining high performance TFTs. Further, with an optimized amount of NaCl treatment, the environmental stability of  $I_{on}/I_{off}$  ratio and on current of the NaCl treated microporous  $\text{SiO}_2$ -based TFTs can be improved with a reasonable field effect mobility which is very important for switching applications.

## Conflicts of interest

There are no conflicts to declare.

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