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# Observation of giant spin–orbit interaction in graphene and heavy metal heterostructures†

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Graphene is a promising material demonstrating some interesting phenomena such as the spin Hall effect, bipolar transistor effect, and non-trivial topological states. However, graphene has an intrinsically small spin–orbit interaction (SOI), making it difficult to apply in spintronic devices. The electronic band structure of graphene makes it possible to develop a systematic method to enhance SOI extrinsically. In this study, we designed a graphene field-effect transistor with a Pb layer intercalated between graphene (Gr) and Au layers and studied the effect on the strength of the SOI. The SOI in our system was significantly increased to 80 meV, which led to a giant non-local signal ( $\sim 180 \Omega$ ) at room temperature due to the spin Hall effect. Further, we extract key parameters of spin transport from the length and width dependence of non-local measurement. To support these findings, we also measured the temperature and gate-dependent weak localization (WL) effect. We obtained the magnitude of the SOI and spin relaxation time of Gr *via* quantitative analysis of WL. The SOI magnitudes estimated from the non-local signal and the WL effect are close in value. The enhancement of the SOI of Gr at room temperature is a potential simple manipulation method to explore the use of this material for spin-based applications.

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## Introduction

Graphene (Gr), with its honey-comb structure of carbon atoms, has enormous potential for used in spintronics, electronics, data storage, and information processing devices.<sup>1–3</sup> It has become a strong contender for next-generation spin-based devices owing to its high mobility, weak hyperfine coupling, long diffusion length, and gate tuneable spin transport at room temperature.<sup>4–6</sup> However, its lack of an intrinsic spin–orbit interaction (SOI) and a bandgap are serious limitations and make it difficult to apply Gr to spintronic devices.<sup>7</sup> The SOI in Gr has attracted much theoretical attention to date but is less investigated experimentally.<sup>8</sup>

The decoration of the surface of Gr provides a platform for manipulating its SOI due to its surface sensitivity.<sup>9</sup> Different procedures have been proposed to improve the SOI in Gr. For example, the intrinsic SOI of Gr has been enhanced significantly by creating a curve in flat Gr and by carbon nanotubes.<sup>10,11</sup> Balakrishnan *et al.*<sup>12</sup> and Avsar *et al.*<sup>13</sup> have, respectively, suggested enhancing the SOI in GR *via* chemical treatment with 2.5 meV of hydrogen and 5.1–9.1 meV of fluorine. Unfortunately, these chemisorbed light atoms make devices unstable in

ambient conditions because these atoms can be easily unglued at moderate temperature.

Zhang *et al.*<sup>14</sup> and Weeks *et al.*<sup>15</sup> proposed a similar approach to enhance SOI in Gr. They recommended depositing heavy metals onto the hollow centre of the honeycomb lattice of the carbon sheet that modified the SOI in Gr and opened a bandgap at the Dirac point. Similarly, Ir and W 5d atoms can intercede in a quantum anomalous Hall state.<sup>14</sup> Alternatively, d-block metals such as Au, Ti, and In have strong SOIs and can open a bandgap at the Dirac point in Gr; they can also introduce the quantum spin Hall effect in the Gr sheet.<sup>15</sup> 4d and 5d heavy transition metals demonstrate intriguing physical properties necessary for spintronics devices, especially when interlinked with a magnetic materials.<sup>16,17</sup> Despite these promising applications, one genuine problem that remains is that the surface adsorption induces additional electronics scattering. The coherence of the Dirac fermions can be suppressed by such electronic scattering.<sup>14,15,18</sup> Recently, a van der Waals (vdW) heterostructure assembled by vertically stacking Gr and two-dimensional materials resulted in a compact system with possibility to manipulate and modulate the spin-based field-effect transport properties. The hybridization of transition metal dichalcogenides (TMDs) or Gr with magnetic materials can strongly modulate the electronic structure *via* the proximity effect.<sup>19–21</sup> However, suggestions for how to begin SOI enhancement in Gr and spin absorption in two-dimensional TMDs are still elusive or remain

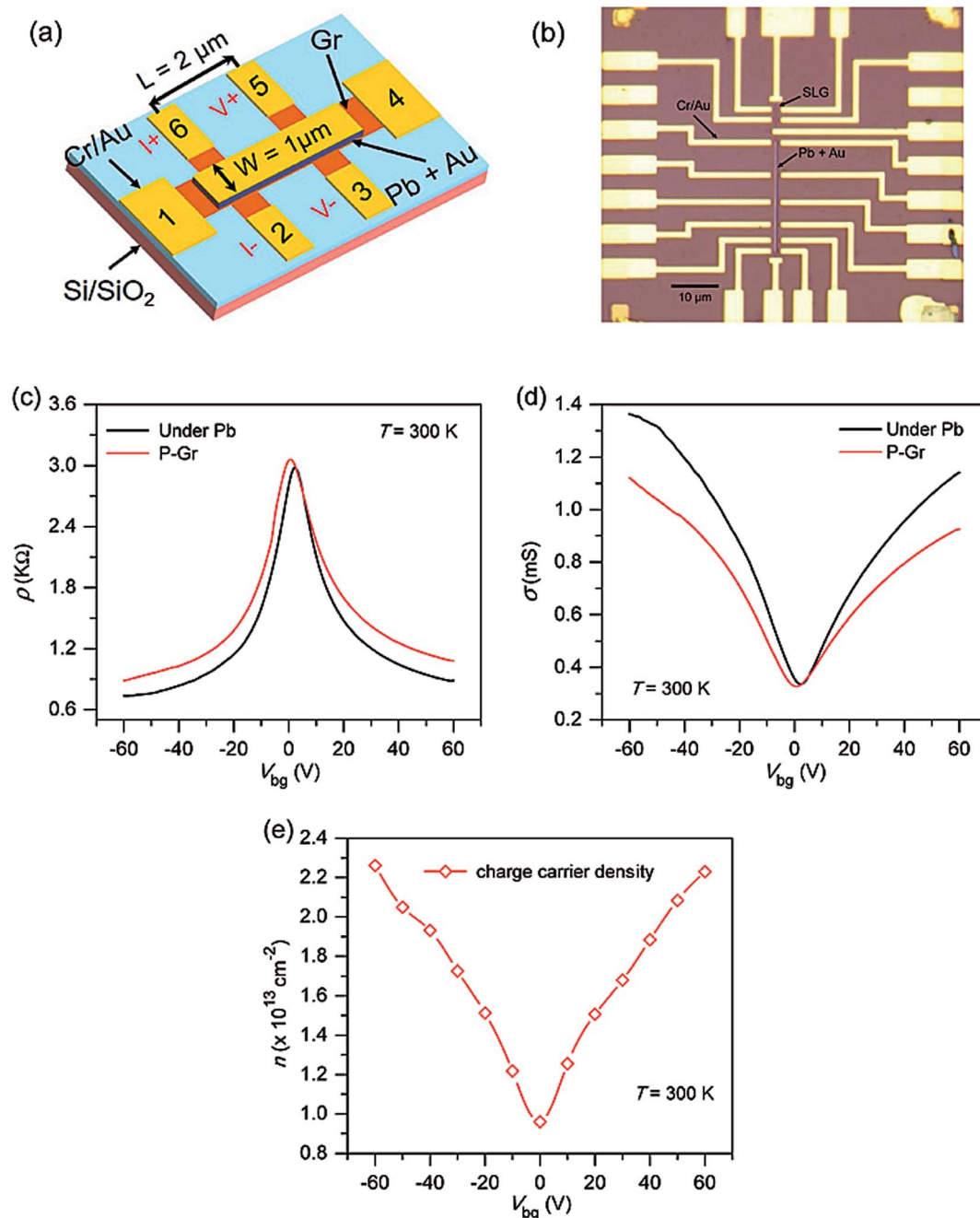
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controversial.<sup>22–24</sup> These approaches are responsible for disorders in the charge transport quality and have a number of limitations for spin transport, making it problematic for use in controlling the SOI in Gr. Moreover, an elusive challenge in this field is the realization of enhanced SOI and induced topological nontrivial gaps inside the Dirac point. In this work, we made a Gr/Pb/Au heterostructure field effect

transistor (FET). For the first time, we used Pb as an intercalated layer between the graphene and Au and measured SOI in the heterostructure device in local and non-local measurement configurations. Intercalation of the Pb thin layer between Au and Gr leads to significant modification of the electronic structure and SOI of the latter. We observed a giant spin Hall effect ( $R_{NL} \sim 180 \Omega$ ) in the device without



**Fig. 1** (a) Schematic of graphene (Gr) device for local and non-local configuration with lead (Pb) and without Pb. In non-local measurement, we applied a current between 6 and 2 and perceived the voltage drop between 5 and 3. For local measurement, we applied a current between electrodes 1 and 4 and detected the voltage drop between 2 and 3. In each case, the length and width of the channel is  $L = 2 \mu\text{m}$  and  $W = 1 \mu\text{m}$ , respectively. (b) Optical image of a completed device with multiple Hall bars on pristine graphene (P-Gr) and graphene with Pb + Au. (c) Resistivity of P-Gr and covered graphene with Pb + Au as a function of back-gate voltages at room temperature. (d) Conductivity of P-Gr and covered graphene as a function of back-gate voltages at room temperature. (e) Charge carrier density as a function of back-gate voltages.



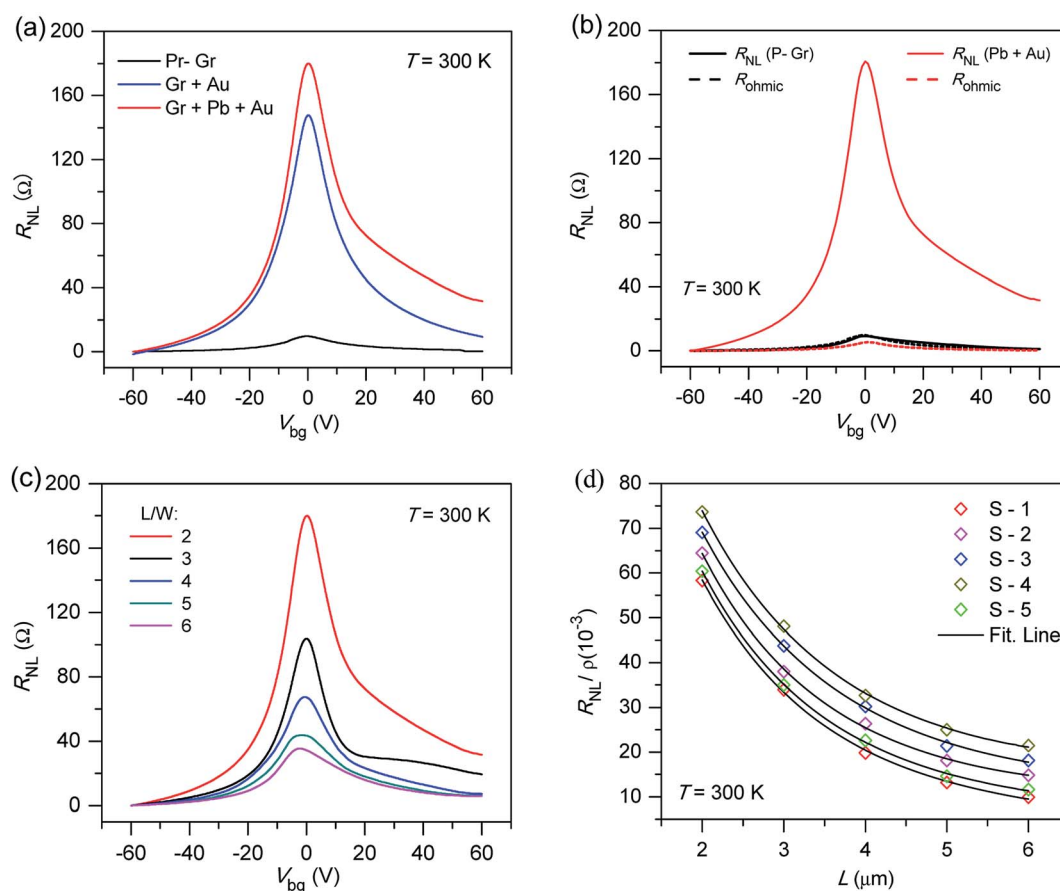
ferromagnetic electrodes. To further quantitatively examine the improvement in the magnitude of SOI of Gr, we fit our magneto-conductivity data to an equation from the theory of weak localization (WL). The SOI values calculated for Gr through non-local configuration and WL analysis are similar, supporting the internal consistency of experimental results in this study. The effective improvement of SOI in the Gr-based system opens a new gateway to explore new areas of the spin FET phenomenon.

## Characterization of the SLG/Pb + Au heterostructure device

Fig. 1a is a schematic of the Gr/Pb/Au heterostructure device and Fig. 1b is an optical image of the final device in one of its two basic Hall bar configurations. In one of these configurations, Gr is underneath the Pb and Au; in the other, the Gr does not have any top layers. The Raman spectra of graphene on SiO<sub>2</sub> are shown in Fig. S1c,† in which the G and 2D peaks appear around 1580 cm<sup>-1</sup> and 2700 cm<sup>-1</sup>. The ratio of intensities of G and 2D peaks ( $I_{2D}/I_G$ ) is  $\sim 2.6$ , which is

related to a previously reported value of single layer Gr (SLG).<sup>25,26</sup> Atomic force microscopy (AFM) was used to confirm the thickness. Fig. S2b† shows the height profile of Gr on SiO<sub>2</sub>, with the thickness of SLG being  $\sim 0.337$  nm.

First, the charge transport was characterized. Fig. 1c and d show resistivity and conductivity as a function of back-gate voltage. The back-gate voltage was swept from  $-60$  V to  $+60$  V and resistivity and conductivity were measured. In cases of pristine Gr, the charge neutrality point (CNT) (*i.e.*, the Dirac point) lies nearly at zero back-gate voltage. When Pb is deposited, the Dirac point shifts in a positive direction. The mobility of Gr can be calculated by  $\mu = (1/C_g) (\partial\sigma/\partial V)$ , where  $C_g$  is the gate capacitance and  $\sigma$  is conductivity. The calculated mobilities are  $25\,000\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  and  $29\,100\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  in pristine graphene and graphene with Pb + Au, respectively.<sup>27–30</sup> Furthermore, we measured the charge carrier density as a function of back-gate voltage at room temperature, as shown in Fig. 1e. For the charge carrier density, we measured the Hall measurement at the different back-gates and calculated charge carrier density using  $n = 1/(eR_{xy}/B)$ , where  $R_{xy}$  is the Hall resistance at magnetic field  $B$  and  $e$  is the charge of an electron.



**Fig. 2** (a) Comparison of non-local signal in pristine graphene, graphene + Au, and graphene + Pb + Au. (b) Non-local resistance measurement of pristine graphene (P-Gr) and under Pb device for  $L/W = 2$  as a function of back-gate voltages. Dotted lines are the ohmic contributions in P-Gr and under Pb + Au. (c) Non-local measurement of the device under Pb + Au (c) non-local resistance of graphene devices under Pb as a function of back-gate voltages at different  $L/W$  ratios. (d) Non-local resistance near charge neutrality points of Gr as a function of a length under Pb + Au. We measured different samples as denoted by S-1, S-2, S-3, S-4, and S-5.

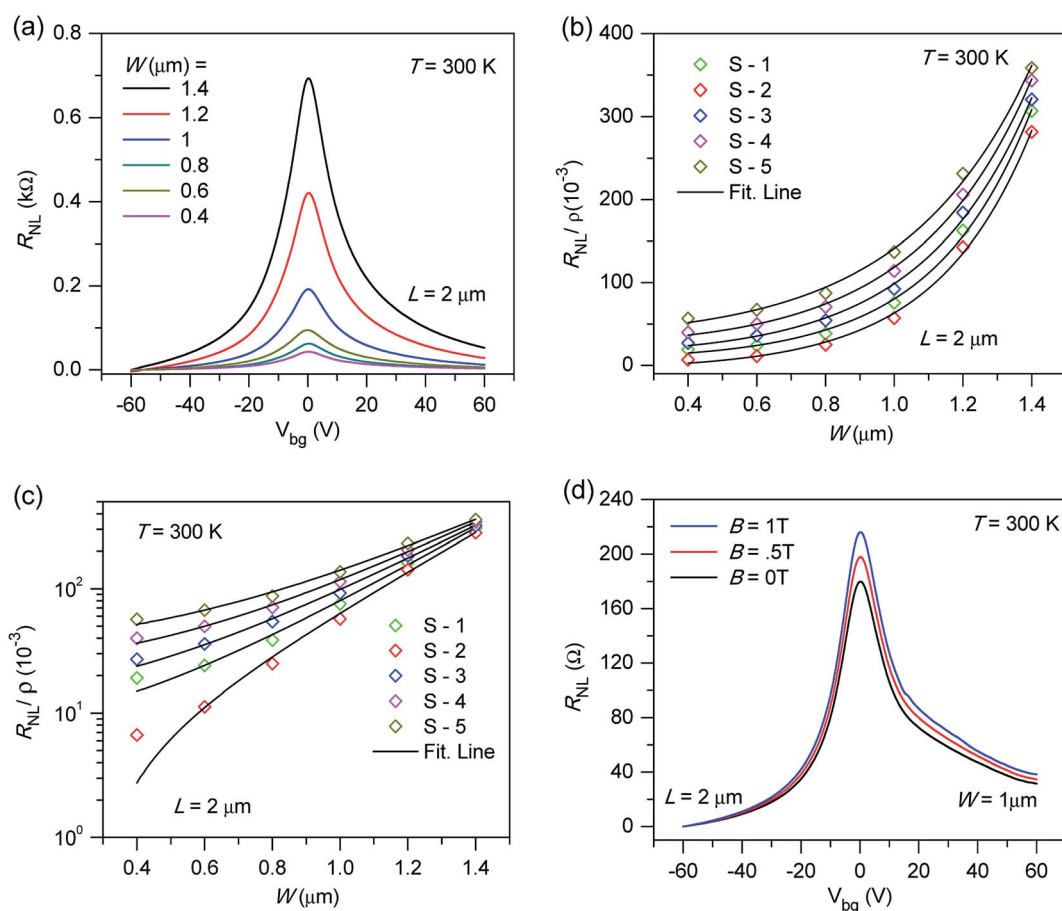


Next, we turned our attention to confirming the origin of the large non-local (NL) signal we obtained; this was done by measuring the NL charge transport. We measured the length ( $L$ ) and width ( $W$ ) of the channel-dependent non-local signal to confirm its nature. We studied the length dependence of the non-local signal by keeping width fixed ( $W = 1 \mu\text{m}$ ) and varying  $L$  (2–6  $\mu\text{m}$ ). We measured the non-local signal of pristine Gr by applying a constant current ( $I = 0.99 \mu\text{A}$ ) between two electrodes (6 & 2) and a detected a decrease in voltage ( $V$ ) across the neighbouring pair of electrodes (5 & 3). The NL signal obtained ( $R_{\text{NL}} \sim 9.6 \Omega$ ) was not very high; it was also comparable in magnitude with the ohmic contribution shown in Fig. 2b. For ohmic measurement, we applied the current between the pair of electrodes (1 & 4) and measured the drop in  $V$  between the other pair (2 & 3). The dimensions of the channel are  $L = 2 \mu\text{m}$  and  $W = 1 \mu\text{m}$ . The ohmic leakage contribution is calculated using the following:

$$R_{\text{ohmic}} = \frac{4}{\pi} \rho e^{(-\pi L/W)}.$$

Thus there is no spin Hall effect (SHE) signature in pristine Gr;<sup>13,31–33</sup> therefore, it must have an extrinsic origin. To examine this, we deposited an Au layer on the Gr and measured an NL signal up to  $R_{\text{NL}} = 150 \Omega$ . Next, we used a thin layer of Pb as an intercalated layer between the Gr and Au. Following this, we measured the Gr under the Pb and Au in the non-local configuration. We observed a very large and strong non-local signal ( $R_{\text{NL}} = 180 \Omega$ ), and the contribution of ohmic leakage contribution is very small compared to pristine Gr, as shown in Fig. 2b. The value of  $R_{\text{NL}}$  we obtained is much higher than those previously reported.<sup>13,31</sup>

We have measured the non-local spin Hall signal of Gr/Pb/Au heterostructure by changing the thickness of Pb layer with a constant layer of Au (10 nm). We used different thickness of intercalated Pb layer (0 nm, 0.5 nm, 1 nm, and 2 nm). We confirmed the thickness of Pb and Au as shown in Fig. 4a and b. We observed that the NL signal was increased with the increasing thickness of Pb layer (Fig. 4c). For the Pb layer thicker than 1 nm, the magnitude of the NL signal saturates as shown in Fig. 4d. We attribute the large NL signal to the lattice



**Fig. 3** (a) Non-local resistance of graphene devices under Pb + Au as a function of back-gate voltages at different widths ( $W$ ) at a fixed length at 300 K. (b) Non-local resistance near charge neutrality points of Gr as a function of width under Pb in different samples denoted as S-1 to S-5. (c) Linear dependence of  $\ln R_{\text{NL}}$  with  $W$  that confirms that the signal comes from the SHE. We measured in different samples denoted by S-1, S-2, S-3, S-4, and S-5. (d) Non-local resistance as a function of back-gate voltage at different perpendicular magnetic field room temperature with  $L/W = 2 \mu\text{m}$ . This increment in the  $R_{\text{NL}}$  near the neutrality point (NP) can be assumed to be the combined effect of Zeeman splitting and bipolar transport at the charge NP under the perpendicular external magnetic field.



mismatch among the graphene, Pb, and Au. It was previously reported that the intercalated Pb atoms were located in the equivalent positions to graphene A and B sublattices as compared to other heavy metal atoms. The intercalated layer of Pb significantly modified the band structure of graphene.<sup>34</sup> Basically, the Pb atoms are more diffusive in the closer vicinity of the carbon atoms and enhance the SOI in graphene. At a larger thickness, the effect of Pb saturates due to filling of vicinity states of graphene. In addition, the Pb and Gr make a larger hybridization because the Pb layer is adjacent to the Gr sheet. The hybridization and charge carrier transfer to Gr sheet strongly affect electronic properties of graphene and SOI in graphene.<sup>35</sup>

When Pb was used as an intercalated layer between graphene and Au, the electronic structure of Gr was improved and electrons maintained a strong SOI during tunneling through Pb.<sup>34–36</sup> Further, we estimated the NL signals as a function of the back-gate voltage at different values of  $L$  (2–5  $\mu\text{m}$ ) as shown in Fig. 2c. We noted that the NL signal decreased as the length of the device decreased. To determine key spin parameters such as the spin diffusion length ( $\lambda_s$ ), spin relaxation time ( $\tau_s$ ), and SOI in our heterostructure device, we performed length-dependent NL signal measurements. We plotted  $R_{\text{NL}}/\rho$  against the length of

the channel and the resultant curves were fitted by the spin diffusion equation,<sup>32,37</sup>

$$R_{\text{NL}} = \frac{1}{2} \gamma^2 \rho \frac{W}{\lambda_s} e^{-L/\lambda_s} = \frac{1}{2} \gamma^2 \rho \frac{W^2}{\lambda_{\text{so}}^2} e^{-LW/\lambda_{\text{so}}^2}$$

where  $\lambda_s$  is the spin diffusion length and  $\gamma$  is the spin Hall coefficient.<sup>32</sup>  $\lambda_{\text{so}}$  is the spin precession length, which is related to the spin relaxation length as  $\lambda_s = \frac{\lambda_{\text{so}}^2}{W}$ .<sup>38</sup> We extract  $\lambda_s$  and  $\gamma$  in the range of (0.20–0.62  $\mu\text{m}$ ) and (0.170–0.32) near the CNP, respectively.

Further, confirming the nature of the SHE, we performed width-dependent NL signal measurements. Fig. 3a shows the NL signal as a function of the back-gate voltage with a fixed channel length ( $L = 1 \mu\text{m}$ ) and varying channel width ( $W = 0.4$ – $1.4 \mu\text{m}$ ). In this case, when varying the width, we only selected the devices in which the condition  $\lambda_s, \lambda_{\text{so}} > W$  was fulfilled. The NL signal increased as the width of channel increased. Then, we plotted  $R_{\text{NL}}/\rho$  as a function of  $W$  near the CNP again and fitted the data by using the spin diffusion equation as shown in Fig. 3b. Thus, the estimated values of  $\lambda_s$  and  $\gamma$  were in the range of (0.120–0.72  $\mu\text{m}$ ) and (0.12–0.36) and showed good agreement with the length-dependent NL signals. Fig. 3c shows the linear

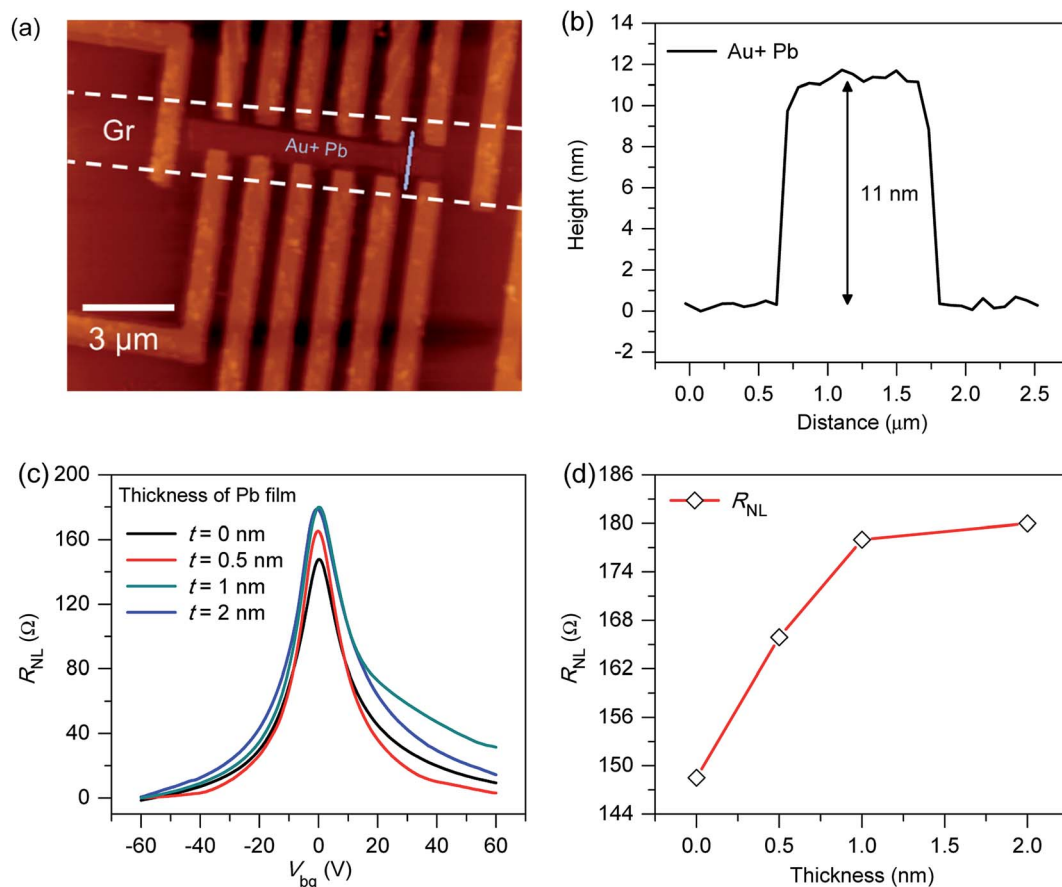


Fig. 4 (a) Atomic force microscopy image of the final device to confirm the thickness of Au and Pb (b) height profile of Au and Pb region. Total height is 11 nm which includes 1 nm Pb and 10 nm Au. (c) Non-local signal with respect to the thickness of Pb layer. (d) Change in non-local signal with respect to thickness of the Pb film.



dependence of  $\ln(R_{\text{NL}}/\rho)$  on  $W$ , which also confirms that the SHE shows a power-law dependence. The linear dependency of NL signals confirmed that the expected signal is caused by the SHE. If the dominant NL signal is caused owing to ohmic contribution, non-linear behaviour should be observed.<sup>31</sup> Notably, we observed a giant and obvious NL signal as compared to ohmic contribution. Such a signal can only be observed when the width of the channel is smaller than the distance between the electrode.<sup>32,37</sup> Fig. 3d shows NL resistance as a function of the back-gate voltage ( $V_{\text{bg}}$ ) at different perpendicular magnetic fields ( $B_{\perp}$ ) at 300 K with  $L/W = 2$ . This increment in  $R_{\text{NL}}$  and Hall resistivity near the CNP can be attributed to the combined effect of Zeeman splitting and bipolar transport at the CNP under an external magnetic field.<sup>39,40</sup>

Moreover, to confirm the origin of the observed NL signal, we measured the current–voltage ( $I$ – $V$ ) characteristics. The linear relationship between NL voltage and the applied current excludes any possible contribution of the thermoelectric effect as shown in Fig. S2.† We can exclude such a contribution because we measured the NL voltage drop along the width of the channel and the temperature gradient due to the thermoelectric effect acts along the length of the sample. We also measured the resistance as a function of the temperature as shown in Fig. S3.† We observed a logarithmic increase in resistance with

temperature at the CNP. This effect may originate from WL, disorderness due to electron–electron interaction, or the Kondo effect.<sup>41,42</sup>

We evaluated  $\tau_s$  by  $\tau_s = \lambda_s^2/D$ , where  $D$  is the diffusion constant and can be obtained using the Einstein relation  $\sigma = e^2 D_c N_{2D}(E_F)$ . Here,  $N_{2D}(E_F)$  is the density of the state of Gr. Thus, the estimated value of the spin relaxation time was  $\tau_s \sim 2$ –4 ps. We also estimated the strength of the SOI in our device. For this estimation, we assumed that the Elliott–Yafet dephasing mechanism is dominant in our case because the spin relaxation time is directly proportional to the momentum scattering time ( $\tau_s \propto \tau_p$ )<sup>31,43,44</sup> as shown in Fig. 6a. We calculated the SOI using the following relation,  $\Delta_{\text{so}} = \varepsilon_F \sqrt{\frac{\tau_p}{\tau_s}}$ , where  $\varepsilon_F$  is the Fermi energy and  $\tau_p$  is the momentum scattering time ( $\tau_p = \frac{\hbar \sqrt{\pi/n}}{\rho e^2 v_F}$ ), and  $v_F$  is the Fermi velocity. The obtained value of SOI is 70–90 meV, which is greater than previously reported values in hydrogenated graphene ( $\approx 2.5$  meV) and fluorinated graphene ( $\approx 5.1$  meV).<sup>12,13</sup> Thus, the presence and enhancement in the NL signal at room temperature and zero magnetic field ( $B = 0$  T) enhances the SOI in Gr.<sup>2,30,45</sup> The spin current in Gr *via* the SHE and the inverse SHE could be detected as the SOI was enhanced.

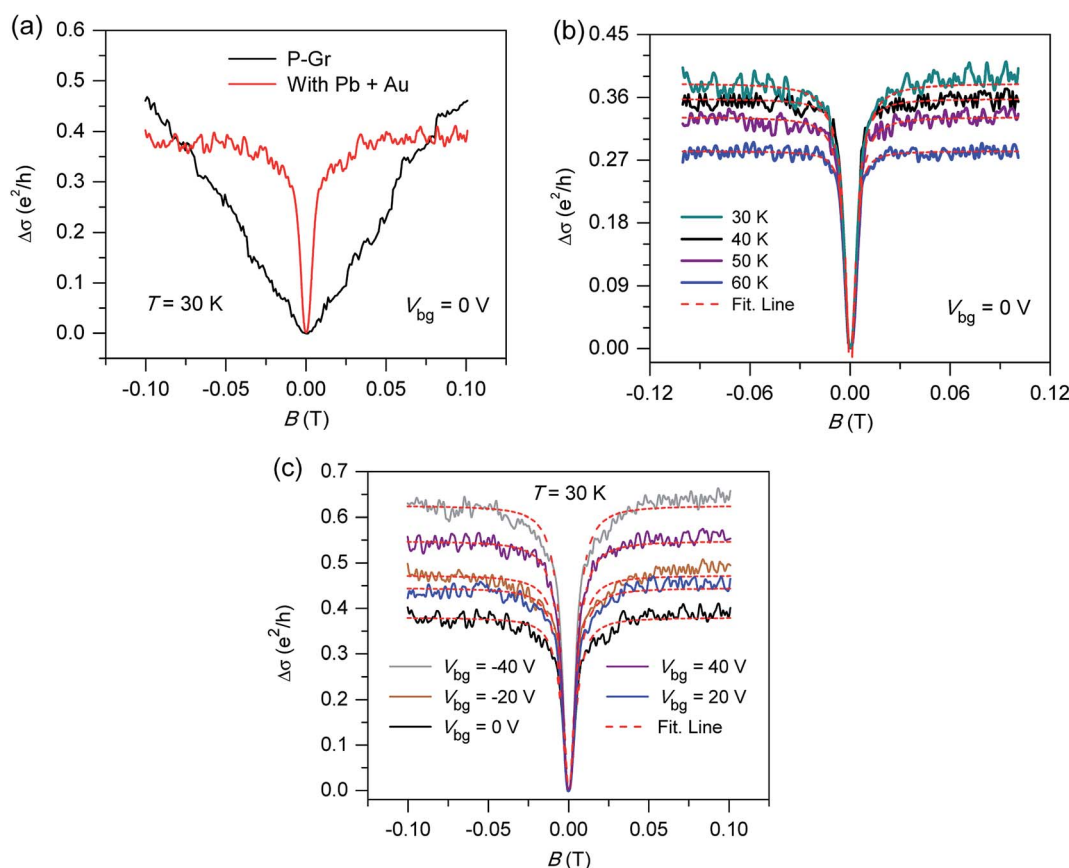


Fig. 5 (a) Magnetoconductance (MC) curves measured before and after the deposition of Pb + Au at 30 K (b) weak localization curves at various temperatures; the red line is the fitting line. (c) Back gate-dependent weak localization curves at 30 K.



We further estimated the strength of the SOI in the Gr sheet by analyzing the WL signal near the zero field.<sup>46,47</sup> The WL signal was dominant near  $B = 0$  T as compared to pristine Gr as shown in Fig. 5a. We could observe the WL signals only at low temperatures as shown in Fig. 5b. We also measured the gate dependent magnetoconductance at 30 K, as shown in Fig. 5c. The suppression of the WL signal agreed with the Kane–Mele SOI in our measurement. Thus, we used the following equation to fit the WL data,<sup>46</sup>

$$\Delta\sigma(B) = -\frac{e^2}{2\pi h} \left[ -F\left(\frac{B}{B_\phi + B_i}\right) - 2F\left(\frac{B}{B_\phi + B_{so}}\right) + 4F\left(\frac{B}{B_\phi + B^* + B_{so}}\right) + 2F\left(\frac{B}{B_\phi + 2B_i + B_{so}}\right) + F\left(\frac{B}{B_\phi + 2B^*}\right) \right]$$

Here,  $F(x) = \ln(x) + \Psi(1/2 + 1/x)$ , where  $\Psi(x)$  is the digamma function,  $B_{\phi,i,*so} = \left(\frac{\hbar c}{4De}\right) \tau_{\phi,i,*so}^{-1}$ ,  $D$  is the charge carrier diffusion constant, and  $\tau_\phi^{-1}$  is the dephasing rate.  $\tau_i^{-1}$  describes the inter-valley scattering time,  $\tau_s^{-1}$  is the intra-valley scattering time, and  $\tau_{so}^{-1}$  is the intrinsic KM spin relaxation time. Using the temperature-dependent WL data, we extracted

the dephasing rate as shown in Fig. 6b. The dephasing length decreased as the temperature increased from 30 to 60 K, which can be attributed to electron–electron interaction. Moreover,  $\tau_{so}$  can be related to the strength of the SOI by  $\Delta_{so} = \varepsilon_F \sqrt{\frac{\tau_p}{\tau_s}}$ , where  $\varepsilon_F$  is the Fermi energy. The estimated value of the SOI is 50–60 meV. The temperature- and gate-dependent SOIs are shown in Fig. 6c and d. The SOI was observed to vary with temperature; this change may be owing to the contribution of temperature-dependent scattering, ionic centers, and phonons.

In summary, we fabricated Gr FET, in which Pb was used as the intercalated layer between Gr and Au. We measured the electronic properties in pristine Gr and Gr with Pb + Au; their mobilities were  $25\,000\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  and  $29\,100\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$ , respectively. The mobility increased by the intercalation of Pb because it modified the electronic structure of Gr. To confirm the SHE, we performed length- and width-dependent NL signal measurements in pristine Gr and with adatoms. We observed a giant NL signals of  $150\ \Omega$  and  $180\ \Omega$  with only Au and with Pb + Au, respectively. Further, we calculated key parameters such as spin  $\lambda_s$ ,  $\tau_s$ , and  $\gamma$  in the range of  $(0.20\text{--}0.62\ \mu\text{m})$ ,  $(0.40\text{--}0.62\ \text{ps})$ , and  $(0.170\text{--}0.32)$ , respectively, near the CNP. The strength of the SOI was 80–90 meV. Moreover, we performed the WL

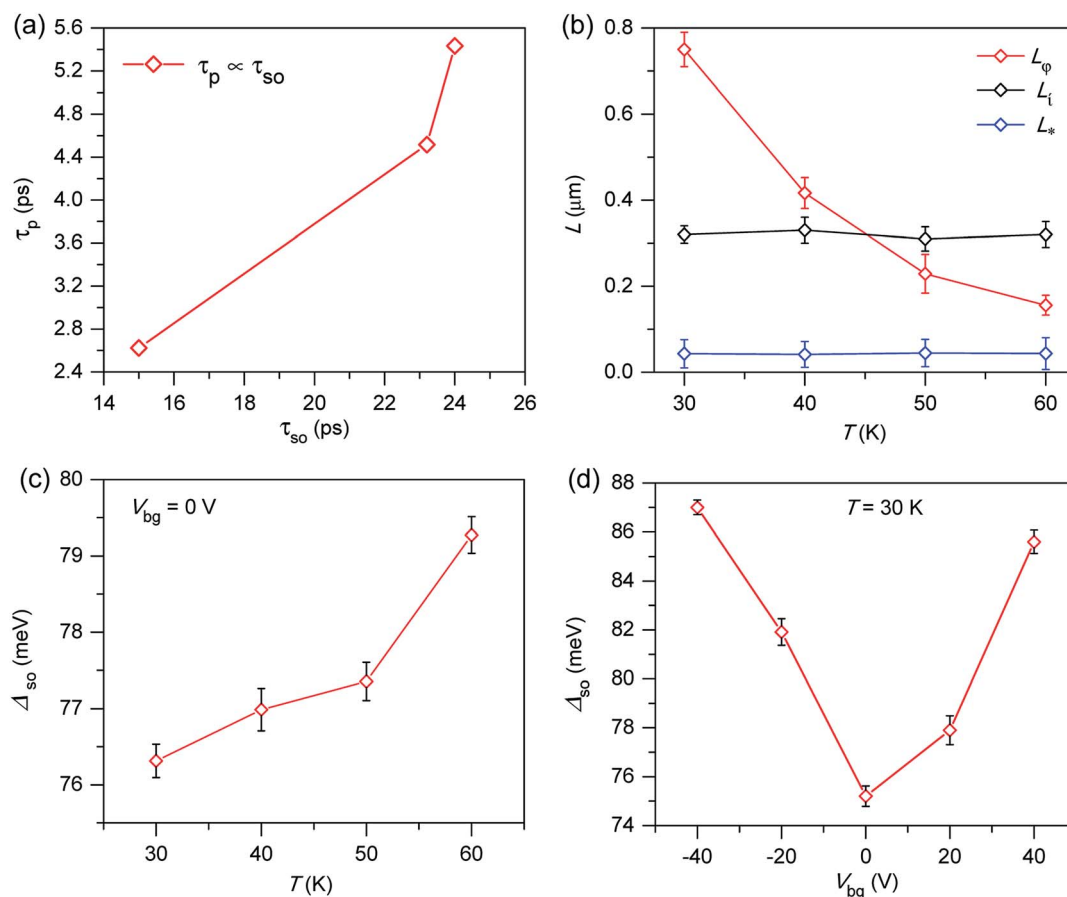


Fig. 6 (a) Relation between spin relaxation time and momentum scattering time (b) temperature-dependent dephasing length  $L_\phi$  with error bars derived from WL formula fitting. (c) Temperature-dependent spin-orbital interaction of graphene under Pb and Au that linearly increases with temperature. (d) Gate-dependent SOI in graphene under Pb and Au.



measurements to confirm the self-consistency. The SOI strength was close to the calculated value according to the NL signal measurements. The thin metal films on Gr lead to an enhanced SOI and a SHE with a large spin Hall coefficient. The SHE and SOI were enhanced by the proximity-induced effect due to adatoms of heavy metals.<sup>34,45</sup> In particular, the observed SHE stems from extrinsic origins such as skew scattering and quantum-side jumps.

## Device fabrication

A Gr flake from commercial graphite was exfoliated micro-mechanically on SiO<sub>2</sub>/highly p-doped Si (300 nm) substrate by using the standard Scotch tape method. A monolayer Gr can be identified with appropriate thickness due to the interference effect. Further, Raman spectroscopy was used to confirm the thickness of Gr. After making the large pattern, the samples were annealed at 400 °C for 6 h under Ar/H<sub>2</sub> (97.5% Ar/2.5% H<sub>2</sub>) gas flow. Electron-beam lithography (EBL) and O<sub>2</sub> plasma were used to design the electrodes in the Hall bar (*H*) configuration, and Cr/Au (6/60 nm) was deposited by thermal evaporation. Moreover, we made a small window with EBL and deposited Pb and Au (1 nm/10 nm) thin films through thermal evaporation. The length and width of the Gr channel were 1–6 μm and 0.4–1.4 μm, respectively.

The Raman spectra of Gr were measured with a Renishaw micro-spectrometer over a wavenumber range of 1100–3200 cm<sup>-1</sup>, with a laser wavelength of 514.5 nm. To prevent the device from experiencing local heating, the spot size was fixed as 1 μm, and the power was maintained at 1.0 mW. To examine the surface morphology of Gr, an atomic force microscope (AFM) was used. The local and non-local measurements of the Gr/Pb/Au heterostructure device were performed by using the standard lock-in technique at different temperatures with or without a magnetic field.

## Conflicts of interest

There are no conflicts to declare.

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