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# Controllable etching-induced contact enhancement for high-performance carbon nanotube thin-film transistors†

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Semiconducting single-walled carbon nanotubes (s-SWNTs) show great promises in advanced electronics. However, contact resistance between the nanotubes and metal electrode has long been a bottleneck to the development of s-SWNTs in high-performance electronic devices. Here we demonstrate a simple and controllable strategy for enhancing the electrode contact and therefore the performance of s-SWNT thin film transistors by plasma etching treatment, which effectively removes the polymer residues, including the photoresist and the conjugated molecules, adsorbed on the surface of s-SWNTs. As a result, the contact resistance is reduced by 3 times and the carrier mobility rises by up to 70%. Our method is compatible with current silicon semiconductor processing technology, making it a viable effective approach to large-scale application of s-SWNTs in the electronics industry.

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## Introduction

Semiconducting single-walled carbon nanotubes (s-SWNTs) have drawn increasing interest in the development of flexible and transparent electronic devices due to their unique one-dimensional structure, tuneable bandgap and excellent electronic properties.<sup>1–3</sup> Recently, great progress in selectively sorting s-SWNTs with purity higher than 99.9%<sup>4,5</sup> has enabled the fabrication of s-SWNT based field effect transistors (FETs) with high carrier mobility, high on/off ratio, low off-state current and steep sub-threshold swing. These FETs are desirable for developing complementary metal-oxide-semiconductor (CMOS) electronics with low dynamic and static power consumption. Meanwhile, s-SWNT network-based thin-film transistors (TFTs) have also been extensively investigated and many potential applications have been demonstrated, including infrared

detection,<sup>6,7</sup> flexible displays,<sup>8,9</sup> radio frequency devices,<sup>10,11</sup> integrated circuits<sup>12,13</sup> and other functional devices.<sup>14,15</sup> However, despite significant improvements to the subthreshold swing and on/off ratio of s-SWNT thin film devices through various optimised techniques, the low and uncontrollable carrier mobility and current density are still major hurdles to achieving practical utility.

Establishing ohmic contact between the s-SWNTs and the metal electrode is crucial to fabricate high-performance transistors. For a s-SWNT TFT device, the total resistance is composed of channel and contact resistance. The channel resistance tends to vary inversely with the channel length of TFTs. When the channel length is reduced to microscale or nanoscale, the channel resistance becomes a negligible component of the total resistance relative to the contact resistance. The latter, therefore, tends to be the main hurdle to improving the performance of s-SWNT TFTs. Furthermore, the contact interfaces between the s-SWNTs and the metal electrodes directly determine the injection efficiency of carriers. Therefore, three main approaches have been developed to solve contact-related problems, including the removal of dispersant organic molecules on the sorted s-SWNTs,<sup>16–18</sup> the use of electrode metals with a relatively low work function to improve transport at the interfaces<sup>19</sup> and the formation of chemical bonds between SWNTs and electrode.<sup>20,21</sup> However, dispersant removal may lead to the aggregation of carbon nanotubes (CNTs), which is hard to produce uniform films in large-area. Deposited metal electrodes with low work function are not stable in the air, resulting in unstable and uncontrollable performance of the devices. Formation of chemical bond between SWNT and electrode is often complex and difficult to

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control. For these reasons, it is necessary to develop a simple and effective method for the fabrication of controllable s-SWNT TFTs with high performance.

Here, we demonstrate a simple and controllable method to fabricate high-performance s-SWNT TFTs, which is compatible with current silicon semiconductor processing technology. Through simply introducing a plasma etching procedure into the normal photolithography process, the performance of the s-SWNT TFTs is greatly improved. In our modified procedures, the contact resistance decreases more than threefold and the average carrier mobility reaches up to  $91 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ , which is in the highest level compared with previously reported results. After thoroughly investigating the influence of etching time on the performance of s-SWNT TFT devices, we concluded that the removal of impurity and the structural damage of s-SWNT upon plasma etching may play a key role in enhancing and stabilizing not only the contact between s-SWNT films and metal electrodes, and thus promote the performance of s-SWNT TFTs.

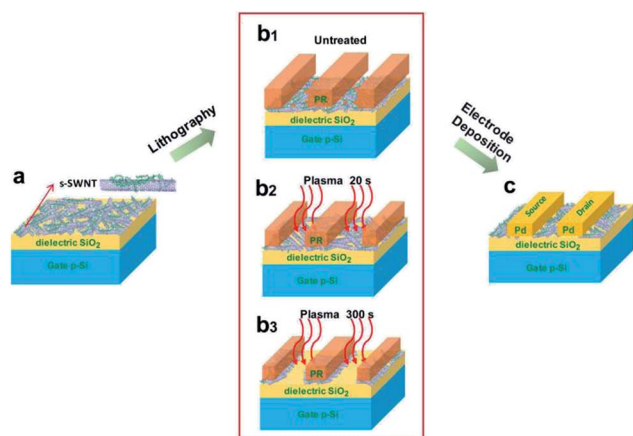
## Results and discussion

To prepare s-SWNT dispersion with high purity, conjugated polymer-assisted separation method was chosen, using poly[9-(1-octylonoyl)-9H-carbazole-2,7-diyl] (PCz) as dispersant, as reported in our previous studies.<sup>5,16</sup> Arc-discharged SWNTs were employed because they have proper diameters and bandgap distribution. After typical procedure of sonication followed by two rounds of centrifugation, the dispersion was collected for further analyses and applications. The detailed process and purity evaluation in terms of the  $\Phi$  value<sup>22</sup> are shown in ESI 1.†

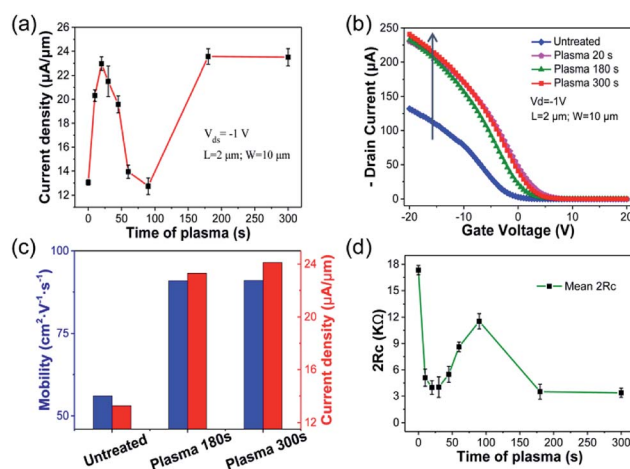
Fig. 1 shows the modified fabrication process for high-performance s-SWNT TFTs. The TFTs were fabricated with

channel length of  $2 \mu\text{m}$  and channel width of  $10 \mu\text{m}$ . Firstly, s-SWNT networks were fabricated on the  $\text{SiO}_2/\text{Si}$  substrate by dip-coating and the photoresist was spin-coated on the surface of the s-SWNTs/ $\text{SiO}_2/\text{Si}$  under condition of yellow light. Then, the source and drain electrodes of the transistors were patterned by lithography. Next, the exposed electrode pattern areas were treated by plasma etching, a procedure with an important influence on device performance. In this procedure, the power was set at 100 W and oxygen flow was 100 sccm. Different etching times ranging from 10 s to 300 s were performed. Finally, Pd (30 nm) was deposited as the electrode through thermal evaporation followed by lift-off. The s-SWNT TFTs fabricated with and without plasma etching were investigated in detail.

The performances of the as-fabricated s-SWNT TFTs are shown in Fig. 2. To evaluate the reliability of the etching procedure, the statistical current densities of the TFTs etched for different times were calculated, as shown in Fig. 2a. Two peak current distributions can be observed. When the etching time was increased from 0 to 90 s, the peak current appeared at around 20 s, indicating that relatively brief etching markedly enhanced the device performance compared with no treatment or slightly longer etching times. Interestingly, however, the absolute maximum current was recorded when the treatment time exceeded 180 s and remained relatively stable when the etching time was prolonged to 300 s. The on-current increased from  $130 \mu\text{A}$  (untreated) to  $240 \mu\text{A}$  after 300 s plasma etching. Fig. 2b shows the characteristic linear transfer curves of s-SWNT TFTs with selected plasma etching times of 0 s, 20 s, 180 s and 300 s. The transfer curves for all of the plasma etching times are shown in Fig. S4.† The shapes of the transfer curves indicate that the s-SWNT TFTs display p-type carrier transports. As



**Fig. 1** Schematic diagram of the fabrication process of high performance s-SWNT TFT. (a) s-SWNT network was fabricated on  $\text{SiO}_2/\text{Si}$  substrate by dip-coating; (b) the photoresist was spin-coated on the surface of s-SWNTs/ $\text{SiO}_2/\text{Si}$  wafer under illumination by yellow light. Next, patterns of source and drain electrodes of transistors were prepared by lithography. Then, the exposed electrode pattern areas were etched by plasma. (b1) Untreated; (b2) 20 s; (b3) 300 s; (c) Pd (30 nm) electrodes were deposited through thermal evaporation followed by lift-off. Finally, the s-SWNTs TFTs were obtained.



**Fig. 2** The performance of s-SWNT TFTs. (a) The on-state currents of the s-SWNT TFTs ( $L = 2 \mu\text{m}$ ,  $W = 10 \mu\text{m}$ ) treated by plasma etching for different times; (b) the characteristic linear transfer curves of s-SWNT TFTs ( $L = 2 \mu\text{m}$ ,  $W = 10 \mu\text{m}$ ) treated by plasma etching for 0 s (blue curve), 20 s (magenta curve), 180 s (olive curve) and 300 s (red curve); (c) the mobility (blue) and current density (red) of s-SWNT TFTs ( $L = 2 \mu\text{m}$ ,  $W = 10 \mu\text{m}$ ) treated by plasma etching for 0 s, 180 s and 300 s. (d) Variation of the average contact resistance with different etching times.



mentioned above, for all plasma etching times equal to or greater than 180 s, the s-SWNT TFT devices exhibit almost identical, greatly enhanced performance. Besides the obvious increase of current density, the threshold voltages were also decreased. Fig. 2c exhibits the average carrier mobilities and the average current densities of the fabricated TFTs with plasma etching times of 0 s, 180 s and 300 s. Under 180 s treatment, the on-current density was increased to  $24 \mu\text{A } \mu\text{m}^{-1}$  and the mobility of the device was increased from  $56 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  to  $91 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . Fig. S7† shows the logarithmic transfer curves of s-SWNT TFTs with different etching times, which are consistent with Fig. 2a, indicating the stable and reliable performance of the as-fabricated TFTs.

Fig. 2d shows the relationship between average contact resistance and plasma etching time. To evaluate the change of contact resistance ( $2R_c$ ) between s-SWNTs and metal electrode, the Y function was calculated.<sup>23,24</sup> The detailed calculation process is shown in ESI 2.† The contact resistance decreased from 14.46 k $\Omega$  for the untreated sample to 3.36 k $\Omega$  for the sample with the longest etching time of 300 s. The variation of  $2R_c$  with etching time was consistent with the response of the current density shown in Fig. 2a, indicating enhancement of the performance substantially caused by better contact between s-SWNT and metal electrode after plasma treatment. The performances of s-SWNT transistors reported in recent papers were compared with ours in this work. Fig. S6† presents the statistics for on/off ratio and carrier mobility of the s-SWNT TFTs.<sup>25–37</sup> For the random s-SWNT film-based TFTs fabricated in our work, merely by plasma treatment, the devices were able to markedly outperform the previously reported random film-based devices, and were even comparable with devices fabricated using aligned s-SWNT films.

To understand the role of plasma etching in the performance enhancement of the s-SWNT TFT devices, we first characterised the s-SWNT films by atomic force microscopy (AFM) and scanning electron microscopy (SEM) under different etching times. As shown in Fig. 3a and S8,† the AFM image for the untreated s-

SWNT film exhibits numerous bright spots, suggesting residual impurities on the surface of s-SWNT film, which may be ascribed to the small amounts of photoresist and conjugated polymers wrapped on s-SWNTs. The height of the untreated s-SWNTs was about 6–8 nm, which were consisted of 2–3 layers of s-SWNTs. For the s-SWNT film treated with 20 s plasma etching, the surface of s-SWNTs became clean, and the height decreased to 3–5 nm, which indicates the removal of organic impurities and may be accompanied by the slightly destroy of s-SWNT. As the etching time increased, the s-SWNTs were progressively destroyed. When the etching time was prolonged to 90 s, the height of the s-SWNT films was lower than 2.0 nm, suggesting that the top 1–2 layers of s-SWNTs had already been etched, and only the bottom layer of s-SWNTs left, which formed a sparse network. Similarly, after 90 s etching, no s-SWNTs were observed in the SEM image, and the height of a single s-SWNT was measured, ranging from 0.49 to 1.47 nm as shown in Fig. 3c. Considering that previous research of SWNT etching process determined the height of PCz-wrapped s-SWNT is about 1.5–2.0 nm, it can be inferred that the remaining s-SWNTs were broken into tubular fragments. When the etching time increased to 180 s, no remaining s-SWNTs could be seen in the AFM and SEM images, meaning that most of the s-SWNTs had been greatly etched. In addition, as shown in Fig. 3b, the height of the overall s-SWNT film was also found to gradually decrease with the increase of etching time, further demonstrating the destruction of the s-SWNTs.

Further evidence of structural damage by plasma etching was provided by the Raman, XPS and absorption spectra. Fig. 4a shows the Raman spectrum of s-SWNT film excited by a 633 nm laser. Typically, s-SWNTs have two typical peaks, corresponding to the D and G band. The D band peak is usually located at  $1300 \text{ cm}^{-1}$  and originates from structural defects. The G band peak is generally located at  $1590 \text{ cm}^{-1}$  and stems from planar vibrations of carbon atoms in SWNTs. When the etching time was less than 20 s, the G band peak is obvious and strong, indicating that most of the s-SWNTs remained intact. However, with prolonged etching time, the intensity of the D band peak gradually increased, suggesting increased structural defects of the s-SWNTs introduced by plasma etching treatment. After the etching time reached to 45 s, the intensity of the G band peak decreased to that of the D band peak and became very weak. When the etching time was extended to 90 s, the Raman signals

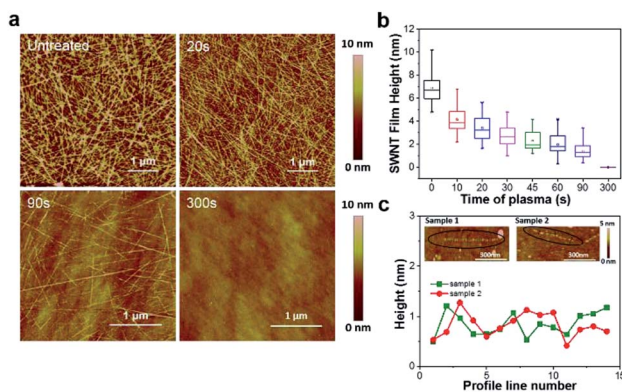


Fig. 3 (a) The AFM images of s-SWNT films treated by plasma etching for different times. The etching time is displayed in the upper left corner of each graph. (b) The height statistics of s-SWNT films with different plasma etching times for 50 profile lines. (c) The AFM images and height statistics of two samples of single s-SWNTs.

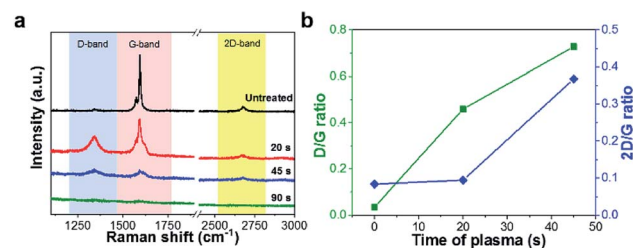


Fig. 4 (a) Raman spectra of s-SWNTs treated by plasma etching for 0 s (black curve), 20 s (red curve), 45 s (blue curve) and 90 s (green curve). (b) The changes of D/G ratio (green curve) and 2D/G ratio (blue curve) as a function of plasma etching time.



almost disappeared, suggesting that the density of remaining s-SWNTs was very low. In contrast, as shown in Fig. S9,<sup>†</sup> a 2D band peak at  $2650\text{ cm}^{-1}$  arose with the increase of etching time, indicating the generation of a planar  $\text{sp}^2$  structure, which may originate from SWNTs with a high density of defects. Fig. 4b exhibits the D/G and 2D/G ratios at different plasma treatment times, which are in good agreement with the structural observations from AFM and SEM. The increase of these ratios indicates that defects and a planar  $\text{sp}^2$  fragments increased with prolonged plasma treatment time. Because the PCz and s-SWNTs both have their own characteristic absorption peaks, absorption spectroscopy can be used to track their changing contents. To obtain the absorption spectra, the s-SWNT film was deposited on a transparent quartz substrate and then etched for different times. As shown in Fig. S10,<sup>†</sup> the absorption peaks of s-SWNTs and PCz gradually disappear with the increase of etching time, which again demonstrates that the plasma etching treatment not only degrades the conjugated polymer but also destroys the structure of the s-SWNTs.

To further understand the degradation of polymer and the structural variation of SWNTs during plasma treatment, the XPS spectrum of the s-SWNT film was used to evaluate the contents of carbon and nitrogen, as shown in Fig. 5. (The XPS spectra for all of the plasma etching times are shown in Fig. S11.<sup>†</sup>) The C 1s peak can be deconvoluted into five peaks,<sup>38,39</sup> i.e.  $\pi-\pi^*$  (289.2 eV), C-N (287 eV), C-O (286.1 eV), C=C (284.9 eV) and C-C

(284.5 eV). As shown in Fig. 5a and b, the C 1s peak of untreated and etched for 45 s samples explained intensity of the peak decreased and the  $\text{sp}^2$  structure transformed to  $\text{sp}^3$  structure with the increase of plasma treatment time, indicating the decreased content of s-SWNTs and edge defect introduced by plasma treatment, which is consistent with the above analyses. As shown in Fig. 5c, the intensity of N 1s was greatly reduced after 45 s treatment, further demonstrating the rapid degradation of PCz. Fig. 5d shows the contents of four chemical bond types after etching times ranging from 0 to 45 s. The decreasing content of C-N groups demonstrates the degradation of PCz, as nitrogen only existing in PCz, while the increasing content of C-O groups implies the introduction of more defects into the s-SWNTs. Moreover, the increasing content of C-C groups and decreasing content of C=C groups may be ascribed to destruction of SWNTs during plasma treatment, revealing that  $\text{sp}^2$  carbon was gradually converted into  $\text{sp}^3$  carbon. We illustrated the structural change of the s-SWNT film under plasma etching in Fig. 5e, which can be seen to proceed through several stages. The initial s-SWNTs are wrapped by PCz and a small amount of photoresist. In the first stage, the residual PCz and photoresist on the s-SWNTs are degraded. In the second stage, the s-SWNTs on the top layer are gradually destroyed and transformed into planar fragments of  $\text{sp}^2$  structure. In the third stage, the s-SWNTs on the top layer are almost completely removed while the exposed bottom s-SWNTs still maintain their integrity. In the fourth stage, the bottom s-SWNTs are also transformed into planar fragments after greatly destroyed. Finally, most of the s-SWNTs are greatly etched while the s-SWNTs near the edge of the photoresist are continuously exposed and converted into planar fragments.

Based on the above analyses, a possible mechanism is proposed for the performance enhancement by plasma etching. Here, graphene-like fragments were envisaged to explain the enhanced contact between electrode and s-SWNTs according to the results discussed above. When photoresist is used, the photoresist is etched faster than the s-SWNTs.<sup>40</sup> After photolithography, the residual photoresist and conjugated polymers adsorbed on the surface of s-SWNTs strongly impede the contact between s-SWNTs and metal electrode, and therefore reduce the injection efficiency of the carriers (as shown in Fig. 5e-f and S12<sup>†</sup>). Under a short etching time of less than 20 s, the photoresists and conjugated polymers on the s-SWNTs are degraded by oxygen etching. When further increasing the etching time to 20 s, the s-SWNTs on the top layer are gradually thinned and converted into broken tubular or graphene-like fragments, resulting in better contact between s-SWNTs and metal electrode, which improves the carrier injection efficiency. The synergy between the degradation of residues and the generation of graphene-like fragments results in performance improvement. Thus, the first performance maximum is located at around 20 s. When the s-SWNT films that were etched for times ranging from 20 to 90 s, the s-SWNTs on the top layer are almost completely etched while the bottom s-SWNTs still maintain their integrity, which may result in the increase of contact resistance. Furthermore, Fig. 5g shows the contact interfaces for s-SWNT films that were etched for times

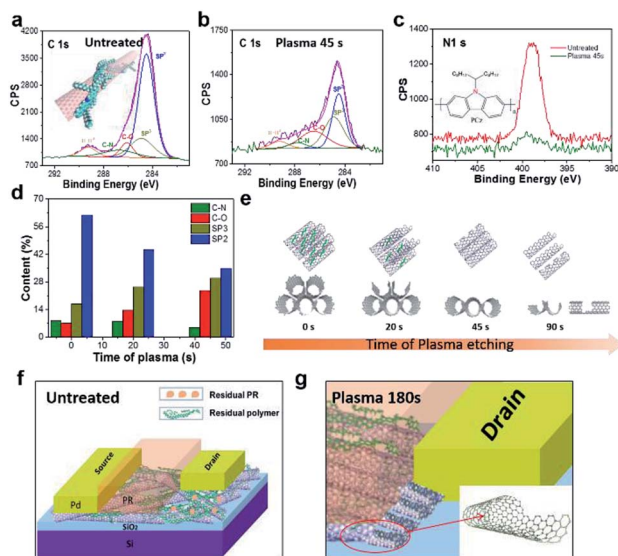


Fig. 5 (a) and (b) The deconvoluted C 1s XPS spectra of s-SWNTs treated by plasma etching for 0 s and 45 s (orange curve,  $\pi-\pi^*$ ; olive curve, C-N; red curve, C-O; dark yellow curve, C=C; blue curve, C-C). Inset: model of an s-SWNT is wrapped by PCz. (c) N 1s XPS spectra for the samples surface treated by plasma etching for 0 s (red curve) and 45 s (olive curve). Inset: the structure of PCz. (d) Contents of chemical bonds on the s surface of samples treated by plasma etching for 0 s, 20 s and 45 s. (e) The model of s-SWNT films treated by plasma etching with different times. (f) Schematic diagram of an untreated s-SWNT TFT with Si as back gate,  $\text{SiO}_2$  (100 nm) as gate dielectric, and Pd (30 nm) as source and drain contacts. (g) Contact interface for s-SWNT film that was etched for time exceeding 180 s.



exceeding 180 s. The s-SWNTs in the electrode pattern area were completely etched except some destroyed structure on the edge of s-SWNTs in the channel. The graphene-like structure is considered to improve the contact and effectively enhance the carrier injection efficiency. The similar performance of the etched devices for 20 s and 180 s could be ascribed to the similar contact model between electrode and s-SWNT, because both of which are helped by the graphene-like fragments. As mentioned above, the photoresist is etched faster than the s-SWNTs. Therefore, the s-SWNTs near the edge of the photoresist are continuously exposed and converted into graphene-like fragments, giving rise to better contact. As a consequence, the performance of the s-SWNT TFTs is optimised after 180 s plasma etching and thereafter remains almost constant. Due to the similar good contact formed in TFTs etched for 20 s and 180 s, the resulting performance of TFTs reaches to the maximum.

## Conclusions

In summary, we have fabricated s-SWNT TFTs *via* controllable etching treatment to enhance the contact between the s-SWNTs and electrodes and thus improved the device performance. The average contact resistance was reduced more than threefold to 3.36 k $\Omega$  and the mobility reached up to 91 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>, which outperforms the results reported for similar devices. Furthermore, the on/off ratio was higher than 10<sup>6</sup> and the average current density was 24  $\mu$ A  $\mu$ m<sup>-1</sup>. The enhancement of the device can be attributed to the removal of organic residues on the s-SWNT surface by oxygen-etching, which resulted in good contact between the s-SWNTs and metal electrode. Plasma etching treatment may serve as a simple and controllable way to fabricate high-performance s-SWNT thin film devices, and provide significant opportunities for large-area and low-cost electronic applications.

## Conflicts of interest

There are no conflicts to declare.

## Acknowledgements

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