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Graphene/Si Schottky solar cells: a review of recent advances and prospects

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Graphene has attracted tremendous interest due to its unique physical and chemical properties. The atomic thickness, high carrier mobility and transparency make graphene an ideal electrode material which can be applied to various optoelectronic devices such as solar cells, light-emitting diodes and photodetectors. In recent years, there has been a growing interest in developing graphene/silicon Schottky junction solar cells and the power conversion efficiency has reached up to 15.8% with an incredible speed. In this review, we introduce the structure and mechanism of graphene/silicon solar cells briefly, and then summarize several key strategies to improve the performance of the cells. Finally, the challenges and prospects of graphene/silicon solar cells are discussed in the development of the devices in detail.

1. Introduction

As the population continuously grows, the rising environmental pollution and increasing energy demand have created a surge in exploring renewable, clean energy sources other than fossil fuels. Among the various renewable energy sources such as tidal energy, wind energy and so on, solar cells have drawn tremendous interest and are being investigated intensively to meet the greatly increasing demand for clean energy.¹ To date, solar cells have experienced generations with the core photovoltaic conversion material ranging from p–n junction solar cells, dye solar cells (DSCs), and perovskite solar cells. Dye solar cells (DSCs) have drawn tremendous interest due to their low-cost, environment friendly nature and low-energy manufacturing process,² but the maximum efficiency is only 13.0%.³ Solid state perovskite solar cells, have reached efficiency as high as 22.1%,⁴ but there is a great challenge that it has a low stability, the cells currently only last for months outdoors, whereas silicon solar panels are usually guaranteed to work for at least 25 years. Although various light-harvesting materials have been explored and developed in solar cells, so far, silicon is the most representative because of its broad absorption band. The efficiency of a conventional silicon solar cells based on p–n junction could exceed 26.6% as reported in 2017.⁵ In addition to its high power conversion efficiency, the dominant role of p–n junction-based silicon solar cell in the market results from highly commercialized fabrication techniques. However, these involved techniques usually require high temperature ion diffusion and expensive ion implantation processes to introduce dopants into

silicon substrate.⁶ Despite of the complicated processes, they are of high pollution and energy-consumption which is contradictory to the aim of clean energy. In addition, the high temperature processes also result in decrease of Si minority carrier life time, which greatly deteriorates the solar cell performance.⁷ Compared to the traditional solar cells, Schottky junction solar cells have the merits of easy fabrication and low cost,⁸ it can be formed by depositing metal film or transparency electrode on semiconductor wafer, two-dimensional materials have also been introduced as transparent electrodes in recent years.

2. Graphene–Si Schottky junction solar cells

Schottky junction solar cells, fabricated by directly depositing a thin layer of metal or transparency electrode on a moderate doped semiconductor wafer, are receiving much attention in photovoltaic field.⁹ Compared to the traditional p–n junction solar cells, the Schottky junction solar cells have the merits of easy fabrication and low cost.⁸ However, the metal layer should be thick enough to form a junction in a conventional Schottky junction solar cell, which will have a negative effect on the absorption of the solar radiation.¹⁰ Indium tin oxide (ITO), as an alternative for the metal layer, has been chosen by researchers.¹¹ However, the limited resource of indium will lead to high production cost, and the brittle nature of ITO will limit its application for flexible devices.¹²

Graphene, the model of two-dimensional material, was first fabricated by mechanical exfoliation out of graphite by Geim and Novoselov in 2004.¹³ As a single layer of carbon atoms packed together with hexagonal structure,¹⁴ it is a promising material with many unique characteristics. The transparency

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can reach to 97.7% in the near-infrared and visible region,^{15,16} the excellent optical properties of graphene can effectively avoid the loss of photons. The carrier mobility achieves to $10^4 \text{ cm}^2 \text{ v}^{-1} \text{ s}^{-1}$ at room temperature,^{17,18} all the superior optical and electronic properties render graphene a promising transparent electrode in organic light-emitting diodes,^{19,20} sensors^{21,22} and solar cells.^{23,24} Additionally, graphene is not only flexible but abundant in the globe,^{25,26} so it has drawn great attention to replace indium tin oxide (ITO) and fluorine tin oxide (FTO) for producing low-cost devices.^{27,28} Its tunable work function allows graphene to be served as either n-type or p-type electrodes.^{29–31} Moreover, graphene is generally superior in its great mechanical strength,³² environment stability,³³ and crystalline quality in large-scale synthesis compare with other two-dimensional materials, such as MoS_2 , hexagonal boron nitride (h-BN) and so on.^{34,35}

In consideration the excellent optical and electrical properties of graphene, there is a great interest in developing graphene/Si Schottky junction solar cells in recent years. In 2010, the first graphene/n-Si Schottky junction solar cell was reported by Zhu *et al.*²³ They showed that graphene film can be combined with Si to form efficient solar cells. In this kind of solar cells, graphene not only acts as a transparency electrode, but also plays an important role in photo-carriers separation and transport.²³

In this review, the structure and mechanism of the graphene/Si solar cells are exhibited. Afterwards, several key ways which can enhance the power conversion efficiency (PCE) are introduced in detail. Lastly the challenges and prospects are discussed in the development of the devices, all of which may provide researchers a better understanding of the graphene/Si solar cells.

3. Structure and mechanism of graphene/Si solar cells

The structure of graphene/Si solar cells is illustrated in Fig. 1a. The SiO_2 layer is wet-etched with pure or buffered HF solution

from Si wafer to expose a square window which defines the active area of the solar cell. The front contact is prepared by photolithography and metal deposition, then single layer, bilayer or multilayer graphene is directly transferred onto the top of the patterned substrates *via* a solution method to create a conformal coating with the front contact and underlying n-Si. In such devices, a built-in electric field is established due to work function difference of the two materials, graphene and n-type Si adjust their Fermi level to the common position (Fig. 1b).^{36,37} When incident light penetrate into the junction, the electron-hole pairs are created in Si and then carriers are separated by the built-in electric field. The electrons drift toward the n-type Si direction and holes drift to the graphene side, resulting in the output of current and power. In graphene/Si solar cells, the built-in potential Φ_{SBH} is determined by the difference between the work function of graphene Φ_{G} and the electron affinity of n-Si χ_{Si} .³⁸ Since the work function of graphene is tunable, we have more freedom in device design to optimize the separation and collection of the electrons and holes in graphene/Si solar cells, and result in larger potential drop across the depletion width, all of which can allow a more efficient collection of carriers.

Open-circuit voltage (V_{OC}), short-circuit current (I_{SC}), filling factor (FF) and PCE of the solar cells are the main parameters to evaluate the performance of solar cells. All of them can be obtained from light J - V curves when the devices are tested with a solar simulator under AM 1.5 conditions. When the graphene/Si solar cell is open-circuited, the separation of photon-generated carriers will create V_{OC} . When the solar cell is short-circuited, the extracted photogenerated carriers can transit through the external circuit, generating I_{SC} . At $V = V_{\text{OC}}$, these two currents will cancel each other and result in a zero net current. The PCE (η) of a solar cell is defined as the incident power which is converted to electricity and it can be expressed as below:

$$\eta = \frac{\max(I \times V)}{P_{\text{in}}} \quad (1)$$

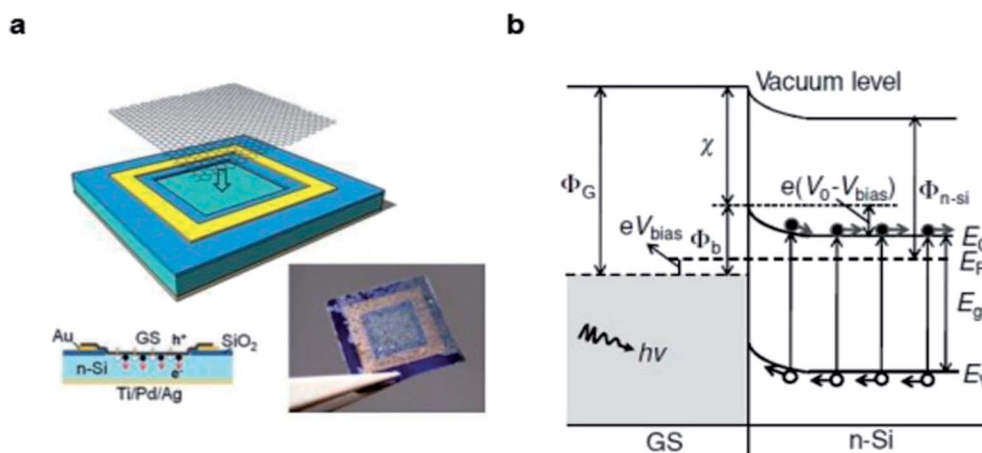


Fig. 1 Characterizations of the graphene/n-Si Schottky junction. (a) Schematic illustration of the device configuration. (b) Energy diagram of the forward-biased graphene/n-Si Schottky junction upon illumination. Reproduced from²³ with permission from Wiley.



where P_{in} is the power of incident light. FF of a solar cell representing the characteristic of the maximum power of the devices under optimal load, which is always less than 1. It can be defined as:

$$FF = \frac{V_m I_m}{V_{OC} I_{SC}} \quad (2)$$

The non-linear dark J - V characteristics of the graphene/Si solar cells can be described by the formula as follows:³⁹

$$J = J_S \left[\exp\left(\frac{eV}{nk_B T}\right) - 1 \right] \quad (3)$$

where J is the current density across the interface of graphene/silicon, J_S is the reverse dark-saturation current density, e is the elementary charge, V is the applied voltage, n is the diode ideality factor, k_B is the Boltzmann constant, T is the absolute temperature. The ideality factor n reflects the quality of the contact interface between graphene and silicon of Schottky junction and describes the carrier recombination of the interface,⁴⁰ n and J_S of the solar cells can be obtained by fitting the $\ln J$ - V curve.

According to Schottky–Mott mode, the metal with work function (Φ_{metal}) higher than the electron affinity (χ_e) of the a certain semiconductor can create a Schottky barrier height,⁴¹ $\Phi_{SBH} = \Phi_{metal} - \chi_e$, which is helpful to produce a relatively large built-in field to block the recombination of holes and electrons. Similarly, the junction barrier height between graphene and silicon also observes the rules. The barrier height Φ_{SBH} can be obtained by the thermal emission theory:⁴²

$$J_S = A^* T^2 \exp\left(\frac{-e\Phi_{SBH}}{k_B T}\right) \quad (4)$$

where A^* is the effective Richardson constant ($252 \text{ A cm}^{-2} \text{ K}^{-2}$ for n-Si). The barrier height Φ_{SBH} is about 0.75 eV, namely the difference between Φ_G and χ_e ($\Phi_{SBH} = \Phi_G - \chi_e = 4.8 - 4.05 = 0.75 \text{ eV}$).²³

4. The optimization of graphene/Si solar cells

The PCE of the first graphene/Si solar cell is only about 1.5%, far below the requirements for industrial application.²³ The poor performance of the solar cells can be attributed to: (1) the pristine single-layer graphene has low work function ($\sim 4.4 \text{ eV}$) and large sheet resistance (a few kohms).²⁰ The low work function can decrease the barrier height, which will lower the built-in electric field and prevent electron–hole pairs separating,⁴³ while the large sheet resistance can lead to large series resistance. (2) The absorbed photon number is reduced by the planar Si wafers which is 30–40% for visible light (350–800 nm).⁴⁴ After graphene/Si solar cells was reported in 2010,²³ lots of methods are developed to tune the work function and conductivity of graphene and the reflectance Si wafers, such as chemical doping,^{45–50} increasing layer number of graphene^{51,52} and introducing an interlayer,^{53–59} all of which can help to optimize the performance of solar cells significantly.

4.1 Optimization of the work function and conductivity of graphene

4.1.1 Doping of graphene. Doping of graphene can remarkably improve the performance of graphene/Si solar cells since it has been deemed to be the most effective way which would not only improve the electrical conductivity of graphene but also tune its work function.⁴⁷ Up to now, different dopants such as HNO_3 , SOCl_2 , and H_2O_2 , HCl , bis(trifluoromethanesulfonyl)amide (TFSA), Au or Pt nanoparticles have proved to effectively improve the performance of solar cells.^{45–50} Among them, HNO_3 doping is deemed to be the most commonly used method.⁶⁰ Feng *et al.* demonstrated that after immersing the device in HNO_3 vapor for several seconds, (Fig. 2a and b). The PCE of the solar cell boosted from 2.9% to 4.35% under AM1.5 illumination with the V_{OC} , J_{SC} , FF increased to 495 mV, 17.22 mA cm^{-2} and 51%, respectively.⁴⁷ The series resistance (R_s) of the solar cell also decreased from $6.11 \text{ } \Omega \text{ cm}^2$ to $4.07 \text{ } \Omega \text{ cm}^2$ after HNO_3 treatment. They attributed the obvious performance enhancement to the heavy p-type doping by HNO_3 , and the V_{OC} increased after treatment due to the work function upshift of graphene (Fig. 2e); the increase in J_{SC} , as well as the decrease in R_s , demonstrated an increase in graphene conductivity.

Similar to HNO_3 , SOCl_2 and H_2O_2 can also lead to the improved performance of solar cells. Four volatile oxidants HNO_3 , SOCl_2 , HCl , and H_2O_2 were employed by Cui *et al.* to dope the graphene films in graphene/Si solar cells.⁴⁸ They placed the assembled graphene/Si solar cells above a vial containing HNO_3 (65 wt%), SOCl_2 (99.5 wt%), HCl (36 wt%), or H_2O_2 (30 wt%) for 1 min. It has been shown that the PCE could be remarkably enhanced after being treated by either of the four volatile oxidants. The PCE enhancement of the volatile oxidant-treated cells are primarily contributed by the improvement in FF and V_{OC} . Among all the four volatile oxidants, the PCE enhanced by SOCl_2 doping showed the best improvement (Fig. 2d). A solar cell with an initial PCE of 2.58% ($V_{OC} = 412.10 \text{ mV}$, $J_{SC} = 17.65 \text{ mA cm}^{-2}$, FF = 35.42%) could be increased to 5.95% ($V_{OC} = 547.78 \text{ mV}$, $J_{SC} = 17.92 \text{ mA cm}^{-2}$, FF = 60.64%) after SOCl_2 doping treatment (Fig. 2c). They also investigated the stability of the volatile oxidant-treated cells by keeping them in ambient conditions for 8 days. Due to the volatilization of the volatile oxidant, the PCE of the volatile oxidant treated cells decreased with storage time in the air, and the results also showed that solar cells treated by SOCl_2 and HCl have relatively better stability than that of HNO_3 and H_2O_2 ones.

In addition to the volatile oxidants, doping with organic polymer dopant TFSA is also an effect way to improve the performance of graphene/Si solar cells. Miao *et al.* reported that TFSA could increase the conductivity of graphene significantly by p-doping.⁴⁵ They demonstrated that the PCE of the device doping with TFSA was increased to 8.6% from the original (undoped) PCE of 1.9% and the J_{SC} increased from 14.2 to 25.3 mA cm^{-2} , V_{OC} increased from 0.43 to 0.54 V, respectively. Fig. 3a and b notes that p-type doping can increase the work function of graphene, which will lead to an improvement of Φ_{SBH} , creating a larger potential drop across the depletion width and



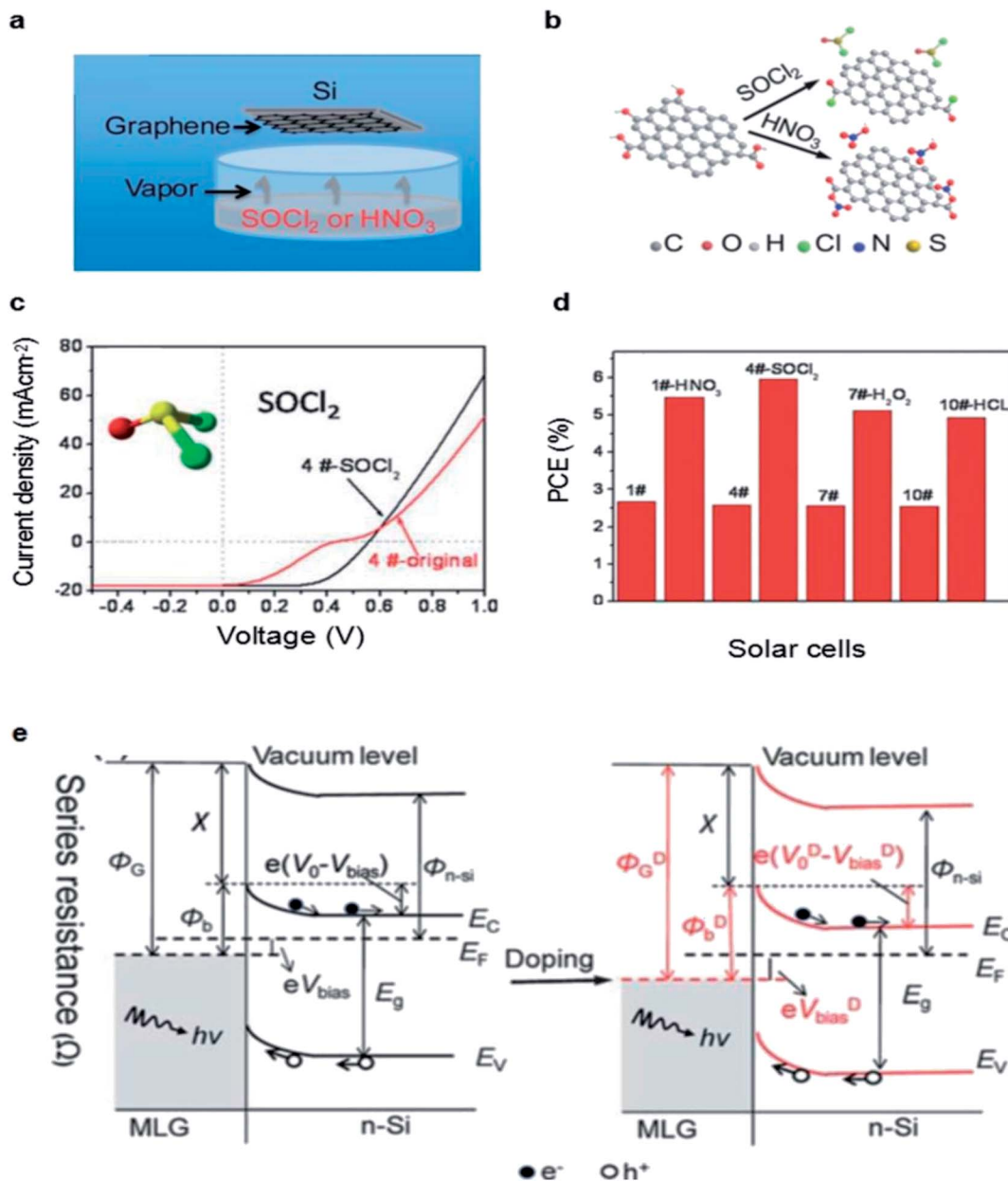


Fig. 2 (a) Schematic illustration of the vapor doping process. The device was exposed to the SOCl₂ (or HNO₃) vapor for a few seconds and chlorine (nitrate) anions were doped on the films. (b) Scheme of the doping mechanism. (c) *J*-*V* characteristics of the solar cell before (red curve) and after (black) infiltration of dilute SOCl₂. (d) Light *J*-*V* curves and PCE of solar cells before and after volatile oxidant treatment. (e) The band schematics at the MLG/Si interface before and after doping. Reproduced from⁴⁶ and⁴⁸ with permission from The Royal Society of Chemistry.

allowing a more efficient of electrons and holes separation and collection. Due to the p-type doping effect, the enhanced work function of the graphene therefore leads to an decrease the sheet resistance and improvement of the barrier height in graphene/n-Si junction.⁶¹ The electrical and optical properties of monolayer graphene after TFSA doping were also investigated by Lai *et al.*⁶² The results showed that doping with TFSA can efficiently lower the sheet resistance of graphene because the resistance decreased from 1200 Ω cm² to 300 Ω cm² rapidly, and the UV-visible transmittance spectra also presented that the TFSA preserved graphene's optical properties without any significant reduction that single layer graphene still remained

extremely high transmittance after chemical doping (Fig. 3c), all of which make it possible to fabricate graphene-Si solar cells with superior electrical-optical properties.

In addition to volatile oxidants and TFSA, several groups also attempted to improve the PCE of graphene/Si solar cells by metal nanoparticles (NPs) such as Au, Pt.^{49,60,63,64} Introducing nanoparticles not only improves the work function and decreases the sheet resistance of graphene, but can possibly increase the light absorption and PCE of the devices.⁶⁵ For example, Kim and the coworkers deposited Au layer on graphene by radio frequency sputtering and annealed in nitrogen atmosphere to gain Au NP-doped graphene-Si solar cells.⁶⁴ As



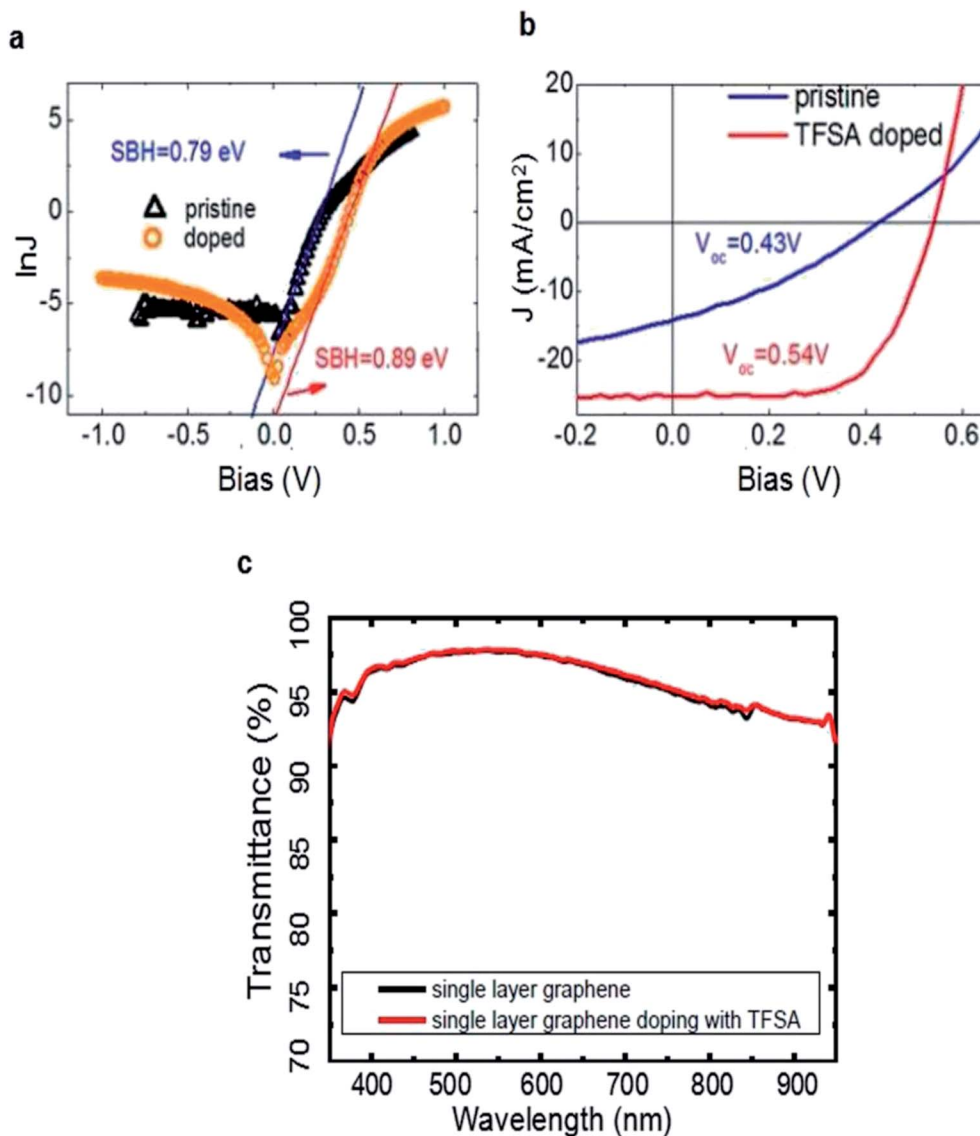


Fig. 3 (a) J - V characteristics in the semi-logarithmic scale. (b) Amplified J - V characteristics of graphene/ n -Si diodes. (c) The transmittance spectra of graphene. (a and b) Reproduced from⁴⁵ with permission from The American Chemical Society (c) reproduced from⁶² with permission from IEEE Xplore.

displayed in Fig. 4a, the discrete spherical nanoparticles are uniformly distributed on the graphene surface. In the XPS spectra, shown in Fig. 4b, indicating Au NPs are successfully doped into graphene. The PCE was increased from 3.52% to 7.51% after doping of Au nanoparticles. It was further enhanced to 10.69% by co-doping it with TFSA.

Liu *et al.* also reported a structure of Au nanoparticles/graphene/Si solar cells by incorporating Au nanoparticles onto the graphene layers to improve the device performance.⁴⁹ Different with Kim *et al.*, they deposited Au thin layer with various thickness onto the monolayer graphene (MLG) and few-layer graphene (FLG), then annealed under nitrogen flow to gain Au NPs with different sizes. Fig. 4c showing that the V_{OC} increases monotonically with Au thickness for the two batches of devices, and the V_{OC} of the solar cells with multilayer graphene (0.40–0.45 V) is much higher than that of single layer

devices (0.26–0.42 V), which is in good agreement with the previous works in which the V_{OC} difference is attributed to the increased work function of graphene with more layers.⁵¹ Unlike V_{OC} , the value of J_{SC} is found to exhibit a first-rise then decrease behavior of which the critical Au thickness was 9 nm, and FF showed a similar tendency as J_{SC} (Fig. 4d). Due to the increased V_{OC} , J_{SC} , and FF after incorporating Au nanoparticles, a maximum PCE of 7.34% has been obtained for the Au nanoparticles/multilayer graphene/Si cells when the Au thickness is 9 nm, which is more than three times of the original value (Fig. 4e). In this work, Au-doping gives rise to an enhanced built-in electric field and thus an improved V_{OC} in the solar cell, meanwhile, the enhanced electrical conductivity of graphene and the reduced R_s of cell lead to a higher FF and J_{SC} . This work provides a route for developing stable, and highly efficient graphene/Si solar cells.



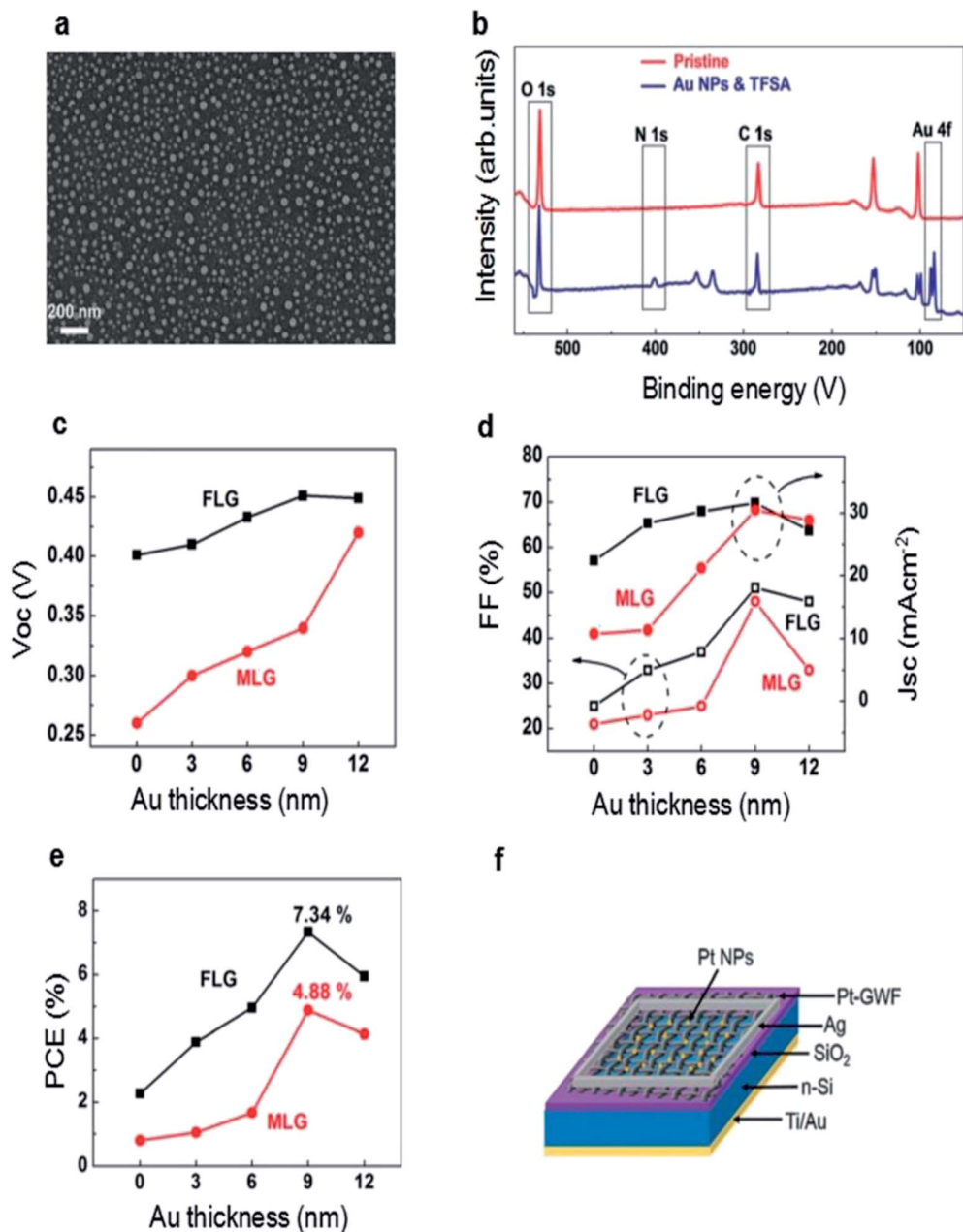


Fig. 4 (a) SEM image of the surface of Au NPs/TFSA-doped graphene. The scale bar indicates 200 nm. (b) XPS spectra of graphene layers with and without Au NPs/TFSA dopants. (c) V_{OC} . (d) J_{SC} and FF, (e) PCE as functions of the Au thickness for both Au NPs/MLG/Si and Au NPs/FLG/Si solar cells. (f) Schematic diagrams of Pt-GWF/n-Si solar cell. (a and b) Reproduced from⁶⁴ with permission from The Royal Society of Chemistry. (c–e) Reproduced from⁶⁰ with permission from American Institute of Physics. (f) Reproduced from⁶³ with permission from The Royal Society of Chemistry.

Kang *et al.* developed an effective and practical method to deposit Pt nanoparticles on graphene woven fabrics (GWF) to improve the performance of graphene/silicon solar cells.⁶³ (Fig. 4f) The deposition of Pt NPs increased the work function of GWF and also reduced the sheet resistance of GWF. The increased work function of GWF resulted in an enhanced built-in electric field and thus an improved V_{OC} , whereas the enhanced conductivity led to a higher J_{SC} , thereby improving the PCE of graphene/silicon solar cells. To further enhance the PCE of the solar cells, they also coated solid electrolyte ($HNO_3 + PVA$) onto the Pt NPs–GWF/n-Si solar cells which serves as

photoelectron chemical channel, and the PCE was improved to 10.02%. In comparison, the efficiency of the solar cell that was only coated with solid electrolyte (without Pt NPs deposition) was 7.51%. The results suggest that the deposition of Pt NPs can improve the performance of the GWF/n-Si solar cells significantly. But it is worth noting that Pt is expensive compared with other metals, so it is necessary to find cheaper metal nanoparticles that can enhance the efficiency of graphene/Si solar cells in the future.

4.1.2 Controlling the layer number of graphene. While chemical doping is effective to the performance of the



graphene/Si solar cells, it was found that the layer number of graphene also play a critical role in determining the PCE of solar cells.

The effect of the number of graphene layers on the performance of graphene/n-Si solar cells were investigated by Li *et al.*⁵¹ They found that the V_{OC} improved monotonically when the layer number of graphene increased (Fig. 5b). The reason is that the graphene work function could be tuned by its layer number, leading to improved barrier height of graphene/n-Si Schottky junction. Fig. 5 shows that with increasing graphene layer number, J_{SC} first linear increases when the layer number of graphene less than 4, and then decreases when it further increases. They explained that the sheet resistance (R_S) decreases when the layer number is less than 4, and then it has an opposite trend when the layer number more than 4. In addition, with the increase of layer number, the transmission of graphene reduced. In consideration of the sheet resistance and the transmittance, the 4-layer graphene solar cells exhibit the best performance.

Ihm *et al.* reported that the layer number-dependent trend is reversed for graphene/p-Si solar cells.⁵² They found that V_{OC} decreased as the layer number of graphene increased. Similar in graphene/n-Si junction solar cells, when the layers increase, the work functions of graphene shows a sequential increase accordingly. And in the graphene/p-Si cell, $\Phi_B = \chi_{Si} - \Phi_G$, so the barrier height of graphene/p-Si reduces, while improves that of graphene/n-Si junction solar cells. Jiao *et al.* fabricated PMMA/bilayer graphene/CNWS-Si solar cell with a maximum PCE of 8.9%.⁶⁶ A PMMA/few-layer graphene could be achieved by a layer-by-layer (LBL) transfer method^{67,68} and poly(methylmethacrylate) (PMMA) film was kept after transferring graphene, serving as an

efficient antireflection layer. As the layer number of graphene increased from 1 to 4, the transmittance decreased from 96% to 87% in the visible range, while the R_S declined from 458 to 183 $\Omega \text{ cm}^2$. When the R_S (337 $\Omega \text{ cm}^2$) and transmittance (94.5% at 550 nm) had the best tradeoff with 2 layers, the optimal PCE of 8.9% was attained with J_{SC} , V_{OC} and FF values of 29.1 mA cm^{-2} , 0.44 V, and 69.8%, respectively. When the layer number increased to 4, the FF improved to 71.8% due to low R_S , and the J_{SC} decreased to 25.1 mA cm^{-2} as a result of low transmittance, the efficiency declined to 7.6% (Table 1).

4.2 Optimization of the reflectivity of silicon

4.2.1 Nano- and microstructures for light trapping.

Because of its high reflection, the planar Si surface has a high absorption loss about 30–40% in the visible range.⁶⁹ In order to enhance the light harvesting, the silicon wafers have been engineered to different nano- or microstructures, such as pyramids,^{70,71} nanowires,^{72,73} nanopillars^{74–76} and so forth. The use of Si nano- or microstructures can significantly enhance light harvesting capability because of the strong light trapping effect which suppresses the reflection of the junction surface in the visible and near-infrared region, and can also achieve higher charge-collection efficiency.^{77,78} Therefore, it provides a potential route to fabricate high-efficiency graphene/Si solar cells based on Si nano- or microstructures. In recent years, nano- or microstructures to improve PCE of this kind of solar cells have been studied widely. For example, Fan *et al.* prepared silicon nanowire (SiNW) arrays by a silver-assisted etching method,^{79,80} and then transferred the graphene film directly on the n-silicon nanowire (SiNW) arrays.⁸¹ It is clearly shown in Fig. 6a and b that the SiNWs impart a significant reduction of

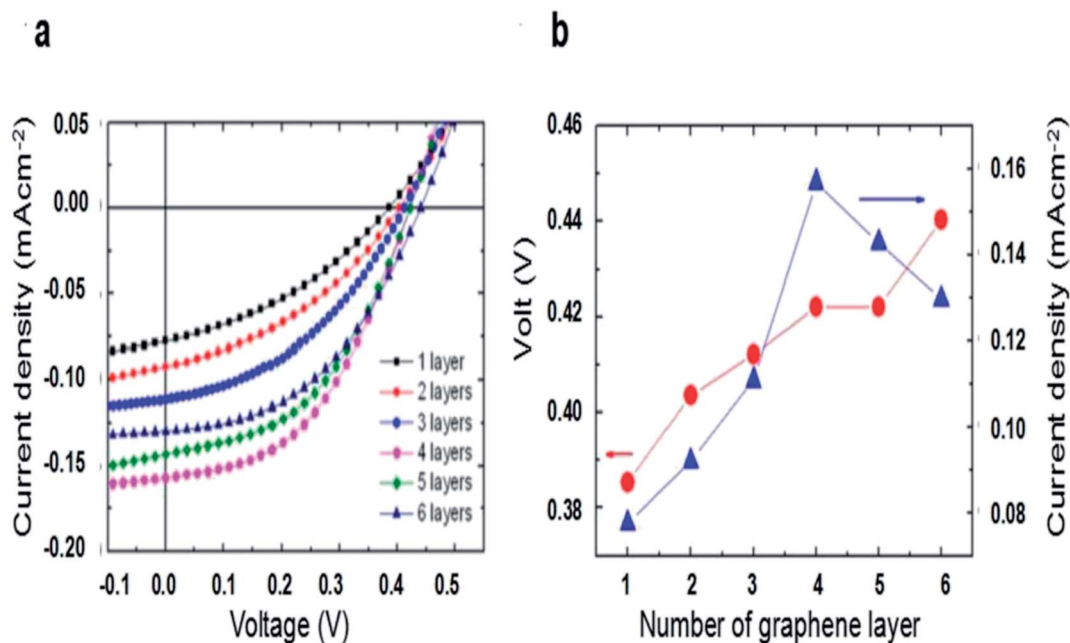


Fig. 5 (a) Current–voltage curves of solar cells based on graphene with 1, 2, 3, 4, 5, and 6 layers under illumination of 730 nm LED (2 mW cm^{-2}). (b) The open-circuit voltage and short-circuit current of solar cells measured as functions of the number of graphene layers. (a and b) Reproduced from⁵¹ with permission from American Institute of Physics.



Table 1 Summary of engineering strategies and corresponding PCE values of the graphene/silicon Schottky solar cells studied by different research groups over the last ten years to date

| Method | Cell structure | PCE (%) | Year | Ref. |
|-----------------------|-------------------------------------------------------------|---------|------|------|
| Original | Graphene/n-Si | 1.65 | 2010 | 23 |
| Chemical doping | HNO ₃ /graphene/n-Si | 4.35 | 2011 | 47 |
| | SOCl ₂ /graphene/n-Si | 5.95 | 2013 | 48 |
| | TFSA/graphene/n-Si | 8.6 | 2012 | 45 |
| | Au NPs/graphene/n-Si | 7.34 | 2014 | 49 |
| Nanoparticle doping | Pt NPs/GWF/n-Si | 10.02 | 2016 | 63 |
| | 4 layer graphene/n-Si | 1.48 | 2014 | 51 |
| Layer number | SOCl ₂ /graphene/SiNW array | 2.86 | 2011 | 81 |
| | HNO ₃ /graphene/SiPA | 3.55 | 2011 | 75 |
| | AuCl ₃ /graphene/SiHA | 10.4 | 2013 | 84 |
| | HNO ₃ /TiO ₂ /graphene/n-Si | 14.5 | 2013 | 44 |
| Antireflection layer | PMMA/bilayer graphene/CNWs/Si | 8.9 | 2016 | 66 |
| | HNO ₃ /PMMA/graphene/n-Si | 13.34 | 2016 | 85 |
| | MgF ₂ /ZnS/graphene/n-Si | 14.6 | 2018 | 87 |
| | Graphene/SiO ₂ /n-Si | 12.4 | 2015 | 53 |
| Interface engineering | HNO ₃ /4-layer graphene/P3HT/CH ₃ -Si | 10.56 | 2013 | 59 |
| | Au NPs/HNO ₃ /graphene/h-BN/n-Si | 10.93 | 2016 | 58 |

the reflectance. Upon illumination, the graphene/SiNW device displayed fairly good photovoltaic response, achieving a PCE of 1.25% with a V_{OC} , J_{SC} , FF of 0.462 V, 9.2 mA cm⁻², 30% respectively. It can mainly attribute to two reasons. First, the SiNWs provide direct and fast pathway to increase the electrical-hole collection and transport. Second, the SiNWs can improve light absorption and suppress the reflection of the junction surface in the visible and near-infrared region. Then SOCl₂ vapor was used to dope the graphene film to further improve the PCE of the solar cells to 2.86%. Feng *et al.* introduced pillar-array-patterned silicon (SiPA) by photolithography and inductive couple plasma (ICP) etching^{82,83} to enhance the light harvesting of the solar cells, showing energy conversion efficiencies of 1.96%.⁷⁵ Then HNO₃ was used to dope graphene film and the energy conversion efficiency of graphene/SiPA Schottky solar cell improves to 3.55%. Xie *et al.* demonstrated the construction of graphene/micro-hole array (SiHA) devices with enhanced performance and stability (Fig. 6c and d).⁸⁴ They fabricated micro-hole array with a smooth surface *via* photolithography and RIE (reaction ion etching) possesses, and by controlling the etching time, the depth of the holes can be adjusted to tune the light harvesting of the SiHA. The light harvest capability of the solar cells were improved significantly when the hole depth increase to 12.8 nm and the maximum PCE was improved to 10.4% when combined the micro-hole array with AuCl₃ doping. The promotion of the J_{SC} and EQE (external quantum efficiency) can be ascribed to the enhanced optical absorption of the SiHA with deeper holes (Fig. 6e and f). Additionally, the device showed remarkable stability and exhibited a high performance even storing in air for about 3 months. In view of the stability, nano- or microstructures can be a feasible technique for high efficiency graphene/Si solar cells.

4.2.2 Anti-reflection coating. In addition to the nano- or microstructures can reduce the high reflectance of planar Si, some anti-reflection coating of transparent materials such as TiO₂,⁴⁴ PMMA,^{66,85} PDMS,⁸⁶ MgF₂/ZnS^{87,88} are also options to

decrease the optical reflectivity of the solar cells. The antireflection effect is obtained when a suitable refractive index and film thickness are chosen.⁴⁴ The antireflection layer is transparent to visible light and the refractive index (n_{ARC}) is between Si ($n_{Si} \approx 4$) and air ($n_{air} \approx 1$) approximately correlated by $n_{ARC} = (n_{Si}n_{air})^{1/2}$,⁸⁹ serves as a planar antireflection layer to suppress reflected light from polished Si surface. On the other hand, the thickness of the antireflection layer should correspond to the range of visible light according to $d = \lambda/4n_{ARC}$,⁹⁰ thus it could serve as an effective antireflection layer.

Shi *et al.* introduced TiO₂ as an antireflection layer, the structure can be illustrated as sandwich structure with TiO₂ layer on the top, graphene in the middle, n-Si substrate at the bottom (Fig. 7a).⁴⁴ The efficiency of the control cell without TiO₂ antireflection layer is 8.9% after HNO₃ doping. Then TiO₂ colloidal solution was spin-coated on the window of graphene/Si solar cells to form a smooth and thin film. After coating TiO₂, the reflectance was lowered to about 10% in the visible region (500–800 nm). Consequently, the J_{SC} was increased significantly by 30% (from 23.9 to 32.5 mA cm⁻²). The resulting HNO₃ doped-TiO₂-graphene-Si cell showed a V_{OC} of 0.60 V, a FF of 73%, and an efficiency of 14.1% (Fig. 7c). Here, HNO₃ doping mainly improve the V_{OC} and FF, while TiO₂ coating significantly enhance the J_{SC} . After optimized with both routes, the power conversion efficiency of both of graphene/Si solar cell reached 14.1%. The device stability was studied after storing the solar cell for about 20 days in the ambient air. The results showed that the J_{SC} remained unchanged, V_{OC} dropped from 0.60 to 0.45 V and the FF reduced from 73% to 45%, resulting in a degradation of cell efficiency (from 14.1% to 6.5%). So it can be deduced that doping by volatile acid is indeed not stable and cell degradation occurs after loss of acid and doping effect. However, the antireflection effect by TiO₂ coating was excellent since the J_{SC} remained unchanged about 32 mA cm⁻² during the storage period. After that, they treated it by HNO₃ vapor again, the V_{OC} returned from 0.45 to



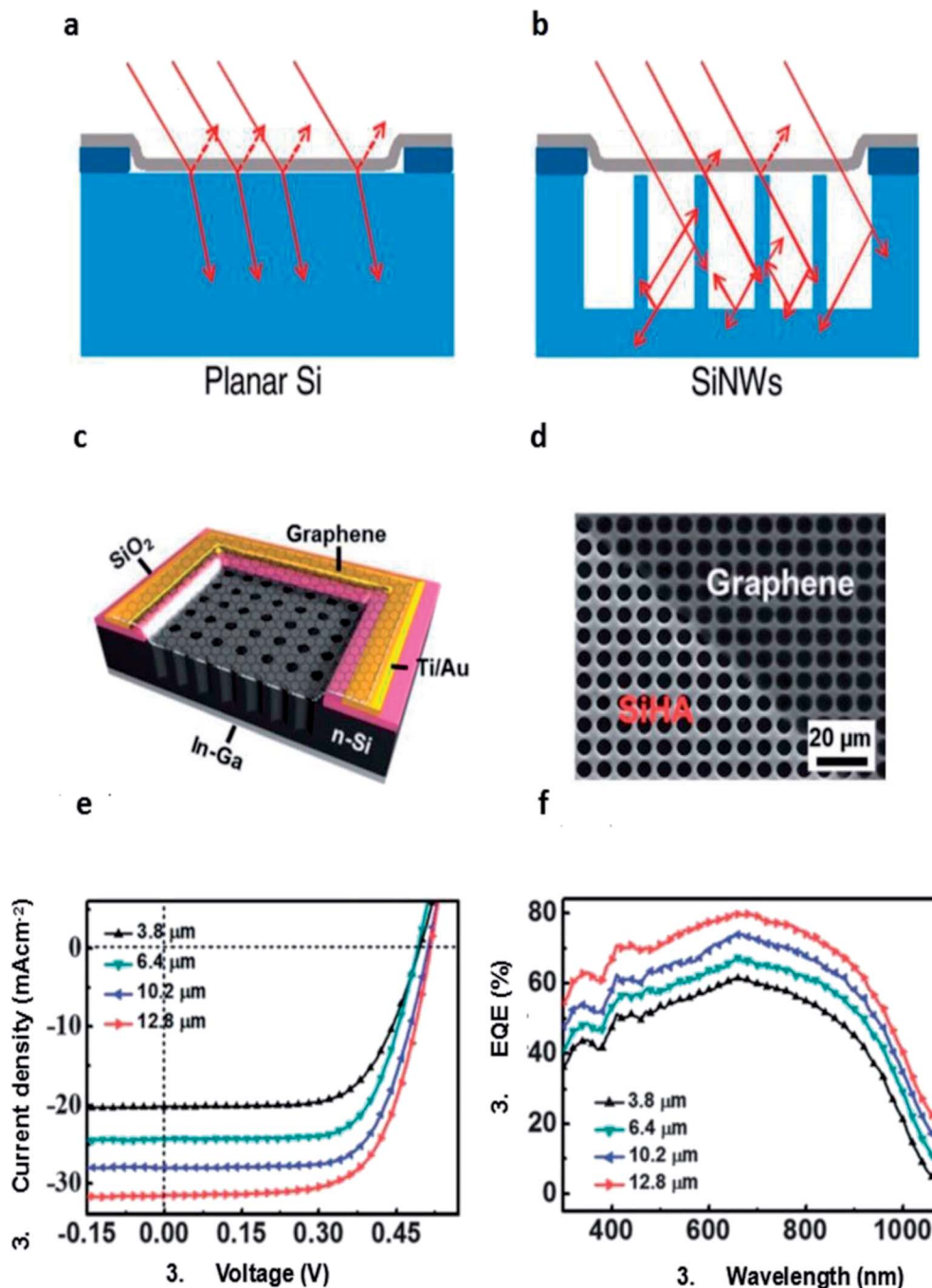


Fig. 6 Schematics diagrams of (a) graphene/planar Si and (b) graphene/SiNW junctions. (c) Schematic illustration of the graphene/SiHA Schottky junction solar cell. (d) Top-view SEM image of the graphene/SiHA device. (e) Photovoltaic characteristics and (f) EQE spectra of the graphene/SiHA Schottky junction solar cells with various hole depths. (a and b) Reproduced from⁸¹ with permission from The American Chemical Society. (c–f) Reproduced from⁸⁴ with permission from The Royal Society of Chemistry.

0.6 V, FF to 72%. The efficiency of the re-doping solar cell can recover to 14.5%, slightly higher than that before.

Gan *et al.* demonstrated a novel way of PMMA film as an antireflection layer on a graphene/silicon solar cell.⁸⁵ Traditionally, PMMA protective coating was regarded as a protection layer during graphene transfer process, and is usually removed after transfer process.^{91,92} In this work, the PMMA was remained on the graphene film acting as an anti-reflection layer. And the Fig. 7d shows that the transmittance at 550 nm measured was

96.0% with PMMA coatings. With the PMMA-coating and HNO₃ doping, the PCE reached up to 13.34%. They also compared the antireflection effect of TiO₂ with that of PMMA. The result showed that the efficiency of TiO₂-coated graphene/Si solar cells are significantly inferior to those of PMMA-coated graphene/Si solar cells.

Ding *et al.* fabricated multi-color graphene/Si solar cells by taking advantage of the combination of MgF₂/ZnS anti-reflection coating and graphene (Fig. 7b).⁸⁷ ZnS owns the



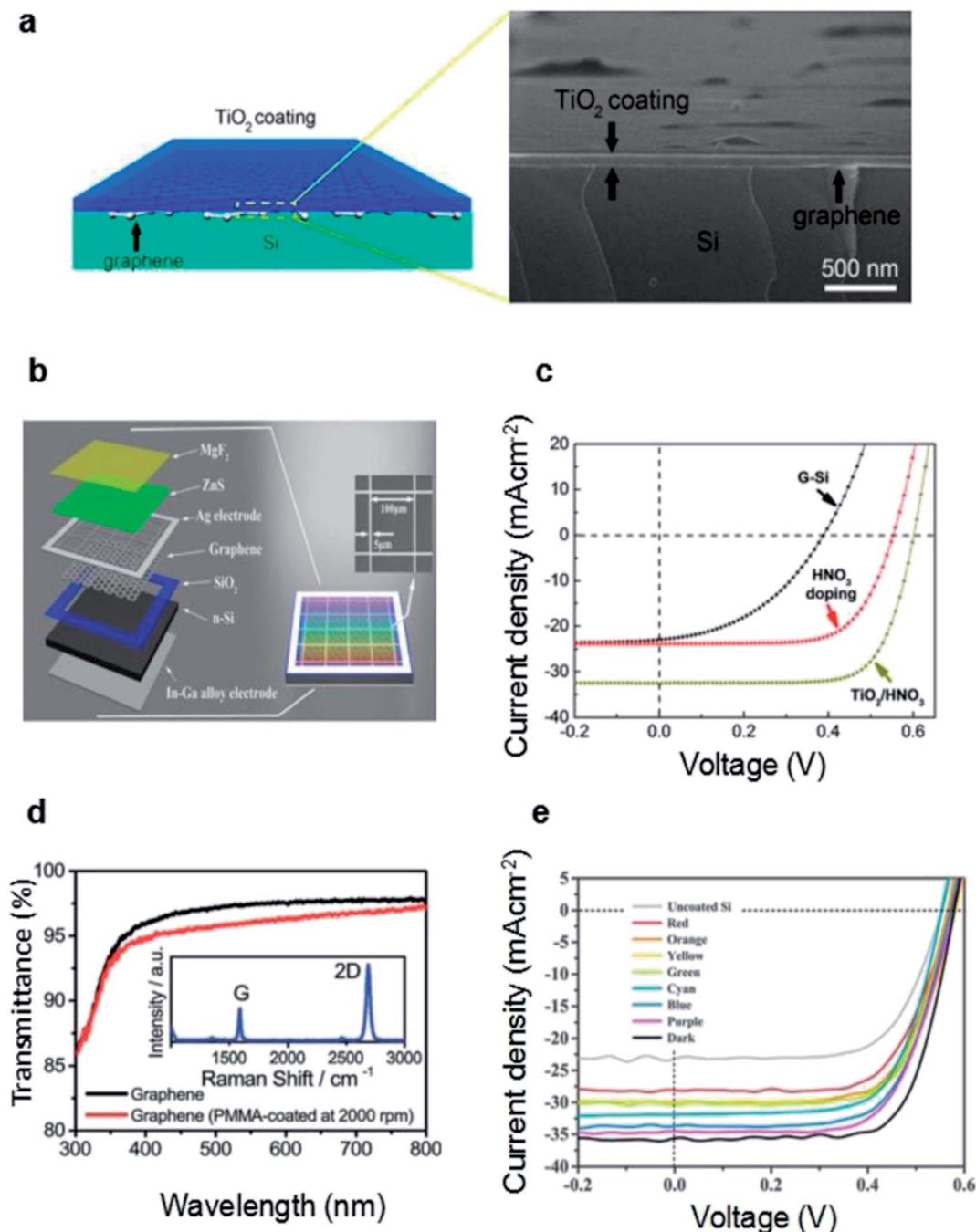


Fig. 7 (a) Illustration of the TiO₂/graphene/Si structure (left) and cross-sectional SEM image showing a uniform TiO₂ coating (thickness of ~65 nm) on top of the graphene–Si cell. (b) Schematic illustration of color graphene/Si heterojunction solar cells with double layer–MgF₂/ZnS films. Inset shows the SEM image of the Ag grid mesh. (c) *J*–*V* characteristics of an as fabricated graphene–Si solar cell, after HNO₃ vapor doping, and after TiO₂ coating (combined with HNO₃ doping), respectively. (d) Transmittance spectra of PMMA-removed and PMMA-coated (2000 rpm) graphene. The inset is a typical Raman spectrum of graphene on SiO₂/Si. Transmittance values at 550 nm are 96.0% and 97.4% for PMMA-coated and PMMA-removed samples, respectively. (e) *J*–*V* spectra of the devices with different structural colors measured under AM1.5, 100 mW cm⁻². *J*–*V* spectra of the devices without coating and with optimized anti-reflection coating were also presented for comparison. (a and c) Reproduced from¹⁰⁴ with permission from The American Chemical Society. (b and e) Reproduced from⁸⁷ with permission from Elsevier. (d) Reproduced from⁸⁵ with permission from The Royal Society of Chemistry.

maximum refractive index nearly 2.5 while MgF₂ owns the minimum refractive index of 1.4.^{93,94} Therefore, the double layer MgF₂/ZnS films can serve as efficient antireflection layers for solar cells. Meanwhile, they can enable the graphene/Si solar cells with high color saturation. In this work, Ding

et al. deposited MgF₂/ZnS in sequence onto the graphene/Si solar cell by thermal evaporation. Different structural colors can be achieved by carefully tuning the thickness of the films. And the coated graphene/Si solar cells exhibited respectable power conversion efficiency in the range of 10.7–13.2%. With



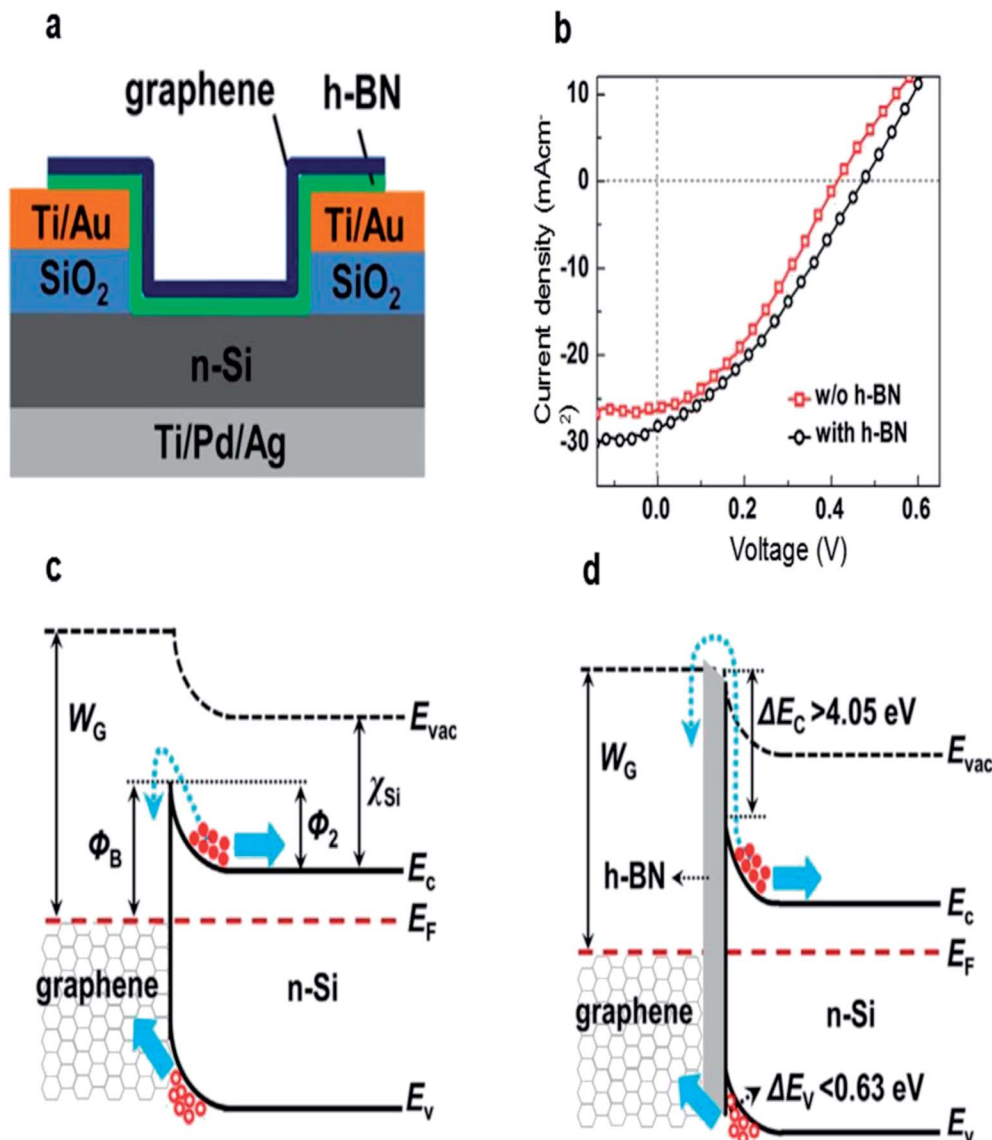


Fig. 8 (a) Schematic illustration of graphene/h-BN/Si solar cells. (b) Illuminated J - V characteristics of the graphene/Si solar cells with and without an h-BN interlayer. (c and d) Energy band diagrams of the graphene/Si Schottky junction solar cells without and with an h-BN electron blocking layer. Reproduced from ⁵⁸ with permission from Elsevier.

the optimized MgF₂/ZnS antireflection coating, PCE of the device can reach up to 14.6% (Fig. 7e). Their work demonstrates the great potential to fabricate colorful graphene/Si solar cells for future high-efficiency, low-cost and designable PV panels.

4.3 Interface engineering

The PCE of the graphene/Si solar cells can reach up to 10% after reflectivity optimization of silicon, but the efficiency is still much lower than the commercial silicon solar cells based on p-n junction, which is mainly restricted by the strong recombination of carriers along the interface due to the low barrier height. One solution to this problem is to introduce a passivation dielectric film between graphene and Si, thereby forming a metal-insulator-semiconductor (MIS) structure.^{95,96}

In a MIS solar cell, the additional insulating film can reduce the carrier recombination by suppressing diffusion of electrons from n-Si to graphene, and help the holes transfer to the graphene layer through tunnelling processes. Both mechanisms lead to improved V_{OC} and PCE of solar cells.

The best insulating layer materials include SiO₂,^{53,54} MoS₂,⁵⁵ Al₂O₃,⁵⁶ graphene oxide (GO),⁵⁷ h-BN,⁵⁸ 3-hexylthiophene (P3HT).⁵⁹ Several groups have demonstrated that the interface of the graphene/Si heterojunction play an important role in high efficient photoelectronic devices.⁵³⁻⁵⁹

Song *et al.* inserted SiO₂ as an electron blocking layer by exposing the substrate in air to allow the native oxide to regrow and layer thicknesses were optimized for achieving the best performance for graphene/Si solar cells.⁵³ In the conventional Si wafer pretreatment, the native silicon oxide was usually



completely etched before the graphene films were transferred. However, it has been demonstrated that leaving the silicon exposed to air for some time can allow a moderately thick oxide to regrow,^{97,98} yielding improved performance. To systematically study the effect of oxide thickness on the performance of the graphene/Si solar cells, the devices were left in air for varied duration before transferring graphene. The results showed that with the increasing oxide thickness from 5 Å to 15 Å, the efficiency can be improved from 7.9% to 12.4%. V_{OC} and FF were both improved further if the device left in air longer before transfer. For devices with 15 Å oxide, V_{OC} increased to 0.59 V and the FF increased to 0.74. Furthermore, TiO₂ antireflective coating (ARC) and AuCl₃ doping were also applied to the device, the overall efficiency can reach up to 15.6%.

P3HT organic layer were introduced by Xie *et al.* and Zhang *et al.* as an interfacial layer, and in their work, P3HT was inserted into the graphene–Si interface as an electron blocking layer, preventing electrons diffusion from n-Si to the graphene, minimizing the dark saturation current and a large leakage current.⁵⁹ P3HT layer was spin-coated on Si within the window area and the thickness of the P3HT layer was carefully controlled by adjusting the solution concentration. With the increasing of the P3HT thickness, the PCE improved from 4.24% with 0 nm-thick P3HT layer to 9.70% with a 10 nm-thick P3HT layer. Further increase of the P3HT thickness to 40 nm would result in an obvious performance degradation. Supplemented with the by doping and optimization of the graphene layer number, a PCE of 10.56% was achieved.

Meng *et al.* introduced a few-layer h-BN between graphene and n-Si to improve the performance of the solar cells.⁵⁸ As shown in Fig. 8a, the few-layer h-BN and the CVD-grown few-layer graphene were successively transferred onto the patterned substrate by LBL transfer process. After introducing the h-BN layer, the V_{OC} and PCE were increased from 0.412 to 0.474 V and from 3.75% to 4.40%, respectively in pristine cells (Fig. 8b). They explained that the h-BN can not only act as an effective electron-blocking/hole-transporting layer, but also has suitable band alignment with Si, and thus the interface recombination was suppressed and the open circuit voltage was significantly increased (Fig. 8c and d). In order to avoid the interface contamination and defects arising from the LBL process, they also transferred the directly grown graphene/h-BN heterostructure onto the graphene/h-BN/Si solar cell by one step method.⁹⁹ A maximum efficiency of 10.93% was achieved for the graphene/h-BN/Si solar cells by combining the directly grown graphene/h-BN heterostructures with co-doping of graphene with Au nanoparticles and HNO₃.

Ma *et al.* demonstrate the fabrication of high efficiency graphene/MoS₂/Si Schottky barrier solar cells with MoS₂ interlayers.⁵⁵ The inserted MoS₂ layers function as hole transport layer to facilitate the separation of electron–hole pairs as well as electron blocking layer to suppress the recombination at graphene/silicon interfaces. By optimizing the thickness of MoS₂ layers, a high photovoltaic conversion efficiency of 15.8% was achieved in graphene/MoS₂/Si solar cells, which is a new record reported so far.

5. Conclusions, challenges and prospects

In this article, we briefly reviewed the optimization approaches of the graphene/Si Schottky solar cells in recent years. These approaches could be categorized as optimization of (1) the work function and conductivity of graphene, (2) the reflectivity of silicon, (3) passivation and energy band engineering of the graphene/Si interface. Assisted with these approaches, the PCE has increased from 1.5% to 15.8% in less than a decade. In spite of the rapid progress in this field, there are still some issues need to be addressed.

(1) Till now, different approaches have been tried to improve the efficiency of the solar cells, such as chemical doping of graphene, passivation and band-engineering of the graphene/Si interface, introduction of antireflection layer or nano/microstructures to improve the light harvest, the graphene layer number controlling. Among them, chemical doping has been recognized as the most effective method to optimize the performance of the solar cells. Although many doping dopants have been introduced, the stability of solar cells is still a serious challenge due to the instability of the doping methods. Cui *et al.* presented a significant high-stability single-walled carbon nanotube (SWNT)/Si solar cells with a PCE over 11% after 10 months of exposure to ambient conditions.¹⁰⁰ Jia *et al.* demonstrated CNT (carbon nanotube)-oxide–Si cells with a PDMS anti-reflection layer, and the efficiency showed a slight drop from 10.9% to 9.1% for 20 days in air.¹⁰¹ Yang *et al.* reported a new structure of graphene–Si solar cells by introducing a graphene oxide (GO) interlayer to engineer the graphene–Si interface for improving device performance, the graphene/GO/Si solar cell retains 95% of its original level after storing in the open air for one week.¹⁰² Rehman *et al.* inserted an Al₂O₃ interlayer between the Si and graphene to suppress the surface charge recombination at the interface, and the efficiency of solar cell was not degraded after 9 months period exposed in ambient air.¹⁰³ So, a high stability of materials such as carbon nanotube can be used for a long-term stability of the solar cell. In addition, exploring new structure of the solar cells, for example, introduce an interlayer to engineer a graphene–Si interface to form a metal insulator semiconductor (MIS) structure may also be an important direction in this field.

(2) The PCE of the graphene/Si solar cells has reached up to 15.8% in just a few years, but it is still lower than that of commercial Si p–n junction solar cells. Due to the limitation of low electrical conductivity and low work function of graphene, the active area of the solar cell is relatively small (<10 mm²) in current research. Xie *et al.* and Zhang *et al.* demonstrated the device performance with various device areas from 4 to 100 mm², they found that with the increasing device area, the PCE decreased from 10.56% to 3.62%,⁵⁹ similarly, Shi *et al.* also reported that the PCE of graphene/Si solar cells decreased from 14.5% to 10.6% when the active area was increased from 4.7 mm² to 14.5 mm²,¹⁰⁴ all these results show that there is a close relationship between the active area and PCE. With a larger size of active area, the collection efficient of photogenerated charge



carriers would be lower because of the low conductivity of the graphene layer. In order to improve the conductivity of the graphene layer, Suhail *et al.* introduced with grid electrode to the conventional window-based device structure,¹⁰⁵ which could provide a low resistance contact to the graphene layer, and it might also help to improve the efficiency of carrier collection. So a fine metal electrode design should be adopted on the front to facilitate carrier collection through the entire device area.

(3) It is known that graphene is transferred on the Si substrate to form the Schottky junction, but graphene is easy to be cracked and torn, and copper particles residual may remain on the graphene film after the wet etching,^{91,106} all the defects and impurities at the graphene/Si interface can serve as short-circuit channels and lead to current leakage. Predictably, the current leakage will be more obvious at a larger device area. And large area of graphene is therefore a critical factor influencing the collection efficiency. In this regard, more matured production and transfer skills should be developed to decrease the defects and impurities arisen in the wet transfer process, and it is also a reliable method to grow graphene directly on semiconductor surface to get less structural defects and uniform film of graphene.¹⁰⁷

Conflicts of interest

There are no conflicts to declare.

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