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## Replacement of n-type layers with a non-toxic APTES interfacial layer to improve the performance of amorphous Si thin-film solar cells†

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Hydrogenated amorphous Si (a-Si:H) thin-film solar cells (TFSCs) generally contain p/n-type Si layers, which are fabricated using toxic gases. The substitution of these p/n-type layers with non-toxic materials while improving the device performance is a major challenge in the field of TFSCs. Herein, we report the fabrication of a-Si:H TFSCs with the n-type Si layer replaced with a self-assembled monolayer (3-aminopropyl) triethoxysilane (APTES). The X-ray photoelectron spectroscopy results showed that the amine groups from APTES attached with the hydroxyl groups (–OH) on the intrinsic Si (i-Si) surface to form a positive interfacial dipole towards i-Si. This interfacial dipole facilitated the decrease in electron extraction barrier by lowering the work function of the cathode. Consequently, the TFSC with APTES showed a higher fill factor (0.61) and power conversion efficiency (7.68%) than the reference device (without APTES). This performance enhancement of the TFSC with APTES can be attributed to its superior built-in potential and the reduction in the Schottky barrier of the cathode. In addition, the TFSCs with APTES showed lower leakage currents under dark conditions, and hence better charge separation and stability than the reference device. This indicates that APTES is a potential alternative to n-type Si layers, and hence can be used for the fabrication of non-toxic air-stable a-Si:H TFSCs with enhanced performance.

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### Introduction

The photovoltaic technology, which converts solar energy into electricity has been extensively studied,<sup>1,2</sup> leading to the development of hydrogenated amorphous Si (a-Si:H) thin film solar cells (TFSCs).<sup>3,4</sup> They are favorable due to their simple and inexpensive fabrication, possibility of deposition on flexible/bendable substrates<sup>5,6</sup> and the highly transparent nature has been recently exploited for “building integrated photovoltaic (BIPV)” systems.<sup>7,8</sup> However, the greatest challenge in further development of high performance a-Si:H TFSCs is without (w/o) the involvement of hazardous/toxic doping gases.<sup>5,6,9,10</sup> The fabrication of such devices should be carried out under extremely safe environments. In addition, the use of hazardous/toxic doping gases causes device instabilities because of carrier trapping and the formation

of Si dangling bonds.<sup>5,6</sup> Many alternate processes have been investigated and toxic-free layers have been utilized using various processes in the fabrication of a-Si:H TFSCs. For example, Jung *et al.*<sup>9</sup> demonstrated the deposition of a  $V_{2}O_{5-x}$  window layer using RF magnetron sputtering as an efficient replacement of p-type silicon layer resulting in a power conversion efficiency (PCE) of about 7.04%. Ryu *et al.*<sup>5</sup> presented the scheme for replacement of both p- and n-type layers with  $MoO_3$  and LiF, respectively processed with thermal deposition achieving a PCE of ~7.08%. Furthermore, in crystalline silicon solar cells, Battaglia *et al.*<sup>11</sup> substituted the p-type Si layer with  $MoO_3$  and achieved a PCE of 14% while, Bullock *et al.*<sup>12</sup> presented a maximum efficiency of 20% by incorporating dopant-free alkali metal fluorides and metal oxides for electron and hole selective carriers, respectively. Masuda *et al.*<sup>13</sup> exhibited a completely toxic-free and solution-processable method for the deposition of p-type, n-type and amorphous (intrinsic) i-Si layers. However, the low PCE (0.3–0.51%) makes it unsuitable for the commercial adoption and therefore, the exploration of other alternatives is highly desirable.

Among all the alternatives, materials with amine groups (–NH<sub>2</sub>) have drawn greater attention as they are not only dopant-free but are also capable of generating an interfacial dipole, which can effectively tune the work function ( $\phi$ ) of the electrodes.<sup>6,14</sup> In this regard, Song *et al.*<sup>15</sup> proposed a promising approach to fabricate air-stable inverted organic solar cells using

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a self-assembled monolayer (SAM) of 3-amino-propyltriethoxsilane (APTES). However, it is to be noted that in majority of the proposed methods, the orientation of the APTES molecule initiated with  $-\text{Si}$  binding itself on the substrate and  $-\text{NH}_2$  facing outwards close to the cathode.<sup>15,16</sup> This resulted in the generation of a positive interfacial dipole<sup>17</sup> from the cathode towards the active layer. Interestingly, in this study, the orientation of APTES was in the opposite direction, *i.e.* the  $-\text{NH}_2$  groups bonded with the hydroxyl ( $-\text{OH}$ ) groups present on the substrate through hydrogen bonding, as revealed by the X-ray photoelectron spectroscopy (XPS) and as demonstrated by Acres *et al.*<sup>18</sup> In this study, we investigated the performance of the SAM interfacial layer (APTES) with inverse orientation as an alternative to n-type layers for a-Si:H TFSCs. To the best of our knowledge such an investigation has not been carried out till date. The APTES SAM generated an interfacial dipole, lowering the  $\varphi$  of the cathode. This lowering of  $\varphi$  improved the charge extraction from the active layer. The devices with an APTES n-type interfacial layer showed significantly higher fill factors (FFs), PCEs, and stability (low leakage current) than the TFSCs w/o APTES.

## Experimental

### Materials and methods

Textured fluorine-doped tin oxide (FTO, Pilkington)-coated glass (Type NSG TEC™ 7 with a sheet resistance of  $6\text{--}8\ \Omega \text{sq}^{-1}$ , thickness of about 3 mm, visible light transmittance 80–82%) was cleaned by ultrasonication and rinsing in ethanol and deionized water (DI) for 15 min. The intrinsic Si (i-Si) layer (500 nm) was deposited using a mixture of silane ( $\text{SiH}_4$ ) and hydrogen ( $\text{H}_2$ ) (50 sccm/50 sccm) with chamber's working pressure of 400 mTorr with a very high frequency (VHF, 40.7 MHz) and power of 20 W. The p-type Si layer (12 nm) was deposited under the RF power of 50 W using a combination of different gases including  $\text{SiH}_4/\text{H}_2$  (30 sccm/150 sccm) and  $\text{B}_2\text{H}_6$  (9 sccm). For both layers, the plasma-enhanced chemical vapor deposition (PECVD) power density was maintained at  $0.074\ \text{W cm}^{-2}$  keeping the electrode dimensions in consideration. Approximately 15.7 mm distance was maintained between the shower head and the upper electrode at 250 °C.

In order to develop a silanol-terminated i-Si layer, we used ultra-violet ozone at 187 nm for 20 min, which increased the amount of O and H components on the surface. The APTES solution was purchased ( $\geq 98\%$  purity, Sigma Aldrich, USA) with a shelf-life of 2 years and was used w/o any further processing. A thin APTES layer ( $\sim 2\ \text{nm}$ ) was spin coated on the i-Si layer at 2000 rpm for 40 s followed by drying<sup>18</sup> for 60 s. The APTES layer is usually unstable under intense environmental conditions such as presence of moisture, high humidity and any other chemical substances,<sup>19</sup> therefore, to eliminate any possible adverse effects, the deposition process was conducted in a glove box with a precise control of humidity/temperature. After deposition of APTES layer, the substrate was immediately transferred to the vacuum chamber where a double layer of LiF (1.5 nm)/Al (100 nm) was deposited as the cathode at a base pressure of  $\sim 2 \times 10^{-6}$  torr. A shadowed mask was used by thermal evaporation to define the active area ( $0.25\ \text{cm}^2$ ) of the

cell. The overall process ensured that the APTES layer was not considerably exposed to any harsh environmental/chemical conditions and indicates its adequate stability.

### Characterizations

The surface morphology of the deposited APTES layer was examined using a multimode 8 atomic force microscope (AFM, Bruker) equipped with a Nanoscope V controller. The AFM measurements were carried out in the tapping mode at room temperature with  $384 \times 384$  pixels over a scan size of  $3 \times 3\ \mu\text{m}$  at a scan rate of 1 Hz. We used an Al-coated silicon probe (NCHR, Nanoworld, Germany) with a nominal spring constant of  $40\ \text{N m}^{-1}$  and a resonance frequency of 320 kHz. The compositional analysis was carried out using X-ray photoelectron spectroscopy (XPS, VG Microtech ESCA 2000). An Al  $\text{K}\alpha$  photon source was used to examine the composition of the APTES layer. The chemical bonding states of the layers were investigated in detail using their N1s and Si2p spectra normalized and de-convoluted with difference Gaussian peaks. Ultraviolet photoelectron spectroscopy (UPS) analysis was carried out at an energy resolution of 0.05 eV and the samples were biased at  $-15.0\ \text{V}$ . The performance of the a-Si:H TFSCs under light and dark conditions was evaluated using a Keithley 2400 source meter with an intensity of  $100\ \text{mW cm}^{-2}$ . The simulated illumination using a solar simulator (Pecell Technologies, PEC-L11) was carried out under AM1.5. The external quantum efficiency of the devices was measured using an incident photocurrent efficiency measurement system equipped with a 200 W Xe lamp and a grating monochromator. The light intensity measurements were carried out using a calibrated Si solar cell. The device efficiency data in Table 1 presents the average performance from 5 devices with a cell active area of  $0.25\ \text{cm}^2$  and the approximate yield of the devices was observed to be more than 60%. The surface wettability of the APTES layer was analyzed using the smart drop standard (FEMTOBIOMED, South Korea). The contact angle (CA) system was integrated with single non-approximated algorithm numerical calculations of Bash forth-admans (equation) and was free from disturbance. An ultra high-resolution field emission scanning electron microscope (UHR FE-SEM, S-5500, Hitachi, at Korea Basic Science Institute, Jeonju Center) was used at an accelerating voltage of 7 kV for the analysis of the surface morphology of the layers.

## Results and discussion

Fig. 1(a) the schematic of the a-Si:H TFSCs device structure using the conventional layout (textured fluorine-doped tin oxide (FTO, Pilkington)/p-type Si/intrinsic Si (i-Si)/APTES/lithium fluoride (LiF)/Al). Due to the requirement of toxic/hazardous doping gases in the processing of n-type layer deposition, APTES was utilized as the replacement. This SAM layer consists of a specific molecular arrangement owing to the presence of  $-\text{NH}_2$ , silane, and carboxyl groups ( $-\text{C}=\text{O}$ ).<sup>15,20,21</sup> The positive and negative charge distributions on  $-\text{NH}_2$  and  $-\text{O}$ , respectively created a dipole moment, which depended on the electron



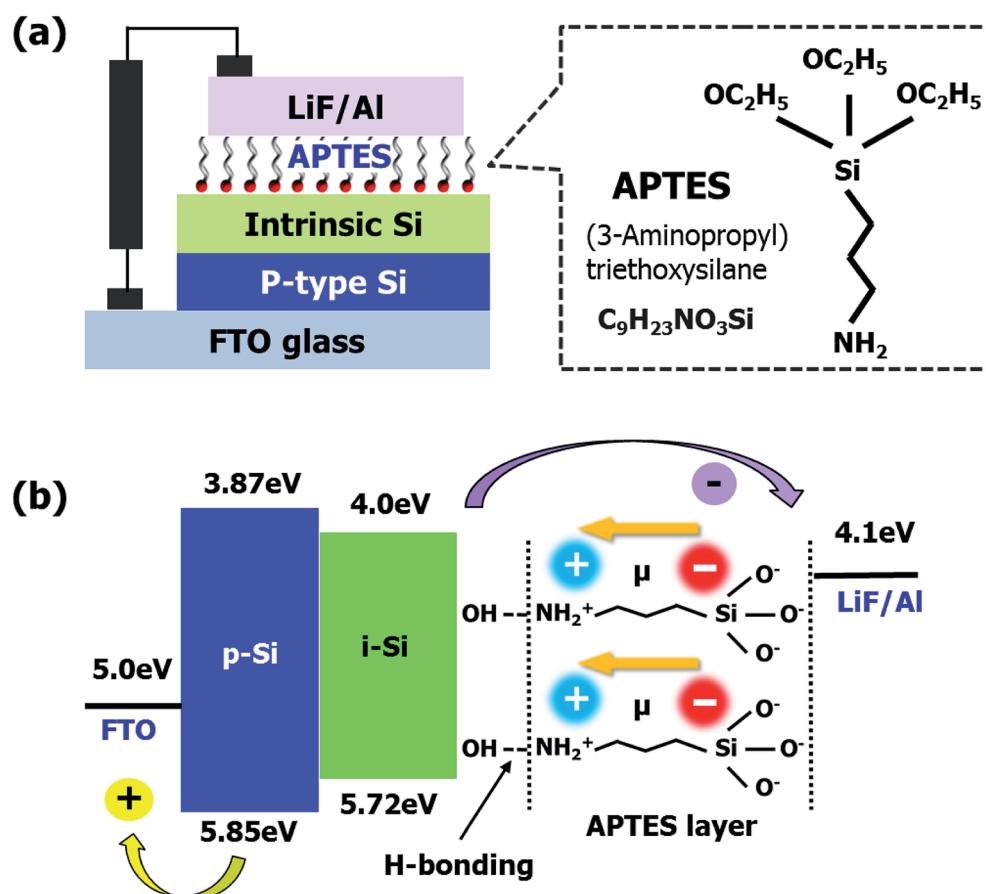
**Table 1** APTES TFSCs device performance (including standard deviation) comparison with various device structures including bare i-Si, P-I-N and PFN

Device structure	$J_{sc}$ (mA cm $^{-2}$ )	$V_{oc}$ (V)	FF (fill factor)	PCE (%)	$R_s$ ( $\Omega$ cm $^2$ )	$R_{SH}$ ( $\Omega$ cm $^2$ )
FTO/P-I Si/Al	$13.82 \pm 0.12$	$0.63 \pm 0.01$	$0.48 \pm 0.01$	$4.30 \pm 0.06$	18.20	$3.2 \times 10^3$
FTO/P-I Si/APTES/Al	$14.42 \pm 0.10$	$0.75 \pm 0.01$	$0.50 \pm 0.01$	$5.52 \pm 0.05$	27.60	$7.35 \times 10^3$
FTO/P-I Si/LiF/Al	$15.62 \pm 0.15$	$0.78 \pm 0.01$	$0.58 \pm 0.01$	$7.18 \pm 0.06$	12.00	$9.92 \times 10^3$
FTO/PIN-Si/Al <sup>a,10</sup>	15.15	0.82	0.61	7.60	6.4	$6.1 \times 10^3$
FTO/PI-Si/PFN/Al <sup>a,6</sup>	15.04	0.80	0.59	7.17	19.0	$1.1 \times 10^5$
FTO/P-I Si/APTES/LiF/Al	$15.51 \pm 0.08$	$0.80 \pm 0.01$	$0.61 \pm 0.01$	$7.68 \pm 0.03$	11.70	$9.52 \times 10^3$

<sup>a</sup> The data has been reported before, using the same fabrication tool and conditions as mentioned in this work and was added in the table for comparison purpose.

withdrawing and donating natures of the atoms placed transversely in the molecule (Fig. 1(b)).<sup>16</sup> It is well-known that in APTES-related processes, the silane present in APTES attached itself to the substrate through hydrogen bonding, while the  $-\text{NH}_2$  groups pointing outwards contribute to the generation of an interfacial dipole with a dipole direction opposite to the direction of electron extraction.<sup>15,16</sup> However, in this study, the molecular orientation of the APTES deposited on i-Si was reversed (Fig. 1(b)), where  $-\text{NH}_2$  attached to the  $-\text{OH}$  from i-Si through hydrogen bonding while silane and  $-\text{C}=\text{O}$  (EtO)

acted as the terminal groups near the LiF/Al cathode. Consequently, an interfacial dipole with the dipole direction opposite to the direction of electron extraction was obtained thus reducing the electron extraction barrier ( $\delta$ ). The presence of these functional groups and orientation was analyzed in detail using XPS (Fig. 2) with full survey shown in Fig. S1.† Fig. 2(a) shows the Si2p XPS spectra of the bare and APTES-treated i-Si layers. The APTES-treated i-Si layer showed Si-O bonds (100% contribution) at  $\sim 103$  eV.<sup>22</sup> The bare i-Si sample showed an additional Si-O peak with  $\sim 22\%$  contribution at  $\sim 102$  eV



**Fig. 1** (a) Schematic of the a-Si:H TFSCs with APTES as the n-type layer. Chemical structure of APTES demonstrating the presence of  $-\text{NH}_2$ ,  $-\text{Si}$ , and  $-\text{C}=\text{O}$  functional groups with an inverse orientation and dipole direction towards the i-Si layer. (b) Band-gap alignment of the layers used in the devices with APTES as an interfacial layer between the i-Si layer and the cathode demonstrating the arrangement of APTES molecules on the i-Si layer and the direction (yellow arrow) of the interfacial dipole.



(Fig. 2(d)) along with the bulk Si peak at  $\sim 99$  eV.<sup>23</sup> This could be attributed to the oxidation/hydroxylation of bare i-Si, leading to the availability of  $-\text{OH}$  groups on the layer surface. The N1s profile<sup>15</sup> of the APTES-treated i-Si layer showed the presence of free  $-\text{NH}_2$  groups ( $\sim 400$  eV) from APTES (Fig. 2(a)). The deconvolution process (Fig. 2(c)) revealed the presence of an additional  $-\text{NH}_2$  peak at  $\sim 401$  eV (with 20% contribution) corresponding to the hydrogen-bonded  $-\text{NH}_2$  ( $\text{NH}_3$ ).<sup>18</sup> This analysis provided a concrete evidence for the inverse molecular orientation of APTES on i-Si, as mentioned above and reported previously.<sup>18</sup> Regarding the nature of the APTES bonding with the i-Si surface, Kim *et al.*<sup>24</sup> reported that the APTES films could be formed in both covalent and non-covalent (physical bond) manners, however if the process is carried out using aqueous solutions, the greater probability favors the formation of hydrogen bonds or electrostatic interactions (van der Waals). In our work, the APTES layers were deposited using spin coating of the APTES solution onto the i-Si substrate, therefore we could speculate the establishment of hydrogen bonds between APTES

( $-\text{NH}_2$ ) and  $-\text{OH}$  from the i-Si substrate. The deposition process for device fabrication was carried out in a glove box, however for UPS/XPS analysis, the samples were taken out of the chamber and transferred. As APTES is a hydrolytically unstable material,<sup>25</sup> an exposure to air/moisture might have disengaged the weak hydrogen bonds or even possibly reoriented the molecules due to the change in molecular environment.<sup>18</sup> Therefore, the observed contribution (20%) only provides an indication about the possible orientation of the molecules on the surface. However, if we may perform XPS/UPS analysis within the controlled environmental conditions (*in situ* measurement), it might be possible to observe an appropriate percentage of the  $\text{NH}_2\text{-H}$  bonding. Overall, this orientation of APTES on i-Si was found to be coherent with the attachment possibilities of APTES on Si substrates demonstrated by Acres *et al.*<sup>18</sup> (Fig. S2†) and is also in accordance with the decrease in work function observed due to the interfacial dipole (towards i-Si). The surface morphologies of the bare and APTES-treated i-Si layers were examined using atomic force microscopy (AFM, Fig. S3†) and

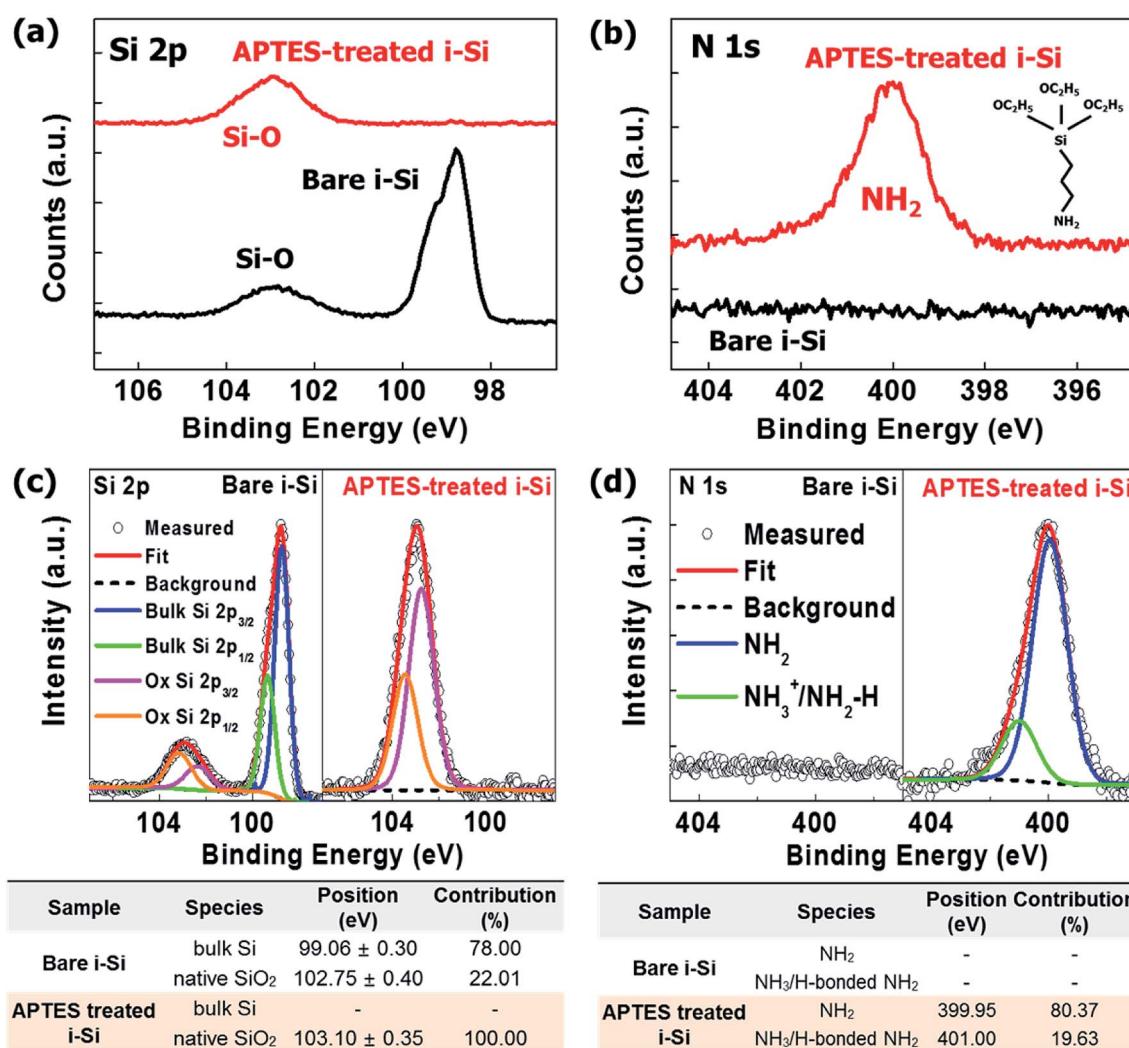


Fig. 2 High resolution (a) Si2p and (b) N1s XPS profiles of the bare i-Si and APTES-treated i-Si layers to analyze the composition of the layer and nature of bonds. (c) Si2p and (d) N1s spectra were carefully normalized and de-convoluted with difference Gaussian peaks and the table summarizes the contribution of the analyzed functional groups and bonds.



were further analyzed by the ultra-high-resolution field emission scanning electron microscopy (UHR FE-SEM, Fig. S4†). Because of the underlying FTO with a surface roughness of  $30.81 \pm 0.3$  nm,<sup>26</sup> the bare i-Si layer samples demonstrated a matching surface roughness (root mean square (RMS) =  $\sim 30.21 \pm 0.1$  nm) and hence a higher contact angle (CA =  $\sim 63^\circ$ ) of water droplet with the surface was observed (Fig. S4†). The RMS value for the APTES-based i-Si layer ( $29.69 \pm 0.1$  nm) was also found to be almost similar indicating a uniform deposition of the layer. However, a significant decrease in the CA (to  $\sim 29^\circ$ ) of the APTES-treated i-Si layer was because of the presence of an interfacial dipole (chemical aspect, Fig. S4†) generated by the APTES layer.

Fig. 3(a and b) shows the changes in the Fermi level (Fermi edge) of the i-Si layer after the deposition of the APTES layer. The i-Si layer had a  $\varphi$  of  $\sim 1.72$  eV, and the deposition of APTES resulted in the generation of interface dipoles in the i-Si/cathode interface, which in turn reduced the electron interface barrier significantly. This improved the vacuum level between the i-Si layer and the cathode, which caused a reduction of  $\sim 0.5$  eV in the electron barrier. Even though, the  $\varphi$  changes were observed for the i-Si layer and not for the LiF/Al cathode, and the reduction was oddly high (1.2 eV) (Fig. 3(c and d)), which could be due to the un-wanted oxidation of the i-Si layer owing to delayed ultraviolet photoelectron spectroscopy (UPS) measurement. However, APTES deposition reduced the  $\varphi$  of the cathode by  $\sim 0.5$  eV.<sup>15</sup> The analysis suggests that the deposition of APTES resulted in the reduction of the Schottky barrier at the interface between the i-Si layer and the cathode.

This improved the built-in potential ( $V_{bi}$ ) and band-gap alignment for efficient electron extraction, while suppressing the charge recombination.<sup>6</sup> This enhancement could be observed from the current density–voltage ( $J-V$ ) characteristics of the a-Si:H TFSCs analyzed under light (Fig. 4(a)) and dark conditions (Fig. 4(b)). The device performance parameters such as the short-circuit current ( $J_{sc}$ ), open-circuit voltage ( $V_{oc}$ ), FF, and PCE are summarized in Table 1. The FTO/PIN-Si/Al<sup>10</sup> device performance incorporated for comparison in the table were realized using the same fabrication tool and conditions as mentioned in this work and therefore can be suitably utilized for evaluation purposes. The comparison of devices w/o and with APTES using Al cathode demonstrated the worst  $J_{sc}$  ( $13.82$  mA cm<sup>-2</sup>) and  $V_{oc}$  (0.63 V), which was improved to  $14.42$  mA cm<sup>-2</sup> and 0.75 V, respectively with the insertion of APTES. In general, the overall lower  $V_{oc}$  observed for all devices could be further enhanced by proper optimization of the process conditions for deposition of i-Si.

Also, the shunt resistances ( $R_{sh}$ ) of the devices with and w/o APTES were found to be  $7.35 \times 10^3$  and  $3.2 \times 10^3$   $\Omega$  cm<sup>2</sup>, respectively. This indicates that APTES reduced the  $\varphi$  of the cathode, and hence enhanced the electron extraction and  $V_{bi}$  by the generation of an interfacial dipole. However, a discrepancy in terms of series resistance ( $R_s$ ) was observed, where despite of providing a higher  $V_{oc}$ , the APTES-treated devices demonstrated a high  $R_s$  of  $\sim 27.60$   $\Omega$  cm<sup>2</sup> as compared to the device w/o APTES ( $18.20$   $\Omega$  cm<sup>2</sup>) (Table 1). This can be attributed to the reverse polarization of the APTES molecules near the surface of the Al cathode, which occurred because of the direct contact of the

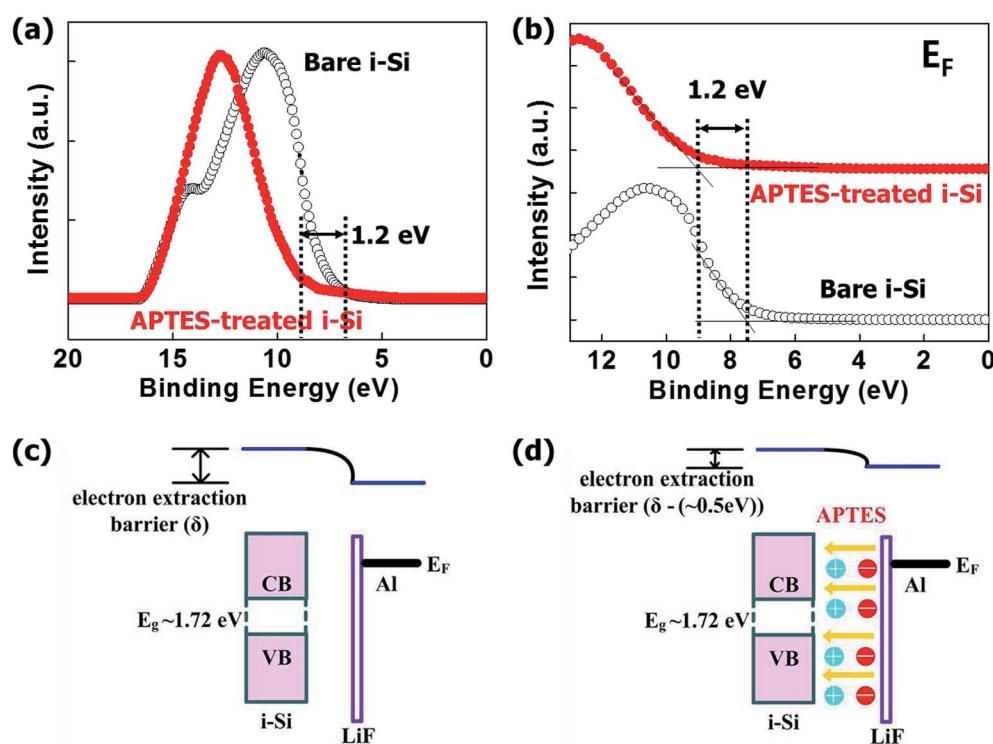


Fig. 3 (a) Normalized UPS spectra showing the Fermi levels of the bare i-Si and APTES-treated i-Si layers (b) enlarged (smaller scale) spectra to demonstrate the difference between the Fermi edges (c)  $\delta$  at the interface w/o APTES, (d) reduced  $\delta$  upon the addition of APTES layer.



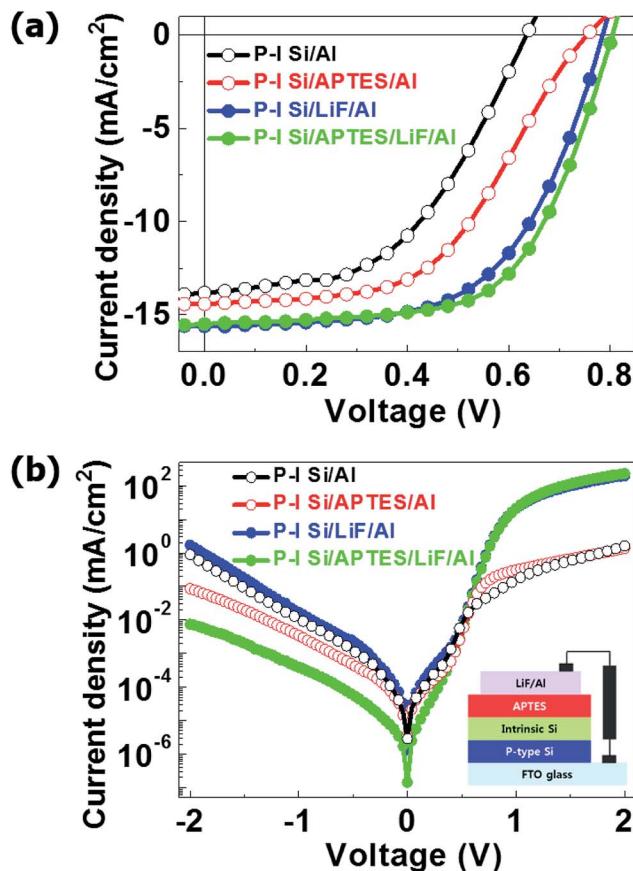


Fig. 4 Current density–voltage ( $J$ – $V$ ) curves of the devices w/o and with APTES layer, also the comparison with the presence/absence of LiF at the cathode (a) under illumination (b) under dark conditions.

SAM layer with the metal surface (Fig. S5†).<sup>17</sup> The polarization of APTES molecules reversed the dipole direction at the APTES/Al cathode interface, thus reducing the net dipole. The direct contact of APTES with Al increased the  $R_s$  and would have decreased the  $J_{sc}$  however, the  $J_{sc}$  was improved. The  $J_{sc}$  of TFSCs depends on their  $R_s$  and  $V_{oc}$ . In this study, the addition of the n-type APTES layer improved the  $V_{oc}$  of the TFSCs significantly, which in turn improved the  $J_{sc}$ .<sup>10,17</sup> The polarization effect was not observed when a LiF layer was used at the Al cathode. This prevented the direct contact between APTES and the metal surface. However, it is possible to overall improve the  $R_s$  of a device by utilizing different techniques to enhance the front/rear electrode contact with the underlying silicon layers. Moreover, the presence of a LiF layer in TFSCs reduces the  $\phi$  of the cathodes.<sup>27</sup> Hence, the LiF/Al cathode devices w/o APTES showed higher  $J_{sc}$  ( $15.62 \text{ mA cm}^{-2}$ ) and  $V_{oc}$  ( $0.78 \text{ V}$ ) than the Al cathode devices. The incorporation of APTES in the LiF/Al cathode TFSCs, further enhanced their  $V_{oc}$  to  $0.80 \text{ V}$  and reduced the  $J_{sc}$  slightly to  $15.51 \text{ mA cm}^{-2}$ . This discrepancy could be due to two reasons: first that the interfacial dipole was not efficiently generated by APTES, which was not the case as observed by the improvement of  $J_{sc}$  and  $V_{oc}$  in Al cathode device. Also, the creation of an interfacial dipole mainly improves the  $V_{bi}$  as mentioned before<sup>28,29</sup> and the resulting  $V_{oc}$  for APTES incorporated devices was enhanced. Second, the addition of LiF

increased the thickness of the interface between the i-Si layer and the Al cathode, thus increasing the Schottky barrier slightly. This is consistent with the slightly lowered  $J_{sc}$  and increased  $V_{oc}$  of the APTES-treated devices. The low  $R_s$  (Table 1) of the APTES-based devices ( $11.70 \Omega \text{ cm}^2$ ) as compared to that of the reference device ( $12 \Omega \text{ cm}^2$ ) confirms that the addition of APTES improved the  $V_{bi}$  of the device by generating an interfacial dipole. The devices with ( $9.52 \times 10^3 \Omega \text{ cm}^2$ ) and w/o ( $9.92 \times 10^3 \Omega \text{ cm}^2$ ) APTES showed similar  $R_{SH}$  values, indicating the saturation of  $R_{SH}$ . Moreover, the  $J$ – $V$  analysis under dark conditions of the devices w/o APTES (with LiF only) showed high leakage currents and hence low stability. This could be attributed to the low thickness and density of the LiF layer w/o APTES, which might have resulted in a poor thin-film coverage.<sup>5,9</sup> The addition of APTES with LiF reduced the leakage current and improved the  $V_{oc}$  of the devices significantly, which indicates that the addition of APTES improved the LiF layer density by providing adequate thin-film coverage and suppressed the charge recombination. Moreover, in terms of device stability enhancement Wang *et al.*<sup>30</sup> presented that the deposition of a thin ( $\sim 5 \text{ nm}$ ) APTES SAM could efficiently inhibit the diffusion of metals into adjacent dielectrics and could simultaneously improve the interfacial adhesion. Also, Ohta K. *et al.*<sup>31</sup> demonstrated a combination layer of CoWB and APTES to suppress Cu diffusion into  $\text{SiO}_2$ . Therefore, we could speculate that the use of APTES/LiF combination in our fabricated a-Si:H TFSC devices could significantly suppress the Al (cathode metal) diffusion into the amorphous silicon layers, thus improving the stability of the devices. We also evaluated the device performance of APTES a-Si:H TFSCS with the reported values of conventional P-I-N device<sup>10</sup> and another amine-based conjugated polymer interlayer poly[(9,9-bis(3'-(*N,N*-dimethylamino)propyl)-2,7-fluorene)-alt-2,7(9,9-diethylfluorene)] (PFN)-based solar cell.<sup>6</sup> In comparison to P-I-N devices, APTES TFSCs demonstrated an enhanced  $J_{sc}$ , PCE and  $R_{SH}$  even though FF remained unchanged due to an increased  $R_s$  attributed to the different nature of the interfacial contacts. While, in comparison to the PFN devices (amine-based), APTES-treated TFSCs exhibited superior performance in all device parameters. Overall, the APTES-incorporated devices exhibited higher FF (0.61) and PCE (7.68%) attributed to their enhanced  $V_{bi}$ , electron extraction and stability, thus projecting this toxic-free SAM layer as a competent alternative to other n-type/conjugated polymer layers in a-Si:H TFSCs.

## Conclusions

In this study, we used APTES as an alternative to n-type layers for a-Si:H TFSCs with improved performance and stability. The facile solution processability of the APTES layer w/o the involvement of any toxic doping gases is highly advantageous for the development of hazard-free solar cells. The  $-\text{NH}_2$  groups from the deposited APTES layer reacted with the  $-\text{OH}$  groups on the i-Si surface through hydrogen bonding to form a positive interfacial dipole towards the i-Si layer (opposite to the direction of electron extraction). The interfacial dipole generated by the APTES layer decreased the Schottky barrier at the interface and



$\varphi$  of the cathode. This resulted in enhanced electron extraction, suppressed charge recombination, and improved  $V_{bi}$ . As a result, the devices with APTES showed enhanced  $V_{oc}$  (0.80), FF (0.61), and PCE (7.68%) as compared to the devices w/o APTES. The low leakage current of the APTES-based TFSCs presented a strong stability of the devices, which further acknowledges the improvement in device performance and significance of the proposed method.

## Author contributions

S. Y. R. and C. S. K. are corresponding authors designed and conceived this work. H. H. and P. J. J. drafted the manuscript by providing comprehensive analysis. D. H. K., and D. K. C. fabricated the solar cell devices. C. M. L. and J. F. M. participated in the electrical characterizations. A. S. and K. B. C. analyzed the XPS/UPS results in details. M. K. S. provided valuable analysis and discussion.

## Conflicts of interest

The authors declare no conflicts of interests.

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